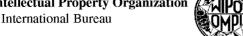
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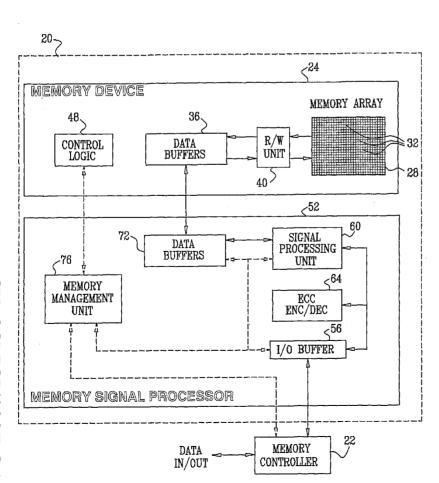
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(54) Title: MEMORY DEVICE WITH ADAPTIVE CAPACITY



(57) Abstract: A method for data storage in a memory (28) that includes a plurality of analog memory cells (32) includes estimating respective achievable storage capacities of the analog memory cells. The memory cells are assigned respective storage configurations defining quantities of data to be stored in the memory cells based on the estimated achievable capacities. The data is stored in the memory cells in accordance with the respective assigned storage configurations. The achievable storage capacities of the analog memory cells are re-estimated after the memory has been installed in a host system and used for storing the data in the host system. The storage configurations are modified responsively to the re-estimated achievable capacities.



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Category *	Citation of document, with indication, where ap	opropriate, o	of the relevant passages	Relevant to claim No.		
X	US 2005/0169051 A1 (Khalid et al.) 04 August 2005 (04,08,2005), paragraphs [0005],[0032],[0035],[0052],[0082]			1-117		
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