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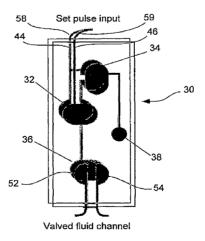
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(54) Title: MULTIPLEXED LATCHING VALVES FOR MICROFLUIDIC DEVICES AND PROCESSORS





(57) Abstract: Membrane valves and latching valve structures for microfluidic devices are provided. A demultiplexer can be used to address the latching valve structures. The membrane valves and latching valve structures may be used to form pneumatic logic circuits, including processors.

INTERNATIONAL SEARCH REPORT

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A. CLASSIFICATION OF SUBJECT MATTER			
IPC: F16K 11/22(2006.01)			
USPC: 137/597,907 According to International Patent Classification (IPC) or to both national classification and IPC			
B. FIELDS SEARCHED			
Minimum documentation searched (classification system followed by classification symbols) U.S.: 137/597, 907			
Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched			
Electronic data base consulted during the international search (name of data base and, where practicable, search terms used) EAST seach terms: logic, vacuum, diaphragm, membrane			
C. DOCI	JMENTS CONSIDERED TO BE RELEVANT		
Category *	Citation of document, with indication, where ap		Relevant to claim No.
X	US 5,775,371 A (PAN et al.) 07 July 1998 (07.07.199	98), column 1, line 30 to column 9, line	16-21
Υ	US 6,802,342 B2 (FERNANDES et al) 12 October 2004 (12.10.2004), column 1, line 14 to column 29, line 22.		1-16
x			17-21
Y			1-15
Y	US 3,433,257 A (JENSEN) 18 March 1969 (18.03.1969), column 1, line 20 to column 4, line 70.		1-15
Further documents are listed in the continuation of Box C. See patent family annex.			
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Date of the actual completion of the international search 15 May 2008 (15.05.2008)		Date of mailing of the international search report 1 5 1111 2008	
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