



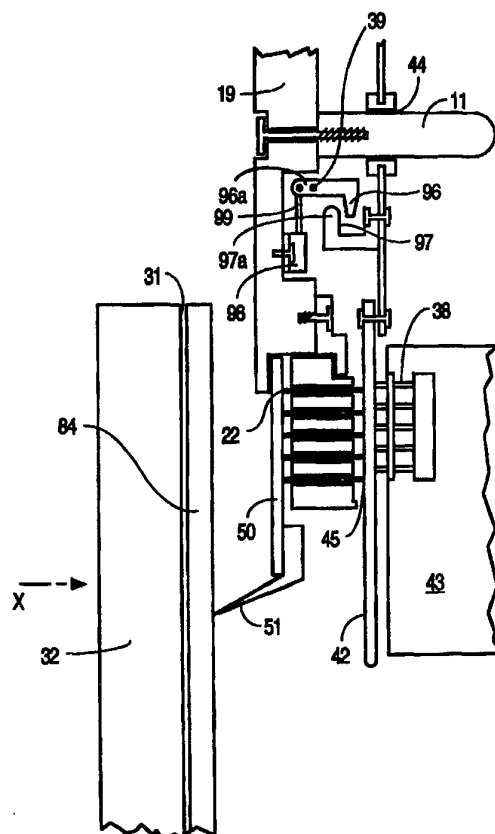
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(54) Title: PROBER AND TESTER WITH COMPACT INTERFACE FOR INTEGRATED CIRCUITS-CONTAINING WAFER HELD DOCKED IN A VERTICAL PLANE

(57) Abstract

A tester (40) for testing integrated circuits-containing semiconductor wafers or substrates (84), includes a vertically oriented performance board (42) with D/A converters (43) mounted and pin (38) connected immediately therebehind. A prober (10) including a vertical array of connector pins (12) mounts a vertical probe card (50) and a vertically-mounted chuck (32) on which a vertically oriented wafer or substrate (84) is held. One of the tester (40) and prober (10) are moved with respect to the other to dock and latch the tester (40) and prober (10) together. Simultaneously, the array of connector pins (22) is electrically connected to electrical connectors on the performance board (42) and probe needles (51) extending from a probe card (50) are placed into test contact with contact pads on the integrated circuits on the wafer or substrate (84).



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PROBER AND TESTER WITH COMPACT INTERFACE FOR INTEGRATED
CIRCUITS-CONTAINING WAFER HELD DOCKED IN A VERTICAL
PLANE

5 BACKGROUND OF THE INVENTION

Field of the Invention

This invention relates to a prober and tester for testing electrical characteristics of integrated circuits (IC's) which have been fabricated on silicon
10 or other semiconductor wafers or other substrates to determine which IC's are operational. More particularly, the invention is directed to an apparatus which permits testing of a wafer in vertical orientation where a prober can be docked to a tester
15 with required precision and with simplicity.

Related Art

Integrated circuits (IC's) are fabricated on silicon wafers and are routinely tested on the wafer to
20 determine which IC's are operational. After testing, the wafer is broken up into individual dies and each operational working die(s) is mounted into a ceramic, plastic or other package with external leads that can be connected to other devices. The operational
25 objective for testing on the wafer, for the IC manufacturers, is to only pay for packaging IC's which are operational i.e. have passed the testing. During testing, IC's which "FAIL" are suitably marked and then discarded prior to packaging of IC's which have
30 "PASSED".

Testing has become progressively more challenging because as individual device geometry in the integrated circuit shrink, the frequency of electrical signals in the device, commonly called clock speed, of the devices
35 increases roughly in proportion. Further, testing at the wafer probes must mimic the final application as closely as possible. As a result, the test equipment

also must operate at progressively higher frequencies.

In most recent wafer prober prior art, a test head of a tester is mounted on a head plate of the wafer prober, as typically seen in U.S. 4,517,512. An insert
5 ring is mounted in the head plate. A contact board is disposed on an upper surface of the insert ring with a card socket on the bottom surface. These are connected by cabling and a probe card is attached to the card
10 socket by pogo pins or sockets. A test head is positioned over the head plate with a performance board therebetween. The performance board and contact board are connected by pogo pins. A chuck with an IC's-
15 containing wafer mounted thereon is positioned below the probe card and needle-like contacts from the probe card contact predetermined bonding pads on each IC.

A major problem in mimicking the final application in test is that, in the final application, the packaged IC's are mounted a few millimeters apart connected by fine copper lines on a printed circuit board. In test,
20 the connection to the IC must be made from a test computer that generates the logic test patterns, to D/A (digital-to-analog) converters that convert the logic signals to an electrical signal that varies in time, through a set of interconnections, typically cables, to
25 a probe card, and finally to the IC. In traditional test configurations, a cable extends from the probe card positioned above a wafer to a tester cabinet containing the D/A converters. In U.S. 4,517,512 the D/A converters are mounted on the probe card of a test
30 head and cabling extends from D/A converters to the tester. The probe card comprises a set of probe needles arranged to make contact with the pads on the integrated circuit. The system is designed in this way so that specific test conditions can be generated on
35 the host computer under software control. The tester must exercise the chip through as many sets of

conditions as possible.

The actual testing process uses both a tester and a prober. A prober, as made by Electroglas Inc., Santa Clara, CA as model number 4080 or 4085, is a machine
5 that moves the wafer around underneath a probe card, and probes ICs on a horizontally oriented wafer by making repetitive contacts between numbers of small pads on each IC and the probe needles. A similar system is seen in U.S. 5,172,053. The base of the
10 prober contains electronics, a ring carrier consisting of a metal plate with a hole for a probe card, the ring carrier being mounted to the base by screws into a series of posts. The probe card is mounted in the hole in the ring carrier. The wafer is held on an XY stage
15 chuck more particularly on the chuck top via a set of vacuum rings. The chuck top is mounted, using screws to a Z/theta stage, that can move vertically and in rotation using stepper motors coupled to lead screws as seen in U.S. 4,066,943. The Z stage also has vacuum
20 pins that are used to transfer wafers as will be described later. The Z/theta stage is mounted to an XY stage, and the Z/theta stage is used to force the probe needles into contact with the pads on each successive IC. Every IC on the wafer can be tested. As used by
25 Electroglas, the Z/theta stage can be a ball bearing assembly as described in US 5,344,238 and sold as Electroglas Model #PZ7, and the XY motor can be a linear stepper motor as described in US 3,940,676. There is often a robot added to the prober to
30 automatically remove wafers from a cassette and load them on the chuck top as in Electroglas model 4080 and 4085.

The earliest tester connections in prior devices were made from D/A converters buried in the tester,
35 through a series of cables running from the tester to the prober. As the test frequencies became higher the

length of the cables from the D/A converters and the probe card limited the maximum test frequency. One solution as taught in US 4,517,512 was to place the D/A converters in a test head directly over the prober and then make a very short cable connections to the probe card. These cables carry less current and therefore allow higher frequency testing than the original cables to the probe card shown in prior devices.

There are a number of practical constraints that affect the length of cable and type of connections that can be used.

- 1) The tester must be able to connect to probers as described above and other types of machines that handle packaged IC's or be used for manual loaded individual packaged IC testing.
- 2) There are no common interface height and position standards.
- 3) The prober may be moved between different testers, and must be available for maintenance. Maintenance or adjustment may include the Z, X, and Y stages. In order to make consistent contact, the chuck top must be level with respect to the horizontal XY motion, and the probe card needles must be leveled with respect to the chuck top. The adjustment is usually provided by compression of pads underneath the chuck top or beneath the Z stage by mounting screws or adding thin strips of metal or "shimming". Ring carrier screws mounted at the corners of the ring carrier provide leveling of the probe card.
- 4) The D/A converters must be available for maintenance. The D/A converters may require adjustment or replacement.
- 5) The test head must be docked to the prober or

5 make electrical connection between the D/A
converters and the probe card. The outside
surface of the performance board typically
consists of radial connector points. The top
10 surface of the probe card consists of a
matching set of connector pads. Electrical
contact is achieved using a pogo pin ring.
The pogo pins are miniature compressible
connectors that are typically of two-piece
15 construction with an internal spring so
multiple contact can be made between two
uneven surfaces. A more complex structure
with the same operational objective is taught
in US 5,187,431 where the pogo pins are
20 typically held in a plastic ring that is
mounted to the top of the ring carrier.

When the performance board is brought into contact
with the pogo pins, the radial contact points must be
aligned with the pogo pins. This is achieved using
25 mechanical keys such as a pin that mates with a hole in
the test head. This procedure is commonly called
docking. The contact points are 1-2 mm in diameter, so
this defines the docking accuracy requirement.

There are additional complex interfaces with
25 additional layers and using connectors other than pogo
pins, as taught in US 5,329,226. However, there
remains a general need to accurately dock the test head
with the prober. In all cases the test head is
provided with the mechanical freedom in all axes to be
30 moved to align to the prober.

A test head can be as large as 91 cm in diameter,
and weighs 227 Kg. They contain a large collection of
very high performance D/A converters to generate the
signals. These test heads have become progressively
35 larger, more unwieldy, needing their own manipulator to
move them around and change their orientation.

Manipulators are described in US 5,241,870. The test head, manipulator and cables take up more and more costly facility space. Some manufacturers have gone to the extreme of erecting a gantry over the prober in order to move the test head around without using up floor space.

The test frequencies have increased by at least a factor of 10 since the introduction of modern test heads. The tester to D/A converter cables have now become a factor again in test frequency. There is thus a need for a more cost effective solution to providing a very high speed tester interface.

Kensington Laboratories, Inc. of Richmond, CA has developed a wafer inspection and defect review station in which a microscope is mounted to inspect a vertically mounted and insertable wafer semiconductor.

SUMMARY OF THE INVENTION

In accord with one aspect of this invention, a prober and tester is provided which allows the critical D/A converters to be built into the tester providing short connection paths from tester control to the D/A converters and to the device under test. The short connections are a key requirement for high frequency testing.

Further, the invention includes having the test head of the tester and the test head performance board mounted vertically with the D/A converters behind the performance board. Likewise, a prober is provided including a base with a YZ stage assembly which is capable of horizontal travel in an X direction, travelling on linear bearings. The stage assembly is vertically oriented so that the overall prober travels horizontally into docking relationship with a vertically-oriented test head of the tester. To dock the prober to the tester, the prober is rolled

horizontally into rough alignment with the tester and a rough Z height alignment performed. The YZ stage assembly is then moved in the X direction on linear bearings driven by a manually operated or motor driven lead screw. Alignment pins on the prober engage holes in the tester and a ring carrier substage moves in Y and Z directions to align pogo pins mounted in a probe card to contact points on the performance board. When the stage completes a relative motion toward the tester and is latched, the prober is docked vertically flush to the front of the tester so that the performance board and the probe card make electrical contact through a pogo-pin assembly. Thus a highly compact interface for very high speed testing is provided.

Advantages of the above arrangements are:

- 1) The D/A converters can be mounted in the tester minimizing the range of motion required in the D/A converter assemblies and reducing cable lengths or eliminating the need for external cable completely by replacing all cabling by pin connectors. The result will be a significantly higher speed electrical interface.
- 2) The footprint of the prober and tester combination will be greatly reduced compared to a conventional test head solutions. This will be a particular advantage as the wafer size and probes become larger in the future.

An important element of this invention is the movement of the ring carrier substage in the ring carrier which permits the ring carrier substage relatively free motion in the plane of wafer motion (Y and Z) while allowing very little motion orthogonal to the wafer motion (X) so that the probe needles remain level with the wafer. The wafer is mounted on the chuck top.

The apparatus of the invention has a tester including a test head having a vertically oriented performance board which has electrical connectors or contact points freely accessible from one vertical surface of the tester; a prober including a vertical array of connector pins adapted to be connected to the electrical connectors of the tester, a vertical probe card mounting a series of probe needles and an adjustable vertically-mounted chuck vertically mounting an integrated circuits-containing semiconductor wafer, each of the integrated circuits containing contact pads; and wherein at least one of said tester and said prober are movable along a horizontal path with respect to each other to latch and dock the tester and prober together, while electrically connecting selected ones of the connector pins to selected ones of the electrical connectors and the probe needles to the wafer contact pads.

20 BRIEF DESCRIPTION OF THE DRAWINGS

Fig. 1 is schematic side view of a vertically-oriented prober prior to docking with a vertically-oriented tester.

25 Fig. 2 is a schematic side view therefor, after docking with the tester partially broken away to show the D/A connection internal location.

Fig. 3 is a schematic perspective view of the prober showing the pogo-pin array end.

30 Fig. 4 is a schematic perspective partial view of the tester showing an exposed performance board cut-away to show the location of the D/A converters.

Fig. 5 is a schematic end view of the ring carrier and substage of the tester, cutaway to show a shear bearing connection therebetween.

35 Fig. 6 is a schematic side view of the shear bearing connection taken on lines 6-6 in Fig. 5 and

extending between the ring carrier and substage for fine alignment during docking of the prober.

Fig. 7 is a schematic end view of a second embodiment of the ring carrier connection including
5 orthogonal Z and Y axis linear bearings.

Fig. 8 is a schematic side view thereof taken on the lines 8-8 of Fig. 7.

Fig. 9 is a schematic perspective view of the stage chuck, a wafer mounted thereon and a stage X-Y
10 linear drive.

Fig. 10 is a schematic cross-sectional view of a magnetic air bearing usable in the Fig. 9 stage.

Fig. 11 is a schematic side view thereof taken on the line 11-11 of Fig. 10.

Fig. 12 is a schematic perspective partial view of the prober including an associated robotic system for transferring IC's-containing wafers from a wafer-holding cassette to interface with a vertically oriented chuck.
15

Fig. 13 is a schematic partially cutaway perspective view of a tester performance board and pogo-pins extending from a pogo-pin ring usable in the prober of Fig. 1.
20

Fig. 14 is a schematic side view showing a needle connection from the pogo-pin ring to the wafer.
25

Fig. 15 is a cross-sectional view of a pogo-pin.

DETAILED DESCRIPTION

The present invention is described below with
30 reference to the accompanying drawings.

Fig. 1 shows a prober 10 including a base 15 and vertical wall 35, with a YZ stage assembly 30 movably mounted on the wall and traveling on linear bearings 36, including a rail 36a and a rollers-containing cap 36b, with travel in the X direction that provides part
35 of a docking function. An example of an appropriate

linear bearing are crossed roller ways IKO #CRWU-60/205 made by Nippon Precision of Gifu, Japan and are used with an approximate travel of 100 mm. Equivalent products are made by Del-Tron Precision of Bethel, Connecticut, the bearings as seen more clearly in Fig. 3. The YZ stage assembly 30 comprises a chuck 32, a YZ stage 23 with linear bearings 36, and a ring carrier 19. The ring carrier may be mounted to corner posts 37 extending from the prober vertical wall 35. As in the previous examples of a conventional prober, the wafer is held on a chuck top 31 typically by vacuum. The chuck 32 is mounted to the YZ stage. A vertical-therefor array 22 of horizontal connector pins, such as pogo pins, is mounted over a probe card (hidden) in the ring carrier 19. Within the ring carrier is a substage 21. On the substage are mounted three alignment pins 11. The substage 21 is mounted in such a way as to provide from about 2 to about 5 cm of YZ motion to allow fine alignment of the prober 10 to a tester 40 as the alignment pins slide into the alignment holes 44 (Fig. 4) in the tester 40.

To dock the prober 10 to the tester 40, the prober is rolled into rough alignment on casters 16. An example of a suitable unit is made by Caster Technology Corp. Union City CA Model #75, and a set of fixed feet 17 are lowered to a fixed horizontal floor surface 20 on which the prober rolls. An example of a suitable foot unit is made by Vlier of Brighton, MA, known as the metric stud type. The feet 17 are used to ensure rough Z height alignment. Once the prober is ready for docking as shown in Fig. 1, the YZ stage assembly 30 is moved in the X direction on the linear bearings 36. The alignment pins 11 extending from the prober engage the holes 44 in the tester (Fig. 4). To effect docking, the ring carrier substage 21 moves in Y and Z, for example, by the flexibility of rubber plugs between

the substage and the ring carrier (Figs. 5/6), to align all the horizontal pogo pins 12 in the array 22 to electrical connectors or contact points 45 on the performance board 42 (as shown by arrow 46) of the tester 40 at the interface 14 (Fig. 13). The prober has a pivotable latching arm 96 (Fig. 14) in the ring carrier 19 which latches with a latch keeper 97 attached to the tester to effect completed docking. While the invention has been described in terms of the prober being moved into the tester horizontally, the tester unit 40 can be moved or both units may move into latching condition or one of the units may pivot on a vertical axis to close (like a door) on the other unit.

When the YZ stage assembly 30 moved forward and docked flush to the front of the tester at the interface 14, the performance board 42 and the pogo pins 12 in the pogo pin array 22 make electrical contact forming a highly compact interface 14 for very high speed testing, i.e. preferably in a range of from about 200 to about 500 megahertz in the case of wafer testing. Base extension 18 of the prober 10 is designed to mount a robotic loading assembly including a wafer cassette as described with respect to Fig. 12.

The D/A converters take the digital commands from the tester and convert them to analog signals that actually drive the electrical circuit under test. The performance of the electrical connections to these boards is a determiner of test performance. The D/A converters 43 are mounted in the tester (Fig. 4) eliminating the range of motion required in the prior art D/A converter assemblies and reducing cable lengths or eliminating cables completely. The result will be a significantly higher speed electrical interface.

The footprint of the prober and tester combination will be greatly reduced compared to a conventional test head solution. This will be a particular advantage as

the wafer size and prober size become larger in the future.

Performance board 42 is mounted in a vertical plane on a vertical 47 surface of the tester 40. The
5 tester 40 is laterally fixed to floor support surface 20 by feet 41. D/A converters 43 are mounted immediately behind the performance board 42 as seen in Figs. 2, 4 and 14.

As shown in Figs. 5 and 6, the ring carrier
10 substage 21 provides relatively free motion in the plane of wafer motion (Y and Z) while allowing very little motion orthogonal to the wafer motion (X) so that the probe needles remain level with the wafer. In a first implementation two overlapping lips 25 and 26
15 are formed, one on the ring carrier 19 and the other on the substage 21. Rubber plugs 27 are mounted in between the lips, by screws 28 to a metal or plastic plate 28a at the distal ends of the plugs. The rubber plugs are located at several locations around the lips.
20 The number and sizes of the plugs are determined by the forces that are available, and the need to minimize motion in the X direction. For example, eight plugs may be positioned evenly spaced about the periphery of the ring carrier and substage. The typical rubber plug
25 has a very low shear modulus, but a high compression modulus. The rubber plug is constructed of natural rubber having respective moduli of about 10^{-3} GPa and about 10 GPa. Motions in the Y and Z directions tend to move the rubber in shear, and in the X direction
30 compress the rubber. The rubber plugs produce a shear bearing with the specific functionality to allow fine alignment during docking of the prober.

An alternative assembly shown in Figs. 7 and 8, that will provide the same functionality shown in Figs.
35 5 and 6 uses a second pair of linear bearings 29 under the YZ stage assembly to provide < 50 mm of Y motion.

The ring carrier substage 21 has a pair of bearings 34 that allows motion in the Z direction. The bearing can be a linear bearing assembly. An example of a suitable bearing is a low profile crossed linear roller bearing
5 made by Del-Tron Precision. The substage is suspended by rubber strips 33. The two pairs of linear bearings 29, 34 provide orthogonal YZ motion for the ring carrier substage and the rubber strips 33 provide a counter force against gravity for the ring carrier
10 substage. Thus substage has an upper suspension provided by the pair of rubber strips.

The assembly in the ring carrier substage could equivalently be used to mount the performance board and D/A converters in the tester. The performance board
15 and D/A converters would then move in Y and Z under the forces of the alignment pins in order to provide fine alignment.

The next operation in making contact from the tester 40 to the wafer 84 mounted on the chuck top 31
20 is through a probe card 50 mounted in the ring carrier 19. Behind the ring carrier there is the Y-Z stage assembly 30 for moving the wafer mounted on the chuck 31 in the YZ plane in front of the probe card 50 (Fig. 14) and moving the wafer in the X direction into
25 contact with the probe needle 51. After each chip is tested, the wafer is moved in Y and Z to the next chip on the wafer. This is similar to the conventional process described above in the fifth paragraph of Related Art, except that the planes of motion have been
30 changed.

The YZ stage for this application preferably would operate in the vertical plane at high speeds up to 250 mm/sec-1 and accelerations up to 1 G. It provides
resistance to large forces in the X direction
35 perpendicular to the plane of motion of up to 100 kilograms. The forces are generated when up to 2000

probe needles must be forced into contact with the contact pads of an IC or a group of IC's. The YZ stage preferably will move less than 2 microns in the X direction under load.

5 A conventional YZ stage assembly suitable for this application consists of two linked linear motors in one axis and one in the other axis, in the form of an H bridge. The stage rides on the arm of the H bridge as in New England Assoc. Model OFS. A system that uses
10 lead screws to move a stage supported on air bearings is taught in US 4,677,649. DC servo motors and air bearings can be used as in the Dover Instruments Model XH stage. A vertical lead screw-based stage is also described in US 5,241,183.

15 The drawback to such a bridge system is that the force orthogonal to the plane of motion of the stage from compression of the probe needles must be supported by the H bridge. Also any twisting loads in acceleration or deceleration must be resisted by the H
20 bridge bearings. Finally, the motion flatness must be provided by the entire H assembly. If the orthogonal force is much greater than the motion load, then the H bridge must be strengthened to support the force alone.

 In a motor operating in a horizontal plane, the
25 load can ride on an air bearing on a controlling flat surface to provide very flat motion, or to provide resistance to orthogonal loads (US 5,040,431). A complex vertical stage with an air bearing reference plane and two linked stages is described in US
30 5,285,142 for aligning a wafer in a microlithography system. An air bearing system using a single stage cannot operate successfully in a vertical plane since there is no gravity to compress the air bearing.

 Figs. 9-11 illustrate a stage assembly that is
35 particularly effective for a stage assembly operable in the vertical plane with resistance to orthogonal loads

and twisting loads. The stage assembly includes a magnetically preloaded air bearing stage. As shown in Fig. 10, the stage 32 contains air passages 63 between a stage intermediate member 72 and a stage base 60 and
5 air passages 64 in stage base 60 for introduction of compressed air (arrow 66), and permanent magnets 62 mounted in the stage base 60. A stage cover 71 containing chuck top 31 is screw connected to stage intermediate member 72. The XY stage assembly 30 is
10 attached to a linear drive system 67 that moves the stage assembly in one (Y) direction on bar 67a (Fig. 9). The bar 67a that supports the Y drive is connected to two linear drive systems 68 that move the stage in Z along bars 68a. These linear drive systems can include
15 encoders (not shown) for accurate motion, and can have an air or linear ball bearing support and a linear stepper motor, linear DC servo motors, ball screw or lead screw drives. The most effective solution is an air bearing-supported DC servo motor available from
20 Dover Instruments, for example Model ES-600-S-250-CLS.

The base of the stage assembly more particularly the wall 35 is covered by a sheet of ferromagnetic material 61 such as iron or steel. The layout of the bottom of the stage base 60 is a regular pattern of
25 magnets 62 and air nozzles 69. The permanent magnets attract the magnetic sheet, and the compressed air floats the stage on a magnetically preloaded air bearing formed in gap 65. The compressed air is supplied to passage 63 through a hose in an umbilical
30 (not shown) that also carries drive signals to and from the Y linear drive. The use of the magnets 62 allows the stage to operate in any plane, even upside down. The use of a magnetic preload also minimizes the compression of the air bearing under load. The preload
35 on the air bearing is set by the number and strength of the magnets. Magnets such as rare earth cobalt magnets

such as M30,779 available from Edwards Scientific of Barrington, N.J. may be employed. Previous developments have used magnetic air bearings as a integral element of a motor, such as taught in US
5 3,940,676 or a DC servo motor running over a platen containing permanent magnets (US 4,654,571 and US 4,742,286). The present invention thus preferably uses a novel simple, magnetically constrained air bearing solely for resistance to orthogonal and twisting loads
10 in a low cost, high accuracy long travel stage.

An alternative implementation would be to use a two axis motor as the stage as described in US 4,654,571. The motor is physically linked to an H bar mechanism. Linear motion encoders would replace the
15 linear Y and Z motors. The combination of these elements provides a motor with drive elements close to the center of gravity of the load which minimizes twisting loads. Low cost encoding of the motion provides high accuracy, and automatic alignment of the
20 stepper motor with the steps in the platen. The requirement for alignment is shown in US 3,940,676. Alignment is currently a manual operation in a conventional motor as used in the Electroglas prober Models 4080 and 4085.

25 In addition to having a docking mechanism, a ring carrier and a YZ stage, the system includes a means for automatically loading and unloading wafers. There are commercial systems available such as the robot system used in the Kensington Laboratory Ergonomic Vertical
30 wafer/FPD inspection tool. A custom robot system shown in Fig. 12 provides the functionality in the smallest footprint including an XZ robot 79, a prealign station 80, a Y axis transfer robot 81, and a buffer station 82. The XZ robot includes two linear motion systems 83
35 such as those made by STAR LINEAR SYSTEMS. These motion systems use either a ball screw or a belt drive

to move a table in a ball rail system. The XZ robot picks up a wafer 84 from a cassette 85 by moving down in Z and then forward in X. The wand 86 is thin enough to pass between wafers in the cassette. Vacuum holes
5 87 in the front of the wand are connected through passages in the wand to a port (not shown) connected to a vacuum source. Vacuum pick up systems like this are standard in the wafer robot industry and are used by Kensington Laboratories. When the wand is in contact
10 with the wafer, the vacuum is turned on so the wafer is securely held. The XZ robot moves up in Z and forward in X to the front of the prober. It then moves down and back to deposit the wafer on the prealign chuck 88. The prealign chuck has a vacuum hole and connections
15 similar to the wand. The prealign chuck or station is a common device used to find the center of the wafer and orientation of the wafer through mechanical keys such as a notch. Stations for this purpose are sold by Kensington Laboratories under the designation High
20 Speed Optical Wafer Prealigner or a Non-contact Wafer Prealigner by ADE of Newton, MA Models PA-408/428. Typically these systems use the wafer to shadow mask a light source onto an array of detectors.

Once the wafer center and orientation have been
25 found, the wafer is rotated to the desired orientation and the transfer robot wand 89 moves forward to pick the wafer from the prealign station. The transfer robot is placed behind the prealign chuck before a wafer is placed on the prealign chuck. The X motion
30 for the transfer robot wand is provided by a small motorized stage 90 with approximately 1 cm travel oriented in the X direction, mounted to the linear motion table. An example of a suitable stage would be a NEWPORT of Irvine, CA, Model 433 with 850A-05-HS
35 motor. The transfer robot wand also has vacuum holes similar to the XZ robot wand. The transfer robot can

be another linear motion system as supplied by STAR LINEAR SYSTEMS of Charlotte, N.C. or equivalent.

If this is the first wafer of a new cassette, transfer robot wand is moved in the Y direction until
5 it is over the YZ stage 23. The chuck top 31 in the YZ stage is moved in X until the vacuum pins 93 are exposed. The transfer robot arm moves in X until vacuum is pulled on the pins and the wafer is held, the wafer is shown in outline 94. Vacuum is released on
10 the transfer robot, and the YZ stage moves up in Z so the chuck top clears the wand. The chuck top is then raised to support the wafer and the wafer is ready for probing. The transfer robot is then moved in Y back to the prealign station, ready for the next wafer.

15 Once the wafer on the chuck top is completely tested, the chuck top retracts, so that the wafer is supported on the pins. The YZ stage moves the wafer over a buffer arm 95 that also has a vacuum hole. The buffer arm moves on a small travel stage 91, similar to
20 the one on the transfer robot, in the X direction to pick up the wafer. Now the transfer arm moves the next wafer over the YZ stage and transfers the wafer as described above. After the YZ stage has moved away, the buffer arm moves back and/or the transfer robot
25 moves forward to transfer the wafer back onto the transfer robot. The transfer robot moves back to the prealign station where the XY robot picks up the wafer and returns it to the cassette.

Wands are selected to work together to accomplish
30 the goal. There are many other possible sizes and shapes that could be used. The essential functionality is to pick up a wafer, prealign it, and load it onto the YZ stage with a minimum delay.

Where the invention is described in terms of a
35 chuck-mounted wafer, the chuck may mount a glass or other material substrate having electronic circuits

thereon, such as active matrix liquid crystal displays, including contact pads which are to be probed for test purposes. Thus the term "wafer" as used herein includes such substrates.

5 Fig. 13 illustrates the contacting of pogo pins 12 which upon docking and undocking (arrow 46) contact and disengage, respectively, from contact points 45 on the performance board 42. Fig. 15 shows a typical pogo pin 12 comprising a first cylindrical, closed-end shell 55,
10 an inner cylindrical, closed end shell 56 telescoped therein, and a spring 57 extending in the interior of the shells between the closed ends.

 Fig. 14 illustrate the details of interface 14, where pogo pin array 22 makes contact with the
15 performance board 42. Latching is provided by the ring carrier 19 pivotally mounting (by pin 39) one end 96a of a pivoted latch arm 96. The arm 96 rides up an outward surface 97a of a latch keeper 97 mounted to the performance board, as the carrier moves into latching
20 engagement to engage the latch arm 96 with the keeper 97. Unlatching, after test, is accomplished by operation of a hydraulic cylinder 98 having a piston rod 99, pin attached to the end 96a of the arm 96, for pivoting the arm 96 out of the keeper 97 by a downward
25 stroke of the piston rod. A second set of pogo pins positioned in an array 38, connect the appropriate contact points 45 of the performance board 42 through plated-through holes (not shown) therein, directly to an edge connector on one more D/A converters 43. Thus
30 no cabling is required.

 The probe needles 51 may be of conventional or other construction, such as membrane probe cards supplied by Probe Tech of Santa Clara, CA. The needles extend from probe card 50 to contact the IC contact
35 pads on a wafer 84 mounted on the chuck top 31. A slight gap is shown between the chuck top and wafer so

as to more clearly show the two parts. The probe card is mounted in the hole in the ring carrier. The wafer is held on an XY stage chuck more particularly on the chuck top via a set of vacuum rings. The chuck top is
5 mounted, using screws to a X/theta stage, that can move in X and in rotation using stepper motors coupled to lead screws as seen in U.S. 4,066,943. The X stage also has vacuum pins that are used to transfer wafers. The X/theta stage is mounted to the YZ stage 32, and
10 the X/theta stage is used to force the probe needles into contact with the pads on each successive IC. Every IC on the wafer can be tested.

While the pogo pin assembly 22 is shown in Fig. 14 as connected to the ring carrier substrate 21, the
15 assembly may be connected to the performance board 42. Likewise, while Figs. 3 and 14 show the substage as being used for the fine alignment, it is contemplated that the performance board and the D/A converters may be mounted so as to be movable in the YZ direction by a
20 small amount i.e. about 2-5 cm., to provide the fine alignment of the pogo pin assembly to the probes.

The above description of embodiments of this invention is intended to be illustrative and not limiting. Other embodiments of this invention will be
25 obvious to those skilled in the art in view of the above disclosure.

CLAIMS

We claim:

1. Apparatus for testing integrated circuits-
containing semiconductor wafers comprising:

5 a tester having a vertically oriented
performance board including electrical connectors
freely accessible from a vertical surface of the
tester;

10 a prober including a vertical array of
horizontal connector pins adapted to be connected
to the electrical connectors of said tester, a
vertical probe card mounting a series of probe
needles and an adjustable vertically-mounted chuck
15 vertically mounting an integrated circuits-
containing semiconductor wafer, each of said
integrated circuits containing contact pads, said
chuck and mounted wafer being movable with respect
to said probe card; and

20 wherein at least one of said tester and said
prober are movable along a horizontal path with
respect to each other to dock said tester and
prober together to electrically connect selected
ones of said vertical array of horizontal
connector pins to selected ones of said electrical
25 connectors; and

 wherein movement of said chuck and mounted
wafer makes electrical contact of said contact
pads to said probe needles.

30 2. The apparatus of Claim 1 further including at
least one D/A converter in said tester, said at least
one converter being mounted behind said performance
board and electrically connected thereto.

35 3. The apparatus of Claim 2 wherein said at
least one converter is connected to said performance

board by an array of pogo pins.

4. The apparatus of Claim 1 wherein said prober includes a base and said chuck is attached to a YZ
5 stage assembly, said base travelling in an X direction to dock with said tester.

5. The apparatus of Claim 4 wherein said base includes a set of roller casters and wherein said
10 prober is rolled on said casters into rough alignment with said tester.

6. The apparatus of Claim 5 in which said base includes a set of adjustable feet, said feet being
15 adapted to be lowered to a fixed support surface for a Z height rough alignment with said tester.

7. The apparatus of Claim 4 wherein said YZ stage assembly is actuatable to align said vertical
20 array of horizontal connector pins to said electrical connectors and actuatable in an X direction to contact said probe needles to said contact pads of a first integrated circuit and successively to contact pads of other integrated circuits on said mounted wafer.

25
8. The apparatus of Claim 4 wherein said prober includes a vertical wall mounting a sheet of ferromagnetic material, and wherein said YZ stage assembly comprises a stage base, a pattern of permanent
30 magnets in said stage base, air passages and air nozzles in said stage base and wherein said nozzles are adapted to discharge a supply of compressed air to float the stage base on said sheet as a magnetically preloaded air bearing such that said air bearing
35 resists orthogonal and twisting loads on said vertically-mounted chuck.

9. The apparatus of Claim 4 wherein said YZ stage assembly comprises a YZ stage and a ring carrier including a substage, and wherein said substage is movable in YZ motion to allow fine alignment of said prober to said tester.

10. The apparatus of Claim 9 wherein said substage has free motion in a YZ plane of wafer motion while restricting motion orthogonal to wafer X motion such that the probe needles remain level with respect to a chuck-mounted wafer.

11. The apparatus of Claim 10 wherein said substage and said ring carrier include overlapping lips, and wherein a low shear modulus and high compression modulus mechanical connection extends between said lips.

12. The apparatus of Claim 11 wherein said mechanical connection comprises a series of rubber plugs.

13. The apparatus of Claim 4 wherein said YZ stage assembly has motion in the Y direction and said YZ stage assembly comprises a YZ stage and a ring carrier including a substage, and wherein said substage is movable in Z motion, and wherein the Y motion of the YZ stage assembly and the Z motion of the substage combine to allow fine alignment of said prober to said tester.

14. The apparatus of Claim 13 wherein said substage is mounted to said ring carrier, and further including an upper suspension between said ring carrier and said substage including a rubber strip and a pair of linear bearings in a Z axis and a linear bearing on

a bottom of the YZ stage for adjustment in the Y axis.

15. The apparatus of Claim 1 wherein said prober includes a latching arm and said tester includes a
5 latching keeper, said arm being latchable into said keeper upon docking of said prober and tester.

16. A method for testing integrated circuits-
containing semiconductor wafers comprising:

10 providing a tester having a vertically oriented performance board including electrical connectors;

15 providing a prober including a vertical array of horizontal connector pins and a wafer-mounting chuck;

moving one of the tester and prober toward the other along a predetermined path;

20 docking the tester and prober together to electrically connect said electrical connectors to said array of horizontal connector pins;

wherein the prober further includes a vertical probe card mounting a series of probe needles; and

25 further including the step, after the docking step, of moving said chuck and the mounted wafer to make an electrical contact of contact pads on integrated circuits on a wafer mounted on said chuck to said series of probe needles.

30 17. The method of Claim 16 further comprising:
prior to said docking step roughly aligning the prober and the tester; and

35 latching the prober and the tester with respect to one another to effect said docking step.

AMENDED CLAIMS

[received by the International Bureau on 12 November 1996 (12.11.96);
original claims 1-3, 8, 9, 13, 15 and 16 amended; original claim
17 cancelled; remaining claims unchanged (10 pages)]

1. Apparatus for testing semiconductor wafers
containing integrated circuits comprising:

5 a tester having a vertically oriented
performance board fixedly positioned with respect
to the tester, said performance board including
electrical connectors freely accessible from a
vertical surface of the tester;

10 a prober including a vertical array of
horizontal connector pins adapted to be connected
to the electrical connectors of said tester, a
vertical probe card mounting a series of probe
needles and an adjustable vertically-mounted chuck
15 vertically mounting a semiconductor wafer
containing integrated circuits, each of said
integrated circuits containing contact pads, said
chuck and mounted wafer being movable with respect
to said probe card; and

20 wherein at least one of said tester and said
prober are movable along a fixed horizontal path
with respect to each other to dock said tester and
prober together to electrically connect selected
ones of said vertical array of horizontal
25 connector pins to selected ones of said electrical
connectors and to said probe needles; and

wherein movement of said chuck and mounted
wafer makes an electrical contact between said
contact pads and said probe needles.

30

2. Apparatus for testing semiconductor wafers
containing integrated circuits comprising:

35 a tester having a vertically oriented
performance board fixedly positioned with respect
to the tester, said performance board including

electrical connectors freely accessible from a vertical surface of the tester;

5 a prober including a vertical array of horizontal connector pins adapted to be connected to the electrical connectors of said tester, a vertical probe card mounting a series of probe needles and an adjustable vertically-mounted chuck vertically mounting a semiconductor wafer containing integrated circuits, each of said
10 integrated circuits containing contact pads, said chuck and mounted wafer being movable with respect to said probe card; and

15 wherein at least one of said tester and said prober are movable along a fixed horizontal path with respect to each other to dock said tester and prober together to electrically connect selected ones of said vertical array of horizontal connector pins to selected ones of said electrical connectors;

20 wherein movement of said chuck and mounted wafer makes an electrical contact between said contact pads and said probe needles; and

25 further including at least one D/A converter in said tester, said at least one converter being mounted behind said performance board and electrically connected thereto.

3. Apparatus for testing semiconductor wafers containing integrated circuits comprising:

30 a tester having a vertically oriented performance board fixedly positioned with respect to the tester, said performance board including electrical connectors freely accessible from a vertical surface of the tester;

35 a prober including a vertical array of

horizontal connector pins adapted to be connected
to the electrical connectors of said tester, a
vertical probe card mounting a series of probe
needles and an adjustable vertically-mounted chuck
5 vertically mounting a semiconductor wafer
containing integrated circuits, each of said
integrated circuits containing contact pads, said
chuck and mounted wafer being movable with respect
to said probe card; and
10 wherein at least one of said tester and said
prober are movable along a fixed horizontal path
with respect to each other to dock said tester and
prober together to electrically connect selected
ones of said vertical array of horizontal
15 connector pins to selected ones of said electrical
connectors;
wherein movement of said chuck and mounted
wafer makes an electrical contact between said
contact pads and said probe needles;
20 further including at least one D/A converter
in said tester, said at least one converter being
mounted behind said performance board and
electrically connected thereto; and
wherein said at least one converter is
25 connected to said performance board by a second
array of connector pins.

4. The apparatus of Claim 1 wherein said prober
includes a base and said chuck is attached to a YZ
30 stage assembly, said base travelling in an X direction
to dock with said tester.

5. The apparatus of Claim 4 wherein said base
includes a set of roller casters and wherein said
35 prober is rolled on said casters into rough alignment

with said tester.

6. The apparatus of Claim 5 in which said base includes a set of adjustable feet, said feet being adapted to be lowered to a fixed support surface for a Z height rough alignment with said tester.

7. The apparatus of Claim 4 wherein said YZ stage assembly is actuatable to align said vertical array of horizontal connector pins to said electrical connectors and actuatable in an X direction to contact said probe needles to said contact pads of a first integrated circuit and successively to contact pads of other integrated circuits on said mounted wafer.

8. Apparatus for testing semiconductor wafers containing integrated circuits comprising:

a tester having a vertically oriented performance board fixedly positioned with respect to the tester, said performance board including electrical connectors freely accessible from a vertical surface of the tester;

a prober including a vertical array of horizontal connector pins adapted to be connected to the electrical connectors of said tester, a vertical probe card mounting a series of probe needles and an adjustable vertically-mounted chuck vertically mounting a semiconductor wafer containing integrated circuits, each of said integrated circuits containing contact pads, said chuck and mounted wafer being movable with respect to said probe card; and

wherein at least one of said tester and said prober are movable along a fixed horizontal path with respect to each other to dock said tester and

prober together to electrically connect selected ones of said vertical array of horizontal connector pins to selected ones of said electrical connectors;

5 wherein movement of said chuck and mounted wafer makes an electrical contact between said contact pads and said probe needles;

 wherein said prober includes a base and said chuck is attached to a YZ stage assembly, said
10 base travelling in an X direction to dock with said tester; and

 wherein said prober includes a vertical wall mounting a sheet of ferromagnetic material, and wherein said YZ stage assembly comprises a stage
15 base, a pattern of permanent magnets in said stage base, air passages and air nozzles in said stage base and wherein said nozzles are adapted to discharge a supply of compressed air to float the stage base on said sheet as a magnetically
20 preloaded air bearing such that said air bearing resists orthogonal and twisting loads on said vertically-mounted chuck.

9. Apparatus for testing semiconductor wafers
25 containing integrated circuits comprising:

 a tester having a vertically oriented performance board fixedly positioned with respect to the tester, said performance board including electrical connectors freely accessible from a
30 vertical surface of the tester;

 a prober including a vertical array of horizontal connector pins adapted to be connected to the electrical connectors of said tester, a vertical probe card mounting a series of probe
35 needles and an adjustable vertically-mounted chuck

vertically mounting a semiconductor wafer
containing integrated circuits, each of said
integrated circuits containing contact pads, said
chuck and mounted wafer being movable with respect
5 to said probe card; and

wherein at least one of said tester and said
prober are movable along a fixed horizontal path
with respect to each other to dock said tester and
prober together to electrically connect selected
10 ones of said vertical array of horizontal
connector pins to selected ones of said electrical
connectors;

wherein movement of said chuck and mounted
wafer makes an electrical contact between said
15 contact pads and said probe needles;

wherein said prober includes a base and said
chuck is attached to a YZ stage assembly, said
base travelling in an X direction to dock with
said tester; and

20 wherein said YZ stage assembly comprises a YZ
stage and a ring carrier including a substage, and
wherein said substage is movable in YZ motion to
allow fine alignment of said prober to said
tester.

25
10. The apparatus of Claim 9 wherein said
substage has free motion in a YZ plane of wafer motion
while restricting motion orthogonal to wafer X motion
such that the probe needles remain level with respect
30 to a chuck-mounted wafer.

11. The apparatus of Claim 10 wherein
said substage and said ring carrier include overlapping
lips, and wherein a low shear modulus and high
35 compression modulus mechanical connection extends

between said lips.

12. The apparatus of Claim 11 wherein said
mechanical connection comprises a series of rubber
5 plugs.

13. Apparatus for testing semiconductor wafers
containing integrated circuits comprising:

10 a tester having a vertically oriented
performance board fixedly positioned with respect
to the tester, said performance board including
electrical connectors freely accessible from a
vertical surface of the tester;

15 a prober including a vertical array of
horizontal connector pins adapted to be connected
to the electrical connectors of said tester, a
vertical probe card mounting a series of probe
needles and an adjustable vertically-mounted chuck
vertically mounting a semiconductor wafer
20 containing integrated circuits, each of said
integrated circuits containing contact pads, said
chuck and mounted wafer being movable with respect
to said probe card; and

25 wherein at least one of said tester and said
prober are movable along a fixed horizontal path
with respect to each other to dock said tester and
prober together to electrically connect selected
ones of said vertical array of horizontal
connector pins to selected ones of said electrical
30 connectors;

wherein movement of said chuck and mounted
wafer makes an electrical contact between said
contact pads and said probe needles;

35 wherein said prober includes a base and said
chuck is attached to a YZ stage assembly, said

base travelling in an X direction to dock with said tester; and

5 wherein said YZ stage assembly has motion in the Y direction and said YZ stage assembly comprises a YZ stage and a ring carrier including a substage, and wherein said substage is movable in Z motion, and wherein the Y motion of the YZ stage assembly and the Z motion of the substage combine to allow fine alignment of said prober to
10 said tester.

14. The apparatus of Claim 13 wherein said substage is mounted to said ring carrier, and further including an upper suspension between said ring carrier
15 and said substage including a rubber strip and a pair of linear bearings in a Z axis and a linear bearing on a bottom of the YZ stage for adjustment in the Y axis.

15. Apparatus for testing semiconductor wafers
20 containing integrated circuits comprising:

a tester having a vertically oriented performance board fixedly positioned with respect to the tester, said performance board including electrical connectors freely accessible from a
25 vertical surface of the tester;

a prober including a vertical array of horizontal connector pins adapted to be connected to the electrical connectors of said tester, a vertical probe card mounting a series of probe
30 needles and an adjustable vertically-mounted chuck vertically mounting a semiconductor wafer containing integrated circuits, each of said integrated circuits containing contact pads, said chuck and mounted wafer being movable with respect
35 to said probe card; and

wherein at least one of said tester and said prober are movable along a fixed horizontal path with respect to each other to dock said tester and prober together to electrically connect selected ones of said vertical array of horizontal connector pins to selected ones of said electrical connectors;

wherein movement of said chuck and mounted wafer makes an electrical contact between said contact pads and said probe needles; and

wherein said prober includes a latching arm and said tester includes a latching keeper, said arm being latchable into said keeper upon docking of said prober and tester.

15

16. A method for testing semiconductor wafers containing integrated circuits including contact pads comprising:

providing a tester having a vertically oriented performance board fixedly positioned with respect to the tester, and said board including electrical connectors;

providing a prober including a vertical array of horizontal connector pins and a wafer-mounting chuck;

mounting a semiconductor wafer containing integrated circuits including contact pads on said chuck;

moving one of the tester and prober toward the other along a fixed horizontal predetermined path;

docking the tester and prober together to electrically connect said electrical connectors to said array of horizontal connector pins;

providing a vertical probe card mounting a

series of probe needles on the prober; and

5 further including the step, after the docking step, of moving said chuck and the mounted wafer to make an electrical contact between contact pads on integrated circuit on the wafer and said series of probe needles.

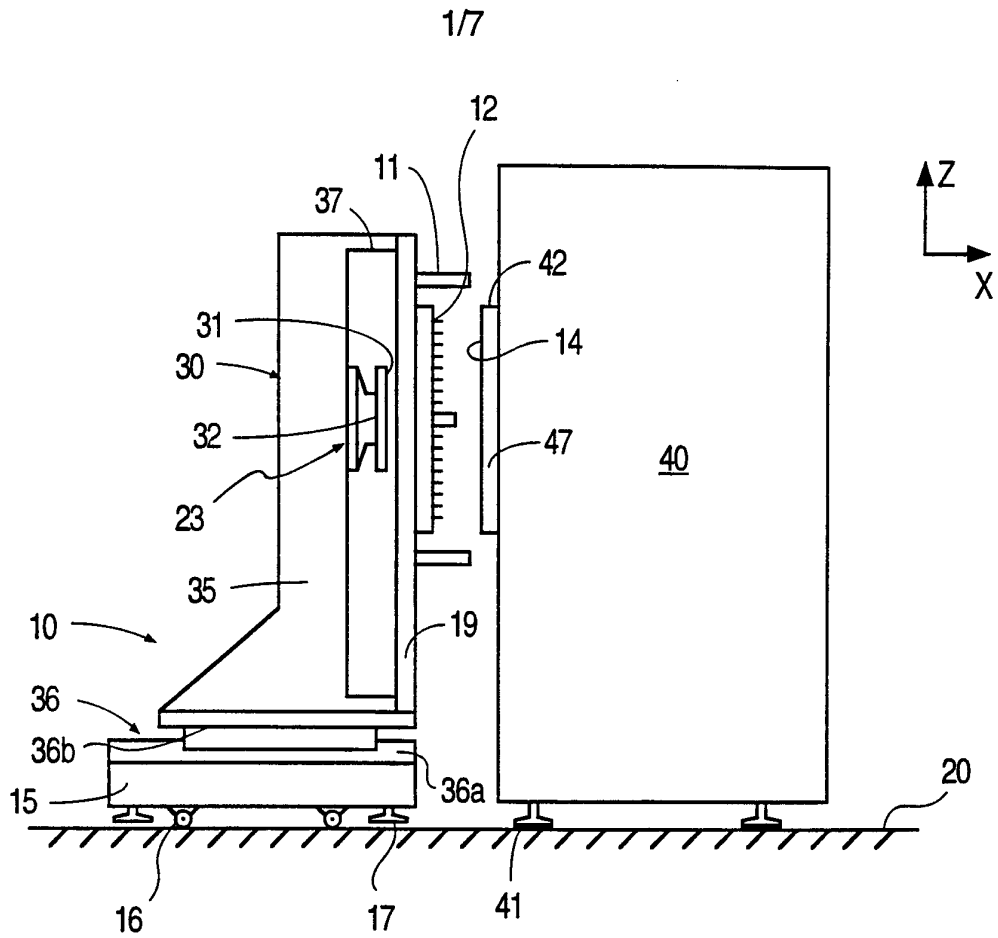


FIG. 1

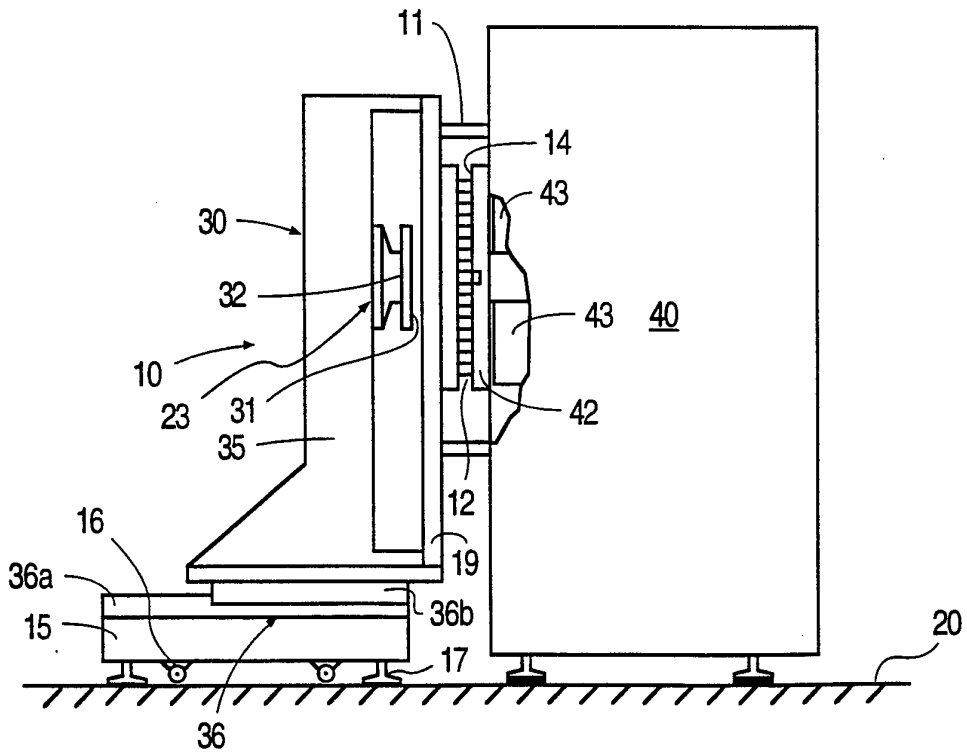


FIG. 2

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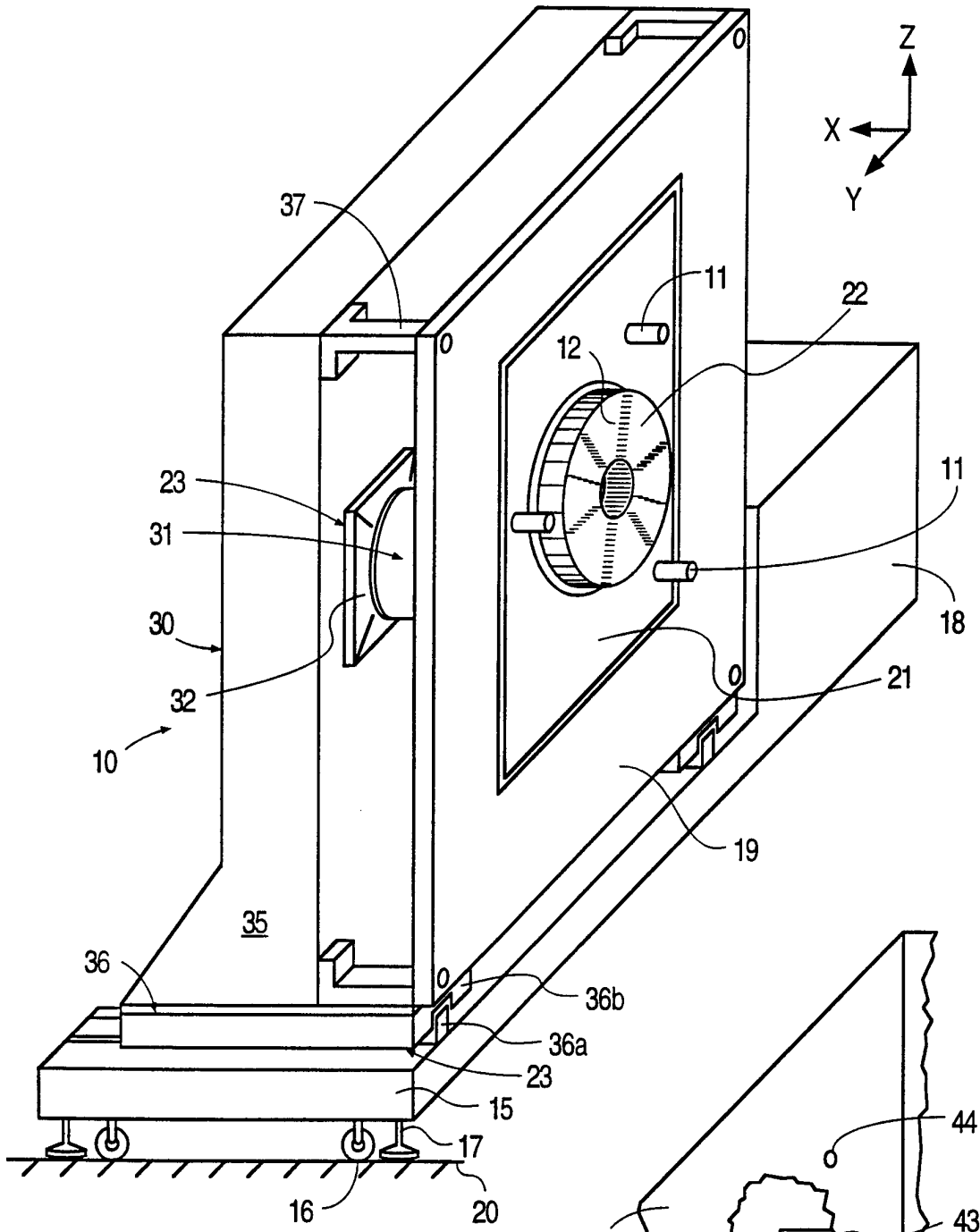


FIG. 3

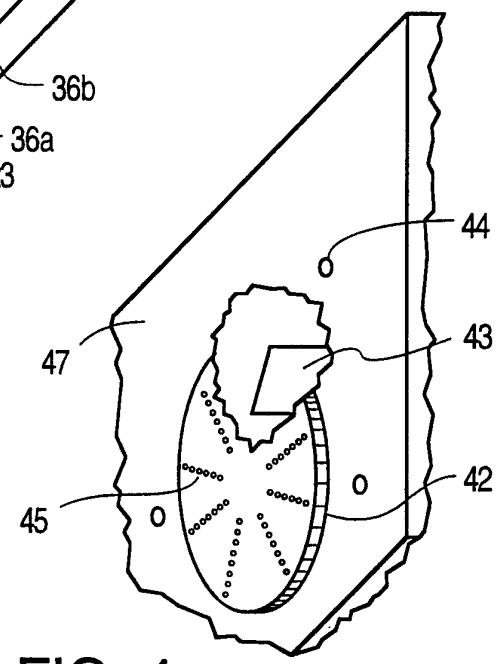


FIG. 4

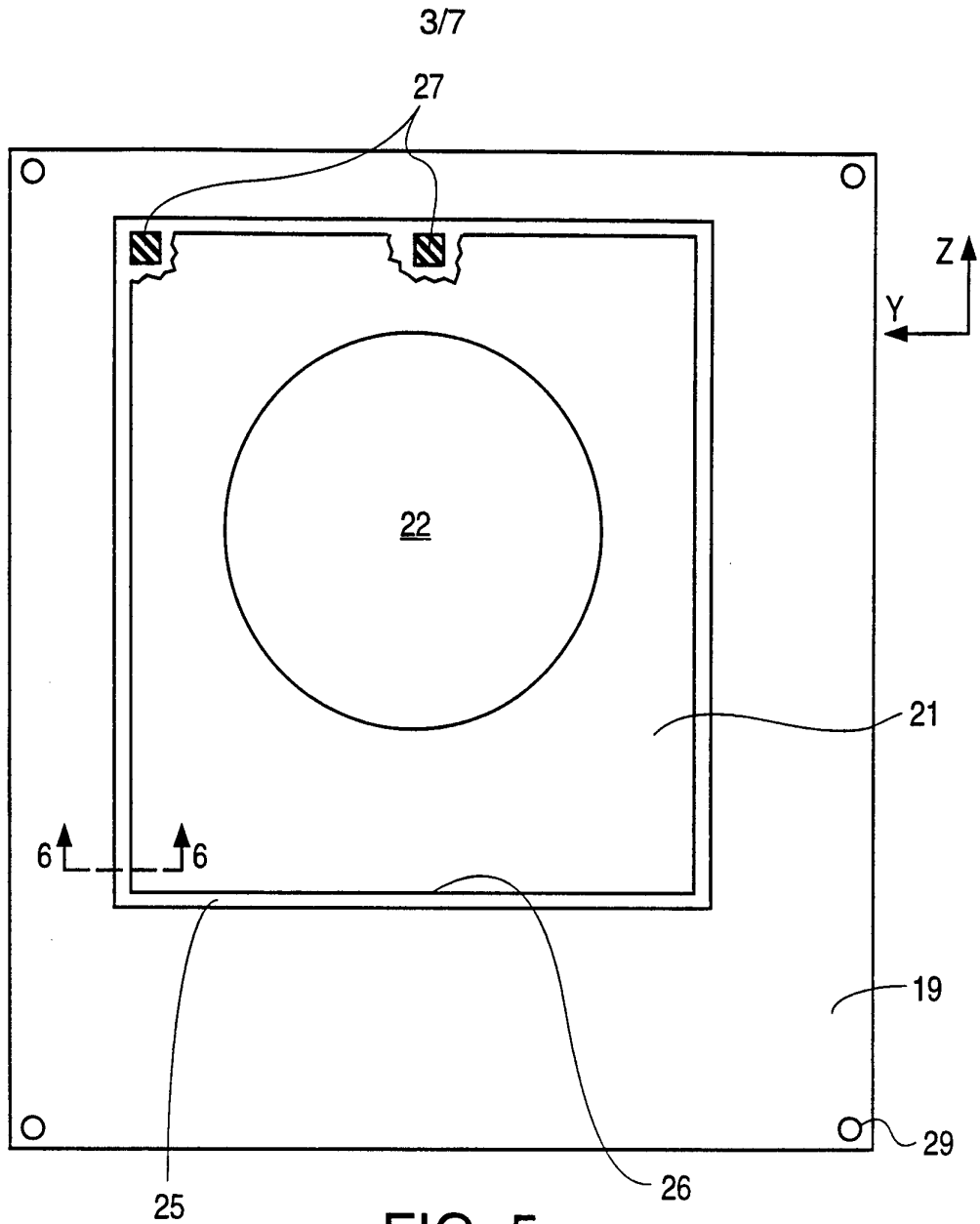


FIG. 5

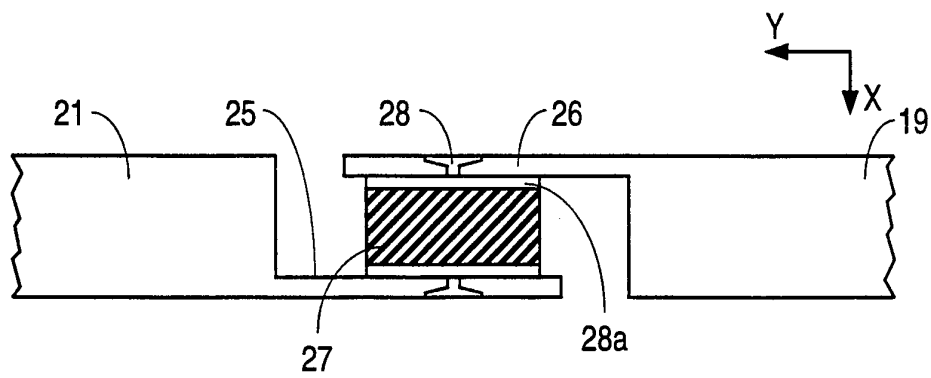


FIG. 6

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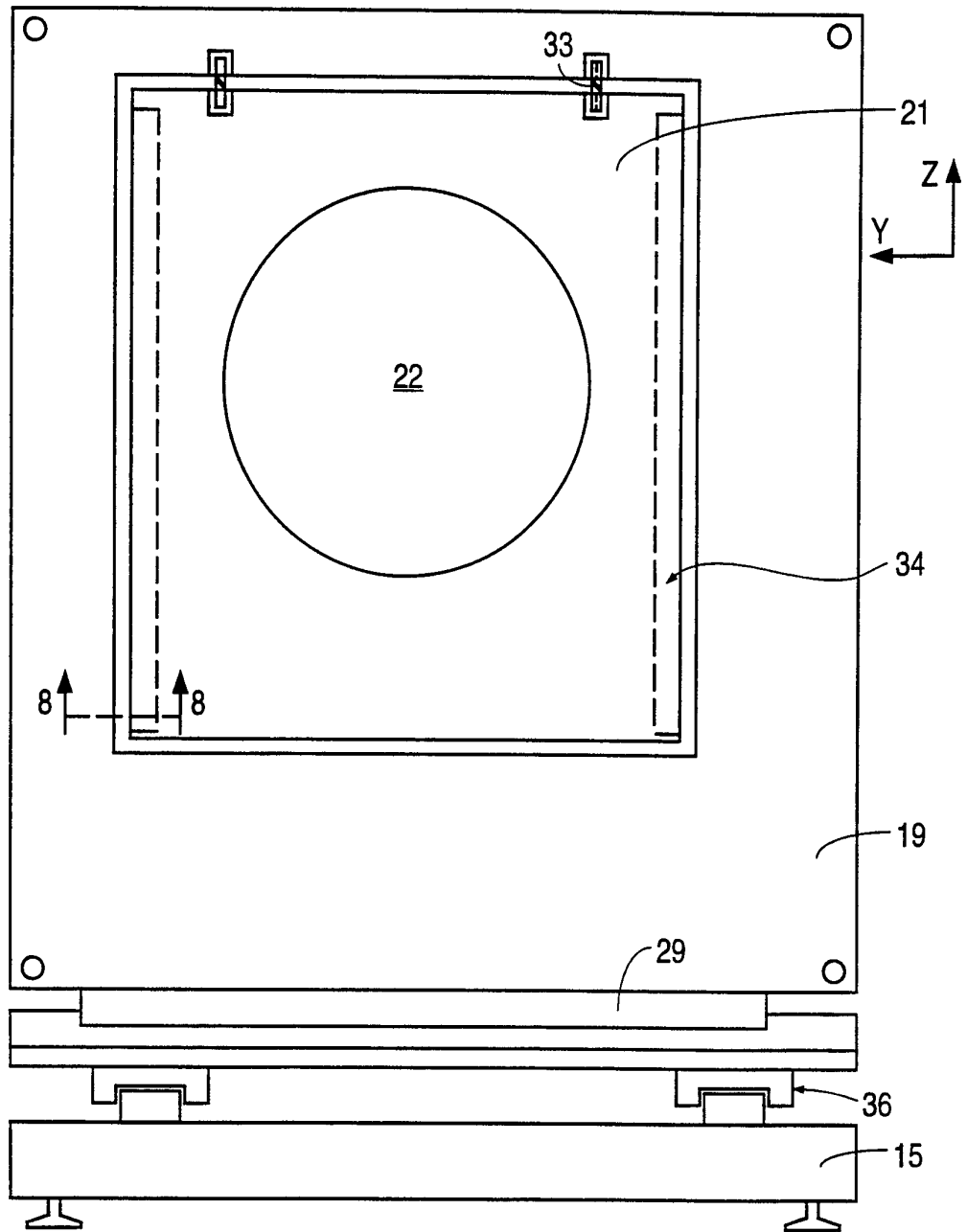


FIG. 7

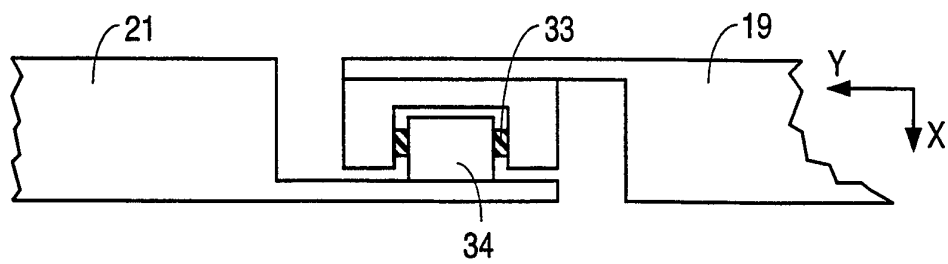


FIG. 8

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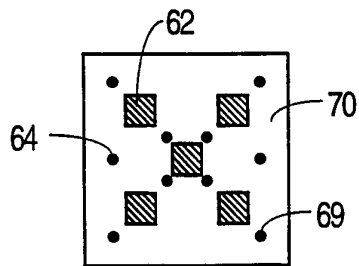
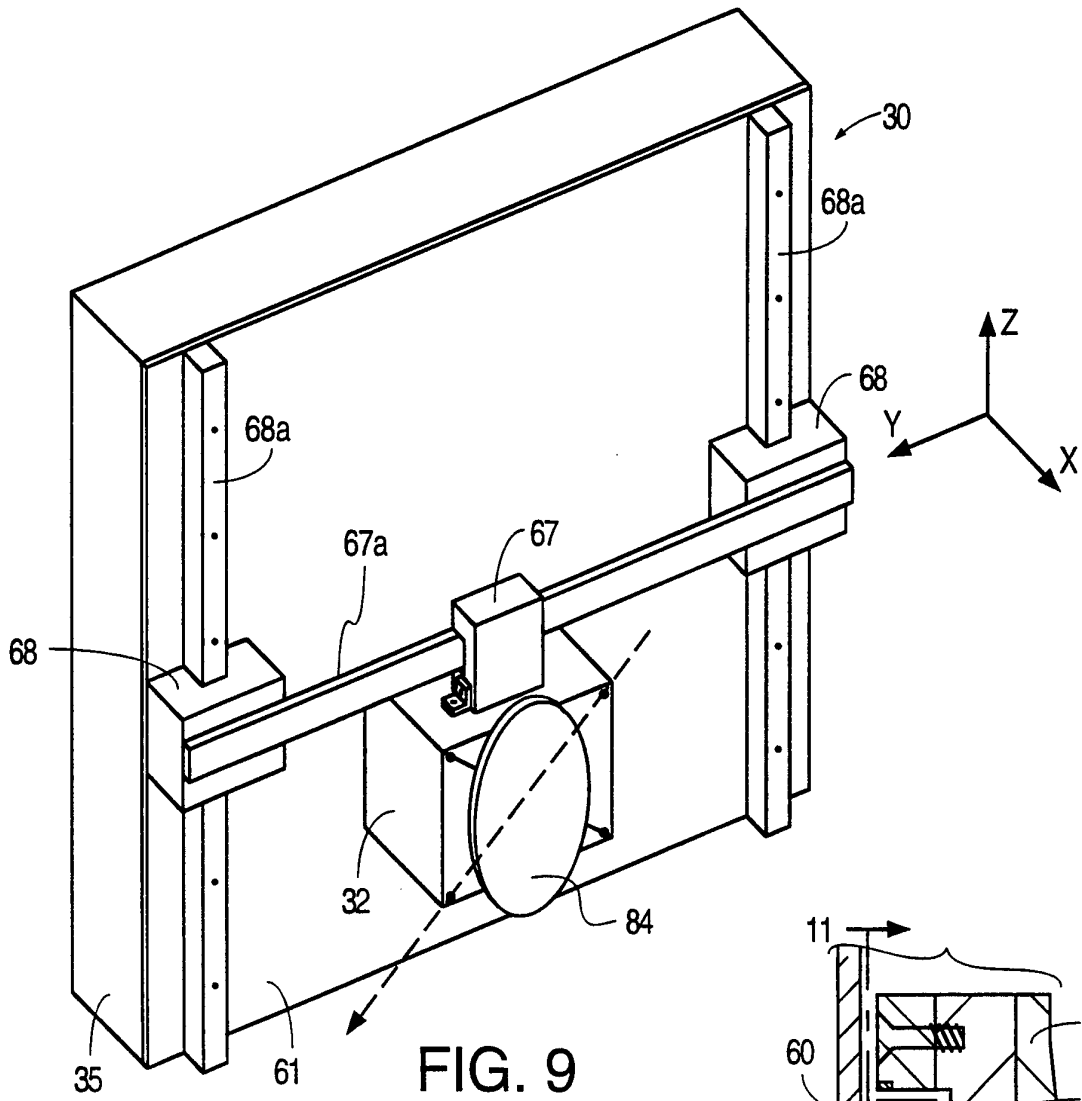
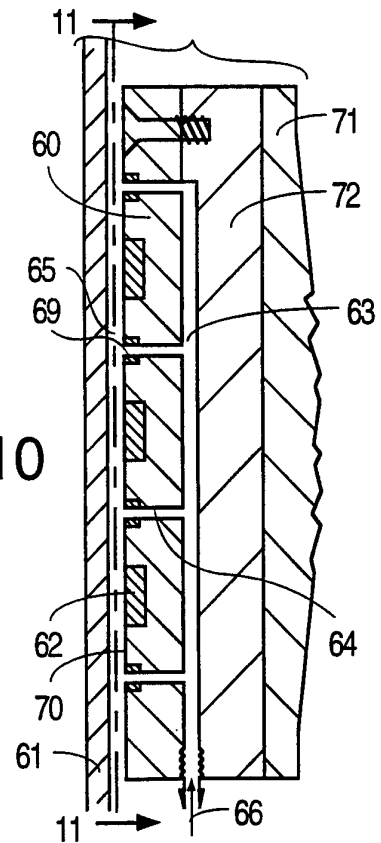


FIG. 10



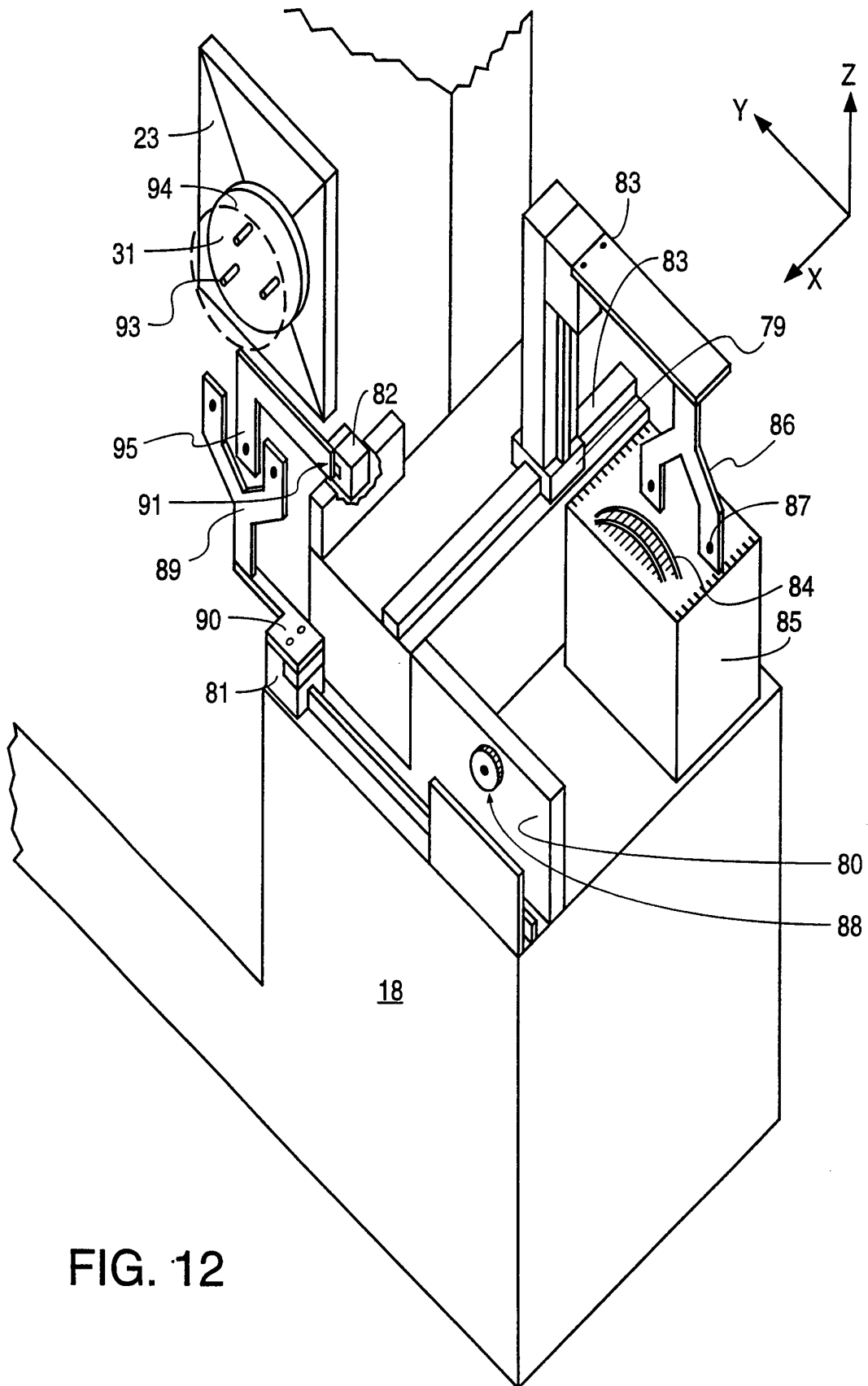


FIG. 12

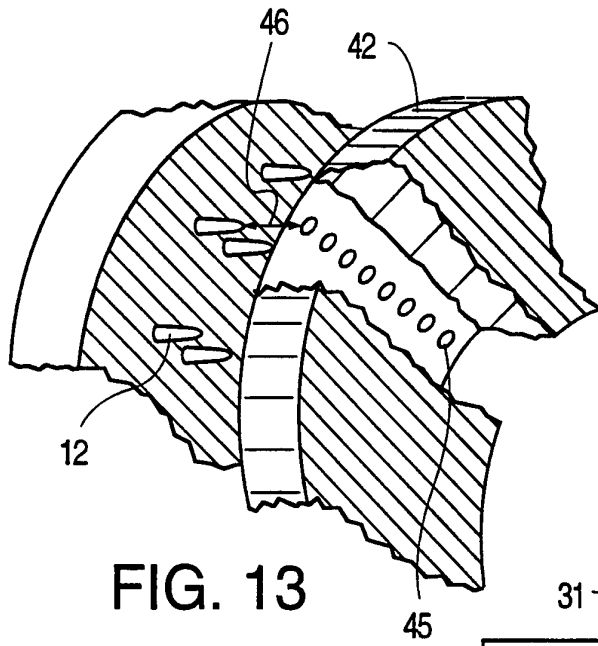


FIG. 13

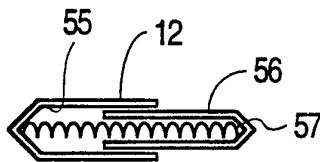
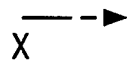


FIG. 15



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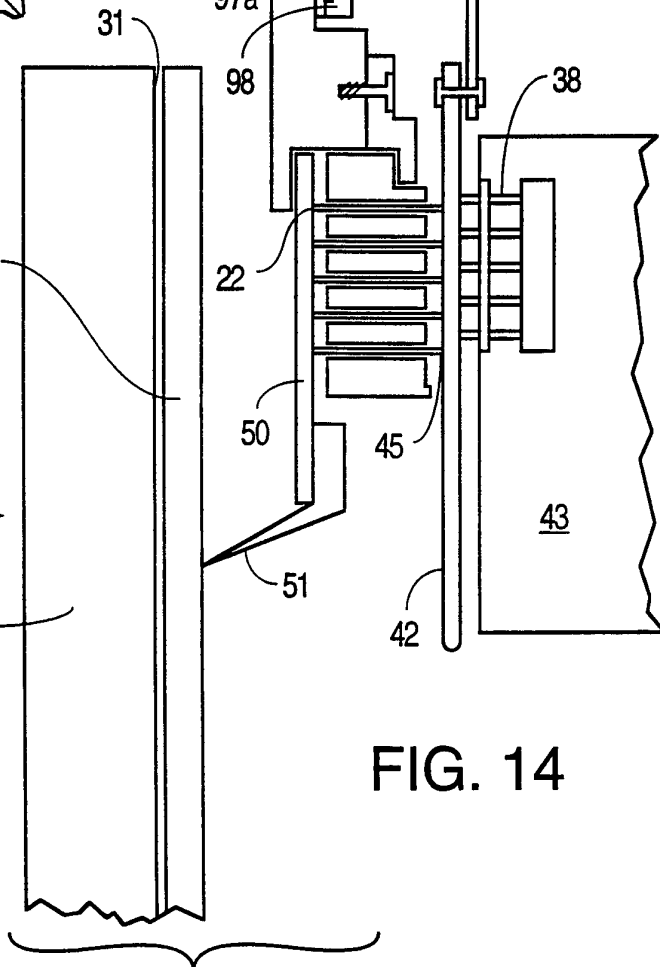


FIG. 14

INTERNATIONAL SEARCH REPORT

International application No.
PCT/US96/11829

A. CLASSIFICATION OF SUBJECT MATTER

IPC(6) :G01R 31/02, 1/06; G01B 3/38

US CL : 324/754, 755

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

U.S. : 324/754, 755, 758, 765, 158.1, 73.1; 439/482; 414/590, 744A

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
Y	US,A, 5,329,226 (MONNET ET AL) 12 JULY 1994, SEE THE ENTIRE DOCUMENT.	1,4,7,16, 17
Y	US,A, 4751,457 (VEENENDAAL) 14 JUNE 1988, SEE THE ENTIRE DOCUMENT.	1,4,7,16,17
Y	US,A, 4,746,857 (SAKAI ET AL) 24 MAY 1988 SEE THE ENTIRE DOCUMENT.	1,4,7,16,17

Further documents are listed in the continuation of Box C. See patent family annex.

* Special categories of cited documents:	"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention
"A" document defining the general state of the art which is not considered to be of particular relevance	"X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone
"E" earlier document published on or after the international filing date	"Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art
"L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)	"&" document member of the same patent family
"O" document referring to an oral disclosure, use, exhibition or other means	
"P" document published prior to the international filing date but later than the priority date claimed	

Date of the actual completion of the international search

21 AUGUST 1996

Date of mailing of the international search report

02 OCT 1996

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