

(19) World Intellectual Property Organization
International Bureau



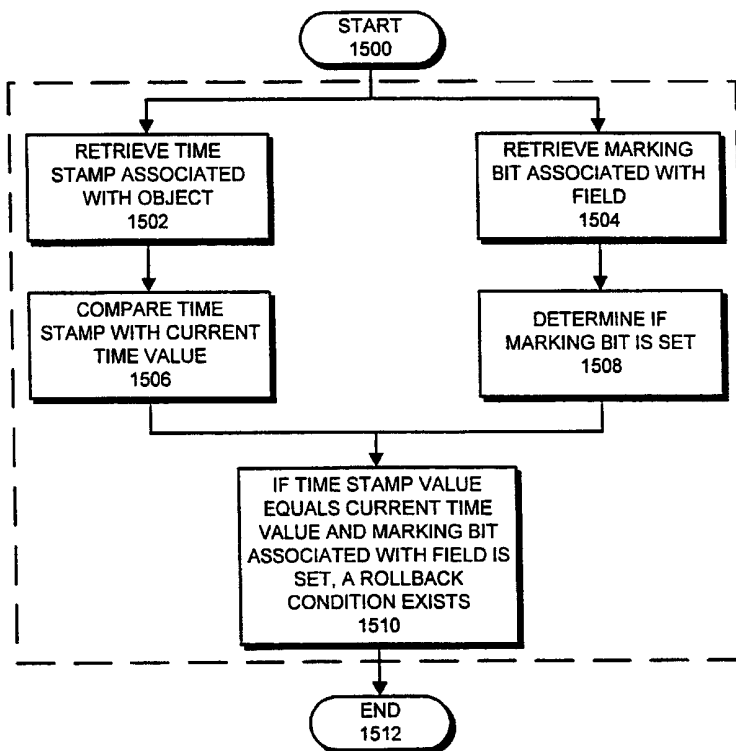
(43) International Publication Date
14 December 2000 (14.12.2000)

PCT

(10) International Publication Number
WO 00/75778 A1

- (51) International Patent Classification⁷: **G06F 9/46** (74) Agent: **PARK, Richard**; Suite 201, 508 2nd Street, Davis, CA 95616 (US).
- (21) International Application Number: **PCT/US00/13792**
- (22) International Filing Date: **18 May 2000 (18.05.2000)** (81) Designated States (*national*): **IL, JP, KR, RU.**
- (25) Filing Language: **English** (84) Designated States (*regional*): **European patent (AT, BE, CH, CY, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE).**
- (26) Publication Language: **English**
- (30) Priority Data:
09/327,399 7 June 1999 (07.06.1999) **US** Published:
— *With international search report.*
— *Before the expiration of the time limit for amending the claims and to be republished in the event of receipt of amendments.*
- (71) Applicant: **SUN MICROSYSTEMS, INC.** [US/US]; 901 San Antonio Road, Palo Alto, CA 94303 (US).
- (72) Inventors: **TREMBLAY, Marc**; 140 Hanna Way, Menlo Park, CA 94025 (US). **CHAUDHRY, Shailender**; 1200 Gough Street, Apt. 10F, San Francisco, CA 94109 (US). *For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.*

(54) Title: **MARKING FIELDS WITHIN OBJECTS WITH TIMESTAMPS IN ORDER TO IMPROVE EFFICIENCY**



(57) Abstract: One embodiment of the present invention provides a system that uses a time stamp in order to more efficiently mark objects to keep track of accesses to fields with the objects. Upon receiving a first reference to a first field in an object, the system determines whether the first field has been accessed within a current time period. The system does so by retrieving a time stamp associated with the object. This time stamp indicates the last time any marking bit associated with any field in the object was updated. The system compares the time stamp with a current time value associated with the current time period. The system additionally retrieves a first marking bit associated with the first field and determines if the first marking bit is set. If the first marking bit is set and if the time stamp equals the current time value, the system determines that the first field has been accessed in the current time period. In one embodiment of the present invention, the system indicates that a second field in the object has been accessed in the current time period upon receiving a second reference to the

second field. In response to the second reference, the system sets a second marking bit associated with the second field. The system also updates the time stamp associated with the object, if necessary, so that the time stamp contains the current time value.

5

MARKING FIELDS WITHIN OBJECTS WITH TIMESTAMPS IN ORDER TO IMPROVE EFFICIENCY

10

Related Application

15 This subject matter of this application is related to the subject matter in a pending U.S. patent application, entitled "Supporting Space-Time Dimensional Program Execution by Selectively Versioning Memory Updates," by the same inventor(s) as the instant application, having serial number TO BE ASSIGNED and a filing date of May 17, 1999 (Attorney Docket No.: SUN-P3902-PJM).

20 The subject matter of this application is also related to the subject matter in a co-pending non-provisional application by the same inventor(s) as the instant application and filed on the same day as the instant application entitled, "Method and Apparatus for Providing Finer Marking Granularity for Fields Within Objects," having serial number TO BE ASSIGNED, and filing date TO BE ASSIGNED (Attorney Docket No. SUN-P3891-PJM).

25

BACKGROUND

Field of the Invention

The present invention relates to performance enhancements in object-oriented programming systems. More specifically, the present invention relates to a method and an

apparatus that supports marking of a field within an object to indicate that the field has been accessed during a read operation or a write operation.

Related Art

5 As increasing semiconductor integration densities allow more transistors to be integrated onto a microprocessor chip, computer designers are investigating different methods of using these transistors to increase computer system performance. Some recent computer architectures exploit “instruction level parallelism,” in which a single central processing unit (CPU) issues multiple instructions in a single cycle. Given proper
10 compiler support, instruction level parallelism has proven effective at increasing computational performance across a wide range of computational tasks. However, inter-instruction dependencies generally limit the performance gains realized from using instruction level parallelism to a factor of two or three.

 Another method for increasing computational speed is “speculative execution” in
15 which a processor executes multiple branch paths simultaneously, or predicts a branch, so that the processor can continue executing without waiting for the result of the branch operation. By reducing dependencies on branch conditions, speculative execution can increase the total number of instructions issued.

 Unfortunately, conventional speculative execution typically provides a limited
20 performance improvement because only a small number of instructions can be speculatively executed. One reason for this limitation is that conventional speculative execution is typically performed at the basic block level, and basic blocks tend to include only a small number of instructions. Another reason is that conventional hardware structures used to perform speculative execution can only accommodate a small number of
25 speculative instructions.

 What is needed is a method and apparatus that facilitates speculative execution of program instructions at a higher level of granularity so that many more instructions can be speculatively executed.

One challenge in designing a system that supports speculative execution is to detect rollback conditions. A rollback condition can occur in a number of situations. For example, a rollback condition occurs when a speculative thread that is executing program instructions in advance of a head thread reads from a memory element before the head thread performs a store to the memory element. In this case, the speculative thread must “rollback” so that it can read the value stored by the head thread. A rollback can be detected by “marking” memory elements as they are read by the speculative thread so that the head thread can subsequently determine if the memory elements have been read by the speculative thread.

Note that after a rollback of the speculative thread, or after a join between the head thread and the speculative thread, the state of the head thread is made consistent with the state of the speculative thread. In this case, all the memory elements must somehow be unmarked. However, unmarking all of the memory elements can be prohibitively time-consuming.

What is needed is a method and an apparatus for unmarking memory elements that does not consume a great deal of computation time.

SUMMARY

One embodiment of the present invention provides a system that uses a time stamp in order to more efficiently mark objects to keep track of accesses to fields with the objects. Upon receiving a first reference to a first field in an object, the system determines whether the first field has been accessed within a current time period. The system does so by retrieving a time stamp associated with the object. This time stamp indicates the last time any marking bit associated with any field in the object was updated. The system compares the time stamp with a current time value associated with the current time period. The system additionally retrieves a first marking bit associated with the first field and determines if the first marking bit is set. If the first marking bit is set and if the time stamp

equals the current time value, the system determines that the first field has been accessed during the current time period.

In one embodiment of the present invention, the system indicates that a second field in the object has been accessed in the current time period upon receiving a second
5 reference to the second field. In response to the second reference, the system sets a second marking bit associated with the second field. The system also updates the time stamp associated with the object, if necessary, so that the time stamp contains the current time value. In a variation on this embodiment, in response to the second reference, the system additionally compares the time stamp with the current time value. If the time stamp does
10 not match the current time value, the system resets all marking bits associated with the object except for the second marking bit.

In one embodiment of the present invention, the system supports space and time dimensional execution. More specifically, the system supports a head thread that executes program instructions and a speculative thread that executes program instructions in
15 advance of the head thread. In this embodiment, the step of determining that the first field has been accessed within the current time period takes place during write operations by the head thread, and the step of indicating that the second field has been accessed in the current time period takes place during read operations by the speculative thread. If, during a write operation by the head thread, the system determines that the first field has been
20 previously read by the speculative thread, the system causes the speculative thread to roll back so that the speculative thread can read a value written by the head thread. In a variation on this embodiment, the system advances the current time value during a rollback operation or during a join operation between the speculative thread and the head thread. In a variation on this embodiment, the system resets the current time value during a garbage
25 collection operation.

In one embodiment of the present invention, the time stamp and any marking bits associated with the object are contained in a single status word within the object.

In one embodiment of the present invention, the steps of comparing the time stamp with the current time value and determining if the first marking bit is set take place in parallel.

5

BRIEF DESCRIPTION OF THE FIGURES

FIG. 1 illustrates a computer system including two central processing units sharing a common data cache in accordance with an embodiment of the present invention.

FIG. 2A illustrates sequential execution of methods by a single thread.

FIG. 2B illustrates space and time dimensional execution of a method in
10 accordance with an embodiment of the present invention.

FIG. 3 illustrates the state of the system stack during space and time dimensional execution of a method in accordance with an embodiment of the present invention.

FIG. 4 illustrates how memory is partitioned between stack and heap in accordance with an embodiment of the present invention.

15 FIG. 5 illustrates the structure of a primary version and a space-time dimensioned version of an object in accordance with an embodiment of the present invention.

FIG. 6 illustrates the structure of a status word for an object in accordance with an embodiment of the present invention.

FIG. 7 is a flow chart illustrating operations involved in performing a write to a
20 memory element by a head thread in accordance with an embodiment of the present invention.

FIG. 8 is a flow chart illustrating operations involved in performing a read to a memory element by a speculative thread in accordance with an embodiment of the present invention.

25 FIG. 9 is a flow chart illustrating operations involved in performing a write to a memory element by a speculative thread in accordance with an embodiment of the present invention.

FIG. 10 is a flow chart illustrating operations involved in performing a join between a head thread and a speculative thread in accordance with an embodiment of the present invention.

FIG. 11 is a flow chart illustrating operations involved in performing a join
5 between a head thread and a speculative thread in accordance with another embodiment of the present invention.

FIG. 12 is a flow chart illustrating the process of setting a marking bit associated with a referenced field within an object in accordance with an embodiment of the present invention.

10 FIG. 13 illustrates how a marking bit number can be determined from a field number or an array element number in accordance with an embodiment of the present invention.

FIG. 14 illustrates how a block transfer operation can set multiple marking bits in accordance with an embodiment of the present invention.

15 FIG. 15 is a flow chart illustrating the process of checking a status word for a possible rollback condition during a write operation by the head thread in accordance with an embodiment of the present invention.

FIG. 16 is a flow chart illustrating the process of updating a status word, if necessary, during a read operation by the speculative thread in accordance with an
20 embodiment of the present invention.

DETAILED DESCRIPTION

The following description is presented to enable any person skilled in the art to make and use the invention, and is provided in the context of a particular application and
25 its requirements. Various modifications to the disclosed embodiments will be readily apparent to those skilled in the art, and the general principles defined herein may be applied to other embodiments and applications without departing from the spirit and scope of the present invention. Thus, the present invention is not intended to be limited to the

embodiments shown, but is to be accorded the widest scope consistent with the principles and features disclosed herein.

The data structures and code described in this detailed description are typically stored on a computer readable storage medium, which may be any device or medium that can store code and/or data for use by a computer system. This includes, but is not limited to, magnetic and optical storage devices such as disk drives, magnetic tape, CDs (compact discs) and DVDs (digital video discs), and computer instruction signals embodied in a carrier wave. For example, the carrier wave may carry information across a communications network, such as the Internet.

Computer System

FIG. 1 illustrates a computer system including two central processing units (CPUs) 102 and 104 sharing a common data cache 106 in accordance with an embodiment of the present invention. In this embodiment, CPUs 102 and 104 and data cache 106 reside on silicon die 100. Note that CPUs 102 and 104 may generally be any type of computational devices that allow multiple threads to execute concurrently. In the embodiment illustrated in FIG. 1, CPUs 102 and 104 are very long instruction word (VLIW) CPUs, which support concurrent execution of multiple instructions executing on multiple functional units. VLIW CPUs 102 and 104 include instruction caches 112 and 120, respectively, containing instructions to be executed by VLIW CPUs 102 and 104.

VLIW CPUs 102 and 104 additionally include load buffers 114 and 122 as well as store buffers 116 and 124 for buffering communications with data cache 106. More specifically, VLIW CPU 102 includes load buffer 114 for buffering loads received from data cache 106, and store buffer 116 for buffering stores to data cache 106. Similarly, VLIW CPU 104 includes load buffer 122 for buffering loads received from data cache 106, and store buffer 124 for buffering stores to data cache 106.

VLIW CPUs 102 and 104 are additionally coupled together by direct communication link 128, which facilitates rapid communication between VLIW CPUs 102 and 104. Note that direct communication link 128 allows VLIW CPU 102 to write into

communication buffer 126 within VLIW CPU 104. It also allows VLIW CPU 104 to write into communication buffer 118 within VLIW CPU 102.

In the embodiment illustrated in FIG. 1, Data cache 106 is fully dual-ported allowing concurrent read and/or write accesses from VLIW CPUs 102 and 104. This dual
5 porting eliminates cache coherence delays associated with conventional shared memory architectures that rely on coherent caches.

In one embodiment of the present invention, data cache 106 is a 16K byte 4-way set-associative data cache with 32 byte cache lines.

Data cache 106, instruction caches 112 and instruction cache 120 are coupled
10 through switch 110 to memory controller 111. Memory controller 111 is coupled to dynamic random access memory (DRAM) 108, which is located off chip. Switch 110 may include any type of circuitry for switching signal lines. In one embodiment of the present invention, switch 110 is a cross bar switch.

The present invention generally applies to any computer system that supports
15 concurrent execution by multiple threads and is not limited to the illustrated computing system. However, note that data cache 106 supports fast accesses to shared data items. These fast accesses facilitate efficient sharing of status information between VLIW CPUs 102 and 104 to keep track of accesses to versions of memory objects.

20 **Space-Time Dimensional Execution of Methods**

FIG. 2A illustrates sequential execution of methods in a conventional computer system by a single head thread 202. In executing a program, head thread 202 executes a number of methods in sequence, including method A 204, method B 206 and method C
208.

25 In contrast, FIG. 2B illustrates space and time dimensional execution of a method in accordance with an embodiment of the present invention. In FIG. 2B, head thread 202 first executes method A 204 and then executes method B 206. (For this example, assume that method B 206 returns a void or some other value that is not used by method C 208.

Alternatively, if method C 208 uses a value returned by method B206, assume that method C 208 uses a predicted return value from method B 206.)

As head thread 202 executes method B 206, speculative thread 203 executes method C 208 in a separate space-time dimension of the heap. If head thread 202
5 successfully executes method B 206, speculative thread 203 is joined with head thread 202. This join operation involves causing state associated with the speculative thread 203 to be merged with state associated with the head thread 202 and the collapsing of the space-time dimensions of the heap.

If speculative thread 203 for some reason encounters problems in executing
10 method C 208, speculative thread 203 performs a rollback operation. This rollback operation allows speculative thread 203 to reattempt to execute method C 208. Alternatively, head thread 202 can execute method C 208 non-speculatively and speculative thread 203 can execute a subsequent method.

There are a number of reasons why speculative thread 203 may encounter problems
15 in executing method C 208. One problem occurs when head thread 202 executing method B 206 writes a value to a memory element (object) after speculative thread 203 has read the same memory element. The same memory element can be read when the two space-time dimensions of the heap are collapsed at this memory element at the time of the read by speculative thread 203. In this case, speculative thread 203 should have read the value
20 written by head thread 202, but instead has read a previous value. In this case, the system causes speculative thread 203 to roll back so that speculative thread 203 can read the value written by head thread 202.

Note that the term “memory element” generally refers to any unit of memory that can be accessed by a computer program. For example, the term “memory element” may
25 refer to a bit, a byte or a word memory, as well as a data structure or an object defined within an object-oriented programming system.

FIG. 3 illustrates the state of the system stack during space and time dimensional execution of a method in accordance with an embodiment of the present invention. Note that since programming languages such as the Java programming language do not allow a

method to modify the stack frame of another method, the system stack will generally be the same before method B 206 is executed as it is before method C 208 is executed. (This is not quite true if method B 206 returns a parameter through the system stack. However, return parameters are can be explicitly dealt with as is described below.) Referring the

5 FIG. 3, stack 300 contains method A frame 302 while method A 204 is executing. When method A 204 returns, method B 206 commences and method A frame 302 is replaced by method B frame 304. Finally, when method B 206 returns, method C 208 commences and method B frame 304 is replaced by method C frame 306. Note that since stack 300 is the same immediately before method B 206 executed as it is immediately before method C

10 208 is executed, it is possible to execute method C 208 using a copy of stack 300 without first executing method B 206.

In order to undo the results of speculatively executed operations, updates to memory need to be versioned. The overhead involved in versioning all updates to memory can be prohibitively expensive due to increased memory requirements, decreased cache

15 performance and additional hardware required to perform the versioning.

Fortunately, not all updates to memory need to be versioned. For example, updates to local variables -- such as a loop counter -- on a system stack are typically only relevant to the thread that is updating the local variables. Hence, even for speculative threads versioning updates to these local variables is not necessary.

20 When executing programs written in conventional programming languages, such as C, it is typically not possible to determine which updates are related to the heap, and which updates are related to the system stack. These programs are typically compiled from a high-level language representation into executable code for a specific machine architecture. This compilation process typically removes distinctions between updates to

25 heap and system stack.

The same is not true for new platform-independent computer languages, such as the JAVATM programming language distributed by SUN Microsystems, Inc. of Palo Alto, California. (Sun, the Sun logo, Sun Microsystems, and Java are trademarks or registered trademarks of Sun Microsystems, Inc. in the United States and other countries.) A

program written in the Java programming language is typically compiled into a class file containing Java byte codes. This class file can be transmitted over a computer network to a distant computer system to be executed on the distant computer system. Java byte codes are said to be “platform-independent,” because they can be executed across a wide range of computing platforms, so long as the computing platforms provide a Java virtual machine.

A Java byte code can be executed on a specific computing platform by using an interpreter or a just in time (JIT) compiler to translate the Java bytecode into machine code for the specific computing platform. Alternatively, a Java byte code can be executed directly on a Java bytecode engine running on the specific computing platform.

Fortunately, a Java bytecode contains more syntactic information than conventional machine code. In particular, the Java bytecodes differentiate between accesses to local variables in the system stack and accesses to the system heap. Furthermore, programs written in the Java programming language do not allow conversion between primitive and reference types. Such conversion can make it hard to differentiate accesses to the system stack from accesses to the system heap at compile time.

Data Structures to Support Space-Time Dimensional Execution

FIG. 4 illustrates how memory is partitioned between stack and heap in accordance with an embodiment of the present invention. In FIG. 4, memory 400 is divided into a number of regions including heap 402, stacks for threads 404 and speculative heap 406. Heap 402 comprises a region of memory from which objects are allocated. Heap 402 is further divided into younger generation region 408 and older generation region 410 for garbage collection purposes. For performance reasons, garbage collectors typically treat younger generation objects differently from older generation objects. Stack for threads 404 comprises a region of memory from which stacks for various threads are allocated. Speculative heap 406 contains the space-time dimensioned values of all memory elements where the two space-time dimensions of the heap are not collapsed. This includes space-time dimensional versions of objects, for example, version 510 of object 500 as shown in

FIG. 5, and objects created by speculative thread 203. For garbage collection purposes, these objects created by speculative thread 203 can be treated as belonging to a generation that is younger than objects within younger generation region 408.

FIG. 5 illustrates the structure of a primary version of object 500 and a space-time dimensioned version of object 510 in accordance with an embodiment of the present invention.

Primary version of object 500 is referenced by object reference pointer 501. Like any object defined within an object-oriented programming system, primary version of object 500 includes data region 508, which includes one or more fields containing data associated with primary version of object 500. Primary version of object 500 also includes method vector table pointer 506. Method vector table pointer 506 points to a table containing vectors that point to the methods that can be invoked on primary version of object 500.

Primary version of object 500 also includes space-time dimensioned version pointer 502, which points to space-time dimensioned version of object 510, if the two space-time dimensions are not collapsed at this object. Note that in the illustrated embodiment of the present invention, space-time dimensioned version 510 is always referenced indirectly through space-time dimensioned version pointer 502. Primary version of object 500 additionally includes status word 504, which contains status information specifying which fields from data region 508 have been written to or read by speculative thread 203. Space-time dimensioned version of object 510 includes only data region 518.

FIG. 6 illustrates the structure of status word 504 in accordance with an embodiment of the present invention. In this embodiment, status word 504 includes checkpoint number 602 and speculative bits 603. Speculative bits 603 includes read bits 604 and write bits 606. When status word 504 needs to be updated due to a read or a write by speculative thread 203, checkpoint number 602 is updated with the current time of the system. The current time in the time dimension of the system is advanced discretely at a join or a rollback. This allows checkpoint number 602 to be used as a qualifier for

speculative bits 603. If checkpoint number 602 is less than the current time, speculative bits 603 can be interpreted as reset.

Read bits 604 keep track of which fields within data region 508 have been read since the last join or rollback. Correspondingly, write bits 606 keep track of which fields within data region 508 have been written since the last join or rollback. In one embodiment of the present invention, read bits 604 includes one bit for each field within data region 508. In another embodiment, read bits includes fewer bits than the number of fields within data region 508. In this embodiment, each bit within read bits 604 corresponds to more than one field in data region 508. For example, if there are eight read bits, each bit corresponds to every eighth field. Write bits 606 similarly can correspond to one or multiple fields within data region 508.

Space-Time Dimensional Update Process

Space-time dimensioning occurs during selected memory updates. For local variable and operand accesses to the system stack, no space-time dimensioned versions exist and nothing special happens. During read operations by head thread 202 to objects in the heap 402, again nothing special happens.

Special operations are involved in write operations by head thread 202 as well as read and write operations by speculative thread 203. These special operations are described in more detail with reference to FIGs. 7, 8 and 9 below.

FIG. 7 is a flow chart illustrating operations involved in a write operation to an object by a head thread 202 in accordance with an embodiment of the present invention. The system writes to the primary version of object 500 and the space-time dimensioned version of object 510 if the two space-time dimensions are not collapsed at this point (step 702). Next, the system checks status word 504 within primary version of object 500 to determine whether a rollback is required (step 704). A rollback is required if speculative thread 203 previously read the data element. The same memory element can be read when the two space-time dimensions of the heap are collapsed at this memory element at the time of the read by speculative thread 203. A rollback is also required if speculative thread

203 previously wrote to the object and thus ensured that the two dimensions of the object are not collapsed at this element, and if the current write operation updates both primary version of object 500 and space-time dimensioned version of object 510.

5 If a rollback is required, the system causes speculative thread 203 to perform a rollback operation (step 706). This rollback operation allows speculative thread 203 to read from (or write to) the object after head thread 202 writes to the object.

Note that in the embodiment of the present invention illustrated in FIG. 7 the system performs writes to both primary version 500 and space-time dimensioned version 510. In an alternative embodiment, the system first checks to determine if speculative
10 thread 203 previously wrote to space-time dimensioned version 510. If not, the system writes to both primary version 500 and space-time dimensioned version 510. If so, the system only writes to primary version 500.

FIG. 8 is a flow chart illustrating operations involved in a read operation to an object by speculative thread 203 in accordance with an embodiment of the present
15 invention. During this read operation, the system sets a status bit in status word 504 within primary version of object 500 to indicate that primary version 500 has been read (step 802). Speculative thread 203 then reads space-time dimensioned version 510, if it exists. Otherwise, speculative thread 203 reads primary version 500.

FIG. 9 is a flow chart illustrating operations involved in a write operation to a
20 memory element by speculative thread 203 in accordance with an embodiment of the present invention. If a space-time dimensioned version 510 does not exist, the system creates a space-time dimensioned version 510 in speculative heap 406 (step 902). The system also updates status word 504 to indicate that speculative thread 203 has written to the object if such updating is necessary (step 903). The system next writes to space-time
25 dimensioned version 510 (step 904). Such updating is necessary if head thread 202 must subsequently choose between writing to both primary version 500 and space-time dimensioned version 510, or writing only to primary version 500 as is described above with reference to FIG. 7.

FIG. 10 is a flow chart illustrating operations involved in a join operation between head thread 202 and a speculative thread 203 in accordance with an embodiment of the present invention. A join operation occurs for example when head thread 202 reaches a point in the program where speculative thread 203 began executing. The join operation causes state associated with the speculative thread 203 to be merged with state associated with the head thread 202. This involves copying and/or merging the stack of speculative thread 203 into the stack of head thread 202 (step 1002). It also involves merging space-time dimension and primary versions of objects (step 1004) as well as possibly garbage collecting speculative heap 406 (step 1006). In one embodiment of the present invention, one of threads 202 or 203 performs steps 1002 and 1006, while the other thread performs step 1004.

FIG. 11 is a flow chart illustrating operations involved in a join operation between head thread 202 and a speculative thread 203 in accordance with another embodiment of the present invention. In this embodiment, speculative thread 203 carries on as a pseudo-head thread. As a pseudo-head thread, speculative thread 203 uses indirection to reference space-time dimensioned versions of objects, but does not mark objects or create versions. While speculative thread 203 is acting as a pseudo-head thread, head thread 202 updates primary versions of objects.

Extension to Additional Speculative Threads

Although the present invention has been described for the case of a single speculative thread, the present invention can be extended to provide multiple speculative threads operating on multiple space-time dimensioned versions of a data object in parallel.

Process of Setting Marking Bits

FIG. 12 is a flow chart illustrating the process of setting a marking bit associated with a referenced field within an object in accordance with an embodiment of the present invention. First, the system receives a reference to the field within the object (step 1202).

This reference may be a read or a write operation. Note that in the case of an array object the field is actually an array element.

Next, the system identifies a marking bit associated with the field (step 1204). In one embodiment of the present invention, the system maintains a separate set of read marking bits 604 for the object to indicate that a read operation has occurred to the field, and a separate set of write marking bits 606 to indicate that a write operation has occurred to the field. In this embodiment, if the operation is a read operation, one of the read marking bits 604 is selected. Otherwise, one of the write marking bits 606 is selected.

In one embodiment of the present invention, the marking bit is identified by performing a modulo operation. For example, if the object includes N marking bits numbered $0, 1, 2, \dots, N-1$ and M fields numbered $0, 1, 2, \dots, M-1$, the system starts with a field number for the field, and applies a modulo N operation to the field number to produce a number for the associated marking bit. This modulo operation can be efficiently performed if N is a power of two because the modulo operation reduces to a simple bit mask operation that isolates the lower order bits of the field number.

The selection a value of N for an object involves a tradeoff. If N is too small, there tends to be lot of aliasing and system performance suffers. If N is too large, a great deal of memory is used for marking bits which can cause cache performance to suffer. In one embodiment of the present invention, $N = 8$. In another embodiment, $N = 16$.

In the case of an array object, the system applies a division operation to the array element number (field number) to identify the associated array element. For example, if the array object has N marking bits numbered $0, 1, 2, \dots, N-1$ and M array elements numbered $0, 1, 2, \dots, M-1$, the step of identifying the marking bit includes dividing the array element number by the ceiling of M/N to produce a number for the associated marking bit. If the ceiling of M/N is a power of two, the division operation can be accomplished by shifting the array index so that the most significant bits of the array index become the number for the associated marking bit.

This type of mapping between array elements and marking bits associates consecutive array locations with a single marking bit. This ensures that not all of the marking bits are set by a block copy operation involving only a portion of the array.

After the marking bit is identified, the marking bit is set (step 1206) and the
5 reference is performed to the field (or array element) within the object (step 1208).

In general the marking mechanism according to the present invention can be used in any application that must keep track of accesses to fields within an object. However, in one embodiment of the present invention, marking is performed for read operations by speculative thread 203. In another embodiment, marking is performed to write bits 606
10 during a write operation by speculative thread 203 and to read bits 604 during a read operation by speculative thread 203.

After the marking bits have been set, if a head thread 202 subsequently performs a write operation to a field in the object, head thread 202 can identify the associated marking bit using the above-described modulo or division operations. Next, the marking bit is
15 extracted for examination purposes using a special bit extract operation that is part of the instruction set of the underlying computer system.

FIG. 13 illustrates how a marking bit number can be determined from a field number or an array element number in accordance with an embodiment of the present invention. The system starts with a field number or an array element number 1302. In the
20 case of a field number, the system performs a modulo operation by masking off all but the lower order three bits of field number 1302 to produce a three bit index (1,0,0) that specifies a marking bit. In the case of an array index, the system performs a division operation by shifting array element number 1302 until only the three most significant bits (0,1,1) remain.

25 FIG. 14 illustrates how a block transfer operation sets multiple marking bits in accordance with an embodiment of the present invention. The example illustrated in FIG. 14 includes an array of data elements 1402. These data elements are numbered 0, 1, 2, ... , 31. FIG. 14 also includes an array of read bits 604. These read bits are numbered 0, 1, 2, ... , 7. In the case of a block transfer operation 1404 that reads array elements seven

through 21, the system first determines that read bits one through five must be set. Next, the system sets read bits one through five. Finally, the system performs the block transfer operation. Note that a special byte shuffle operation provided by the underlying machine architecture can be used to efficiently set a particular pattern of read bits.

5

Checking Status Word for Possible Rollback

FIG. 15 is a flow chart illustrating the process of checking a status word for a possible rollback condition during a write operation by head thread 202 in accordance with an embodiment of the present invention. FIG. 15 illustrates in more detail the operations
10 involved in performing step 704 from FIG. 7.

The system first retrieves a time stamp (checkpoint number 602) associated with the object (step 1502). This is accomplished by reading the time stamp (checkpoint number 602) from status word 504 in FIG. 6. This time stamp (check point number 602) is then compared against a current time value maintained by the system (step 1506).

15 Note that the current time value is incremented during either a rollback or a join operation. Also note that the current time value is reset after a garbage collection operation.

While the time stamp is being retrieved and compared in steps 1502 and 1506, the system retrieves a marking bit associated with the field (step 1504). This is accomplished
20 by using a special bit extract operation that is part of the instruction set of the underlying processor architecture. This bit extract instruction is used to extract a single bit from speculative bits 603 in FIG. 6. Next, the system determines if the extracted marking bit is set or not (step 1508). Note that the time stamp comparison and the marking bit examination take place in parallel in order to improve performance.

25 Next, if the time stamp equals the current time value and the marking bit associated with the field is set, a rollback condition exists. In this case the system proceeds to step 706 in FIG. 7 in which head thread 202 causes speculative thread 203 to perform a rollback.

Updating Status Word

FIG. 16 is a flow chart illustrating the process of updating a status word during a read operation by speculative thread 203 in accordance with an embodiment of the present invention. FIG. 16 illustrates in more detail the operations involved in performing step 903 from FIG. 9. In order to more rapidly update the status word, the system executes a number of operations in parallel.

A first thread of computation loads status word 504 into a register (step 1602). At the same time, a second thread of computation begins moving a "1" value into an appropriate marking bit location (step 1604). This involves shifting the "1" value by a constant equal to the field number for the accessed field modulo 16 and storing the result in a register labeled "BITNUM." (The modulo 16 operation essentially masks off the higher order bits of the field number).

Next, the first thread of computation loads a space-time dimensioned version pointer (step 1606). This operation is unrelated to the updating of the status word.

However, the first thread of computation must wait two cycles for the status word, so the first thread of computation is free to perform this unrelated operation. At the same time, the second thread of computation ORs a register containing the current time value with the register BITNUM. Note that the current time value occupies the upper 16 bits of the status word, so the current time value will not overwrite the marking bit set in the previous cycle.

Next, the first thread of computation ORs the retrieved status word with the register BITNUM and stores the result in a register labeled MAYBEBITNUM (step 1610).

At the same time, the second thread of computation compares the current time value against the status word to determine whether the status word is greater than or equal to the current time value (step 1612). Note that determining that the status word -- including the lower bits -- is greater than or equal to the current time value shows that the time stamp contained in the status word matches the current time value. This is because the time stamp cannot exceed the current time value. However, the lower order bits might make the status word greater than the time value.

Next, the system moves either the contents of the register BITNUM, or the contents of the register MAYBEBITNUM into the register BITNUM depending upon the outcome of the preceding comparison operation (step 1614). If the status word is greater than or equal to the current time value, MAYBEBITNUM moves into BITNUM. Otherwise,
5 BITNUM remains unchanged. Finally, the system stores the register BITNUM out to status word 504 (step 1614).

As a result of the above operations, if status word 504 has the current time stamp, an additional marking bit in status word 504 associated with the referenced field is set. Otherwise, the time stamp in the status word is updated, and all marking bits in status
10 word 504 are reset, except for the marking bit associated with the referenced field. In this way old marking bits are reset as new marking bits are set. This frees the system from the task of clearing all marking bits after a rollback or a join operation.

The foregoing descriptions of embodiments of the invention have been presented for purposes of illustration and description only. They are not intended to be exhaustive or
15 to limit the invention to the forms disclosed. Accordingly, many modifications and variations will be apparent to practitioners skilled in the art. Additionally, the above disclosure is not intended to limit the invention. The scope of the invention is defined by the appended claims.

What Is Claimed Is:

1. A method for using a time stamp to efficiently facilitate marking objects defined within an object-oriented programming system to keep track of accesses to fields with the objects, comprising:

determining that a first field in an object has been accessed within a current time period by,

receiving a first reference to the first field,

retrieving the time stamp associated with the object, the time stamp indicating the last time any marking bit associated with any field in the object was updated,

comparing the time stamp with a current time value associated with the current time period,

retrieving a first marking bit associated with the first field,

determining if the first marking bit is set,

determining that the first field has been accessed in the current time period if the time stamp equals the current time value and if the first marking bit is set, and

performing the first reference to the first field.

2. The method of claim 1, further comprising indicating that a second field in the object has been accessed in the current time period by:

receiving a second reference to the second field;

setting a second marking bit associated with the second field;

updating the time stamp associated with the object so that the time stamp contains the current time value; and

performing the second reference to the second field.

3. The method of claim 2, wherein indicating that the second field has been accessed in the current time period further comprises:

retrieving the time stamp associated with the object;

comparing the time stamp with the current time value; and

5 if the time stamp does not match the current time value, resetting all marking bits associated with the object except for the second marking bit.

4. The method of claim 2, wherein the method operates in a system that supports space and time dimensional execution, the system having a head thread that
10 executes program instructions and a speculative thread that executes program instructions in advance of the head thread,

wherein the step of determining that the first field has been accessed within the current time period takes place if the first reference is a write operation by the head thread;

15 wherein the step of indicating that the second field has been accessed in the current time period takes place if the second reference is a read operation by the speculative thread; and

further comprising, if the system determines that the first field has been accessed during the write operation by the head thread to the first field, causing the speculative thread to roll back so that the speculative thread can read a value written by head thread to
20 the first field.

5. The method of claim 4, further comprising advancing the current time value during a rollback operation.

25 6. The method of claim 4, further comprising advancing the current time value during a join operation between the speculative thread and the head thread.

7. The method of claim 4, further comprising resetting the current time value during a garbage collection operation.

8. The method of claim 1, wherein the time stamp and any marking bits associated with the object are contained in a single status word within the object.

5 9. The method of claim 1, wherein the steps of comparing the time stamp with the current time value and determining if the first marking bit is set take place in parallel.

10 10. A computer readable storage medium storing instructions that when executed by a computer cause the computer to perform a method for using a time stamp to efficiently facilitate marking objects defined within an object-oriented programming system to keep track of accesses to fields with the objects, comprising:

determining that a first field in an object has been accessed within a current time period by,

receiving a first reference to the first field,

15 retrieving the time stamp associated with the object, the time stamp indicating the last time any marking bit associated with any field in the object was updated,

comparing the time stamp with a current time value associated with the current time period,

20 retrieving a first marking bit associated with the first field,

determining if the first marking bit is set,

determining that the first field has been accessed in the current time period if the time stamp equals the current time value and if the first marking bit is set, and

25 performing the first reference to the first field.

11. A method for using a time stamp to efficiently facilitate marking objects defined within an object-oriented programming system to keep track of accesses to fields with the objects, the method operating in a system that supports space and time

24

dimensional execution, the system having a head thread that executes program instructions and a speculative thread that executes program instructions in advance of the head thread, the method comprising:

determining that a first field in an object has been accessed within a current time
5 period by,

receiving a first reference to the first field wherein the first reference
is a write operation by the head thread,

retrieving the time stamp associated with the object, the time stamp
indicating the last time any marking bit associated with any field in the
10 object was updated,

comparing the time stamp with a current time value associated with
the current time period,

retrieving a first marking bit associated with the first field,

determining if the first marking bit is set,

15 determining that the first field has been accessed in the current time
period if the time stamp equals the current time value and if the first
marking bit is set, and

performing the first reference to the first field;

indicating that a second field in the object has been accessed in the current time
20 period by:

receiving a second reference to the second field, wherein the second
reference is a read operation by the speculative thread,

setting a second marking bit associated with the second field,

25 updating the time stamp associated with the object so that the time
stamp contains the current time value, and

performing the second reference to the second field; and

if the system determines that the first field has been accessed during the write
operation by the head thread to the first field, causing the speculative thread to roll back so
that the speculative thread can read a value written by head thread to the first field.

12. The method of claim 11, wherein indicating that the second field has been accessed in the current time period further comprises:

retrieving the time stamp associated with the object;

5 comparing the time stamp with the current time value; and

if the time stamp does not match the current time value, resetting all marking bits associated with the object except for the second marking bit.

13. The method of claim 11, further comprising advancing the current time value during a rollback operation, or during a join operation between the speculative thread and the head thread.

14. An apparatus that uses a time stamp to efficiently facilitate marking objects defined within an object-oriented programming system to keep track of accesses to fields with the objects, comprising:

an access determining mechanism that determines whether a first field in an object has been accessed within a current time period, the access determining mechanism including,

20 a receiving mechanism that receives a first reference to the first field,

a retrieving mechanism that retrieves the time stamp associated with the object, the time stamp indicating the last time any marking bit associated with any field in the object was updated,

25 a comparing mechanism that compares the time stamp with a current time value associated with the current time period,

wherein the retrieving mechanism is further configured to retrieve a first marking bit associated with the first field,

a mechanism that determines that the first field has been accessed in the current time period if the time stamp equals the current time value and if the first marking bit is set, and

a mechanism that performs the first reference to the first field.

5

15. The apparatus of claim 14, further comprising a marking mechanism that: receives a second reference to a second field;

sets a second marking bit associated with the second field;

updates the time stamp associated with the object so that the time stamp contains

10 the current time value; and

performs the second reference to the second field.

16. The apparatus of claim 15, wherein the marking mechanism is further configured to:

15 retrieve the time stamp associated with the object;

compare the time stamp with the current time value; and

if the time stamp does not match the current time value, reset all marking bits associated with the object except for the second marking bit.

20 17. The apparatus of claim 15, further comprising:

an operating system that supports space and time dimensional execution, the operating system supporting a head thread that executes program instructions and a speculative thread that executes program instructions in advance of the head thread;

wherein the access determining mechanism is configured to operate during write

25 operations by the head thread;

wherein the marking mechanism is configured to operate during read operations by the speculative thread; and

further comprising a rollback mechanism that causes the speculative thread to roll back so that the speculative thread can read a value written by the head thread to the first

field if the access determining mechanism determines that the first field has been accessed during a write operation by the head thread to the first field.

18. The apparatus of claim 17, further comprising a time advancing mechanism
5 that advances the current time value during a rollback operation by the speculative thread.

19. The apparatus of claim 17, further comprising a time advancing mechanism
that advances the current time value during a join operation between the speculative thread
and the head thread.

10

20. The apparatus of claim 14, further comprising a time resetting mechanism
that resets the current time value during a garbage collection operation.

21. The apparatus of claim 14, wherein the time stamp and any marking bits
15 associated with the object are contained in a single status word within the object.

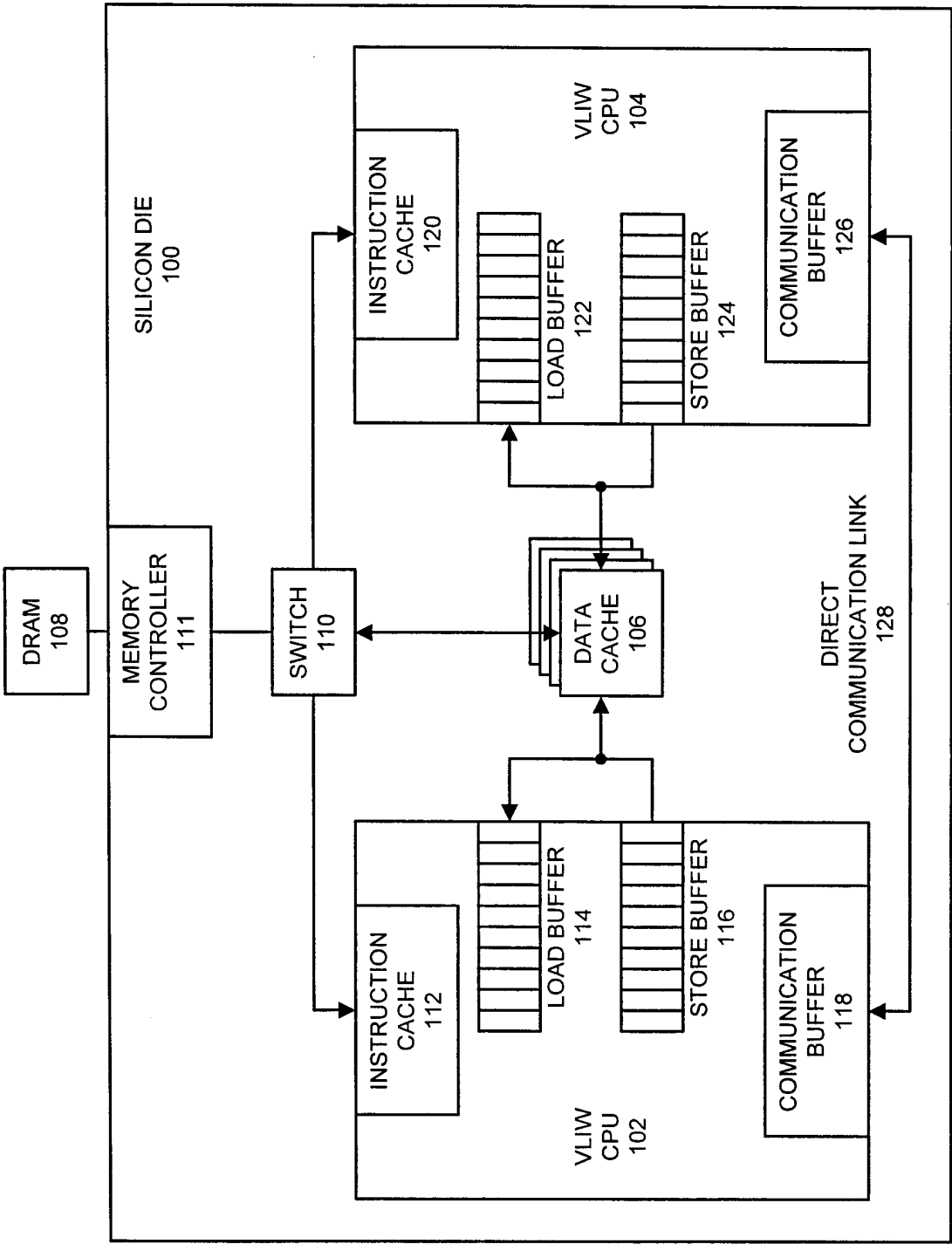


FIG. 1

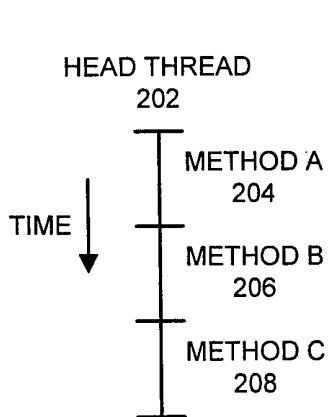


FIG. 2A
(PRIOR ART)

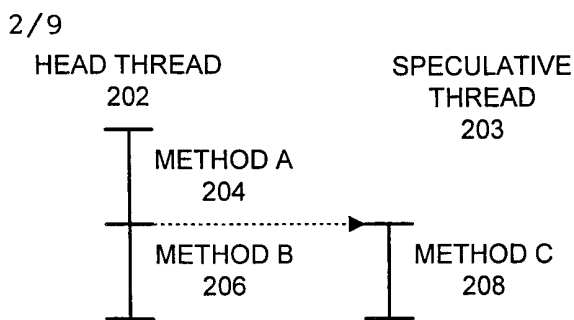


FIG. 2B

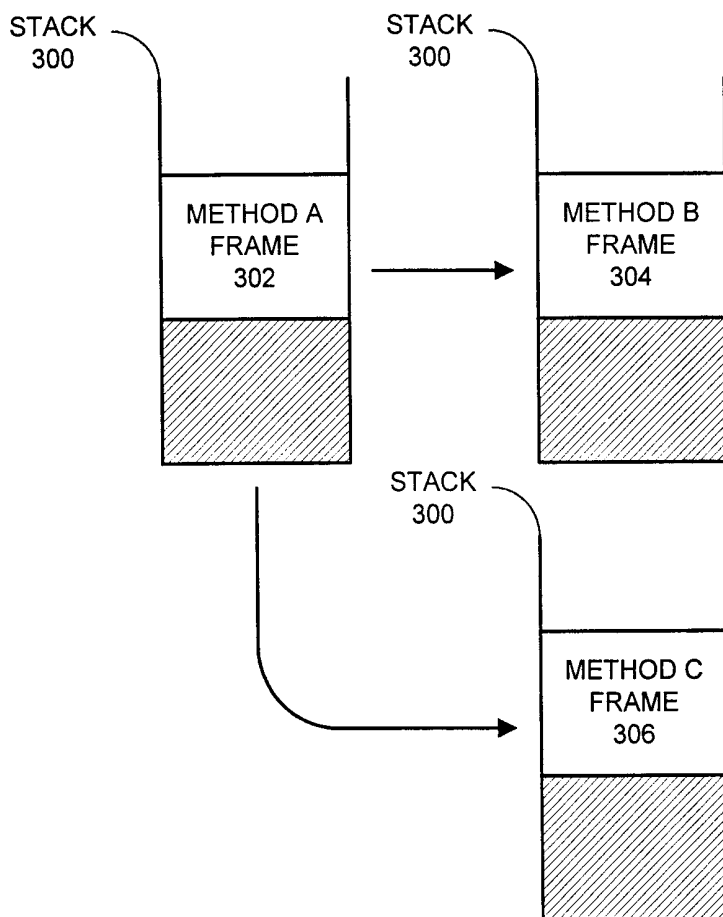


FIG. 3

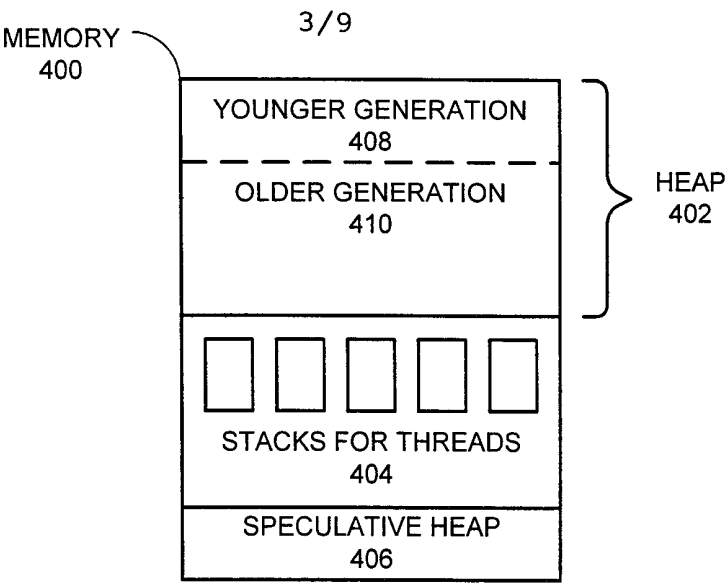


FIG. 4

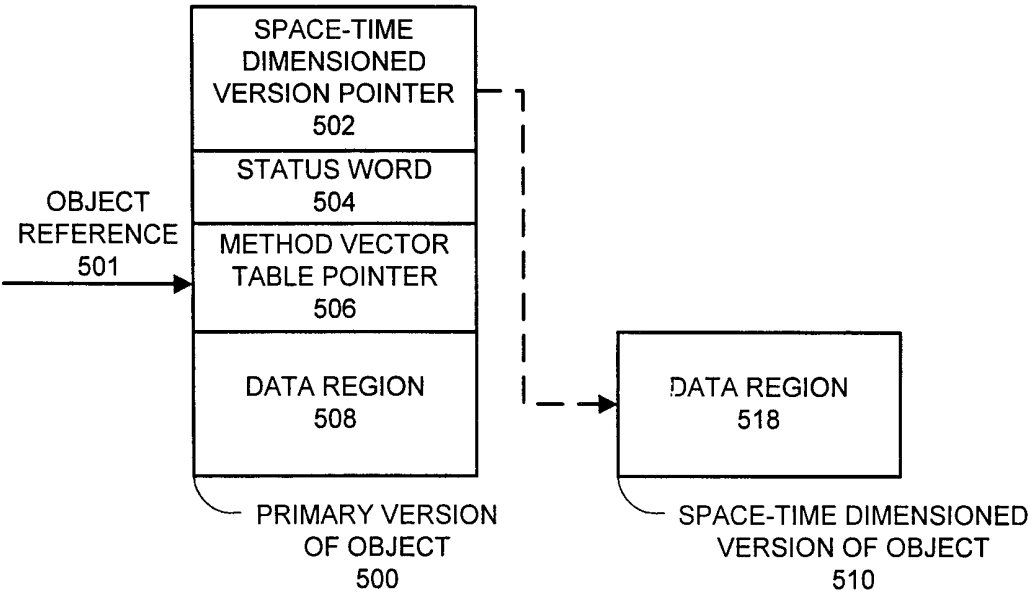


FIG. 5

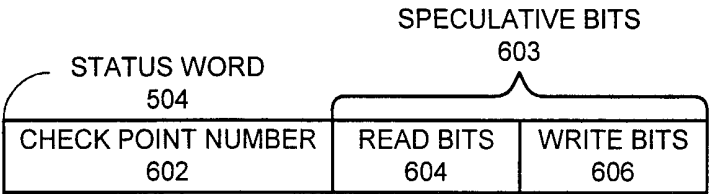
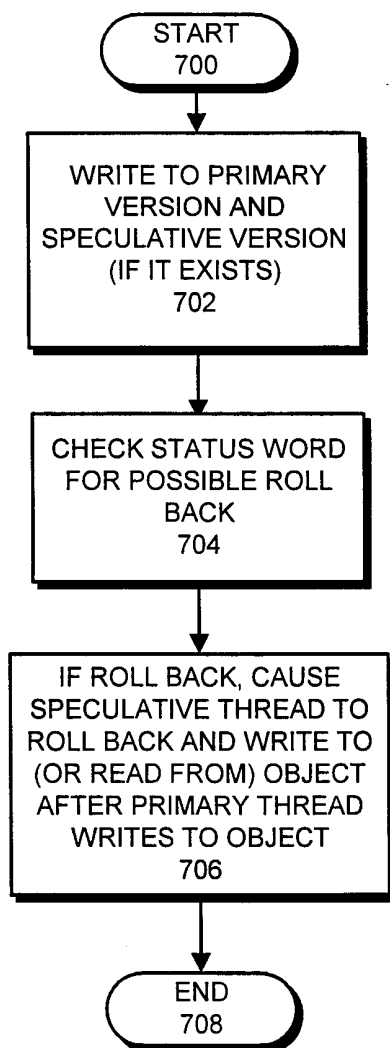
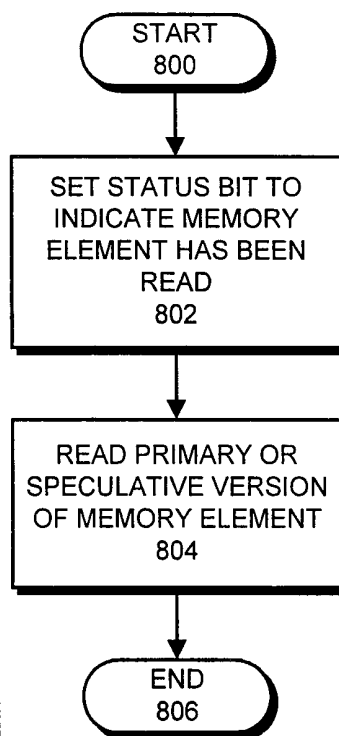
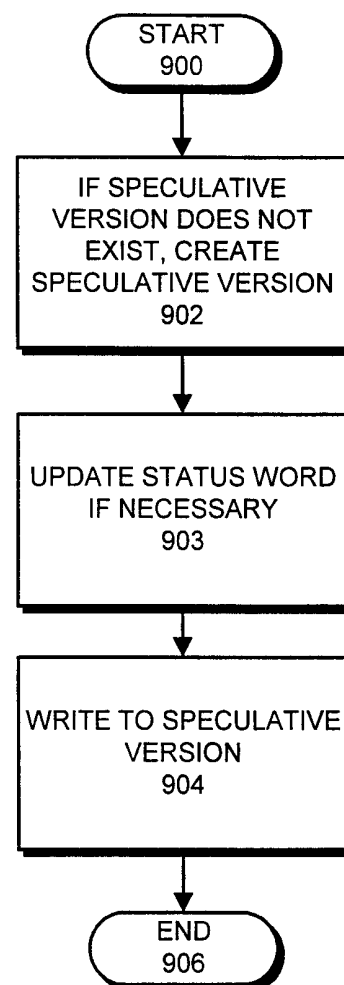
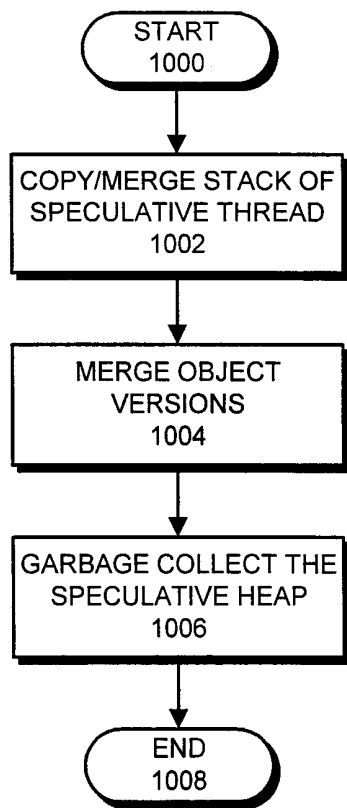
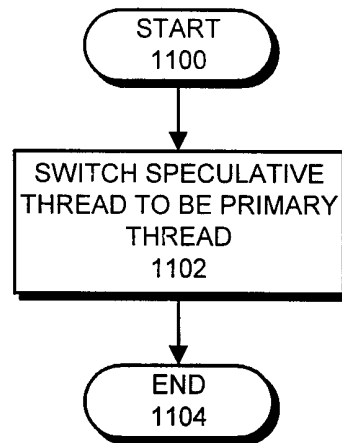


FIG. 6

4/9

**FIG. 7****FIG. 8****FIG. 9**

5/9

**FIG. 10****FIG. 11**

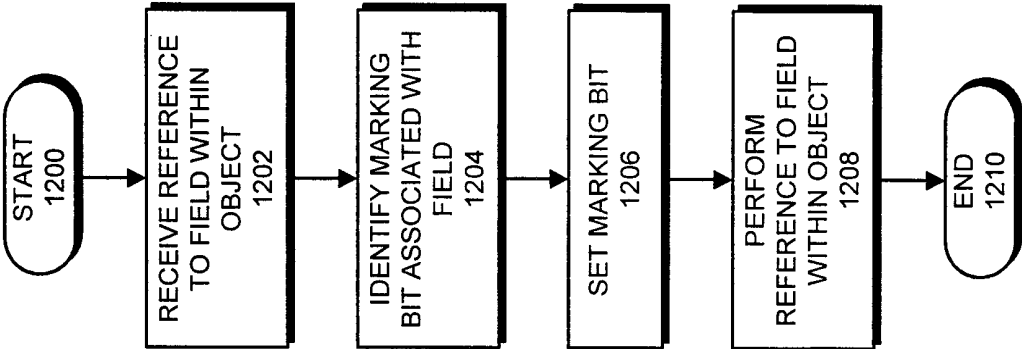


FIG. 12

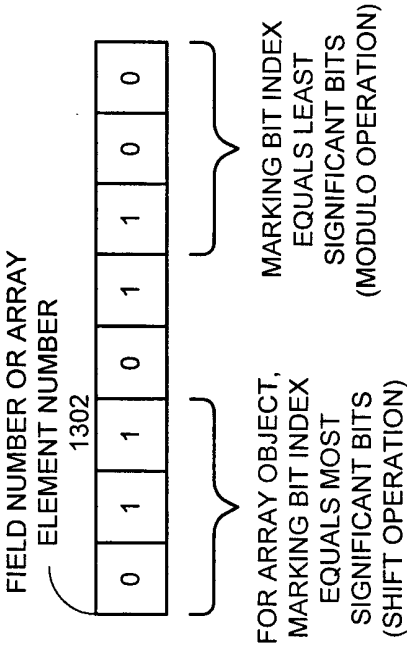


FIG. 13

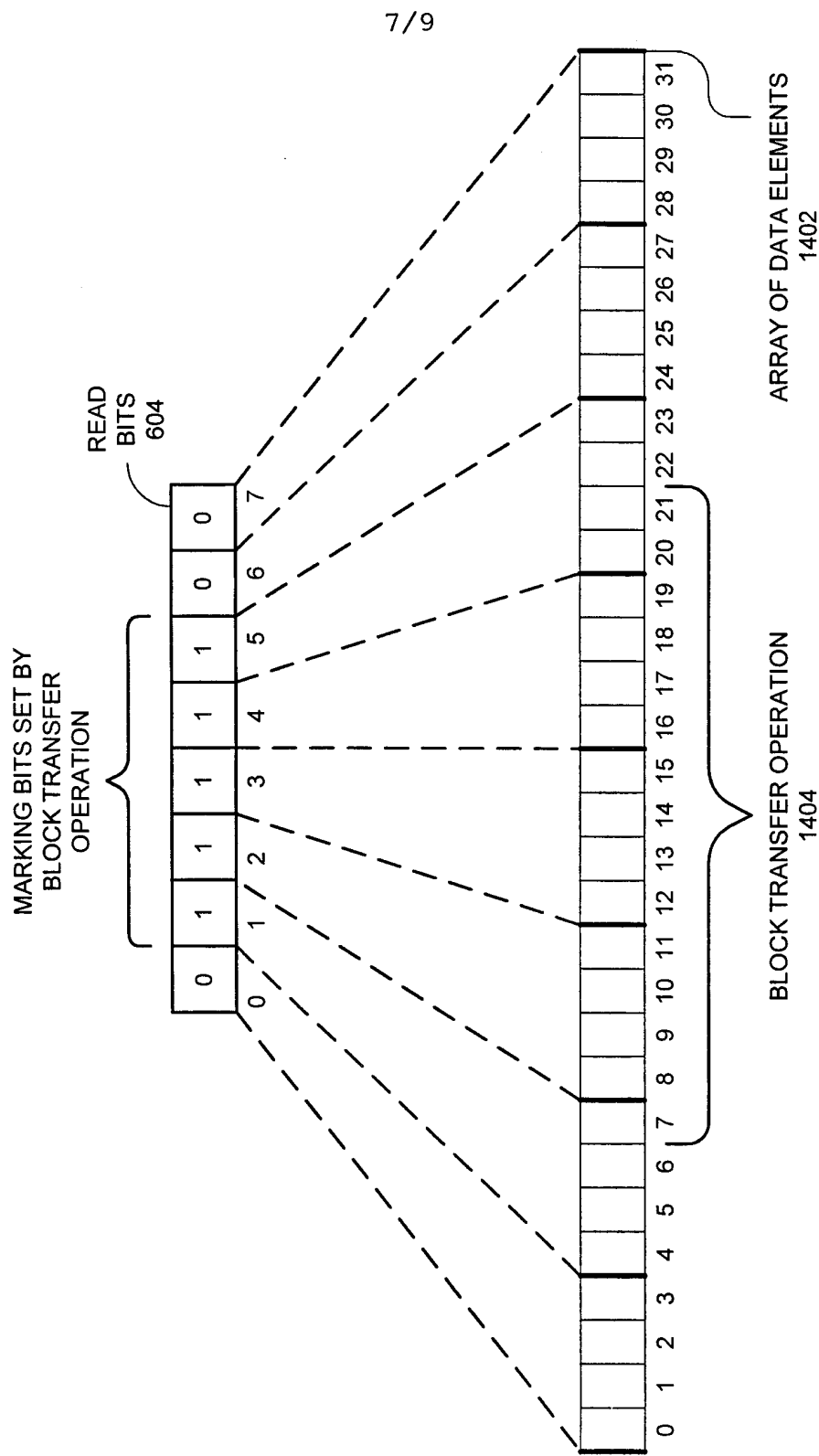


FIG. 14

8/9

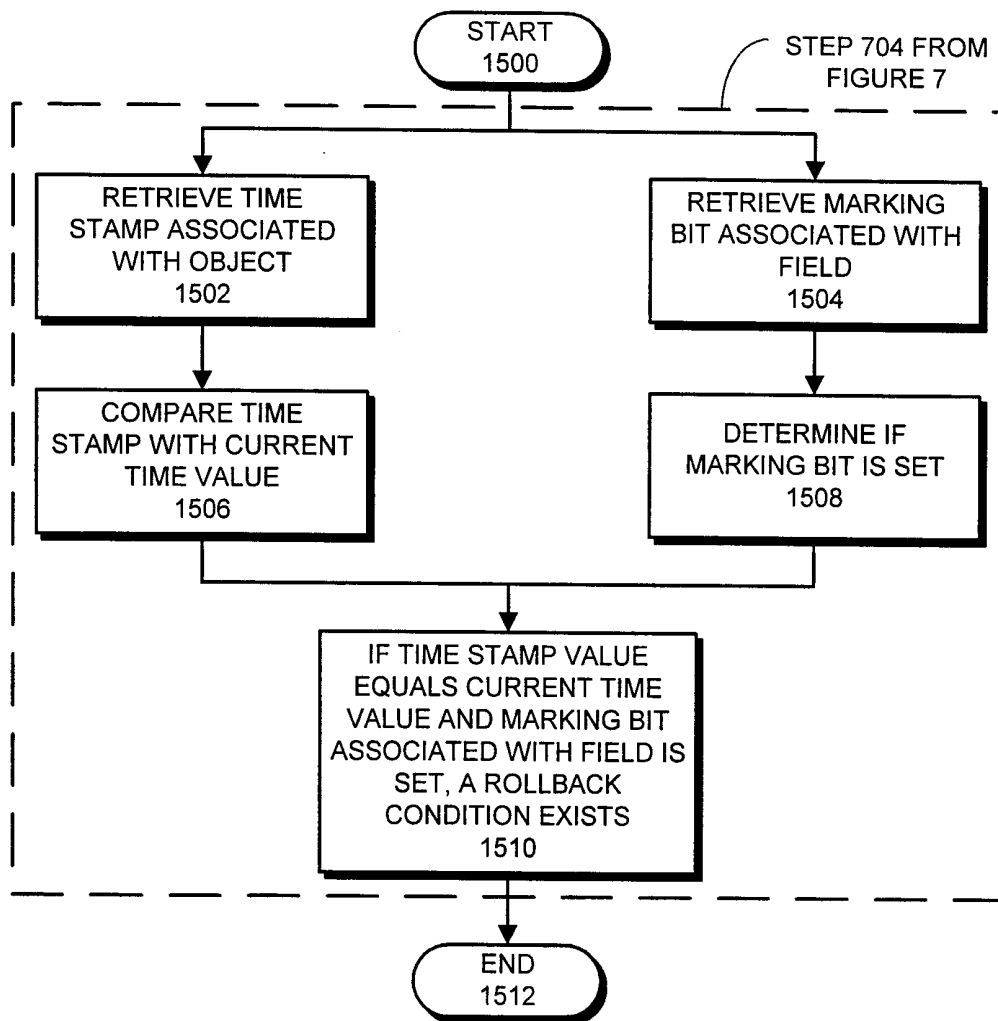


FIG. 15

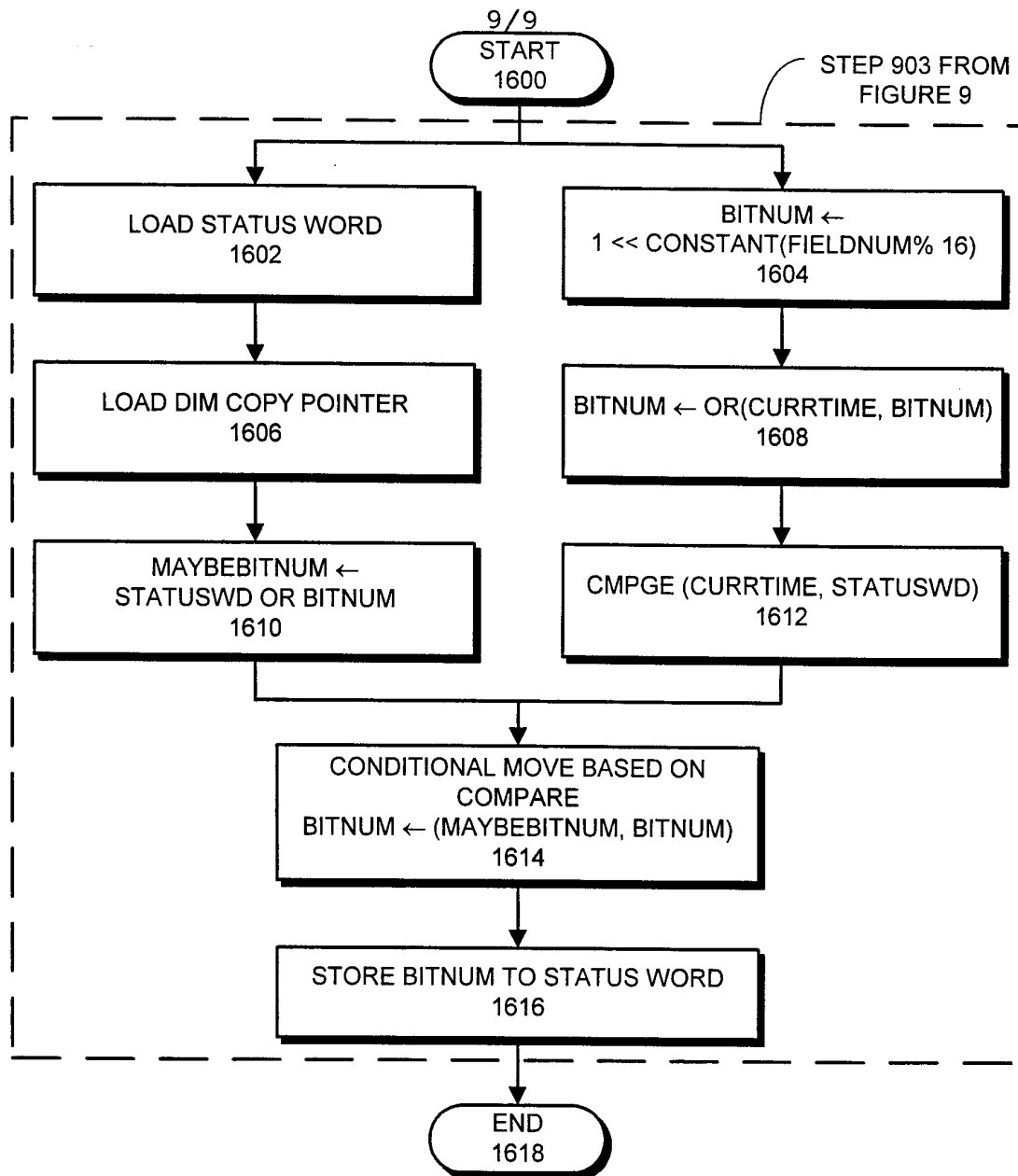


FIG. 16

INTERNATIONAL SEARCH REPORT

Internat Application No

PCT/US 00/13792

A. CLASSIFICATION OF SUBJECT MATTER

IPC 7 G06F9/46

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC 7 G06F

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal, INSPEC, IBM-TDB

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category °	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	WO 97 29436 A (MCCARTNEY MICHAEL DAVID ; SUPERNOW ISSEN DANIEL AARON (US)) 14 August 1997 (1997-08-14) page 6, line 1 - page 7, line 8 ---	1-21
A	US 5 701 480 A (RAZ YOAV) 23 December 1997 (1997-12-23) column 6, line 16 - column 8, line 57 ---	1-21
A	US 5 712 988 A (KHAIRA MANPREET S) 27 January 1998 (1998-01-27) column 1, line 31 - line 47 -----	1-21



Further documents are listed in the continuation of box C.



Patent family members are listed in annex.

° Special categories of cited documents :

"A" document defining the general state of the art which is not considered to be of particular relevance

"E" earlier document but published on or after the international filing date

"L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)

"O" document referring to an oral disclosure, use, exhibition or other means

"P" document published prior to the international filing date but later than the priority date claimed

"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention

"X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone

"Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art.

"&" document member of the same patent family

Date of the actual completion of the international search

17 October 2000

Date of mailing of the international search report

24/10/2000

Name and mailing address of the ISA

European Patent Office, P.B. 5818 Patentlaan 2
NL - 2280 HV Rijswijk
Tel. (+31-70) 340-2040, Tx. 31 651 epo nl,
Fax: (+31-70) 340-3016

Authorized officer

Brandt, J

INTERNATIONAL SEARCH REPORT

Information on patent family members

International Application No

PCT/US 00/13792

Patent document cited in search report	Publication date	Patent family member(s)	Publication date
WO 9729436 A	14-08-1997	US 5778225 A AU 1860097 A	07-07-1998 28-08-1997
US 5701480 A	23-12-1997	US 5504899 A	02-04-1996
US 5712988 A	27-01-1998	US 5574867 A	12-11-1996