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(54) Title: SILICON-CARBON-NITRIDE SELECTIVE ETCH

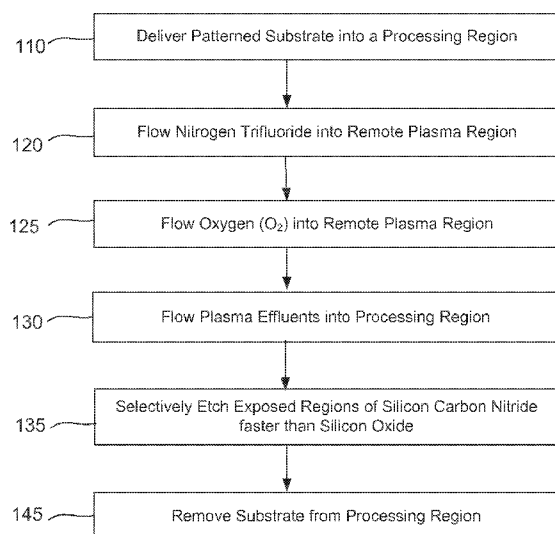


FIG. 1

(57) Abstract: A method of etching exposed silicon-nitrogen-and-carbon-containing material on patterned heterogeneous structures is described and includes a remote plasma etch formed from a fluorine-containing precursor and an oxygen-containing precursor. Plasma effluents from the remote plasma are flowed into a substrate processing region where the plasma effluents react with the exposed regions of silicon-nitrogen-and-carbon-containing material. The plasma effluents react with the patterned heterogeneous structures to selectively remove silicon-nitrogen-and-carbon-containing material from the exposed silicon-nitrogen-and-carbon-containing material regions while very slowly removing selected other exposed materials. The silicon-nitrogen-and-carbon-containing material selectivity results partly from the presence of an ion suppression element positioned between the remote plasma and the substrate processing region. The ion suppression element controls the number of ionically-charged species that reach the substrate. The methods may be used to selectively remove silicon-nitrogen-and-carbon-containing material at a faster rate than exposed silicon oxide or exposed silicon nitride.



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SILICON-CARBON-NITRIDE SELECTIVE ETCH

CROSS-REFERENCES TO RELATED APPLICATIONS

[0001] This application claims the benefit of U.S. Prov. Pat. App. No. 61/703,612 filed September 20, 2012, and titled "SILICON-CARBON-NITRIDE SELECTIVE ETCH," which is
5 incorporated in its entirety herein by reference for all purposes.

BACKGROUND OF THE INVENTION

[0002] Integrated circuits are made possible by processes which produce intricately patterned material layers on substrate surfaces. Producing patterned material on a substrate requires controlled methods for removal of exposed material. Chemical etching is used for a variety of
10 purposes including transferring a pattern in photoresist into underlying layers, thinning layers or thinning lateral dimensions of features already present on the surface. Often it is desirable to have an etch process which etches one material faster than another helping e.g. a pattern transfer process proceed. Such an etch process is said to be selective to the first material. As a result of the diversity of materials, circuits and processes, etch processes have been developed with a
15 selectivity towards a variety of materials. However, there are few options for selectively etching silicon carbon nitride.

[0003] Dry etch processes are often desirable for selectively removing material from semiconductor substrates. The desirability stems from the ability to gently remove material from miniature structures with minimal physical disturbance. Dry etch processes also allow the etch
20 rate to be abruptly stopped by removing the gas phase reagents. Some dry-etch processes involve the exposure of a substrate to remote plasma by-products formed from one or more precursors. For example, remote plasma excitation of ammonia and nitrogen trifluoride enables silicon oxide to be selectively removed from a patterned substrate when the plasma effluents are flowed into the substrate processing region. Some selective remote plasma etch processes
25 produce solid by-products which grow on the surface of the substrate as substrate material is removed. The solid by-products are subsequently removed via sublimation when the temperature of the substrate is raised. As a consequence of the production of solid by-products, SiconiTM etch process can deform delicate remaining structures formed in or nearby exposed regions of etched material.

[0004] A gas phase method of removing silicon-carbon-nitride selectively ideally without
30 forming solid by-products.

BRIEF SUMMARY OF THE INVENTION

[0005] A method of etching exposed silicon-nitrogen-and-carbon-containing material on patterned heterogeneous structures is described and includes a remote plasma etch formed from a fluorine-containing precursor and an oxygen-containing precursor. Plasma effluents from the remote plasma are flowed into a substrate processing region where the plasma effluents react with the exposed regions of silicon-nitrogen-and-carbon-containing material. The plasma effluents react with the patterned heterogeneous structures to selectively remove silicon-nitrogen-and-carbon-containing material from the exposed silicon-nitrogen-and-carbon-containing material regions while very slowly removing selected other exposed materials. The silicon-nitrogen-and-carbon-containing material selectivity results partly from the presence of an ion suppression element positioned between the remote plasma and the substrate processing region. The ion suppression element controls the number of ionically-charged species that reach the substrate. The methods may be used to selectively remove silicon-nitrogen-and-carbon-containing material at a faster rate than exposed silicon oxide or exposed silicon nitride.

[0006] Embodiments of the invention include methods of etching patterned substrates in a substrate processing region of a substrate processing chamber. The patterned substrate has an exposed silicon-nitrogen-and-carbon-containing region. The method includes flowing each of a fluorine-containing precursor and an oxygen-containing precursor into a remote plasma region fluidly coupled to the substrate processing region while forming a plasma in the plasma region to produce plasma effluents. The methods further include etching the exposed silicon-nitrogen-and-carbon-containing region by flowing the plasma effluents into the substrate processing region through through-holes in a showerhead.

[0007] Additional embodiments and features are set forth in part in the description that follows, and in part will become apparent to those skilled in the art upon examination of the specification or may be learned by the practice of the disclosed embodiments. The features and advantages of the disclosed embodiments may be realized and attained by means of the instrumentalities, combinations, and methods described in the specification.

BRIEF DESCRIPTION OF THE DRAWINGS

[0008] A further understanding of the nature and advantages of the disclosed embodiments may be realized by reference to the remaining portions of the specification and the drawings.

[0009] FIG. 1 is a flow chart of a silicon carbide selective etch process according to disclosed embodiments.

[0010] FIG. 2A shows a substrate processing chamber according to embodiments of the invention.

[0011] FIG. 2B shows a showerhead of a substrate processing chamber according to embodiments of the invention.

5 [0012] FIG. 3 shows a substrate processing system according to embodiments of the invention.

[0013] In the appended figures, similar components and/or features may have the same reference label. Further, various components of the same type may be distinguished by following the reference label by a dash and a second label that distinguishes among the similar components. If only the first reference label is used in the specification, the description is
10 applicable to any one of the similar components having the same first reference label irrespective of the second reference label.

DETAILED DESCRIPTION OF THE INVENTION

[0014] A method of etching exposed silicon-nitrogen-and-carbon-containing material on patterned heterogeneous structures is described and includes a remote plasma etch formed from a
15 fluorine-containing precursor and an oxygen-containing precursor. Plasma effluents from the remote plasma are flowed into a substrate processing region where the plasma effluents react with the exposed regions of silicon-nitrogen-and-carbon-containing material. The plasma effluents react with the patterned heterogeneous structures to selectively remove silicon-nitrogen-and-carbon-containing material from the exposed silicon-nitrogen-and-carbon-
20 containing material regions while very slowly removing selected other exposed materials. The silicon-nitrogen-and-carbon-containing material selectivity results partly from the presence of an ion suppression element positioned between the remote plasma and the substrate processing region. The ion suppression element controls the number of ionically-charged species that reach the substrate. The methods may be used to selectively remove silicon-nitrogen-and-carbon-
25 containing material at a faster rate than exposed silicon oxide or exposed silicon nitride.

[0015] The ion suppression element functions to reduce or eliminate ionically charged species traveling from the plasma generation region to the substrate. Uncharged neutral and radical species may pass through the openings in the ion suppressor to react at the substrate. It should be noted that complete elimination of ionically charged species in the reaction region
30 surrounding the substrate is not always the desired goal. In many instances, ionic species are required to reach the substrate in order to perform the etch and/or deposition process. In these instances, the ion suppressor helps control the concentration of ionic species in the reaction region at a level that assists the process.

[0016] In accordance with some embodiments of the invention, an ion suppressor as described in the exemplary equipment section may be used to provide radical and/or neutral species for selectively etching substrates. In one embodiment, for example, an ion suppressor is used to provide fluorine and oxygen containing plasma effluents to selectively etch silicon-nitrogen-and-carbon-containing material. Using the plasma effluents, an etch rate selectivity of silicon-nitrogen-and-carbon-containing material to silicon oxide of as high as about 4000:1 or more can be obtained. The ion suppressor may be used to provide a reactive gas having a higher concentration of radicals than ions. Because most of the charged particles of a plasma are filtered or removed by the ion suppressor, the substrate is typically not biased during the etch process. Such a process using radicals and other neutral species can reduce plasma damage compared to conventional plasma etch processes that include sputtering and bombardment. Embodiments of the present invention are also advantageous over conventional wet etch processes where surface tension of liquids can cause bending and peeling of small features.

[0017] In order to better understand and appreciate the invention, reference is now made to FIG. 1 which is a flow chart of a silicon carbide selective etch process according to disclosed embodiments. Silicon carbon nitride is an example of a silicon-nitrogen-and-carbon-containing material. Prior to the first operation, a structure is formed in a patterned substrate. The structure possesses separate exposed regions of silicon carbon nitride and silicon oxide. The substrate is then delivered into a processing region (operation 110).

[0018] A flow of nitrogen trifluoride is introduced into a plasma region separate from the processing region (operation 120). Other sources of fluorine may be used to augment or replace the nitrogen trifluoride. In general, a fluorine-containing precursor may be flowed into the plasma region and the fluorine-containing precursor comprises at least one precursor selected from the group consisting of atomic fluorine, diatomic fluorine, bromine trifluoride, chlorine trifluoride, nitrogen trifluoride, hydrogen fluoride, sulfur hexafluoride and xenon difluoride. Even carbon containing precursors, such as carbon tetrafluoride, trifluoromethane, difluoromethane, fluoromethane and the like, can be added to the group already listed. The use of carbon-containing precursor generally requires an increased flow of the oxygen-containing precursors described herein. The separate plasma region may be referred to as a remote plasma region herein and may be within a distinct module from the processing chamber or a compartment within the processing chamber. Oxygen (O_2) is also flowed into the plasma region (operation 125) where it is simultaneously excited in a plasma along with the nitrogen trifluoride. Generally speaking, an oxygen-containing precursor may be flowed into the plasma region and

the oxygen-containing precursor may comprise at least one precursor selected from O₂, O₃, N₂O, NO, NO₂, or the like.

[0019] Embodiments of the invention maintain a high atomic flow ratio of oxygen (O) to fluorine (F) in order achieve an etch rate of silicon-nitrogen-and-carbon-containing films which lends itself to use in manufacturing. Essentially the presence of oxygen helps to scavenge carbon from the silicon-nitrogen-and-carbon-containing film. In one embodiment, a gas flow ratio (O₂:NF₃) of between 1:1 and 4:1, or more generally an atomic flow ratio of between 2:3 and 8:3 (O:F), was found to achieve manufacturable etch rates of 50-100Å/minute or more. The present invention may also utilize O:F ratios at or above or about 0.5:1, above or about 1:1 or above or about 2:1 in different embodiments. The O:F atomic flow ratios may be below or about 10:1, below or about 6:1, below or about 5:1 or below or about 4:1 in embodiments of the invention. Upper limits on the atomic flow ratio may be combined with lower limits to create other embodiments. The higher ranges are typically used for fluorine-containing precursors which contain carbon. The more general atomic flow ratio, O:F, is calculated from the gas flow rate of each precursor gas and the total number of each atom per molecule. In the embodiment wherein one precursor is O₂ and another is NF₃, each molecule of oxygen includes two oxygen atoms whereas each molecule of nitrogen trifluoride includes three fluorine atoms. Using mass flow controllers to maintain a gas flow ratio above, e.g. 1:1, will result in an atomic flow ratio of above 2:3. In another embodiment, the precursor gases include at least one gas which contains both oxygen and fluorine. The atomic flow rate of all contributions are included when calculating the atomic flow ratio.

[0020] The plasma effluents formed in the remote plasma region are then flowed into the substrate processing region (operation 130). The patterned substrate is selectively etched (operation 135) such that the exposed silicon carbon nitride is removed at a rate at least twenty times greater than the exposed silicon oxide. The reactive chemical species are removed from the substrate processing region and then the substrate is removed from the processing region (operation 145). The flow of oxygen (O₂) into the plasma and resulting flow of oxygen-containing excited species into the substrate processing region enables the fluorine-containing excited species in the plasma effluents to remove the silicon carbon nitride. The flow of oxygen-containing excited species into the substrate processing region has little effect on the exposed regions of silicon oxide so the fluorine-containing excited species are substantially unable to etch the silicon oxide regions. The substrate processing region is plasma-free during the etching operation in order to ensure high etch selectivity of the silicon-nitrogen-and-carbon-containing layer.

[0021] Using the oxygen-containing precursor increases the etch rate of the silicon carbon nitride with minimal impact on an etch rate of the silicon oxide. The fluorine-containing precursor and/or the oxygen-containing precursor may further include one or more relatively inert gases such as He, N₂, Ar, or the like. The inert gas can be used to improve plasma stability.

5 Flow rates and ratios of the different gases may be used to control etch rates and etch selectivity. In an embodiment, the fluorine-containing gas includes NF₃ at a flow rate of between about 5 sccm and 300 sccm, O₂ at a flow rate of between about 5 sccm and 1500 sccm, He at a flow rate of between about 0 sccm and 3000 sccm, and Ar at a flow rate of between about 0 sccm and 3000 sccm. One of ordinary skill in the art would recognize that other gases and/or flows may
10 be used depending on a number of factors including processing chamber configuration, substrate size, geometry and layout of features being etched, and the like. Some hydrogen-containing precursors may also be combined with the other precursors or flowed separately into the plasma region, however, the concentration should be kept low. Hydrogen may interact with the fluorine-containing precursor in the plasma to form precursors which remove silicon oxide by forming
15 solid residue by-products on the oxide surface. This reaction reduces the selectivity of the exposed silicon carbon nitride regions as compared with exposed silicon oxide regions. Though some hydrogen may be useful to introduce in some embodiments, there may also be no or essentially no flow of hydrogen-containing precursors into the plasma region during the etch process in disclosed embodiments. The fluorine-containing precursor and the plasma effluents
20 may be essentially devoid of hydrogen in embodiments of the invention.

[0022] The method also includes applying energy to the fluorine-containing precursor and the oxygen-containing precursor while they are in the remote plasma region to generate the plasma effluents. As would be appreciated by one of ordinary skill in the art, the plasma may include a number of charged and neutral species including radicals and ions. The plasma may be
25 generated using known techniques (e.g., RF, capacitively coupled, inductively coupled, and the like). In an embodiment, the energy is applied using a capacitively-coupled plasma unit at a source power of between about 10 watts and 2000 watts and a pressure of between about 0.2 Torr and 30 Torr. The capacitively-coupled plasma unit may be disposed remote from a gas reaction region of the processing chamber. For example, the capacitively-coupled plasma unit
30 and the plasma generation region may be separated from the gas reaction region by an ion suppressor.

[0023] Without wishing to bind the coverage of the claims to theoretical mechanisms which may or may not be entirely correct, some discussion of possible mechanisms may prove beneficial. Radical-fluorine precursors and radical-oxygen precursors are concurrently produced

by delivering a fluorine-containing precursor and an oxygen-containing precursor into the remote plasma region. Applicants suppose that a concentration of radical-fluorine fragments, fluorine ions and atoms are produced and delivered into the substrate processing region. Applicants further suppose that radical-oxygen species are concurrently delivered to the substrate processing region. The radical-oxygen species may react with carbon in the near surface region to create a silicon-rich region near the exposed surface of the silicon carbon nitride regions. Volatile species which may be created during this reaction may include carbon dioxide (CO₂) which can then be pumped away through an exhaust system fluidly coupled to the substrate processing region. The silicon-rich near surface region of the silicon carbon nitride can be etched by the radical-fluorine precursors which are concurrently available within the substrate processing region. The silicon oxide regions are essentially unaffected by the radical-oxygen. Radical-fluorine precursors are highly selective towards silicon and leave silicon oxide essentially unetched. As a consequence, the etching methods outlined herein achieve selectivity toward silicon-nitrogen-and-carbon-containing material. Generally speaking, the selectivity of the etching methods presented herein may possess selectivities (silicon-nitrogen-and-carbon-containing material:silicon oxide) greater than or about 50:1, greater than or about 100:1, greater than or about 150:1 or greater than or about 250:1. The selectivity, the non-local plasma, the controlled ionic concentration and the lack of solid byproducts, each make these etch processes well suited for removing or trimming delicate silicon-nitrogen-and-carbon-containing material structures with little deformation and while removing little or no silicon oxide. These selectivities also apply generally to silicon-nitrogen-and-carbon-containing material which will be described shortly.

[0024] In addition to or instead of exposed silicon oxide regions, there may be regions of exposed silicon nitride on the patterned substrate during the etching operation herein. The selectivity of the etching operation (exposed silicon-nitrogen-and-carbon-containing region: exposed silicon nitride region) may be greater than or about 2:1, greater than or about 3:1 or greater than or about 4:1 in embodiments of the invention. Silicon carbide and silicon carbon nitride may etch at more similar rates using the etching processes disclosed herein, whereas silicon nitride and silicon are more slowly etched as quantified above.

[0025] The temperature of the substrate may be between about -30°C and about 150°C in general. The etch rate has been found to be higher for the lower temperatures within this range. In embodiments, the temperature of the substrate during the etches described herein may be about 0°C or more, about 5°C or more or about 10°C or more. The substrate temperatures may be less than or about 50°C, less than or about 30°C, less than or about 20°C, less than or about

15°C or less than or about 10°C in different embodiments. The data further show an increase in etch rate as a function of process pressure. The pressure within the substrate processing region is below or about 50 Torr, below or about 30 Torr, below or about 20 Torr, below or about 10 Torr or below or about 5 Torr. The pressure may be above or about 0.1 Torr, above or about 0.2 Torr, above or about 0.5 Torr or above or about 1 Torr in embodiments of the invention. Any of the upper limits on temperature or pressure may be combined with lower limits to form additional embodiments. Generally speaking, the processes described herein may be used to etch films which contain silicon and carbon (and not just silicon carbide). The remote plasma etch processes may remove silicon-nitrogen-and-carbon-containing material which includes an atomic concentration of about 30% or more silicon. The atomic concentration of carbon in the silicon-nitrogen-and-carbon-containing material may be greater than or about 10%, greater than or about 15% or greater than or about 20% in embodiments of the invention. Analogously, the atomic concentration of nitrogen in the silicon-nitrogen-and-carbon-containing material may be greater than or about 10%, greater than or about 15% or greater than or about 20% in embodiments of the invention. The silicon-nitrogen-and-carbon-containing material may also consist only of silicon, nitrogen and carbon, outside of impurities and dopants. Of course, the silicon-nitrogen-and-carbon-containing material may be silicon carbon nitride in embodiments of the invention.

[0026] Additional process parameters are disclosed in the course of describing an exemplary processing chamber and system.

Exemplary Processing System

[0027] Processing chambers that may implement embodiments of the present invention may be included within processing platforms such as the CENTURA® and PRODUCER® systems, available from Applied Materials, Inc. of Santa Clara, Calif. Examples of substrate processing chambers that can be used with exemplary methods of the invention may include those shown and described in co-assigned U.S. Provisional Patent App. No. 60/803,499 to Lubomirsky et al, filed May 30, 2006, and titled "PROCESS CHAMBER FOR DIELECTRIC GAPFILL," the entire contents of which is herein incorporated by reference for all purposes. Additional exemplary systems may include those shown and described in U.S. Pat. Nos. 6,387,207 and 6,830,624, which are also incorporated herein by reference for all purposes.

[0028] FIG. 2A is a substrate processing chamber 1001 according to disclosed embodiments. A remote plasma system 1010 may process the fluorine-containing precursor which then travels through a gas inlet assembly 1011. Two distinct gas supply channels are visible within the gas inlet assembly 1011. A first channel 1012 carries a gas that passes through the remote plasma

system 1010 (RPS), while a second channel 1013 bypasses the remote plasma system 1010. Either channel may be used for the fluorine-containing precursor, in embodiments. On the other hand, the first channel 1012 may be used for the process gas and the second channel 1013 may be used for a treatment gas. The lid (or conductive top portion) 1021 and a perforated partition 1053 are shown with an insulating ring 1024 in between, which allows an AC potential to be applied to the lid 1021 relative to perforated partition 1053. The AC potential strikes a plasma in chamber plasma region 1020. The process gas may travel through first channel 1012 into chamber plasma region 1020 and may be excited by a plasma in chamber plasma region 1020 alone or in combination with remote plasma system 1010. If the process gas (the fluorine-containing precursor) flows through second channel 1013, then only the chamber plasma region 1020 is used for excitation. The combination of chamber plasma region 1020 and/or remote plasma system 1010 may be referred to as a remote plasma system herein. The perforated partition (also referred to as a showerhead) 1053 separates chamber plasma region 1020 from a substrate processing region 1070 beneath showerhead 1053. Showerhead 1053 allows a plasma present in chamber plasma region 1020 to avoid directly exciting gases in substrate processing region 1070, while still allowing excited species to travel from chamber plasma region 1020 into substrate processing region 1070.

[0029] Showerhead 1053 is positioned between chamber plasma region 1020 and substrate processing region 1070 and allows plasma effluents (excited derivatives of precursors or other gases) created within remote plasma system 1010 and/or chamber plasma region 1020 to pass through a plurality of through-holes 1056 that traverse the thickness of the plate. The showerhead 1053 also has one or more hollow volumes 1051 which can be filled with a precursor in the form of a vapor or gas and pass through small holes 1055 into substrate processing region 1070 but not directly into chamber plasma region 1020. Hollow volumes 1051 may be used for precursors which do not require excitement by a plasma to achieve a specific process. Showerhead 1053 is thicker than the length of the smallest diameter 1050 of the through-holes 1056 in this disclosed embodiment. In order to maintain a significant concentration of excited species penetrating from chamber plasma region 1020 to substrate processing region 1070, the length 226 of the smallest diameter 1050 of the through-holes may be restricted by forming larger diameter portions of through-holes 1056 part way through the showerhead 1053. The length of the smallest diameter 1050 of the through-holes 1056 may be the same order of magnitude as the smallest diameter of the through-holes 1056 or less in disclosed embodiments.

[0030] Showerhead 1053 may be configured to serve the purpose of an ion suppressor as shown in FIG. 2A. Alternatively, a separate processing chamber element may be included (not shown) which suppresses the ion concentration traveling into substrate processing region 1070. Lid 1021 and showerhead 1053 may function as a first electrode and second electrode, respectively, so that lid 1021 and showerhead 1053 may receive different electric voltages. In these configurations, electrical power (e.g., RF power) may be applied to lid 1021, showerhead 1053, or both. For example, electrical power may be applied to lid 1021 while showerhead 1053 (serving as ion suppressor) is grounded. The substrate processing system may include a RF generator that provides electrical power to the lid and/or showerhead 1053. The voltage applied to lid 1021 may facilitate a uniform distribution of plasma (i.e., reduce localized plasma) within chamber plasma region 1020. To enable the formation of a plasma in chamber plasma region 1020, insulating ring 1024 may electrically insulate lid 1021 from showerhead 1053. Insulating ring 1024 may be made from a ceramic and may have a high breakdown voltage to avoid sparking. Portions of substrate processing chamber 1001 near the capacitively-coupled plasma components just described may further include a cooling unit (not shown) that includes one or more cooling fluid channels to cool surfaces exposed to the plasma with a circulating coolant (e.g., water).

[0031] In the embodiment shown, showerhead 1053 may distribute (via through-holes 1056) process gases which contain oxygen, fluorine and/or nitrogen and/or plasma effluents of such process gases upon excitation by a plasma in chamber plasma region 1020. In embodiments, the process gas introduced into the remote plasma system 1010 and/or chamber plasma region 1020 may contain fluorine (e.g. F_2 , NF_3 or XeF_2). The process gas may also include a carrier gas such as helium, argon, nitrogen (N_2), etc. Plasma effluents may include ionized or neutral derivatives of the process gas and may also be referred to herein as a radical-fluorine precursor referring to the atomic constituent of the process gas introduced.

[0032] Through-holes 1056 are configured to suppress the migration of ionically-charged species out of the chamber plasma region 1020 while allowing uncharged neutral or radical species to pass through showerhead 1053 into substrate processing region 1070. These uncharged species may include highly reactive species that are transported with less reactive carrier gas by through-holes 1056. As noted above, the migration of ionic species by through-holes 1056 may be reduced, and in some instances completely suppressed. Controlling the amount of ionic species passing through showerhead 1053 provides increased control over the gas mixture brought into contact with the underlying wafer substrate, which in turn increases control of the deposition and/or etch characteristics of the gas mixture. For example,

adjustments in the ion concentration of the gas mixture can significantly alter its etch selectivity (e.g., SiC_x:SiO_x etch ratios).

[0033] In embodiments, the number of through-holes 1056 may be between about 60 and about 2000. Through-holes 1056 may have a variety of shapes but are most easily made round.

5 The smallest diameter 1050 of through-holes 1056 may be between about 0.5 mm and about 20 mm or between about 1 mm and about 6 mm in disclosed embodiments. There is also latitude in choosing the cross-sectional shape of through-holes, which may be made conical, cylindrical or combinations of the two shapes. The number of small holes 1055 used to introduce unexcited precursors into substrate processing region 1070 may be between about 100 and about 5000 or
10 between about 500 and about 2000 in different embodiments. The diameter of the small holes 1055 may be between about 0.1 mm and about 2 mm.

[0034] Through-holes 1056 may be configured to control the passage of the plasma-activated gas (i.e., the ionic, radical, and/or neutral species) through showerhead 1053. For example, the aspect ratio of the holes (i.e., the hole diameter to length) and/or the geometry of the holes may
15 be controlled so that the flow of ionically-charged species in the activated gas passing through showerhead 1053 is reduced. Through-holes 1056 in showerhead 1053 may include a tapered portion that faces chamber plasma region 1020, and a cylindrical portion that faces substrate processing region 1070. The cylindrical portion may be proportioned and dimensioned to control the flow of ionic species passing into substrate processing region 1070. An adjustable
20 electrical bias may also be applied to showerhead 1053 as an additional means to control the flow of ionic species through showerhead 1053.

[0035] Alternatively, through-holes 1056 may have a smaller inner diameter (ID) toward the top surface of showerhead 1053 and a larger ID toward the bottom surface. In addition, the bottom edge of through-holes 1056 may be chamfered to help evenly distribute the plasma
25 effluents in substrate processing region 1070 as the plasma effluents exit the showerhead and thereby promote even distribution of the plasma effluents and precursor gases. The smaller ID may be placed at a variety of locations along through-holes 1056 and still allow showerhead 1053 to reduce the ion density within substrate processing region 1070. The reduction in ion density results from an increase in the number of collisions with walls prior to entry into
30 substrate processing region 1070. Each collision increases the probability that an ion is neutralized by the acquisition or loss of an electron from the wall. Generally speaking, the smaller ID of through-holes 1056 may be between about 0.2 mm and about 20 mm. In other embodiments, the smaller ID may be between about 1 mm and 6 mm or between about 0.2 mm

and about 5 mm. Further, aspect ratios of the through-holes 1056 (i.e., the smaller ID to hole length) may be approximately 1 to 20. The smaller ID of the through-holes may be the minimum ID found along the length of the through-holes. The cross sectional shape of through-holes 1056 may be generally cylindrical, conical, or any combination thereof.

5 [0036] FIG. 2B is a bottom view of a showerhead 1053 for use with a processing chamber according to disclosed embodiments. Showerhead 1053 corresponds with the showerhead shown in FIG. 2A. Through-holes 1056 are depicted with a larger inner-diameter (ID) on the bottom of showerhead 1053 and a smaller ID at the top. Small holes 1055 are distributed substantially evenly over the surface of the showerhead, even amongst the through-holes 1056
10 which helps to provide more even mixing than other embodiments described herein.

[0037] An exemplary patterned substrate may be supported by a pedestal (not shown) within substrate processing region 1070 when fluorine-containing plasma effluents and oxygen-containing plasma effluents arrive through through-holes 1056 in showerhead 1053. Though substrate processing region 1070 may be equipped to support a plasma for other processes such
15 as curing, no plasma is present during the etching of patterned substrate, in embodiments of the invention.

[0038] A plasma may be ignited either in chamber plasma region 1020 above showerhead 1053 or substrate processing region 1070 below showerhead 1053. A plasma is present in chamber plasma region 1020 to produce the radical-fluorine precursors from an inflow of the
20 fluorine-containing precursor. An AC voltage typically in the radio frequency (RF) range is applied between the conductive top portion (lid 1021) of the processing chamber and showerhead 1053 to ignite a plasma in chamber plasma region 1020 during deposition. An RF power supply generates a high RF frequency of 13.56 MHz but may also generate other frequencies alone or in combination with the 13.56 MHz frequency.

25 [0039] The top plasma may be left at low or no power when the bottom plasma in the substrate processing region 1070 is turned on to either cure a film or clean the interior surfaces bordering substrate processing region 1070. A plasma in substrate processing region 1070 is ignited by applying an AC voltage between showerhead 1053 and the pedestal or bottom of the chamber. A cleaning gas may be introduced into substrate processing region 1070 while the plasma is
30 present.

[0040] The pedestal may have a heat exchange channel through which a heat exchange fluid flows to control the temperature of the substrate. This configuration allows the substrate temperature to be cooled or heated to maintain relatively low temperatures (from room

temperature through about 120°C). The heat exchange fluid may comprise ethylene glycol and water. The wafer support platter of the pedestal (preferably aluminum, ceramic, or a combination thereof) may also be resistively heated in order to achieve relatively high temperatures (from about 120°C through about 1100°C) using an embedded single-loop
5 embedded heater element configured to make two full turns in the form of parallel concentric circles. An outer portion of the heater element may run adjacent to a perimeter of the support platter, while an inner portion runs on the path of a concentric circle having a smaller radius. The wiring to the heater element passes through the stem of the pedestal.

[0041] The substrate processing system is controlled by a system controller. In an exemplary
10 embodiment, the system controller includes a hard disk drive, a floppy disk drive and a processor. The processor contains a single-board computer (SBC), analog and digital input/output boards, interface boards and stepper motor controller boards. Various parts of CVD system conform to the Versa Modular European (VME) standard which defines board, card cage, and connector dimensions and types. The VME standard also defines the bus structure as having
15 a 16-bit data bus and a 24-bit address bus.

[0042] The system controller controls all of the activities of the etching chamber. The system controller executes system control software, which is a computer program stored in a computer-readable medium. Preferably, the medium is a hard disk drive, but the medium may also be other kinds of memory. The computer program includes sets of instructions that dictate the
20 timing, mixture of gases, chamber pressure, chamber temperature, RF power levels, susceptor position, and other parameters of a particular process. Other computer programs stored on other memory devices including, for example, a floppy disk or other another appropriate drive, may also be used to instruct the system controller.

[0043] A process for depositing a film stack on a substrate or a process for cleaning a chamber
25 can be implemented using a computer program product that is executed by the system controller. The computer program code can be written in any conventional computer readable programming language: for example, 68000 assembly language, C, C++, Pascal, Fortran or others. Suitable program code is entered into a single file, or multiple files, using a conventional text editor, and stored or embodied in a computer usable medium, such as a memory system of the computer. If
30 the entered code text is in a high level language, the code is compiled, and the resultant compiler code is then linked with an object code of precompiled Microsoft Windows® library routines. To execute the linked, compiled object code the system user invokes the object code, causing the

computer system to load the code in memory. The CPU then reads and executes the code to perform the tasks identified in the program.

[0044] The interface between a user and the controller may include a flat-panel touch-sensitive monitor. In the preferred embodiment two monitors are used, one mounted in the clean room wall for the operators and the other behind the wall for the service technicians. The two monitors may simultaneously display the same information, in which case only one accepts input at a time. To select a particular screen or function, the operator touches a designated area of the touch-sensitive monitor. The touched area changes its highlighted color, or a new menu or screen is displayed, confirming communication between the operator and the touch-sensitive monitor. Other devices, such as a keyboard, mouse, or other pointing or communication device, may be used instead of or in addition to the touch-sensitive monitor to allow the user to communicate with the system controller.

[0045] The chamber plasma region or a region in a remote plasma system may be referred to as a remote plasma region. In embodiments, the radical precursors (e.g. a radical-fluorine precursor and radical-oxygen precursor) are formed in the remote plasma region and travel into the substrate processing region where the combination preferentially etches silicon-nitrogen-and-carbon-containing material. Plasma power may essentially be applied only to the remote plasma region, in embodiments, to ensure that the radical-fluorine precursor and the radical-oxygen precursor are not further excited in the substrate processing region.

[0046] In embodiments employing a chamber plasma region, the excited plasma effluents are generated in a section of the substrate processing region partitioned from a deposition region. The deposition region, also known herein as the substrate processing region, is where the plasma effluents mix and react to etch the patterned substrate (e.g., a semiconductor wafer). The excited plasma effluents may also be accompanied by inert gases (in the exemplary case, argon). The substrate processing region may be described herein as "plasma-free" during the etch of the patterned substrate. "Plasma-free" does not necessarily mean the region is devoid of plasma. A relatively low concentration of ionized species and free electrons created within the plasma region do travel through pores (apertures) in the partition (showerhead/ion suppressor) due to the shapes and sizes of through-holes 1056. In some embodiments, there is no concentration of ionized species and free electrons within the substrate processing region outside of unavoidable sources of unintentional radiation which are omnipresent. The borders of the plasma in the chamber plasma region are hard to define and may encroach upon the substrate processing region through the apertures in the showerhead. In the case of an inductively-coupled plasma, a small

amount of ionization may be effected within the substrate processing region directly.

Furthermore, a low intensity plasma may be created in the substrate processing region without eliminating desirable features of the forming film. All causes for a plasma having much lower intensity ion density than the chamber plasma region (or a remote plasma region, for that matter) during the creation of the excited plasma effluents do not deviate from the scope of "plasma-free" as used herein.

[0047] Nitrogen trifluoride (or another fluorine-containing precursor) may be flowed into chamber plasma region 1020 at rates between about 25 sccm and about 200 sccm, between about 50 sccm and about 150 sccm or between about 75 sccm and about 125 sccm in different embodiments. Oxygen (O₂) may be flowed into chamber plasma region 1020 at rates between about 25 sccm and about 200 sccm, between about 50 sccm and about 150 sccm or between about 75 sccm and about 125 sccm in different embodiments.

[0048] Combined flow rates of fluorine-containing precursor and oxygen-containing precursor into the chamber may account for 0.05% to about 20% by volume of the overall gas mixture; the remainder being carrier gases. The fluorine-containing precursor and the oxygen-containing precursor are flowed into the remote plasma region but the plasma effluents have the same volumetric flow ratio, in embodiments. In the case of the fluorine-containing precursor, a purge or carrier gas may be first initiated into the remote plasma region before those of the fluorine-containing gas to stabilize the pressure within the remote plasma region.

[0049] Plasma power can be a variety of frequencies or a combination of multiple frequencies. In the exemplary processing system the plasma is provided by RF power delivered between lid 1021 and showerhead 1053. The RF power may be between about 10 watts and about 2000 watts, between about 20 watts and about 1500 watts or between about 50 watts and about 500 watts in different embodiments. The RF frequency applied in the exemplary processing system may be low RF frequencies less than about 200 kHz, high RF frequencies between about 10 MHz and about 15 MHz or microwave frequencies greater than or about 1 GHz in different embodiments.

[0050] Substrate processing region 1070 can be maintained at a variety of pressures during the flow of carrier gases and plasma effluents into substrate processing region 1070. The pressure within the substrate processing region is below or about 50 Torr, below or about 30 Torr, below or about 20 Torr, below or about 10 Torr or below or about 5 Torr. The pressure may be above or about 0.1 Torr, above or about 0.2 Torr, above or about 0.5 Torr or above or about 1 Torr in

embodiments of the invention. Lower limits on the pressure may be combined with upper limits on the pressure to arrive at further embodiments of the invention.

[0051] In one or more embodiments, the substrate processing chamber 1001 can be integrated into a variety of multi-processing platforms, including the ProducerTM GT, CenturaTM AP and EnduraTM platforms available from Applied Materials, Inc. located in Santa Clara, Calif. Such a processing platform is capable of performing several processing operations without breaking vacuum. Processing chambers that may implement embodiments of the present invention may include dielectric etch chambers or a variety of chemical vapor deposition chambers, among other types of chambers.

[0052] Embodiments of the deposition systems may be incorporated into larger fabrication systems for producing integrated circuit chips. FIG. 3 shows one such system 1101 of deposition, etching, baking and curing chambers according to disclosed embodiments. In the figure, a pair of FOUPs (front opening unified pods) 1102 supply substrate substrates (e.g., 300 mm diameter wafers) that are received by robotic arms 1104 and placed into a low pressure holding areas 1106 before being placed into one of the wafer processing chambers 1108a-f. A second robotic arm 1110 may be used to transport the substrate wafers from the low pressure holding areas 1106 to the wafer processing chambers 1108a-f and back. Each wafer processing chamber 1108a-f, can be outfitted to perform a number of substrate processing operations including the dry etch processes described herein in addition to cyclical layer deposition (CLD), atomic layer deposition (ALD), chemical vapor deposition (CVD), physical vapor deposition (PVD), etch, pre-clean, degas, orientation and other substrate processes.

[0053] The wafer processing chambers 1108a-f may include one or more system components for depositing, annealing, curing and/or etching a flowable dielectric film on the substrate wafer. In one configuration, two pairs of the processing chamber (e.g., 1108c-d and 1108e-f) may be used to deposit dielectric material on the substrate, and the third pair of processing chambers (e.g., 1108a-b) may be used to etch the deposited dielectric. In another configuration, all three pairs of chambers (e.g., 1108a-f) may be configured to etch a dielectric film on the substrate. Any one or more of the processes described may be carried out on chamber(s) separated from the fabrication system shown in different embodiments.

[0054] System controller 1157 is used to control motors, valves, flow controllers, power supplies and other functions required to carry out process recipes described herein. A gas handling system 1155 may also be controlled by system controller 1157 to introduce gases to one or all of the wafer processing chambers 1108a-f. System controller 1157 may rely on feedback

from optical sensors to determine and adjust the position of movable mechanical assemblies in gas handling system 1155 and/or in wafer processing chambers 1108a-f. Mechanical assemblies may include the robot, throttle valves and susceptors which are moved by motors under the control of system controller 1157.

- 5 **[0055]** In an exemplary embodiment, system controller 1157 includes a hard disk drive (memory), USB ports, a floppy disk drive and a processor. System controller 1157 includes analog and digital input/output boards, interface boards and stepper motor controller boards. Various parts of multi-chamber processing system 1101 which contains substrate processing chamber 1001 are controlled by system controller 1157. The system controller executes system control software in the form of a computer program stored on computer-readable medium such as a hard disk, a floppy disk or a flash memory thumb drive. Other types of memory can also be used. The computer program includes sets of instructions that dictate the timing, mixture of gases, chamber pressure, chamber temperature, RF power levels, susceptor position, and other parameters of a particular process.
- 10
- 15 **[0056]** A process for etching, depositing or otherwise processing a film on a substrate or a process for cleaning chamber can be implemented using a computer program product that is executed by the controller. The computer program code can be written in any conventional computer readable programming language: for example, 68000 assembly language, C, C++, Pascal, Fortran or others. Suitable program code is entered into a single file, or multiple files, using a conventional text editor, and stored or embodied in a computer usable medium, such as a memory system of the computer. If the entered code text is in a high level language, the code is compiled, and the resultant compiler code is then linked with an object code of precompiled Microsoft Windows® library routines. To execute the linked, compiled object code the system user invokes the object code, causing the computer system to load the code in memory. The CPU then reads and executes the code to perform the tasks identified in the program.
- 20
- 25
- 30 **[0057]** The interface between a user and the controller may be via a touch-sensitive monitor and may also include a mouse and keyboard. In one embodiment two monitors are used, one mounted in the clean room wall for the operators and the other behind the wall for the service technicians. The two monitors may simultaneously display the same information, in which case only one is configured to accept input at a time. To select a particular screen or function, the operator touches a designated area on the display screen with a finger or the mouse. The touched area changes its highlighted color, or a new menu or screen is displayed, confirming the operator's selection.

[0058] As used herein "substrate" may be a support substrate with or without layers formed thereon. The patterned substrate may be an insulator or a semiconductor of a variety of doping concentrations and profiles and may, for example, be a semiconductor substrate of the type used in the manufacture of integrated circuits. Exposed "silicon oxide" of the patterned substrate is predominantly SiO₂ but may include concentrations of other elemental constituents such as nitrogen, hydrogen, carbon and the like. In some embodiments, silicon oxide films etched using the methods disclosed herein consist essentially of silicon and oxygen. The term "precursor" is used to refer to any process gas which takes part in a reaction to either remove material from or deposit material onto a surface. "Plasma effluents" describe gas exiting from the chamber plasma region and entering the substrate processing region. Plasma effluents are in an "excited state" wherein at least some of the gas molecules are in vibrationally-excited, dissociated and/or ionized states. A "radical precursor" is used to describe plasma effluents (a gas in an excited state which is exiting a plasma) which participate in a reaction to either remove material from or deposit material on a surface. A "radical-oxygen precursor" is a radical precursor which contains oxygen but may contain other elemental constituents. A "radical-fluorine precursor" is a radical precursor which contains fluorine but may contain other elemental constituents. "Radical-oxygen precursor" and "radical-fluorine precursor" are equivalent to "oxygen-containing plasma effluents" and "fluorine-containing plasma effluents," respectively. The phrase "inert gas" refers to any gas which does not form chemical bonds when etching or being incorporated into a film. Exemplary inert gases include noble gases but may include other gases so long as no chemical bonds are formed when (typically) trace amounts are trapped in a film.

[0059] The terms "gap" and "trench" are used throughout with no implication that the etched geometry has a large horizontal aspect ratio. Viewed from above the surface, trenches may appear circular, oval, polygonal, rectangular, or a variety of other shapes. A trench may be in the shape of a moat around an island of material. The term "via" is used to refer to a low aspect ratio trench (as viewed from above) which may or may not be filled with metal to form a vertical electrical connection. As used herein, a conformal etch process refers to a generally uniform removal of material on a surface in the same shape as the surface, i.e., the surface of the etched layer and the pre-etch surface are generally parallel. A person having ordinary skill in the art will recognize that the etched interface likely cannot be 100% conformal and thus the term "generally" allows for acceptable tolerances.

[0060] Having disclosed several embodiments, it will be recognized by those of skill in the art that various modifications, alternative constructions, and equivalents may be used without departing from the spirit of the disclosed embodiments. Additionally, a number of well known

processes and elements have not been described in order to avoid unnecessarily obscuring the present invention. Accordingly, the above description should not be taken as limiting the scope of the invention.

[0061] Where a range of values is provided, it is understood that each intervening value, to the
5 tenth of the unit of the lower limit unless the context clearly dictates otherwise, between the
upper and lower limits of that range is also specifically disclosed. Each smaller range between
any stated value or intervening value in a stated range and any other stated or intervening value
in that stated range is encompassed. The upper and lower limits of these smaller ranges may
independently be included or excluded in the range, and each range where either, neither or both
10 limits are included in the smaller ranges is also encompassed within the invention, subject to any
specifically excluded limit in the stated range. Where the stated range includes one or both of
the limits, ranges excluding either or both of those included limits are also included.

[0062] As used herein and in the appended claims, the singular forms "a", "an", and "the"
include plural referents unless the context clearly dictates otherwise. Thus, for example,
15 reference to "a process" includes a plurality of such processes and reference to "the dielectric
material" includes reference to one or more dielectric materials and equivalents thereof known to
those skilled in the art, and so forth.

[0063] Also, the words "comprise," "comprising," "include," "including," and "includes" when
used in this specification and in the following claims are intended to specify the presence of
20 stated features, integers, components, or steps, but they do not preclude the presence or addition
of one or more other features, integers, components, steps, acts, or groups.

WHAT IS CLAIMED IS:

1. A method of etching a patterned substrate in a substrate processing region of a substrate processing chamber, wherein the patterned substrate has an exposed silicon-nitrogen-and-carbon-containing region, the method comprising:

5 flowing each of a fluorine-containing precursor and an oxygen-containing precursor into a remote plasma region fluidly coupled to the substrate processing region while forming a plasma in the plasma region to produce plasma effluents; and

etching the exposed silicon-nitrogen-and-carbon-containing region by flowing the plasma effluents into the substrate processing region through through-holes in a showerhead.

10 2. The method of claim 1 wherein the exposed silicon-nitrogen-and-carbon-containing region is silicon carbon nitride.

3. The method of claim 1 wherein the exposed silicon-nitrogen-and-carbon-containing region consists essentially of silicon, nitrogen and carbon.

15 4. The method of claim 1 wherein the exposed silicon-nitrogen-and-carbon-containing region comprises about 10% or more nitrogen as measured by atomic percentage.

5. The method of claim 1 wherein the exposed silicon-nitrogen-and-carbon-containing region comprises about 15% or more nitrogen as measured by atomic percentage.

6. The method of claim 1 wherein the exposed silicon-nitrogen-and-carbon-containing region comprises about 20% or more nitrogen as measured by atomic percentage.

20 7. The method of claim 1 wherein a temperature of the patterned substrate is greater than or about 0°C and less than or about 50°C.

8. The method of claim 1 wherein a pressure within the substrate processing region is below or about 50 Torr and above or about 0.1 Torr.

25 9. The method of claim 1 wherein forming a plasma in the plasma region to produce plasma effluents comprises applying RF power between about 10 watts and about 2000 watts to the plasma region.

10. The method of claim 1 wherein the plasma is a capacitively-coupled plasma.

11. The method of claim 1 wherein the oxygen-containing precursor comprises at least one of O₂, O₃, N₂O or NO₂.

12. The method of claim 1 wherein the substrate processing region is plasma-free during the etching operation.

5 13. The method of claim 1 wherein the patterned substrate further comprises an exposed silicon oxide region and the selectivity of the etching operation (exposed silicon-nitrogen-and-carbon-containing region: exposed silicon oxide region) is greater than or about 50:1.

10 14. The method of claim 1 wherein the patterned substrate further comprises an exposed silicon nitride region and the selectivity of the etching operation (exposed silicon-nitrogen-and-carbon-containing region: exposed silicon nitride region) is greater than or about 2:1.

15 15. The method of claim 1 wherein the fluorine-containing precursor comprises NF₃.

16. The method of claim 1 wherein the fluorine-containing precursor comprises a precursor selected from the group consisting of atomic fluorine, diatomic fluorine, bromine trifluoride, chlorine trifluoride, nitrogen trifluoride, hydrogen fluoride, sulfur hexafluoride, xenon difluoride, carbon tetrafluoride, trifluoromethane, difluoromethane or fluoromethane.

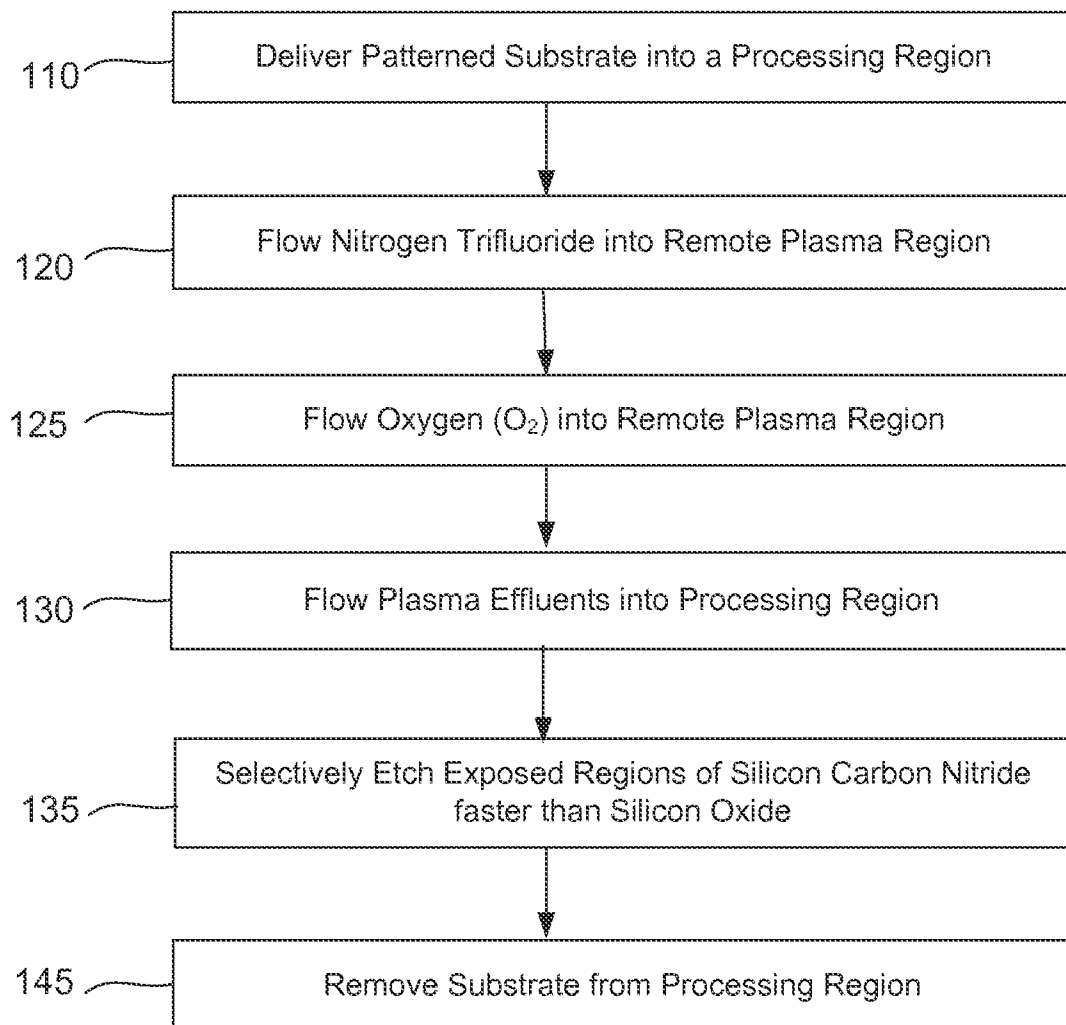
20 17. The method of claim 1 wherein the fluorine-containing precursor and the plasma effluents are essentially devoid of hydrogen.

18. The method of claim 1 wherein there is essentially no concentration of ionized species and free electrons within the substrate processing region.

25 19. The method of claim 1 wherein the minimum ID of the through-holes in the showerhead is between about 0.2 mm and about 5 mm.

20. The method of claim 1 wherein flowing each of the fluorine-containing precursor and the oxygen-containing precursor into the remote plasma region comprises maintaining an O:F atomic flow ratio above or about 0.5:1 and below or about 10:1.

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**FIG. 1**

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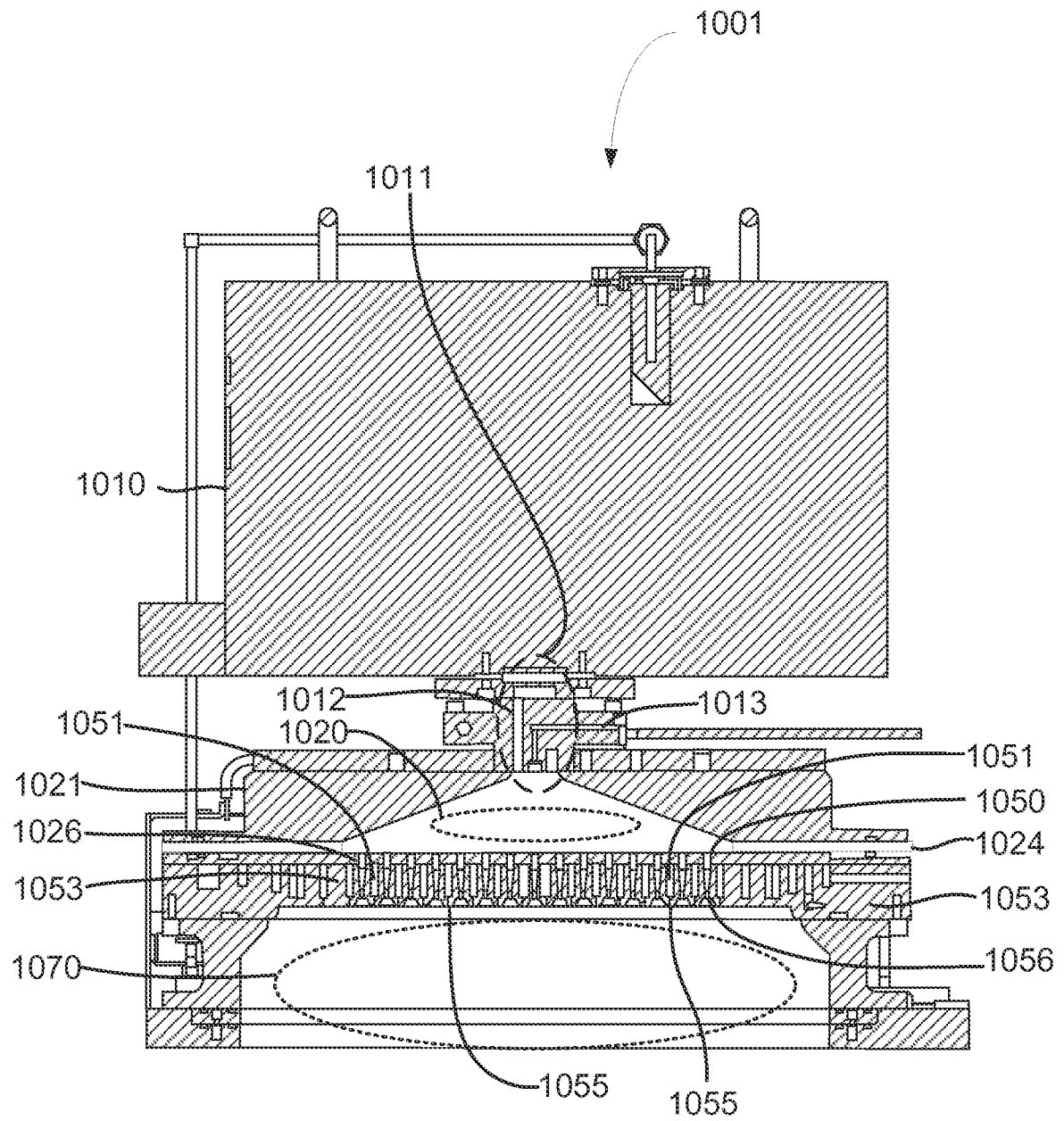


FIG. 2A

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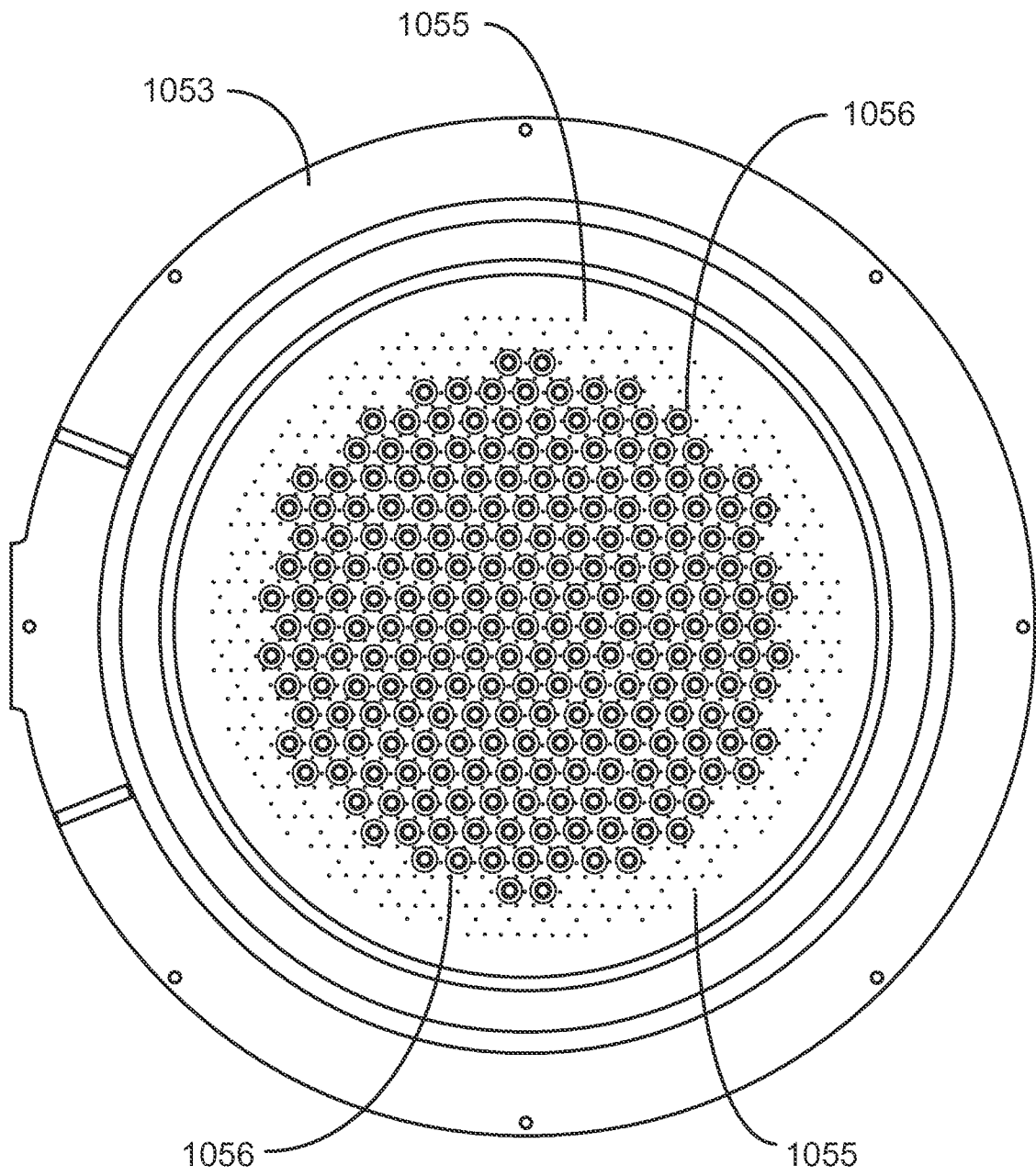
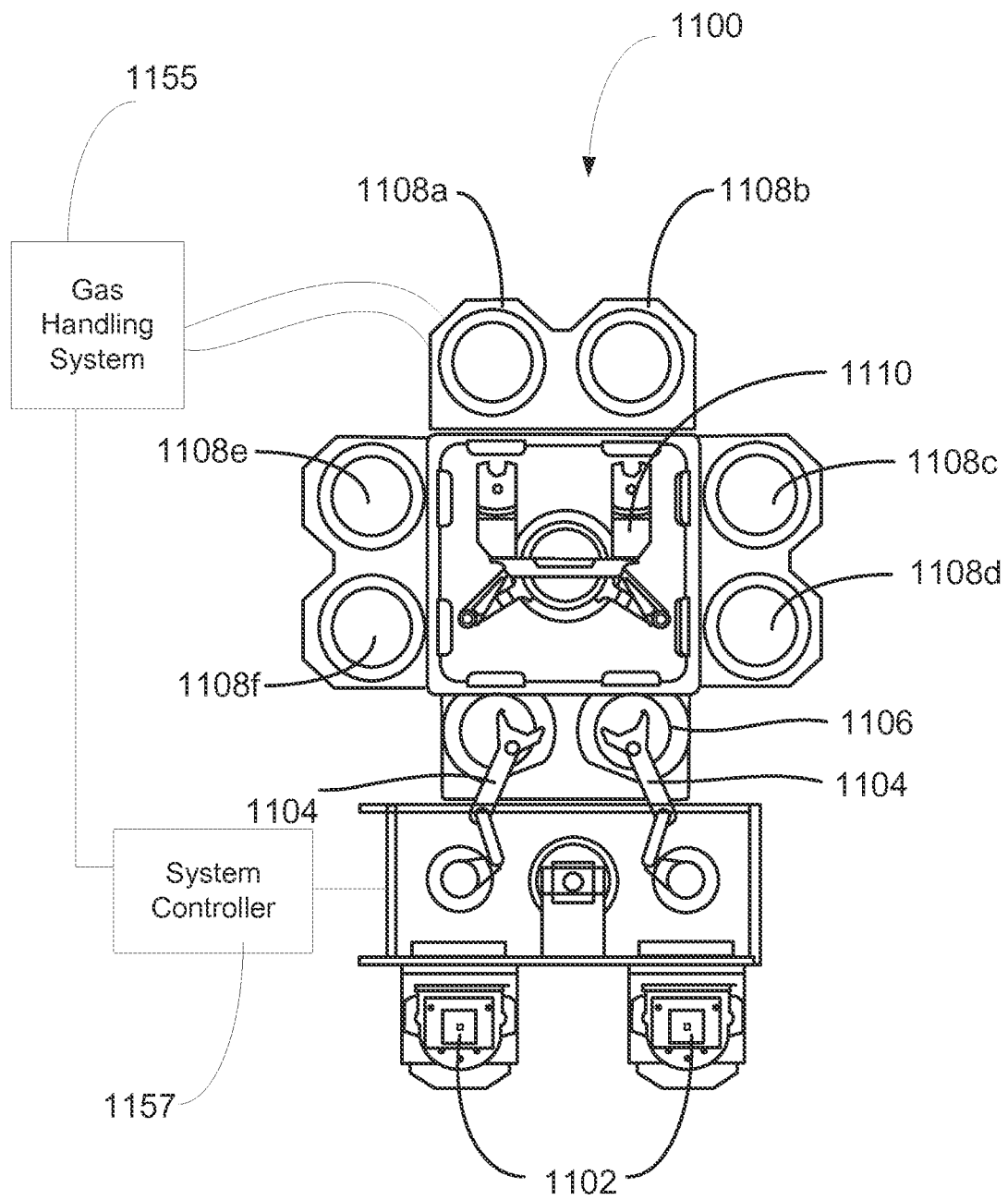


FIG. 2B

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**FIG. 3**

INTERNATIONAL SEARCH REPORT

International application No.
PCT/US2013/057253**A. CLASSIFICATION OF SUBJECT MATTER****H01L 21/3065(2006.01)i**

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

H01L 21/3065; H01L 23/52; H01L 21/768; H01L 21/4763; G06Q 30/00; H01L 23/48; G06Q 40/00; H01L 21/76

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Korean utility models and applications for utility models

Japanese utility models and applications for utility models

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

eKOMPASS(KIPO internal) & Keywords: etching, selectively, silicon-carbon-nitride, plasma, oxygen

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	US 2009-0179300 A1 (SHINYA ARAI) 16 July 2009 See paragraphs [0018]–[0022], claims 9–10 and figures 2–5.	1–20
A	US 2009-0104782 A1 (XINLAING LU et al.) 23 April 2009 See abstract, paragraphs [0023]–[0037], claim 1 and figure 1.	1–20
A	US 2009-0072401 A1 (JOHN C. ARNOLD et al.) 19 March 2009 See abstract, paragraphs [0106]–[0115] and claims 1–7.	1–20
A	JP 2007-250861 A (TOKYO ELECTRON LTD.) 27 September 2007 See abstract, paragraphs [0034]–[0040] and claims 1–2.	1–20



Further documents are listed in the continuation of Box C.



See patent family annex.

* Special categories of cited documents:

"A" document defining the general state of the art which is not considered to be of particular relevance

"E" earlier application or patent but published on or after the international filing date

"L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of citation or other special reason (as specified)

"O" document referring to an oral disclosure, use, exhibition or other means

"P" document published prior to the international filing date but later than the priority date claimed

"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention

"X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone

"Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art

"&" document member of the same patent family

Date of the actual completion of the international search

27 November 2013 (27.11.2013)

Date of mailing of the international search report

28 November 2013 (28.11.2013)

Name and mailing address of the ISA/KR

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INTERNATIONAL SEARCH REPORT

Information on patent family members

International application No.

PCT/US2013/057253

Patent document cited in search report	Publication date	Patent family member(s)	Publication date
US 2009-0179300 A1	16/07/2009	JP 2009-170901 A US 8018023 B2	30/07/2009 13/09/2011
US 2009-0104782 A1	23/04/2009	CN 102160154 A JP 2012-505530 A KR 10-2011-0084419 A TW 201025446 A US 8252696 B2 WO 2010-042552 A2 WO 2010-042552 A3	17/08/2011 01/03/2012 22/07/2011 01/07/2010 28/08/2012 15/04/2010 01/07/2010
US 2009-0072401 A1	19/03/2009	US 2010-0320617 A1 US 7781332 B2 US 8129843 B2	23/12/2010 24/08/2010 06/03/2012
JP 2007-250861 A	27/09/2007	JP 4684924 B2 US 2007-0218698 A1	18/05/2011 20/09/2007