Title: VOLTAGE REGULATOR WITH DYNAMICALLY BOOSTED BIAS CURRENT

Abstract: A voltage regulator with dynamically boosted bias current includes a pass device (14a) for providing current to a load (12a); an error circuit responsive to a difference between a predetermined reference voltage and a function of the voltage on the load (12a) to produce an error signal, a driver circuit (16a) responsive to the error signal for controlling the pass device (14a) to adjust the current to the load (12a) to reduce the error signal, the driver circuit (16a) including an amplifier (30a) responsive to the error signal for controlling the pass device (14a), a bias current source (156) for biasing the amplifier (30a), a sensing circuit (160) for sensing a portion of the error signal, a reference current source (172) for providing a reference current, a second error circuit (170) responsive to a difference between the portion of the error signal and the reference current to produce a second error current; and a boost circuit (174) responsive to the second error signal to increase the bias current provided to the amplifier (30a) when the load demands more current.
INTERNATIONAL SEARCH REPORT

A. CLASSIFICATION OF SUBJECT MATTER
IPC(7) : G05F 1/40
US CL : 323/280, 316
According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)
U.S. : 323/280, 316, 312, 313, 314, 315, 281

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched
NONE

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)
APS

C. DOCUMENTS CONSIDERED TO BE RELEVANT

<table>
<thead>
<tr>
<th>Category</th>
<th>Citation of document, with indication, where appropriate, of the relevant passages</th>
<th>Relevant to claim No.</th>
</tr>
</thead>
<tbody>
<tr>
<td>A</td>
<td>US 5,889,395 A (LUNDBERG) 30 MARCH 1999 (30.03.1999), col. 3, line 24 thru col.6, line 40.</td>
<td>1-9</td>
</tr>
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</table>

☐ Further documents are listed in the continuation of Box C. ☐ See patent family annex.

* Special categories of cited documents:

**A** document defining the general state of the art which is not considered to be of particular relevance

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**L** document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)

**O** document referring to an oral disclosure, use, exhibition or other means

**P** document published prior to the international filing date but later than the priority date claimed

**T** later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention

**X** document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone

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**&** document member of the same patent family

Date of the actual completion of the international search
01 October 2003 (01.10.2003)

Date of mailing of the international search report
04 MAR 2004

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