Abstract: The present invention relates to a testing device and a method for generating cache coherence testing traffic within a data processing system. It further relates to the data processing system besides such a testing device comprising a plurality of processing units, at least one of which comprises an associated cache memory, a shared memory, and interconnecting means. The testing device is connectable to said interconnecting means and comprises a testing means operable to retrieve cache coherence requests output by the interconnecting means, a memory operable to store addresses corresponding to said retrieved cache coherence requests, and a traffic generation means operable to output subsequent cache coherence requests based on the stored addresses to the interconnecting means.
Published:

- with international search report

(88) Date of publication of the international search report:

4 January 2007

For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.
A. CLASSIFICATION OF SUBJECT MATTER
INV. G06F11/263

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED
Minimum documentation searched (classification system followed by classification symbols)
G06F

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)
EPO-Internal, WPI Data, PAJ, IBM-TDB, COMPENDEX, INSPEC

C. DOCUMENTS CONSIDERED TO BE RELEVANT

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Date of the actual completion of the international search: 28 September 2006

Date of mailing of the international search report: 10/10/2006

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