

(19)



(11)

EP 3 767 906 B1

(12)

EUROPEAN PATENT SPECIFICATION

(45) Date of publication and mention of the grant of the patent:
09.11.2022 Bulletin 2022/45

(51) International Patent Classification (IPC):
H04L 27/26 ^(2006.01) **H04L 5/00** ^(2006.01)
H04W 4/40 ^(2018.01) **H04J 13/00** ^(2011.01)

(21) Application number: **20189419.3**

(52) Cooperative Patent Classification (CPC):
H04J 13/0074; H04L 5/0012; H04L 5/0051;
H04L 27/2613; H04W 4/40; H04J 13/0029

(22) Date of filing: **23.09.2016**

(54) APPARATUS FOR CONFIGURING DM-RS FOR V2X

VORRICHTUNG ZUM KONFIGURIEREN VON DM-RS FÜR V2X

APPAREIL DE CONFIGURATION D'UN SIGNAL DM-RS POUR COMMUNICATION V2X

(84) Designated Contracting States:
AL AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HR HU IE IS IT LI LT LU LV MC MK MT NL NO PL PT RO RS SE SI SK SM TR

(73) Proprietor: **Innovative Technology Lab Co., Ltd. Seocho-gu, Seoul 06744 (KR)**

(30) Priority: **25.09.2015 KR 20150136017**
13.05.2016 KR 20160058976
12.08.2016 KR 20160103277

(72) Inventor: **Yoon, Sung Jun Seocho-gu, Seoul 06744 (KR)**

(43) Date of publication of application:
20.01.2021 Bulletin 2021/03

(74) Representative: **Diehl & Partner Patent- und Rechtsanwaltskanzlei mbB Erika-Mann-Straße 9 80636 München (DE)**

(60) Divisional application:
22191646.3

(56) References cited:
EP-A2- 2 487 853 WO-A1-2015/119478

(62) Document number(s) of the earlier application(s) in accordance with Art. 76 EPC:
16849032.4 / 3 353 972

EP 3 767 906 B1

Note: Within nine months of the publication of the mention of the grant of the European patent in the European Patent Bulletin, any person may give notice to the European Patent Office of opposition to that patent, in accordance with the Implementing Regulations. Notice of opposition shall not be deemed to have been filed until the opposition fee has been paid. (Art. 99(1) European Patent Convention).

Description

Technical Field

5 **[0001]** The present disclosure relates to a wireless communication system, and more particularly, to a user equipment for configuring a demodulation reference signal (DM-RS) for vehicle-to-X (V2X).

Background Art

10 **[0002]** This application claims priority from and the benefit of Korean Patent Application Nos. 10-2015-0136017, filed on September 25, 2015, 10-2016-0058976, filed on May 13, 2016, and 10-2016-0103277, filed on August 12, 2016.

[0003] Vehicle-to-everything (V2X, Vehicle-to-X) communication refers to a communication scheme that exchanges or shares information associated with traffic conditions through communication with roadway infrastructures and other vehicles during driving. V2X may include vehicle-to-vehicle (V2V) indicating communication between vehicles, vehicle-to-pedestrian (V2P) indicating communication between terminals carried by a vehicle and a person, and vehicle-to-infrastructure/network (V2I/N) indicating communication between a roadside unit (RSU) and a network. In this instance, the road side unit (RSU) may be a base station or a transportation infrastructure entity embodied by a fixed terminal. For example, it may be an entity that transmits a speed notification to a vehicle.

20 **[0004]** V2X communication may be embodied based on a device-to-device (D2D) communication scheme. For example, control information, such as, scheduling assignment (SA) needs to be transmitted/received for V2X communication, and data may be transmitted/received based on the control information. Also, using a demodulation reference signal (DM-RS) is under discussion so that a side that receives an SA and/or data accurately estimates a channel and demodulates the SA and/or data based on the estimated channel. However, a method of generating a DM-RS sequence, which prevents an increase in DM-RS overhead in a subframe, and minimizes interference between neighbor terminals, has not been provided.

25 **[0005]** WO 2015/119478 A1 discloses that a cyclic shift value of a DMRS for D2D communication of a data region may be generated using a D2D Tx UE ID and may be hopped using a data subframe number or a data slot number. According to this document, the hopping pattern may be initialized whenever a period of a data region is started (e.g., a start point of 40 ms when a data region has a period of 40 ms). In order to initialize hopping every period of the data region, a hopping portion needs to be re-configured every current slot number. In other words, a frame number needs to also be considered during reconfiguration of the hopping pattern.

30 **[0006]** EP 2 487 853 A2 discloses that 17 different group hopping patterns and 30 different sequence shift patterns may exist. Whether to apply group hopping may be indicated by a higher layer. A physical uplink control channel PUCCH and a physical uplink shared channel PUSCH may have the same group hopping pattern. According to this document, a group hopping pattern $f_{gh}(n_s)$ may be defined by the following equation:

$$f_{gh}(n_s) = \begin{cases} 0 & \text{if group hopping is disabled} \\ \left(\sum_{i=0}^7 c(8n_s + i) \cdot 2^i \right) \bmod 30 & \text{if group hopping is enabled} \end{cases}$$

35 **[0007]** In the above equation, $c(i)$ is a pseudo random sequence that is a pseudo-random sequence and may be defined by a Gold sequence of a length-31. The following equation shows an example of a gold sequence $c(n)$.

$$c(n) = (x_1(n + N_c) + x_2(n + N_c)) \bmod 2$$

$$x_1(n + 31) = (x_1(n + 3) + x_1(n)) \bmod 2$$

$$x_2(n + 31) = (x_2(n + 3) + x_2(n + 2) + x_1(n + 1) + x_1(n)) \bmod 2$$

45 **[0008]** Here, $N_c=1600$, $x_1(i)$ is a first m-sequence, and $x_2(i)$ is a second m-sequence. For example, the first m-sequence or the second m-sequence may be initialized according to a cell identifier (ID) for every orthogonal frequency division multiplexing OFDM symbol, a slot number within one radio frame, an OFDM symbol index within a slot, and the type of

$$c_{init} = \left\lfloor \frac{N_{ID}^{cell}}{30} \right\rfloor$$

a cyclic prefix. According to this document, a pseudo random sequence generator may be initialized to in the first of each radio frame.

Disclosure of Invention

Technical Problem

[0009] Exemplary embodiments provide an user equipment for configuring a demodulation reference signal (DM-RS) for vehicle-to-X (V2X).

Solution to Problem

[0010] The above problem is solved by the combination of features of independent claim 1. Preferred embodiments are defined in the dependent claims.

[0011] The document further provides a method for transmitting Demodulation-Reference Signal (DM-RS) configured for Vehicle-to-everything (V2X) communication. The method (which is not in accordance with the claims) includes generating a first DM-RS for V2X communication and a second DM-RS for V2X communication, the first DM-RS for V2X communication being mapped in a first symbol in a first slot of a subframe, the second DM-RS for V2X communication being mapped in a second symbol in the first slot of the subframe; generating a third DM-RS for V2X communication and a fourth DM-RS for V2X communication, the third DM-RS for V2X communication being mapped in a first symbol in a second slot of the subframe, the fourth DM-RS for V2X communication being mapped in a second symbol in the second slot of the subframe; and transmitting the first DM-RS for V2X communication, the second DM-RS for V2X communication, the third DM-RS for V2X communication, and the fourth DM-RS for V2X communication. The first DM-RS for V2X communication is generated based on a first group-hopping, and the second DM-RS for V2X communication is generated based on a second group-hopping.

[0012] The document further provides a method for transmitting Demodulation-Reference Signal (DM-RS) for Vehicle-to-everything (V2X) communication. The method (which is not in accordance with the claims) includes generating a first DM-RS for V2X communication and a second DM-RS for V2X communication, the first DM-RS for V2X communication being mapped in a first symbol in a first slot of a subframe, the second DM-RS for V2X communication being mapped in a second symbol in the first slot of the subframe; generating a third DM-RS for V2X communication and a fourth DM-RS for V2X communication, the third DM-RS for V2X communication being mapped in a first symbol in a second slot of the subframe, the fourth DM-RS for V2X communication being mapped in a second symbol in the second slot of the subframe; and transmitting the first DM-RS for V2X communication, the second DM-RS for V2X communication, the third DM-RS for V2X communication, and the fourth DM-RS for V2X communication. Each of the first slot and the second slot consists of seven symbols, respectively. The first slot precedes the second slot in a time axis. The first symbol in the first slot is symbol #2 and the second symbol in the first slot is symbol #5 if the seven symbols in the first slot are arranged from symbol #0 to symbol #6, and the first symbol in the second slot is symbol #1 and the second symbol in the second slot is symbol #4 if the seven symbols in the second slot are arranged from symbol #0 to symbol #6.

[0013] The document further provides a method for transmitting Demodulation-Reference Signal (DM-RS) configured for Vehicle-to-everything (V2X) communication. The method (which is not in accordance with the claims) includes generating a first DM-RS for V2X communication and a second DM-RS for V2X communication, the first DM-RS for V2X communication being mapped in a first symbol in a first slot of a subframe, the second DM-RS for V2X communication being mapped in a second symbol in the first slot of the subframe; generating a third DM-RS for V2X communication and a fourth DM-RS for V2X communication, the third DM-RS for V2X communication being mapped in a first symbol in a second slot of the subframe, the fourth DM-RS for V2X communication being mapped in a second symbol in the second slot of the subframe; applying first orthogonal sequence [+1 +1 +1 +1] or second orthogonal sequence [+1 -1 +1 -1] in association with the first, second, third, and fourth DM-RSs for V2X communication; and transmitting the first DM-RS for V2X communication, the second DM-RS for V2X communication, the third DM-RS for V2X communication, and the fourth DM-RS for V2X communication.

Advantageous Effects of Invention

[0014] Exemplary embodiments provide an apparatus and a method for configuring a demodulation reference signal (DM-RS) for vehicle-to-X (V2X).

Brief Description of Drawings**[0015]**

- 5 FIG. 1, FIG. 2, and FIG. 3 are diagrams illustrating a V2X scenario associated with the present disclosure.
 FIG. 4 illustrates an example of an uplink (UL) DM-RS in a UL channel and a DM-RS in a sidelink (SL) channel for D2D (or ProSe).
 FIG. 5 illustrates an example of a DM-RS in a channel for PC5 link-based V2X that complies with D2D (or ProSe) of the present disclosure.
 10 FIG. 6 is a block diagram illustrating a wireless communication system where embodiments of the present disclosure are implemented.

Best Mode for Carrying out the Invention

15 **[0016]** Exemplary embodiments of the present invention will be described more fully hereinafter with reference to the accompanying drawings, in which exemplary embodiments of the invention are shown. Throughout the drawings and the detailed description, unless otherwise described, the same drawing reference numerals are understood to refer to the same elements, features, and structures. In describing the exemplary embodiments, detailed description on known configurations or functions may be omitted for clarity and conciseness.

20 **[0017]** Further, the description described herein is related to a wireless communication network, and an operation performed in a wireless communication network may be performed in a process of controlling a network and transmitting data by a system that controls a wireless network, e.g., a base station, or may be performed in a user equipment connected to the wireless communication network.

25 **[0018]** That is, it is apparent that various operations, which are performed for communicating with a terminal in a network formed of a plurality of network nodes including a base station (BS), are executable by the BS or other network nodes excluding the BS. The 'BS' may be replaced with the terms, such as, a fixed station, a Node B, an eNode B (eNB), an access point (AP), and the like. Also, the 'terminal' may be replaced with the terms, such as a User Equipment (UE), a Mobile Station (MS), a Mobile Subscriber Station (MSS), a Subscriber Station (SS), a non-AP station (non-AP STA), and the like.

30 **[0019]** The terms used as abbreviations in the present disclosure are defined as follows.

D2D: Device to Device (communication)

ProSe: (Device to Device) Proximity Services

SL: Sidelink

35 SCI: Sidelink Control Information

PSSCH: Physical Sidelink Shared Channel

PSBCH: Physical Sidelink Broadcast Channel

PSCCH: Physical Sidelink Control Channel

PSDCH: Physical Sidelink Discovery Channel

40 SLSS: Sidelink Synchronization Signal (= D2DSS (D2D Synchronization Signal))

SA: Scheduling assignment

DM-RS: DeModulation Reference Signal

PSSID: Physical-layer Sidelink Synchronization Identity

nSAID: Sidelink group destination identity

45 nSLID : Physical layer sidelink synchronization identity

PUSCH: Physical Uplink Shared Channel

[0020] Also, various operation modes may be defined based on a resource allocation scheme for a direct link (e.g., D2D, ProSe, or SL communication). When data and control information for a direct link (e.g., D2D, ProSe, or SL communication) are indicated as direct data and direct control information, respectively, mode 1 indicates an operation mode in which a base station (or a relay station) accurately schedules a resource that a terminal uses to transmit direct data and direct control information, and mode 2 indicates an operation mode in which a terminal autonomously selects a resource from a resource pool to transmit direct data and direct control information.

[0021] Hereinafter, although embodiments of the present disclosure are described by using V2X communication as an example, the scope of the present disclosure may not be limited to V2X communication. Further, the embodiments of the present disclosure may be applied to direct link based communication, such as D2D, ProSe, SL communication, or the like.

[0022] V2X is a term that generally indicates V2V, V2P, and V2I/N, and each of V2V, V2P, and V2I/N may be defined

as shown in Table 1, in association with LTE communication.

Table 1

5 [0023]

[Table 1]

10	V2V	- covering LTE-based communication between vehicles
	V2P	- covering LTE-based communication between a vehicle and a device carried by an individual (e.g. handheld terminal carried by a pedestrian, cyclist, driver or passenger)
15	V2I/N	- covering LTE-based communication between a vehicle and a roadside unit/network- A roadside unit (RSU) is a stationary infrastructure entity supporting V2X applications that can exchange messages with other entities supporting V2X applications. - Note: RSU is a term frequently used in existing ITS specifications, and the reason for introducing the term in the 3GPP specifications is to make the documents easier to read for the ITS industry. RSU is a logical entity that combines V2X application logic with the functionality of an eNB (referred to as eNB-type RSU) or UE (referred to as UE-type RSU).

20 [0024] For a V2V operation based on PC5 which is a D2D communication link (that is, a direct interface between two devices that support ProSe) out of V2X, various scenarios such as Table 2, Table 3, and Table 4 are considered with reference to FIGs. 1, 2, and 3.

[0025] FIG. 1, FIG. 2, and FIG. 3 are diagrams illustrating a V2X scenario associated with the present disclosure.

25 [0026] Table 2 and FIG. 1 illustrate a scenario that supports a V2X operation that is based on only a PC5 interface. Part (a) of FIG. 1 illustrates a V2V operation, part (b) of FIG. 1 illustrates a V2I operation, and part (c) of FIG. 1 illustrates a V2P operation.

Table 2

30 [0027]

[Table 2]

35 - This scenario supports V2X operation only based on PC5.- In this scenario, a UE transmits a V2X message to multiple UEs at a local area in sidelink.- For V2I, either transmitter UE or receiver UE(s) are UE-type RSU.- For V2P, either transmitter UE or receiver UE(s) are pedestrian UE.

40 [0028] Table 3 and FIG. 2 illustrate a scenario that supports a V2X operation that is based on only a Uu interface (that is, an interface between a UE and an eNB). Part (a) of FIG. 2 illustrates a V2V operation, part (b) of FIG. 2 illustrates a V2I operation, and part (c) of FIG. 2 illustrates a V2P operation.

Table 3

45 [0029]

[Table 3]

50 - This scenario supports V2X operation only based on Uu.- In this scenario, For V2V and V2P, a UE transmits a V2X message to E-UTRAN in uplink and E-UTRAN transmits it to multiple UEs at a local area in downlink. For V2I, when receiver is eNB type RSU, a UE transmits a V2I message to E-UTRAN(eNB type RSU) in uplink; when transmitter is eNB type RSU, E-UTRAN(eNB type RSU) transmits a V2I message to multiple UEs at a local area in downlink.- For V2P, either transmitter UE or receiver UE(s) are pedestrian UE.- To support this scenario, E-UTRAN performs uplink reception and downlink transmission of V2X messages. For downlink, E-UTRAN may use a broadcast mechanism.

55 [0030] Table 4 and FIG. 3 illustrate a scenario that supports a V2X operation that uses both a Uu interface and a PC5 interface. Part (a) of FIG. 3 illustrates scenario 3A of Table 4 and part (b) of FIG. 3 illustrates scenario 3B of Table 4.

Table 4

[0031]

5

[Table 4]

- This scenario supports V2V operation using both Uu and PC5.	
Scenario3 A	- In this scenario, a UE transmits a V2X message to other UEs in sidelink. One of the receiving UEs is a UE type RSU which receives the V2X message in sidelink and transmits it to E-UTRAN in uplink. E-UTRAN receives the V2X message from the UE type RSU and then transmits it to multiple UEs at a local area in downlink. - To support this scenario, E-UTRAN performs uplink reception and downlink transmission of V2X messages. For downlink, E-UTRAN may use a broadcast mechanism.
Scenario3 B	- In this scenario, a UE transmits a V2X message to E-UTRAN in uplink and E-UTRAN transmits it to one or more UE type RSUs. Then, the UE type RSU transmits the V2X message to other UEs in sidelink.- To support this scenario, E-UTRAN performs uplink reception and downlink transmission of V2X messages. For downlink, E-UTRAN may use a broadcast mechanism.

10

15

20

[0032] Hereinafter, a UL DM-RS for a UL PUSCH will be described.

[0033] Basic information associated with a UL DM-RS in a UL PUSCH is defined as shown in Table 5, provided below.

Table 5

25

[0034]

[Table 5]

The PUSCH demodulation reference signal sequence $r_{\text{PUSCH}}^{(\lambda)}$ associated with layer $\lambda \in \{0, 1, \dots, v-1\}$ is defined by	
$r_{\text{PUSCH}}^{(\lambda)}(m \cdot M_{\text{sc}}^{\text{RS}} + n) = w^{(\lambda)}(m) r_{u,v}^{(\alpha_\lambda)}(n)$	
where	$m = 0, 1$
and	$n = 0, \dots, M_{\text{sc}}^{\text{RS}} - 1$
$M_{\text{sc}}^{\text{RS}} = M_{\text{sc}}^{\text{PUSCH}}$	
Subclause of "Generation of the reference signal sequence" defines the sequence $r_{u,v}^{(\alpha_\lambda)}(0), \dots, r_{u,v}^{(\alpha_\lambda)}(M_{\text{sc}}^{\text{RS}} - 1)$.	
The orthogonal sequence $w^{(\lambda)}(m)$ is given by $[w^{(\lambda)}(0) \ w^{(\lambda)}(1)] = [1 \ 1]$ for DCI format 0 if the higher-layer parameter <i>Activate-DMRS-with OCC</i> is not set or if the temporary C-RNTI was used to transmit the most recent uplink-related DCI for the transport block associated with the corresponding PUSCH transmission, otherwise it is given by Table A using the cyclic shift field in most recent uplink-related DCI for the transport block associated with the corresponding PUSCH transmission.	
The cyclic shift α_λ in a slot n_s is given as $\alpha_\lambda = 2\pi n_{\text{cs},\lambda} / 12$ with	
$n_{\text{cs},\lambda} = (n_{\text{DMRS}}^{(1)} + n_{\text{DMRS},\lambda}^{(2)} + n_{\text{PN}}(n_s)) \bmod 12$	
where the values of $n_{\text{DMRS}}^{(1)}$ is given by Table B according to the parameter <i>cyclicShift</i> provided by	
higher layers, $n_{\text{DMRS},\lambda}^{(2)}$ is given by the cyclic shift for DMRS field in most recent uplink-related DCI for the transport block associated with the corresponding PUSCH transmission where the value of $n_{\text{DMRS},\lambda}^{(2)}$ is given in Table A.	
The first row of Table A shall be used to obtain $n_{\text{DMRS},0}^{(2)}$ and $w^{(\lambda)}(m)$ if there is no	

30

35

40

45

50

55

(continued)

uplink-related DCI for the same transport block associated with the corresponding PUSCH transmission, and

- if the initial PUSCH for the same transport block is semi-persistently scheduled, or
- if the initial PUSCH for the same transport block is scheduled by the random access response grant.

The quantity $n_{PN}(n_s)$ is given by

$$n_{PN}(n_s) = \sum_{i=0}^7 c(8N_{\text{sym}}^{\text{UL}} \cdot n_s + i) \cdot 2^i$$

where the pseudo-random sequence $c(i)$ is defined by clause 7.2. The application of $c(i)$ is cell-specific. The pseudo-random sequence generator shall be initialized with c_{init} at the beginning of each radio frame. The

quantity c_{init}

is given by

$$c_{\text{init}} = \left\lfloor \frac{N_{\text{ID}}^{\text{cell}}}{30} \right\rfloor \cdot 2^5 + \left((N_{\text{ID}}^{\text{cell}} + \Delta_{\text{ss}}) \bmod 30 \right) \quad \text{if no}$$

value for $N_{\text{ID}}^{\text{csch_DMRS}}$ is configured

by higher layers or the PUSCH transmission corresponds to a Random Access Response Grant or a retransmission of the same transport block as part of the contention based random access procedure, otherwise it is given by

$$c_{\text{init}} = \left\lfloor \frac{N_{\text{ID}}^{\text{csch_DMRS}}}{30} \right\rfloor \cdot 2^5 + \left(N_{\text{ID}}^{\text{csch_DMRS}} \bmod 30 \right)$$

The vector of reference signals shall be precoded according to

$$\begin{bmatrix} \tilde{r}_{\text{PUSCH}}^{(0)} \\ \vdots \\ \tilde{r}_{\text{PUSCH}}^{(P-1)} \end{bmatrix} = W \begin{bmatrix} r_{\text{PUSCH}}^{(0)} \\ \vdots \\ r_{\text{PUSCH}}^{(P-1)} \end{bmatrix}$$

where P is the number of antenna ports used for PUSCH transmission.

For PUSCH transmission using a single antenna port, $P = 1$, $W = 1$ and $\nu = 1$.

For spatial multiplexing, $P = 2$ or $P = 4$ and the precoding matrix W shall be identical to the precoding matrix used in clause 5.3.3A.2 for precoding of the PUSCH in the same subframe.

Table A: Mapping of Cyclic Shift Field in uplink-related DCI format to $n_{\text{DMRS},\lambda}^{(2)}$ and $[w^{(\lambda)}(0) \ w^{(\lambda)}(1)]$

Cyclic Shift Field in uplink-related DCI format [3]	$n_{\text{DMRS},\lambda}^{(2)}$				$[w^{(\lambda)}(0) \ w^{(\lambda)}(1)]$			
	$\lambda = 0$	$\lambda = 1$	$\lambda = 2$	$\lambda = 3$	$\lambda = 0$	$\lambda = 1$	$\lambda = 2$	$\lambda = 3$
000	0	6	3	9	[1 1]	[1 1]	[1 -1]	[1 -1]
001	6	0	9	3	[1 -1]	[1 -1]	[1 1]	[1 1]
010	3	9	6	0	[1 -1]	[1 -1]	[1 1]	[1 1]
011	4	10	7	1	[1 1]	[1 1]	[1 1]	[1 1]
100	2	8	5	11	[1 1]	[1 1]	[1 1]	[1 1]
101	8	2	11	5	[1 -1]	[1 -1]	[1 -1]	[1 -1]
110	10	4	1	7	[1 -1]	[1 -1]	[1 -1]	[1 -1]
111	9	3	0	6	[1 1]	[1 1]	[1 -1]	[1 -1]

Table B: Mapping of *cyclicShift* to $n_{\text{DMRS}}^{(1)}$ values

<i>cyclicShift</i>	$n_{\text{DMRS}}^{(1)}$
0	0
1	2
2	3
3	4
4	6
5	8
6	9
7	10

[0035] Hereinafter, a DM-RS (hereinafter, an SL DM-RS) for an SL PSSCH/ PSCCH/PSDCH/PSBCH will be described.

[0036] Basic information associated with an SL DM-RS for D2D (or ProSe) is as follows. Unlike the UL DM-RS for the UL PUSCH which has been described with reference to Table 5, the definitions of predetermined parameters and applied equations may be changed for the SL DM-RS as shown in Table 6 and Table 7.

Table 6

[0037]

[Table 6]

Parameter		PSSCH	PSCCH
Group hopping		enabled	disabled
	$n_{\text{ID}}^{\text{RS}}$	$n_{\text{ID}}^{\text{SA}}$	-
	n_{S}	$n_{\text{SS}}^{\text{PSSCH}}$	-
	f_{SS}	$n_{\text{ID}}^{\text{SA}} \bmod 30$	0
Sequence hopping		disabled	disabled
Cyclic shift	$n_{\text{CS},\lambda}$	$\lfloor n_{\text{ID}}^{\text{SA}}/2 \rfloor \bmod 8$	0
Orthogonal sequence	$[w^{(\lambda)}(0) \ w^{(\lambda)}(1)]$	[+1 +1] if $n_{\text{ID}}^{\text{SA}} \bmod 2 = 0$ [+1 -1] if $n_{\text{ID}}^{\text{SA}} \bmod 2 = 1$	[+1 +1]
Reference signal length	$M_{\text{sc}}^{\text{RS}}$	$M_{\text{sc}}^{\text{PSSCH}}$	$M_{\text{sc}}^{\text{PSCCH}}$

(continued)

Parameter		PSSCH	PSCCH
Number of layers	ν	1	1
Number of antenna ports	P	1	1

Table 7

[0038]

[Table 7]

Parameter		PSDCH	PSBCH
Group hopping		disabled	disabled
	f_{ss}	0	$\lfloor N_{ID}^{SL}/16 \rfloor \bmod 30$
Sequence hopping		disabled	disabled
Cyclic shift	$n_{CS,\lambda}$	0	$\lfloor N_{ID}^{SL}/2 \rfloor \bmod 8$
Orthogonal sequence	$[w^{\lambda}(0) \ w^{\lambda}(1)]$	[+1 +1]	$[+1 \ +1]$ if $N_{ID}^{SL} \bmod 2 = 0$ $[+1 \ -1]$ if $N_{ID}^{SL} \bmod 2 = 1$
Reference signal length	M_{sc}^{RS}	M_{sc}^{PSDCH}	M_{sc}^{PSBCH}
Number of layers	ν	1	1
Number of antenna ports	P	1	1

[0039] Hereinafter, embodiments of the present disclosure will be described in detail.

[0040] In the case of a UL DM-RS in a UL PUSCH and a DM-RS in a sidelink (SL) PSSCH/PSCCH/PSDCH/PSBCH for LTE-based D2D (ProSe), a DM-RS is generated by mapping a DM-RS sequence to a single symbol for each slot as shown in FIG. 4 and the DM-RS is transmitted. That is, a single subframe includes two slots (that is, a slot having an even number index (that is, an even slot)) and a slot having an odd number index (that is, an odd slot)), and a single slot may include 6 or 7 symbols based on the length of a cyclic prefix (CP). For example, in the case of a normal CP, 7 symbols (that is, symbol indices #0, #1, ..., #6) are included in a single slot, and a DM-RS may be mapped to a fourth symbol (that is, a symbol index #3) out of the symbols. In the case of an extended CP, 6 symbols (that is, symbol indices #0, #1, ..., #5) are included in a single slot, and a DM-RS may be mapped to a third symbol (that is, a symbol index #2) out of the symbols.

[0041] However, in the case of V2X, a DM-RS may be mapped using a larger number of symbols, when compared to the example of FIG. 4, in a single subframe as shown in FIG. 5, by taking into consideration high Doppler effect.

[0042] In a V2X communication, relatively higher Doppler effect may occur due to the moving speed of the vehicle performing the V2X communication. In order to address such a problem, more symbols may be used to map DM-RS for a V2X communication in a subframe. Further, if the same sequence generation method for V2X DM-RS is used as utilized in LTE PUSCH DM-RS or D2D DM-RS generations, e.g., using the same group-hopping, cyclic shift, and orthogonal sequence, interference among DM-RSs for V2X transmitted from various devices may increase. The increased symbols in a subframe for DM-RS mapping may also exacerbate the interference problem.

[0043] One or more exemplary embodiments illustrated herein address the problems by utilizing more efficient group-hopping, cyclic shift selection, and orthogonal sequence selection for V2X DM-RS generations. One or more exemplary embodiments also reduces the possible interferences among DM-RSs for V2X communication transmitted from various devices, and improves the communication quality in channel measurements for demodulating control information and data through DM-RSs for V2X communication.

[0044] Part (a) of FIG. 5 illustrates that a DM-RS is transmitted through a fourth symbol (symbol #3) and a sixth symbol (symbol #5) of each slot, in the case of the normal CP. However, this is merely an example, and two symbols randomly selected out of a total of 7 symbols included in a single slot may be determined as symbols through which a DM-RS is transmitted. For example, one of the two symbols through which a DM-RS is transmitted in each slot is a fourth symbol

(symbol #3) of each slot, and the other symbol may be one of a first symbol (symbol #0), a second symbol (symbol #1), a third symbol (symbol #2), a fifth symbol (symbol #4), a sixth symbol (symbol #5), and a seventh symbol (symbol #6).

[0045] In the same manner, the part (a) of FIG. 5 illustrates that a DM-RS is transmitted through a third symbol (symbol #2) and a fifth symbol (symbol #4) of each slot, in the case of the extended CP. However, this is merely an example, and two symbols randomly selected out of a total of 6 symbols included in a single slot may be determined as symbols through which a DM-RS is transmitted. For example, one of the two symbols through which a DM-RS is transmitted in each slot is a third symbol (symbol #2) of each slot, and the other symbol may be one of a first symbol (symbol #0), a second symbol (symbol #1), a fourth symbol (symbol #3), and a fifth symbol (symbol #4), a sixth symbol (symbol #5).

[0046] In part (b) of FIG. 5, in the case of the normal CP, two symbols out of seven symbols in each of a first slot and a second slot may be determined as symbols through which a DM-RS is transmitted. For example, as illustrated in the part (b) of FIG. 5, two symbols through which a DM-RS is transmitted out of seven symbols in the first slot may be a third symbol (symbol #2) and a sixth symbol (symbol #5). Two symbols through which a DM-RS is transmitted out of seven symbols in the second slot may be a second symbol (symbol #1) and a fifth symbol (symbol #4).

[0047] In the same manner, in FIG. the part (b) of 5, in the case of the extended CP, two symbols out of six symbols in each of a first slot and a second slot may be determined as symbols through which a DM-RS is transmitted. For example, as illustrated in the part (b) of FIG. 5, two symbols through which a DM-RS is transmitted out of six symbols in the first slot may be a second symbol (symbol #1) and a fifth symbol (symbol #4). Two symbols through which a DM-RS is transmitted out of six symbols in the second slot may be a second symbol (symbol #1) and a fifth symbol (symbol #4).

[0048] In part (c) of FIG. 5, in the case of the normal CP, three symbols out of 14 symbols in a single subframe that includes a first slot and a second slot may be determined as symbols through which a DM-RS is transmitted. For example, as illustrated in the part (c) of FIG. 5, three symbols through which a DM-RS is transmitted out of 14 symbols included in the single subframe may be a fourth symbol (symbol #3) and a seventh symbol (symbol #6) in the first slot, and a fourth symbol (symbol #3) in the second slot.

[0049] In the same manner, in the part (c) of FIG. 5, in the case of the extended CP, three symbols out of 12 symbols in a single subframe that includes a first slot and a second slot may be determined as symbols through which a DM-RS is transmitted. For example, as illustrated in the part (c) of FIG. 5, three symbols through which a DM-RS is transmitted out of 12 symbols included in the single subframe may be a fourth symbol (symbol #3) and a sixth symbol (symbol #5) in the first slot, and a second symbol (symbol #1) in the second slot.

[0050] In this instance, to minimize interference from neighbor terminals, an effective group hopping method, a cyclic shift method, an orthogonal cover code (OCC) or orthogonal sequence mapping method, or the like need to be considered when a DM-RS is generated.

[0051] Hereinafter, group hopping for the present disclosure will be described.

[0052] In the case of D2D (ProSe), group hopping may be applied for each slot when a DM-RS that links with a PSSCH and a PSCCH is transmitted, as shown in Equation 1 below.

MathFigure 1

[Math. 1]

$$f_{gh}(n_s) = \begin{cases} 0 & \text{if group hopping is disabled} \\ \left(\sum_{i=0}^7 c(8n_s + i) \cdot 2^i \right) \bmod 30 & \text{if group hopping is enabled} \end{cases}$$

[0053] In Equation 1, n_s denotes a slot index. Also, $c(i)$ denotes a pseudo-random sequence that is defined as Gold sequence having a degree of 31, and a pseudo-random sequence generator for the pseudo-random sequence is initialized to

$$c_{init} = \left\lfloor n_{ID}^{RS} / 30 \right\rfloor$$

at the beginning of each radio frame. Here, n_{ID}^{RS} may be N_{cell}^{ID} which is a physical cell ID (PCID), or n_{PUCCH}^{ID} or n_{PUSCH}^{ID} which is a parameter indicated by an RRC or a higher layer signaling.

[0054] Group hopping of Equation 1 is appropriate for the case in which a DM-RS is transmitted through a single symbol for each slot. However, by taking into consideration the case in which a DM-RS is transmitted through a plurality of symbols of each slot in V2X, there is a desire for a more effective group hopping method, and the present disclosure proposes the following method.

[0055] Hereinafter, method 1 will be defined for new group hopping according to the present disclosure.

[0056] According to method 1, as shown in Equation 2 below, by taking into consideration the case in which a DM-RS is generated in two symbols in a single slot which corresponds to the example of the part (a) of FIG. 5 or the part (b) of FIG. 5, two different group hopping patterns are defined in each slot, irrespective of the locations of the two symbols. One group hopping pattern is applied to a first symbol through which a DM-RS is transmitted in the slot, and the other group hopping pattern is applied to a second symbol through which the DM-RS is transmitted in the slot. In this instance, $l'=0$ or 1.

MathFigure 2

[Math.2]

$$f_{gh} = \begin{cases} 0 & \text{if group hopping is disabled} \\ \left(\sum_{i=0}^7 c(16n_{ss}^{PSSCH} + 8l' + i) \cdot 2^i \right) \bmod 30 & \text{if group hopping is enabled} \end{cases}$$

[0057] Here, $c(i)$ denotes a pseudo-random sequence that is defined as Gold sequence having a degree of 31, and a pseudo-random sequence generator for the pseudo-random sequence is initialized to

$$c_{init} = \left\lfloor \frac{n_{ID}^{SA}}{30} \right\rfloor$$

at the beginning of each slot that satisfies $n_{ss}^{PSSCH}=0$. In this instance, n_{ss}^{PSSCH} denotes a current slot number in the subframe pool for a sidelink. Here, n_{ID}^{SA} may be a sidelink group destination identity.

[0058] Hereinafter, method 2 will be defined for new group hopping according to the present disclosure.

[0059] According to method 2, as shown in Equation 3 below, by taking into consideration the case in which a DM-RS is generated in three symbols in a single subframe which corresponds to the part (c) of FIG. 5, three different group hopping patterns are defined in each subframe, irrespective of the locations of the three symbols. A first group hopping pattern is applied to a first symbol through which a DM-RS is transmitted in the subframe, a second group hopping pattern is applied to a second symbol through which the DM-RS is transmitted in the subframe, and a third group hopping pattern is applied to a third symbol through which the DM-RS is transmitted in the subframe. In this instance, $l'=0, 1, \text{ or } 2$.

MathFigure 3

[Math.3]

$$f_{gh} = \begin{cases} 0 & \text{if group hopping is disabled} \\ \left(\sum_{i=0}^7 c(8l' + i) \cdot 2^i \right) \bmod 30 & \text{if group hopping is enabled} \end{cases}$$

[0060] Here, $c(i)$ denotes a pseudo-random sequence that is defined as Gold sequence having a degree of 31, and a pseudo-random sequence generator for the pseudo-random sequence is initialized to

$$c_{init} = \left\lfloor \frac{N_{ID}^{SL}}{30} \right\rfloor$$

or

$$c_{init} = \left\lfloor \frac{\lfloor N_{ID}^{SL} / 24 \rfloor}{30} \right\rfloor$$

at the beginning of each PSBCH subframe (a subframe that transmits a PSBCH).

[0061] Here, N_{ID}^{SL} may be a physical layer sidelink synchronization identity.

[0062] Hereinafter, method 3 will be defined for new group hopping according to the present disclosure.

[0063] Method 3 defines a different group hopping pattern for each of the symbols in a single slot, and a corresponding symbol through which a DM-RS is transmitted applies a corresponding group hopping pattern of the symbol, as shown in Equation 4 below. This is applied to all of the cases illustrated through FIGs. 5A, 5B, and 5C. In this instance, N_{symbol}^{SL} denotes the number of symbols in a single slot in a sidelink (SL) (7 in the case of a normal CP, and 6 in the case of an extended CP), and $l=0,1,\dots, N_{symbol}^{SL}$ denotes a symbol index in a single slot.

MathFigure 4

[Math.4]

$$f_{gh} = \begin{cases} 0 & \text{if group hopping is disabled} \\ \left(\sum_{i=0}^7 c(8 \cdot N_{symbol}^{SL} \cdot n_s + 8l + i) \cdot 2^i \right) \bmod 30 & \text{if group hopping is enabled} \end{cases}$$

[0064] In Equation 4, n_s is n_{ss}^{PSSCH} when the equation is applied to a DM-RS for a PSSCH.

[0065] In Equation 4, n_s may have two types of values, that is, 0 or 1, when the equation is applied to a DM-RS for a PSBCH.

[0066] Also, $c(i)$ denotes a pseudo-random sequence defined as Gold sequence having a degree of 31.

[0067] When Equation 4 is applied to the DM-RS for the PSSCH, the pseudo-random sequence generator for the pseudo-random sequence may be initialized to

$$c_{init} = \left\lfloor \frac{n_{ID}^{SA}}{30} \right\rfloor$$

at the beginning of each slot that satisfies $n_{ss}^{PSSCH}=0$.

[0068] When Equation 4 is applied to the DM-RS for the PSBCH, the pseudo-random sequence generator for the pseudo-random sequence may be initialized to

$$c_{init} = \left\lfloor \frac{N_{ID}^{SL}}{30} \right\rfloor$$

or

$$c_{init} = \left\lfloor \frac{\lfloor N_{ID}^{SL} / 24 \rfloor}{30} \right\rfloor$$

at the beginning of every PSBCH subframe (a subframe that transmits a PSBCH).

[0069] In this instance, n_{ss}^{PSSCH} denotes a current slot number in the subframe pool for a sidelink. Here, n_{ID}^{SA} is a sidelink group destination identity, and N_{ID}^{SL} is a physical layer sidelink synchronization identity.

[0070] Method 1 and method 2 for new group hopping according to the present disclosure for V2X define group hopping by taking into consideration only a symbol to which a DM-RS is mapped, in the same manner as LTE PUSCH-based D2D (ProSe).

[0071] Method 3 for new group hopping according to the present disclosure for V2X applies group hopping for each symbol, and a symbol to which a DM-RS is mapped applies a predetermined group hopping of the corresponding symbol.

[0072] When method 3 for new group hopping according to the present disclosure is applied, group hopping for a symbol to which a DM-RS is mapped in the LTE PUSCH based-D2D (ProSe) and group hopping for a symbol to which a DM-RS is mapped in V2X are set to be different from each other, and thus, interference may be reduced that may occur between the DM-RS in D2D (ProSe) and the DM-RS in V2X, which may be transmitted in parallel in the same

symbol having the same c_{init} (e.g., $n_{\text{ID}}^{\text{SA}}$ is different from each other but

$$c_{\text{init}} = \left\lfloor \frac{n_{\text{ID}}^{\text{SA}}}{30} \right\rfloor$$

is identical to each other), which is an advantage.

[0073] For example, in a symbol to which a DM-RS is mapped in a first slot in the LTE PUSCH-based D2D (ProSe), group hopping is determined based on a pseudo-random sequence value of $c(0)$ to $c(7)$. In this instance, when method 1 and method 2 for new group hopping according to the present disclosure for V2X are applied, group hopping may be determined based on a pseudo-random sequence value of $c(0)$ to $c(7)$ in a first symbol of a first slot to which a DM-RS is mapped (herein, this symbol may be a symbol located in the same location of a symbol to which a DM-RS is mapped in a first slot in the LTE PUSCH-based D2D (ProSe)). However, when method 3 for new group hopping according to the present disclosure for V2X is applied, group hopping may be determined based on a pseudo-random sequence value of $c(8(\text{symbol number})+0)$ to $c(8(\text{symbol number})+7)$ in a first symbol of a first slot to which a DM-RS is mapped (herein, this symbol may be a symbol located in the same location of a symbol to which a DM-RS is mapped in a first slot in the LTE PUSCH-based D2D (ProSe)).

[0074] Hereinafter, an orthogonal sequence (OCC) and a cyclic shift according to the present disclosure will be described.

[0075] A cyclic shift and an orthogonal sequence (OCC) are taken into consideration to minimize interference from neighbor terminals when a DM-RS is generated. In the case of D2D (ProSe), a length 2 orthogonal cover code (OCC) is applied as the orthogonal sequence (OCC) by taking into account the case in which a DM-RS is transmitted through two symbols in a single subframe. However, in the case of V2X, a length 4 orthogonal cover code (OCC) may be considered as an orthogonal sequence (OCC) by taking into account the case in which a DM-RS is transmitted through four symbols in a single subframe. Accordingly, an orthogonal sequence (OCC) needs to be changed to effectively minimize interference among neighbor terminals. Also, application of an effective cyclic shift also needs to be considered along with changing the orthogonal sequence (OCC).

[0076] Here, when a DM-RS is transmitted through four symbols in the single subframe, the locations of two symbols out of the four symbols in the subframe are the same as the locations of two symbols through which a DM-RS is transmitted in the single subframe in D2D (ProSe), and the locations of the remaining two symbols are configured additionally. This method may be effective from the perspective of the minimization of interference from a neighbor terminal.

[0077] In the case in which a terminal that transmits a DM-RS in the environment of V2X and a terminal that transmits a DM-RS in the environment of D2D (ProSe) coexist, when the application of an orthogonal sequence (OCC) for the transmission of a DM-RS in the V2X environment is extended based on the application of an orthogonal sequence (OCC) for the transmission of a DM-RS in the D2D (ProSe), mutual interference may be alleviated through an orthogonal sequence (OCC). For example, when an orthogonal sequence (OCC) application scheme for two symbols out of four symbols in a single subframe when a DM-RS is transmitted in the V2X environment is the same as an orthogonal sequence (OCC) application scheme for two symbols in a single subframe when a DM-RS is transmitted in D2D (ProSe), mutual interference may be alleviated in the two symbols.

[0078] As described above, in the case of V2X, a method of transmitting a DM-RS through four symbols in a single subframe may be preferentially considered by taking into consideration high Doppler effect or the like. However, in the case of a PSBCH that transmits together with a sidelink synchronization signal (SLSS) in a subframe, a method of transmitting a DM-RS through three symbols in a single subframe may be considered by taking into account symbols for the SLSS. In this instance, a length 3 orthogonal cover code (OCC) may be considered as an orthogonal sequence (OCC). Accordingly, an orthogonal sequence (OCC) may need to be changed to effectively minimize interference among neighbor terminals. Also, the application of an effective cyclic shift also needs to be considered along with changing the orthogonal sequence (OCC).

[0079] First, the case in which two types of OCCs are used like the D2D (ProSe) and the length of an OCC is 4 (method 1-1 and method 1-2 for an orthogonal sequence (OCC) and a cyclic shift) may be considered for V2X, whereas two length 2 OCCs are used in the case of D2D (ProSe). Although this may be applied to the example illustrated in the part (a) of FIG. 5, the present disclosure may not be limited thereto.

[0080] This will be described in details as follows.

[Method 1-1 for orthogonal sequence (OCC) and cyclic shift]

[0081] This is the case in which a normal CP is used, one of two symbols through which a DM-RS is transmitted in each slot is a fourth symbol (symbol #3) of each slot, in the same manner as a DM-RS that links with an existing UL

PUSCH or a DM-RS that links with a PSSCH/PSCCH/PDSCH/PSBCH in D2D(ProSe), and the other symbol is one of the subsequent symbols (a fifth symbol (symbol #4), a sixth symbol (symbol #5), and a seventh symbol (symbol #6)) in the slot.

5 **[0082]** Also, this is the case in which an extended CP is used, one of two symbols through which a DM-RS is transmitted in each slot is a third symbol (symbol #2) of each slot, in the same manner as a DM-RS that links with an existing UL PUSCH or a DM-RS that links with a PSSCH/PSCCH/PDSCH/PSBCH in D2D(ProSe), and the other symbol is one of the subsequent symbols (a fourth symbol (symbol #3), a fifth symbol (symbol #4), and a sixth symbol (symbol #5)) in the slot.

10 **[0083]** In this instance, this is the case in which the locations of two symbols out of the four symbols in a single subframe are the same as the locations of two symbols for D2D(ProSe) and the remaining two symbols are added, and thus, V2X may always transmit a DM-RS through a total of four symbols in a single subframe by adding two symbols in addition to two existing symbols in the single subframe. Alternatively, one out of two schemes may be selected and used, the schemes including a scheme (scheme #1) that transmits a DM-RS through two existing symbols in a single subframe in the same manner as D2D (ProSe), through a higher layer signaling such as RRC or the like, and a scheme (scheme #2) that transmits a DM-RS through a total of four symbols by adding two symbols in addition to the two existing symbols in the single subframe.

Table 8

20 **[0084]**

[Table 8]

Parameter	PSSCH	PSCCH
Cyclic shift $n_{CS,\lambda}$	$\lfloor N_{ID}^{SA}/2 \rfloor \bmod 8$	0
Orthogonal sequence $[w^\lambda(0) \ w^\lambda(1) \ w^\lambda(2) \ w^\lambda(3)]$	$[+1 \ +1 \ +1 \ +1]$ if $N_{ID}^{SA} \bmod 2 = 0$ $[+1 \ -1 \ -1 \ +1]$ if $N_{ID}^{SA} \bmod 2 = 1$	$[+1 \ +1 \ +1 \ +1]$
Parameter	PSDCH	PSBCH
Cyclic shift $n_{CS,\lambda}$	0	$\lfloor N_{ID}^{SL}/2 \rfloor \bmod 8$
Orthogonal sequence $[w^\lambda(0) \ w^\lambda(1) \ w^\lambda(2) \ w^\lambda(3)]$	$[+1 \ +1 \ +1 \ +1]$	$[+1 \ +1 \ +1 \ +1]$ if $N_{ID}^{SL} \bmod 2 = 0$ $[+1 \ -1 \ -1 \ +1]$ if $N_{ID}^{SL} \bmod 2 = 1$

40 **[0085]** As shown in Table 8, $[+1 \ +1 \ +1 \ +1]$ and $[+1 \ -1 \ -1 \ +1]$ may be used as two types of length 4 OCCs by taking into consideration the DM-RS transmission of a DM-RS that links with a PSSCH and a PSBCH through a total of 4 symbols in a single subframe.

45 **[0086]** By taking into consideration the DM-RS transmission of a DM-RS that links with a PSSCH and a PSBCH in D2D (ProSe) through a total of two symbols in a single subframe, two types of length 2 OCCs, that is, $[+1 \ +1]$ and $[+1 \ -1]$ may be used. This may be extended to $[+1 \ +1 \ +1 \ +1]$ and $[+1 \ -1 \ +1 \ -1]$ by taking into consideration the DM-RS transmission through a total of four symbols in a single subframe. However, in this instance, OCC mapping in two symbols (a first symbol and a third symbol out of four symbols) of which the locations are the same as the existing D2D (ProSe), out of four symbols, may be changed, and thus, the OCC configuration as shown in Table 8 may be preferable.

50 **[0087]** Further, in association with a cyclic shift, the configuration that is the same as the configuration in D2D (ProSe) may be possible.

[Method 1-2 for orthogonal sequence (OCC) and cyclic shift]

55 **[0088]** This is the case in which a normal CP is used, one of two symbols through which a DM-RS is transmitted in each slot is a fourth symbol (symbol #3) of each slot in the same manner as a DM-RS that links with an existing UL PUSCH or a DM-RS that links with a PSSCH/PSCCH/PDSCH/PSBCH in D2D(ProSe), and the other symbol is one of the previous symbols (a first symbol (symbol #0), a second symbol (symbol #1), and a third symbol (symbol #2)) in the slot.

[0089] Also, this is the case in which an extended CP is used, one of two symbols through which a DM-RS is transmitted in each slot is a third symbol (symbol #2) of each slot, in the same manner as a DM-RS that links with an existing UL PUSCH or a DM-RS that links with a PSSCH/PSCCH/PDSCH/PSBCH in D2D(ProSe), and the other symbol is one of the previous symbols (a first symbol (symbol #0) and a second symbol (symbol #1)) in the slot.

[0090] In this instance, this is the case in which the locations of two symbols out of the four symbols in a single subframe are the same as the locations of two symbols for D2D(ProSe) and the remaining two symbols are added, and thus, V2X may always transmit a DM-RS through a total of four symbols by adding two symbols in addition to two existing symbols in the single subframe. Alternatively, one out of two schemes may be selected and used, the schemes including a scheme (scheme #1) that transmits a DM-RS through two existing symbols in a single subframe in the same manner as D2D (ProSe), through a higher layer signaling such as RRC or the like, and a scheme (scheme #2) that transmits a DM-RS through a total of four symbols by adding two symbols in addition to the two existing symbols in the single subframe.

Table 9

[0091]

[Table 9]

Parameter	PSSCH	PSCCH
Cyclic shift $n_{cs,\lambda}$	$\lfloor n_{ID}^{SA}/2 \rfloor \bmod 8$	0
Orthogonal sequence $[w^\lambda(0) \ w^\lambda(1) \ w^\lambda(2) \ w^\lambda(3)]$	$[+1 \ +1 \ +1 \ +1]$ if $n_{ID}^{SA} \bmod 2 = 0$ $[-1 \ +1 \ +1 \ -1]$ if $n_{ID}^{SA} \bmod 2 = 1$	$[+1 \ +1 \ +1 \ +1]$
Parameter	PSDCH	PSBCH
Cyclic shift $n_{cs,\lambda}$	0	$\lfloor N_{ID}^{SL}/2 \rfloor \bmod 8$
Orthogonal sequence $[w^\lambda(0) \ w^\lambda(1) \ w^\lambda(2) \ w^\lambda(3)]$	$[+1 \ +1 \ +1 \ +1]$	$[+1 \ +1 \ +1 \ +1]$ if $N_{ID}^{SL} \bmod 2 = 0$ $[-1 \ +1 \ +1 \ -1]$ if $N_{ID}^{SL} \bmod 2 = 1$

[0092] As shown in Table 9, $[+1 \ +1 \ +1 \ +1]$ and $[-1 \ +1 \ +1 \ -1]$ may be used as two types of length 4 OCCs by taking into consideration the DM-RS transmission of a DM-RS that links with a PSSCH and a PSBCH through a total of 4 symbols in a single subframe.

[0093] By taking into consideration the DM-RS transmission of a DM-RS that links with a PSSCH and a PSBCH in D2D (ProSe) through a total of two symbols in a single subframe, two types of length 2 OCCs, that is, $[+1 \ +1]$ and $[+1 \ -1]$ may be used. This may be extended to $[+1 \ +1 \ +1 \ +1]$ and $[+1 \ -1 \ +1 \ -1]$ by taking into consideration the DM-RS transmission through a total of four symbols in a single subframe. However, in this instance, OCC mapping in two symbols (a second symbol and a fourth symbol out of four symbols) of which the locations are the same as the existing D2D (ProSe), out of four symbols, may be changed, and thus, the OCC configuration as shown in Table 9 may be preferable.

[0094] Further, in association with a cyclic shift, the configuration that is the same as the configuration in D2D (ProSe) may be possible.

[0095] Subsequently, the case in which the number of types of OCCs is extended to 4 and the length of an OCC is 4 (method 2-1, method 2-2, method 3-1, and method 3-2 for an orthogonal sequence (OCC) and a cyclic shift) may be considered for V2X, whereas two length 2 OCCs are used in the case of D2D (ProSe). Although this may be applied to the example illustrated in the part (a) of FIG. 5, the present disclosure may not be limited thereto.

[Method 2-1 for orthogonal sequence (OCC) and cyclic shift]

[0096] This is the case in which a normal CP is used, one of two symbols through which a DM-RS is transmitted in each slot is a fourth symbol (symbol #3) of each slot, in the same manner as a DM-RS that links with a UL PUSCH or a DM-RS that links with a PSSCH/PSCCH/PDSCH/PSBCH in D2D(ProSe), and the other symbol is one of subsequent symbols (a fifth symbol (symbol #4), a sixth symbol (symbol #5), and a seventh symbol (symbol #6)) in the slot.

[0097] Also, this is the case in which an extended CP is used, one of two symbols through which a DM-RS is transmitted in each slot is a third symbol (symbol #2) of each slot, in the same manner as a DM-RS that links with an existing UL

PUSCH or a DM-RS that links with a PSSCH/PSCCH/PDSCH/PSBCH in D2D(ProSe), and the other symbol is one of the subsequent symbols (a fourth symbol (symbol #3), a fifth symbol (symbol #4), and a sixth symbol (symbol #5)) in the slot.

[0098] In this instance, this is the case in which the locations of two symbols out of the four symbols in a single subframe are the same as the locations of two symbols for D2D(ProSe) and the remaining two symbols are added, and thus, V2X may always transmit a DM-RS through a total of four symbols by adding two symbols in addition to two existing symbols in the single subframe. Alternatively, one out of two schemes may be selected and used, the schemes including a scheme (scheme #1) that transmits a DM-RS through two existing symbols in a single subframe in the same manner as D2D (ProSe), through a higher layer signaling such as RRC or the like, and a scheme (scheme #2) that transmits a DM-RS through a total of four symbols by adding two symbols in addition to the two existing symbols in the single subframe.

Table 10

[0099]

[Table 10]

Parameter	PSSCH	PSCCH
Cyclic shift $n_{CS,\lambda}$	$\lfloor n_{ID}^{SA}/2 \rfloor_{\text{mod}8}$	0
Orthogonal sequence $[w^\lambda(0) \ w^\lambda(1) \ w^\lambda(2) \ w^\lambda(3)]$	$[+1 \ +1 \ +1 \ +1]$ if $n_{ID}^{SA} \text{ mod } 4 = 0$ $[+1 \ -1 \ -1 \ +1]$ if $n_{ID}^{SA} \text{ mod } 4 = 1$ $[+1 \ -1 \ +1 \ -1]$ if $n_{ID}^{SA} \text{ mod } 4 = 2$ $[+1 \ +1 \ -1 \ -1]$ if $n_{ID}^{SA} \text{ mod } 4 = 3$	$[+1 \ +1 \ +1 \ +1]$
Parameter	PSDCH	PSBCH
Cyclic shift $n_{CS,\lambda}$	0	$\lfloor N_{ID}^{SL}/2 \rfloor_{\text{mod}8}$
Orthogonal sequence $[w^\lambda(0) \ w^\lambda(1) \ w^\lambda(2) \ w^\lambda(3)]$	$[+1 \ +1 \ +1 \ +1]$	$[+1 \ +1 \ +1 \ +1]$ if $N_{ID}^{SL} \text{ mod } 4 = 0$ $[+1 \ -1 \ -1 \ +1]$ if $N_{ID}^{SL} \text{ mod } 4 = 1$ $[+1 \ -1 \ +1 \ -1]$ if $N_{ID}^{SL} \text{ mod } 4 = 2$ $[+1 \ +1 \ -1 \ -1]$ if $N_{ID}^{SL} \text{ mod } 4 = 3$

[0100] As shown in Table 10, $[+1 \ +1 \ +1 \ +1]$, $[+1 \ -1 \ -1 \ +1]$, $[+1 \ -1 \ +1 \ -1]$, and $[+1 \ +1 \ -1 \ -1]$ may be used as four types of length 4 OCCs by taking into consideration the DM-RS transmission of a DM-RS that links with a PSSCH and a PSBCH through a total of 4 symbols in a single subframe.

[0101] The four length 4 OCCs, that is, $[+1 \ +1 \ +1 \ +1]$, $[+1 \ -1 \ -1 \ +1]$, $[+1 \ -1 \ +1 \ -1]$, and $[+1 \ +1 \ -1 \ -1]$ may be used respectively for the cases having remainders of 0, 1, 2, and 3 that are obtained by dividing n_{ID}^{SA} by 4 in the case of a DM-RS that links with a PSSCH, and may be used respectively for the cases having remainders of 0, 1, 2, and 3 that are obtained by dividing n_{ID}^{SL} by 4 in the case of a DM-RS that links with a PSBCH.

[0102] This is an OCC configuration that maintains OCC mapping in two symbols (a first symbol and a third symbol out of four symbols) of which the locations are the same as the existing D2D (ProSe). That is, in the case of a DM-RS that links with a PSSCH, when remainders obtained by dividing n_{ID}^{SA} by 2 are 0 and 1, OCC values of a first symbol and a third symbol are $[+1 \ +1]$ and $[+1, -1]$, respectively.

[0103] Further, in association with a cyclic shift, the configuration that is the same as the configuration in D2D (ProSe) may be possible. However, different OCCs may be applied to two adjacent cyclic shift values, and thus, interference between neighbor UEs may be further reduced.

[Method 2-2 for orthogonal sequence (OCC) and cyclic shift]

[0104] This is the case in which a normal CP is used, one of two symbols through which a DM-RS is transmitted in each slot is a fourth symbol (symbol #3) of each slot in the same manner as a DM-RS that links with an existing UL

PUSCH or a DM-RS that links with a PSSCH/PSCCH/PDSCH/PSBCH in D2D(ProSe), and the other symbol is one of the previous symbols (a first symbol (symbol #0), a second symbol (symbol #1), and a third symbol (symbol #2)) in the slot.

[0105] Also, this is the case in which an extended CP is used, one of two symbols through which a DM-RS is transmitted in each slot is a third symbol (symbol #2) of each slot, in the same manner as a DM-RS that links with an existing UL PUSCH or a DM-RS that links with a PSSCH/PSCCH/PDSCH/PSBCH in D2D(ProSe), and the other symbol is one of the previous symbols (a first symbol (symbol #0) and a second symbol (symbol #1)) in the slot.

[0106] In this instance, this is the case in which the locations of two symbols out of the four symbols in a single subframe are the same as the locations of two symbols for D2D(ProSe) and the remaining two symbols are added, and thus, V2X may always transmit a DM-RS through a total of four symbols by adding two symbols in addition to two existing symbols in the single subframe. Alternatively, one out of two schemes may be selected and used, the schemes including a scheme (scheme #1) that transmits a DM-RS through two existing symbols in a single subframe in the same manner as D2D (ProSe), through a higher layer signaling such as RRC or the like, and a scheme (scheme #2) that transmits a DM-RS by adding two symbols in addition to the two existing symbols in the single subframe.

Table 11

[0107]

[Table 11]

Parameter	PSSCH	PSCCH
Cyclic shift $n_{CS,\lambda}$	$\lfloor n_{ID}^{SA}/2 \rfloor \bmod 8$	0
Orthogonal sequence $[w^\lambda(0) \ w^\lambda(1) \ w^\lambda(2) \ w^\lambda(3)]$	$[+1 \ +1 \ +1 \ +1]$ if $n_{ID}^{SA} \bmod 4 = 0$ $[-1 \ +1 \ +1 \ -1]$ if $n_{ID}^{SA} \bmod 4 = 1$ $[-1 \ +1 \ -1 \ +1]$ if $n_{ID}^{SA} \bmod 4 = 2$ $[+1 \ +1 \ -1 \ -1]$ if $n_{ID}^{SA} \bmod 4 = 3$	$[+1 \ +1 \ +1 \ +1]$
Parameter	PSDCH	PSBCH
Cyclic shift $n_{CS,\lambda}$	0	$\lfloor N_{ID}^{SL}/2 \rfloor \bmod 8$
Orthogonal sequence $[w^\lambda(0) \ w^\lambda(1) \ w^\lambda(2) \ w^\lambda(3)]$	$[+1 \ +1 \ +1 \ +1]$	$[+1 \ +1 \ +1 \ +1]$ if $N_{ID}^{SL} \bmod 4 = 0$ $[-1 \ +1 \ +1 \ -1]$ if $N_{ID}^{SL} \bmod 4 = 1$ $[-1 \ +1 \ -1 \ +1]$ if $N_{ID}^{SL} \bmod 4 = 2$ $[+1 \ +1 \ -1 \ -1]$ if $N_{ID}^{SL} \bmod 4 = 3$

[0108] As shown in Table 11, $[+1 \ +1 \ +1 \ +1]$, $[-1 \ +1 \ +1 \ -1]$, $[-1 \ +1 \ -1 \ +1]$, and $[+1 \ +1 \ -1 \ -1]$ may be used as four types of length 4 OCCs by taking into consideration the DM-RS transmission of a DM-RS that links with a PSSCH and a PSBCH through a total of 4 symbols in a single subframe.

[0109] The four length 4 OCCs, that is, $[+1 \ +1 \ +1 \ +1]$, $[-1 \ +1 \ +1 \ -1]$, $[-1 \ +1 \ -1 \ +1]$, and $[+1 \ +1 \ -1 \ -1]$ may be used respectively for the cases having remainders of 0, 1, 2, and 3 that are obtained by dividing n_{ID}^{SA} by 4 in the case of a DM-RS that links with a PSSCH, and may be used respectively for the cases having remainders of 0, 1, 2, and 3 that are obtained by dividing n_{ID}^{SL} by 4 in the case of a DM-RS that links with a PSBCH.

[0110] This is an OCC configuration that maintains OCC mapping in two symbols (a second symbol and a fourth symbol out of four symbols) of which the locations are the same as the existing D2D (ProSe). That is, in the case of a DM-RS that links with a PSSCH, when remainders obtained by dividing n_{ID}^{SA} by 2 are 0 and 1, OCC values of a second symbol and a fourth symbol are $[+1 \ +1]$ and $[+1 \ -1]$, respectively. Also, in the case of a DM-RS that links with a PSBCH, when remainders obtained by dividing n_{ID}^{SL} by 2 are 0 and 1, OCC values of a second symbol and a fourth symbol are $[+1 \ +1]$ and $[+1 \ -1]$, respectively.

[0111] Further, in association with a cyclic shift, the configuration that is the same as the configuration in D2D (ProSe) may be possible. However, different OCCs may be applied to two adjacent cyclic shift values, and thus, interference between neighbor UEs may be further reduced.

[Method 3-1 for orthogonal sequence (OCC) and cyclic shift]

[0112] This is the case in which a normal CP is used, one of two symbols through which a DM-RS is transmitted in each slot is a fourth symbol (symbol #3) of each slot, in the same manner as a DM-RS that links with a UL PUSCH or a DM-RS that links with a PSSCH/PSCCH/PDSCH/PSBCH in D2D(ProSe), and the other symbol is one of the subsequent symbols (a fifth symbol (symbol #4), a sixth symbol (symbol #5), and a seventh symbol (symbol #6)) in the slot.

[0113] Also, this is the case in which an extended CP is used, one of two symbols through which a DM-RS is transmitted in each slot is a third symbol (symbol #2) of each slot, in the same manner as a DM-RS that links with an existing UL PUSCH or a DM-RS that links with a PSSCH/PSCCH/PDSCH/PSBCH in D2D(ProSe), and the other symbol is one of the subsequent symbols (a fourth symbol (symbol #3), a fifth symbol (symbol #4), and a sixth symbol (symbol #5)) in the slot.

[0114] In this instance, this is the case in which the locations of two symbols out of the four symbols in a single subframe are the same as the locations of two symbols for D2D(ProSe) and the remaining two symbols are added, and thus, V2X may always transmit a DM-RS through a total of four symbols by adding two symbols in addition to two existing symbols in the single subframe. Alternatively, one out of two schemes may be selected and used, the schemes including a scheme (scheme #1) that transmits a DM-RS through two existing symbols in a single subframe in the same manner as D2D (ProSe), through a higher layer signaling such as RRC or the like, and a scheme (scheme #2) that transmits a DM-RS by adding two symbols in addition to the two existing symbols in the single subframe.

Table 12

[0115]

[Table 12]

Parameter	PSSCH	PSCCH
Cyclic shift $n_{CS,\lambda}$	$\lfloor n_{ID}^{SA}/4 \rfloor \bmod 8$	0
Orthogonal sequence $[w^\lambda(0) \ w^\lambda(1) \ w^\lambda(2) \ w^\lambda(3)]$	$[+1 \ +1 \ +1 \ +1]$ if $n_{ID}^{SA} \bmod 4 = 0$ $[+1 \ -1 \ -1 \ +1]$ if $n_{ID}^{SA} \bmod 4 = 1$ $[+1 \ -1 \ +1 \ -1]$ if $n_{ID}^{SA} \bmod 4 = 2$ $[+1 \ +1 \ -1 \ -1]$ if $n_{ID}^{SA} \bmod 4 = 3$	$[+1 \ +1 \ +1 \ +1]$
Parameter	PSDCH	PSBCH
Cyclic Shift $n_{CS,\lambda}$	0	$\lfloor N_{ID}^{SL}/4 \rfloor \bmod 8$
Orthogonal sequence $[w^\lambda(0) \ w^\lambda(1) \ w^\lambda(2) \ w^\lambda(3)]$	$[+1 \ +1 \ +1 \ +1]$	$[+1 \ +1 \ +1 \ +1]$ if $N_{ID}^{SL} \bmod 4 = 0$ $[+1 \ -1 \ -1 \ +1]$ if $N_{ID}^{SL} \bmod 4 = 1$ $[+1 \ -1 \ +1 \ -1]$ if $N_{ID}^{SL} \bmod 4 = 2$ $[+1 \ +1 \ -1 \ -1]$ if $N_{ID}^{SL} \bmod 4 = 3$

[0116] As shown in Table 12, $[+1 \ +1 \ +1 \ +1]$, $[+1 \ -1 \ -1 \ +1]$, $[+1 \ -1 \ +1 \ -1]$, and $[+1 \ +1 \ -1 \ -1]$ may be used as four types of length 4 OCCs by taking into consideration the DM-RS transmission of a DM-RS that links with a PSSCH and a PSBCH through a total of 4 symbols in a single subframe.

[0117] The four length 4 OCCs, that is, $[+1 \ +1 \ +1 \ +1]$, $[+1 \ -1 \ -1 \ +1]$, $[+1 \ -1 \ +1 \ -1]$, and $[+1 \ +1 \ -1 \ -1]$ may be used respectively for the cases having remainders of 0, 1, 2, and 3 that are obtained by dividing n_{ID}^{SA} by 4 in the case of a DM-RS that links with a PSSCH, and may be used respectively for the cases having remainders of 0, 1, 2, and 3 that are obtained by dividing n_{ID}^{SL} by 4 in the case of a DM-RS that links with a PSBCH.

[0118] This is an OCC configuration that maintains OCC mapping in two symbols (a first symbol and a third symbol out of four symbols) of which the locations are the same as the existing D2D (ProSe). That is, in the case of a DM-RS that links with a PSSCH, when remainders obtained by dividing n_{ID}^{SA} by 2, OCC values of a first symbol and a third symbol are $[+1 \ +1]$ and $[+1 \ -1]$, respectively.

[0119] Further, in association with a cyclic shift, in the case of a DM-RS that links with a PSSCH in D2D (ProSe), one out of 8 cyclic shift values is determined by executing modulo8 (mod 8) on a value obtained by dividing n_{ID}^{SA} by 2, whereas, in the case of a DM-RS that links with a PSSCH in V2X, one out of 8 cyclic shift values is determined by executing modulo8 (mod 8) on a value obtained by dividing n_{ID}^{SA} by 4. In the same manner, in the case of a DM-RS that links with a PSBCH in D2D (ProSe), one out of 8 cyclic shift values is determined by executing modulo8 (mod 8) on a value obtained by dividing n_{ID}^{SL} by 2, whereas, in the case of a DM-RS that links with a PSBCH in V2X, one out of 8 cyclic shift values is determined by executing modulo 8 (mod 8) on a value obtained by dividing n_{ID}^{SL} by 4. Further, in the case of a DM-RS that links with a PSBCH in D2D (ProSe), one out of 30 sequence shift patterns f_{ss} in group hopping is determined by executing modulo30 (mod 30) on a value obtained by dividing n_{ID}^{SL} by 16, whereas, in the case of a DM-RS that links with a PSBCH in V2X, one out of 30 sequence shift patterns f_{ss} in group hopping is determined by executing modulo 30 (mod 30) on a value obtained by dividing n_{ID}^{SL} by 32.

[Method 3-2 for orthogonal sequence (OCC) and cyclic shift]

[0120] This is the case in which a normal CP is used, one of two symbols through which a DM-RS is transmitted in each slot is a fourth symbol (symbol #3) of each slot in the same manner as a DM-RS that links with an existing UL PUSCH or a DM-RS that links with a PSSCH/PSCCH/PDSCH/PSBCH in D2D(ProSe), and the other symbol is one of the previous symbols (a first symbol (symbol #0), a second symbol (symbol #1), and a third symbol (symbol #2)).

[0121] Also, this is the case in which an extended CP is used, one of two symbols through which a DM-RS is transmitted in each slot is a third symbol (symbol #2) of each slot, in the same manner as a DM-RS that links with an existing UL PUSCH or a DM-RS that links with a PSSCH/PSCCH/PDSCH/PSBCH in D2D(ProSe), and the other symbol is one of the previous symbols (a first symbol (symbol #0) and a second symbol (symbol #1)).

[0122] In this instance, this is the case in which the locations of two symbols out of the four symbols in a single subframe are the same as the locations of two symbols for D2D(ProSe) and the remaining two symbols are added, and thus, V2X may always transmit a DM-RS through a total of four symbols by adding two symbols in addition to two existing symbols in the single subframe. Alternatively, one out of two schemes may be selected and used, the schemes including a scheme (scheme #1) that transmits a DM-RS through two existing symbols in a single subframe in the same manner as D2D (ProSe), through a higher layer signaling such as RRC or the like, and a scheme (scheme #2) that transmits a DM-RS by adding two symbols in addition to the two existing symbols in the single subframe.

Table 13

[0123]

[Table 13]

Parameter	PSSCH	PSCCH
Cyclic shift $n_{cs,\lambda}$	$\lfloor n_{ID}^{SA}/4 \rfloor \bmod 8$	0
Orthogonal sequence $[w^\lambda(0) \ w^\lambda(1) \ w^\lambda(2) \ w^\lambda(3)]$	$[+1 \ +1 \ +1 \ +1]$ if $n_{ID}^{SA} \bmod 4 = 0$ $[-1 \ +1 \ +1 \ -1]$ if $n_{ID}^{SA} \bmod 4 = 1$ $[-1 \ +1 \ -1 \ +1]$ if $n_{ID}^{SA} \bmod 4 = 2$ $[+1 \ +1 \ -1 \ -1]$ if $n_{ID}^{SA} \bmod 4 = 3$	$[+1 \ +1 \ +1 \ +1]$
Parameter	PSDCH	PSBCH
Cyclic shift $n_{cs,\lambda}$	0	$\lfloor N_{ID}^{SL}/4 \rfloor \bmod 8$
Orthogonal sequence $[w^\lambda(0) \ w^\lambda(1) \ w^\lambda(2) \ w^\lambda(3)]$	$[+1 \ +1 \ +1 \ +1]$	$[+1 \ +1 \ +1 \ +1]$ if $N_{ID}^{SL} \bmod 4 = 0$ $[-1 \ +1 \ +1 \ -1]$ if $N_{ID}^{SL} \bmod 4 = 1$ $[-1 \ +1 \ -1 \ +1]$ if $N_{ID}^{SL} \bmod 4 = 2$ $[+1 \ +1 \ -1 \ -1]$ if $N_{ID}^{SL} \bmod 4 = 3$

[0124] As shown in Table 13, [+1 +1 +1 +1], [-1 +1 +1 -1], [-1 +1 -1 +1], and [+1 +1 -1 -1] may be used as four types of length 4 OCCs by taking into consideration the DM-RS transmission of a DM-RS that links with a PSSCH and a PSBCH through a total of 4 symbols in a single subframe.

[0125] The four length 4 OCCs, that is, [+1 +1 +1 +1], [-1 +1 +1 -1], [-1 +1 -1 +1], and [+1 +1 -1 -1] may be used respectively for the cases having remainders of 0, 1, 2, and 3 that are obtained by dividing n_{ID}^{SA} by 4 in the case of a DM-RS that links with a PSSCH, and may be used respectively for the cases having remainders of 0, 1, 2, and 3 that are obtained by dividing n_{ID}^{SL} by 4 in the case of a DM-RS that links with a PSBCH.

[0126] This is an OCC configuration that maintains OCC mapping in two symbols (a second symbol and a fourth symbol out of four symbols) of which the locations are the same as the existing D2D (ProSe). That is, in the case of a DM-RS that links with a PSSCH, when remainders obtained by dividing n_{ID}^{SA} by 2 are 0 and 1, OCC values of a second symbol and a fourth symbol are [+1 +1] and [+1 -1], respectively.

[0127] Further, in association with a cyclic shift, in the case of a DM-RS that links with a PSSCH in D2D (ProSe), one out of 8 cyclic shift values is determined by executing modulo8 (mod 8) on a value obtained by dividing n_{ID}^{SA} by 2, whereas, in the case of a DM-RS that links with a PSSCH in V2X, one out of 8 cyclic shift values may be determined by executing modulo 8 (mod 8) on a value obtained by dividing n_{ID}^{SA} by 4. In the same manner, in the case of a DM-RS that links with a PSBCH in D2D (ProSe), one out of 8 cyclic shift values is determined by executing modulo 8 (mod 8) on a value obtained by dividing n_{ID}^{SL} by 2, whereas, in the case of a DM-RS that links with a PSBCH in V2X, one out of 8 cyclic shift values may be determined by executing modulo 8 (mod 8) on a value obtained by dividing n_{ID}^{SL} by 4. Further, in the case of a DM-RS that links with a PSBCH in D2D (ProSe), one out of 30 sequence shift patterns f_{ss} in group hopping is determined by executing modulo30 (mod 30) on a value obtained by dividing n_{ID}^{SL} by 16, whereas, in the case of a DM-RS that links with a PSBCH in V2X, one out of 30 sequence shift patterns f_{ss} in group hopping may be determined by executing modulo30 (mod 30) on a value obtained by dividing n_{ID}^{SL} by 32.

[0128] Subsequently, the case in which two types of OCCs are used like the D2D (ProSe) and the length of an OCC is 4 (method 4 for an orthogonal sequence (OCC) and a cyclic shift) may be considered for V2X, whereas two length 2 OCCs are used in the case of D2D (ProSe). Although this may be applied to the example illustrated in the part (b) of FIG. 5, the present disclosure may not be limited thereto.

[Method 4 for orthogonal sequence (OCC) and cyclic shift]

[0129] This is the case in which a normal CP is used and, two symbols out of seven symbols in each of a first slot and a second slot are determined as symbols through which a DM-RS is transmitted.

[0130] Also, this is the case in which an extended CP is used and, two symbols out of six symbols in each of a first slot and a second slot are determined as symbols through which a DM-RS is transmitted.

[0131] To this end, one out of two schemes may be selected and used, the schemes including a scheme (scheme #1) that transmits a DM-RS through two existing symbols in a single subframe in the same manner as D2D (ProSe), through a higher layer signaling such as RRC or the like, and a scheme (scheme #2) that transmits a DM-RS through a total of four symbols in the single subframe.

Table 14

[0132]

[Table 14]

Parameter	PSSCH	PSBCH
Cyclic shift $n_{CS,\lambda}$	$\lfloor n_{ID}^{SA}/2 \rfloor \bmod 8$	0
Orthogonal sequence $[w^\lambda(0) \ w^\lambda(1) \ w^\lambda(2) \ w^\lambda(3)]$	$[+1 \ +1 \ +1 \ +1]$ if $n_{ID}^{SA} \bmod 2 = 0$ $[+1 \ -1 \ +1 \ -1]$ if $n_{ID}^{SA} \bmod 2 = 1$	$[+1 \ +1 \ +1 \ +1]$
Parameter	PSDCH	PSBCH
Cyclic shift $n_{CS,\lambda}$	0	$\lfloor N_{ID}^{SL}/2 \rfloor \bmod 8$
Orthogonal sequence $[w^\lambda(0) \ w^\lambda(1) \ w^\lambda(2) \ w^\lambda(3)]$	$[+1 \ +1 \ +1 \ +1]$	$[+1 \ +1 \ +1 \ +1]$ if $N_{ID}^{SL} \bmod 2 = 0$ $[+1 \ -1 \ +1 \ -1]$ if $N_{ID}^{SL} \bmod 2 = 1$

[0133] As shown in Table 14, [+1 +1 +1 +1] and [+1 -1 +1 -1] may be used as two types of length 4 OCCs by taking into consideration the DM-RS transmission of a DM-RS that links with a PSSCH and a PSBCH through a total of 4 symbols in a single subframe.

[0134] The two length 4 OCCs, that is, [+1 +1 +1 +1] and [+1 -1 +1 -1] may be used respectively for the cases having remainders of 0 and 1 that are obtained by dividing n_{ID}^{SA} by 2 in the case of a DM-RS that links with a PSSCH, and may be used respectively for the cases having remainders of 0 and 1 that are obtained by dividing n_{ID}^{SL} by 2 in the case of a DM-RS that links with a PSBCH.

[0135] Further, in association with a cyclic shift, the configuration that is the same as the configuration in D2D (ProSe) may be possible.

[0136] Subsequently, the case in which the number of types of OCCs is extended to 4 and the length of an OCC is 4 (method 5 and method 6 for an orthogonal sequence (OCC) and a cyclic shift) may be considered for V2X, whereas two length 2 OCCs are used in the case of D2D (ProSe). Although this may be applied to the example illustrated in the part (b) of FIG. 5, the present disclosure may not be limited thereto.

[Method 5 for orthogonal sequence (OCC) and cyclic shift]

[0137] This is the case in which a normal CP is used and two symbols out of seven symbols in each of a first slot and a second slot are determined as symbols through which a DM-RS is transmitted.

[0138] Also, this is the case in which an extended CP is used and two symbols out of six symbols in each of a first slot and a second slot are determined as symbols through which a DM-RS is transmitted.

[0139] To this end, one out of two schemes may be selected and used, the schemes including a scheme (scheme #1) that transmits a DM-RS through two existing symbols in a single subframe in the same manner as D2D (ProSe), through a higher layer signaling such as RRC or the like, and a scheme (scheme #2) that transmits a DM-RS through a total of four symbols in the single subframe.

Table 15

[0140]

[Table 15]

Parameter	PSSCH	PSBCH
Cyclic shift $n_{CS,\lambda}$	$\lfloor n_{ID}^{SA}/2 \rfloor \bmod 8$	0
Orthogonal sequence $[w^\lambda(0) \ w^\lambda(1) \ w^\lambda(2) \ w^\lambda(3)]$	$[+1 \ +1 \ +1 \ +1]$ if $n_{ID}^{SA} \bmod 4 = 0$ $[+1 \ -1 \ +1 \ -1]$ if $n_{ID}^{SA} \bmod 4 = 1$ $[+1 \ +1 \ -1 \ -1]$ if $n_{ID}^{SA} \bmod 4 = 2$ $[+1 \ -1 \ -1 \ +1]$ if $n_{ID}^{SA} \bmod 4 = 3$	$[+1 \ +1 \ +1 \ +1]$
Parameter	PSDCH	PSBCH
Cyclic shift $n_{CS,\lambda}$	0	$\lfloor N_{ID}^{SL}/2 \rfloor \bmod 8$
Orthogonal sequence $[w^\lambda(0) \ w^\lambda(1) \ w^\lambda(2) \ w^\lambda(3)]$	$[+1 \ +1 \ +1 \ +1]$	$[+1 \ +1 \ +1 \ +1]$ if $N_{ID}^{SL} \bmod 4 = 0$ $[+1 \ -1 \ +1 \ -1]$ if $N_{ID}^{SL} \bmod 4 = 1$ $[+1 \ +1 \ -1 \ -1]$ if $N_{ID}^{SL} \bmod 4 = 2$ $[+1 \ -1 \ -1 \ +1]$ if $N_{ID}^{SL} \bmod 4 = 3$

[0141] As shown in Table 15, [+1 +1 +1 +1], [+1 -1 +1 -1], [+1 +1 -1 -1], and [+1 -1 -1 +1] may be used as four types of length 4 OCCs by taking into consideration the DM-RS transmission of a DM-RS that links with a PSSCH and a PSBCH through a total of 4 symbols in a single subframe.

[0142] The four length 4 OCCs, that is, [+1 +1 +1 +1], [+1 -1 +1 -1], [+1 +1 -1 -1], and [+1 -1 -1 +1] may be used respectively for the cases having remainders of 0, 1, 2, and 3 that are obtained by dividing n_{ID}^{SA} by 4 in the case of a DM-RS that links with a PSSCH, and may be used respectively for the cases having remainders of 0, 1, 2, and 3 that

are obtained by dividing n_{ID}^{SL} by 4 in the case of a DM-RS that links with a PSBCH.

[0143] Further, in association with a cyclic shift, the configuration that is the same as the configuration in D2D (ProSe) may be possible. However, different OCCs may be applied to two adjacent cyclic shift values and thus, interference between neighbor UEs may be further reduced.

[Method 6 for orthogonal sequence (OCC) and cyclic shift]

[0144] This is the case in which a normal CP is used and two symbols out of seven symbols in each of a first slot and a second slot are determined as symbols through which a DM-RS is transmitted.

[0145] Also, this is the case in which an extended CP is used and two symbols out of six symbols in each of a first slot and a second slot are determined as symbols through which a DM-RS is transmitted.

[0146] To this end, one out of two schemes may be selected and used, the schemes including a scheme (scheme #1) that transmits a DM-RS through two existing symbols in a single subframe in the same manner as D2D (ProSe), through a higher layer signaling such as RRC or the like, and a scheme (scheme #2) that transmits a DM-RS through a total of four symbols in the single subframe.

Table 16

[0147]

[Table 16]

Parameter	PSSCH	PSCCH
Cyclic shift $n_{CS,\lambda}$	$\lfloor n_{ID}^{SA}/4 \rfloor \bmod 8$	0
Orthogonal sequence $[w^{\lambda(0)} w^{\lambda(1)} w^{\lambda(2)} w^{\lambda(3)}]$	$[+1 +1 +1 +1]$ if $n_{ID}^{SA} \bmod 4 = 0$ $[+1 -1 +1 -1]$ if $n_{ID}^{SA} \bmod 4 = 1$ $[+1 +1 -1 -1]$ if $n_{ID}^{SA} \bmod 4 = 2$ $[+1 -1 -1 +1]$ if $n_{ID}^{SA} \bmod 4 = 3$	$[+1 +1 +1 +1]$
Parameter	PSDCH	PSBCH
Cyclic shift $n_{SC,\lambda}$	0	$\lfloor N_{ID}^{SL}/4 \rfloor \bmod 8$
Orthogonal sequence $[w^{\lambda(0)} w^{\lambda(1)} w^{\lambda(2)} w^{\lambda(3)}]$	$[+1 +1 +1 +1]$	$[+1 +1 +1 +1]$ if $N_{ID}^{SL} \bmod 4 = 0$ $[+1 -1 +1 -1]$ if $N_{ID}^{SL} \bmod 4 = 1$ $[+1 +1 -1 -1]$ if $N_{ID}^{SL} \bmod 4 = 2$ $[+1 -1 -1 +1]$ if $N_{ID}^{SL} \bmod 4 = 3$

[0148] As shown in Table 16, $[+1 +1 +1 +1]$, $[+1 -1 +1 -1]$, $[+1 +1 -1 -1]$, and $[+1 -1 -1 +1]$ may be used as four types of length 4 OCCs by taking into consideration the DM-RS transmission of a DM-RS that links with a PSSCH and a PSBCH through a total of 4 symbols in a single subframe.

[0149] The four length 4 OCCs, that is, $[+1 +1 +1 +1]$, $[+1 -1 +1 -1]$, $[+1 +1 -1 -1]$, and $[+1 -1 -1 +1]$ may be used respectively for the cases having remainders of 0, 1, 2, and 3 that are obtained by dividing n_{ID}^{SA} by 4 in the case of a DM-RS that links with a PSSCH, and may be used respectively for the cases having remainders of 0, 1, 2, and 3 that are obtained by dividing n_{ID}^{SL} by 4 in the case of a DM-RS that links with a PSBCH.

[0150] Further, in association with a cyclic shift, in the case of a DM-RS that links with a PSSCH in D2D (ProSe), one out of 8 cyclic shift values is determined by executing modulo8 (mod 8) on a value obtained by dividing n_{ID}^{SA} by 2, whereas, in the case of a DM-RS that links with a PSSCH in V2X, one out of 8 cyclic shift values may be determined by executing modulo8 (mod 8) on a value obtained by dividing n_{ID}^{SA} by 4. In the same manner, in the case of a DM-RS that links with a PSBCH in D2D (ProSe), one out of 8 cyclic shift values is determined by executing modulo8 (mod 8) on a value obtained by dividing n_{ID}^{SL} by 2, whereas, in the case of a DM-RS that links with a PSBCH in V2X, one out of 8 cyclic shift values is determined by executing modulo 8 (mod 8) on a value obtained by dividing n_{ID}^{SL} by 4. Further,

in the case of a DM-RS that links with a PSBCH in D2D (ProSe), one out of 30 sequence shift patterns f_{ss} in group hopping is determined by executing modulo 30 (mod 30) on a value obtained by dividing $n_{SL_ID}^{SL}$ by 16, whereas, in the case of a DM-RS that links with a PSBCH in V2X, one out of 30 sequence shift patterns f_{ss} in group hopping may be determined by executing modulo30 (mod 30) on a value obtained by dividing $n_{SL_ID}^{SL}$ by 32.

[0151] Subsequently, the case in which two types of OCCs are used like the D2D (ProSe) and the length of an OCC is 3 (method 7 for an orthogonal sequence (OCC) and cyclic shift) may be considered for V2X, whereas two length 2 OCCs are used in the case of D2D (ProSe). Although this may be applied to the example illustrated in the part (c) of FIG. 5, the present disclosure may not be limited thereto.

[Method 7 for orthogonal sequence (OCC) and cyclic shift]

[0152] This is the case in which a normal CP is used, and three symbols out of 14 symbols in a single subframe are determined as symbols through which a DM-RS is transmitted.

[0153] Also, this is the case in which an extended CP is used, and three symbols out of 12 symbols in a single subframe are determined as symbols through which a DM-RS is transmitted.

[0154] To this end, one out of two schemes may be selected and used, the schemes including a scheme (scheme #1) that transmits a DM-RS through two existing symbols in a single subframe in the same manner as D2D (ProSe), through a higher layer signaling such as RRC or the like, and a scheme (scheme #2) that transmits a DM-RS through a total of three symbols in the single subframe.

Table 17

[0155]

[Table 17]

Parameter	PSBCH
Cyclic shift $n_{cs,l}$	$\lfloor N_{D}^{SL}/2 \rfloor \bmod 8$
Orthogonal sequence $[w^s(0) \ w^s(1) \ w^s(2)]$	$[+1 \ +1 \ +1]$ if $N_{D}^{SL} \bmod 2 = 0$ $[+1 \ e^{j2\pi/3} \ e^{j4\pi/3}]$ if $N_{D}^{SL} \bmod 2 = 1$

[0156] As shown in Table 17, $[+1 \ +1 \ +1]$ and $[+1 \ e^{j2\pi/3} \ e^{j4\pi/3}]$ may be used as two types of length 3 OCCs by taking into consideration the DM-RS transmission of a DM-RS that links with a PSBCH through a total of 3 symbols in a single subframe. Alternatively, $[+1 \ +1 \ +1]$ and $[+1 \ e^{j4\pi/3} \ e^{j2\pi/3}]$ may be used as the two types of length 3 OCCs.

[0157] Each of the two length 3OCCs, that is, $[+1 \ +1 \ +1]$ and $[+1 \ e^{j2\pi/3} \ e^{j4\pi/3}]$ (or $[+1 \ +1 \ +1]$ and $[+1 \ e^{j4\pi/3} \ e^{j2\pi/3}]$) may be used respectively for the cases having remainders of 0 and 1 that are obtained by dividing $n_{SL_ID}^{SL}$ by 2 in the case of a DM-RS that links with a PSBCH.

[0158] Further, in association with a cyclic shift, the configuration that is the same as the configuration in D2D (ProSe) may be possible.

[0159] Subsequently, the case in which the number of types of OCCs is extended to 3 and the length of an OCC is 3 (method 8 and method 9 for an orthogonal sequence (OCC) and a cyclic shift) may be considered for V2X, whereas two length 2 OCCs are used in the case of D2D (ProSe). Although this may be applied to the example illustrated in the part (c) of FIG. 5, the present disclosure may not be limited thereto.

[Method 8 for orthogonal sequence (OCC) and cyclic shift]

[0160] This is the case in which a normal CP is used, and three symbols out of 14 symbols in a single subframe are determined as symbols through which a DM-RS is transmitted.

[0161] Also, this is the case in which an extended CP is used, and three symbols out of 12 symbols in a single subframe are determined as symbols through which a DM-RS is transmitted.

[0162] To this end, one out of two schemes may be selected and used, the schemes including a scheme (scheme #1) that transmits a DM-RS through two existing symbols in a single subframe in the same manner as D2D (ProSe), through a higher layer signaling such as RRC or the like, and a scheme (scheme #2) that transmits a DM-RS through a total of three symbols in the single subframe.

Table 18

[0163]

5

[Table 18]

Parameter	PSBCH
Cyclic shift $n_{CS,\lambda}$	$\lfloor N_{ID}^{SL}/2 \rfloor \bmod 8$
Orthogonal sequence $[w^{\lambda}(0) \ w^{\lambda}(1) \ w^{\lambda}(2)]$	$[+1 \ +1 \ +1]$ if $N_{ID}^{SL} \bmod 3 = 0$ $[+1 \ e^{j2\pi/3} \ e^{j4\pi/3}]$ if $N_{ID}^{SL} \bmod 3 = 1$ $[+1 \ e^{j4\pi/3} \ e^{j2\pi/3}]$ if $N_{ID}^{SL} \bmod 3 = 2$

10

15

[0164] As shown in Table 18, $[+1 \ +1 \ +1]$, $[+1 \ e^{j2\pi/3} \ e^{j4\pi/3}]$, and $[+1 \ e^{j4\pi/3} \ e^{j2\pi/3}]$ may be used as three types of length 3 OCCs by taking into consideration the DM-RS transmission of a DM-RS that links with a PSBCH through a total of 3 symbols in a single subframe.

20

[0165] Each of the three length 3OCCs, that is, $[+1 \ +1 \ +1]$, $[+1 \ e^{j2\pi/3} \ e^{j4\pi/3}]$, and $[+1 \ e^{j4\pi/3} \ e^{j2\pi/3}]$ may be used respectively for the cases having remainders of 0, 1, and 2 that are obtained by dividing n_{ID}^{SL} by 3 in the case of a DM-RS that links with a PSBCH.

[0166] Further, in association with a cyclic shift, the configuration that is the same as the configuration in D2D (ProSe) may be possible.

25

[Method 9 for orthogonal sequence (OCC) and cyclic shift]

[0167] This is the case in which a normal CP is used, and three symbols out of 14 symbols in a single subframe are determined as symbols through which a DM-RS is transmitted.

30

[0168] Also, this is the case in which an extended CP is used, and three symbols out of 12 symbols in a single subframe are determined as symbols through which a DM-RS is transmitted.

[0169] To this end, one out of two schemes may be selected and used, the schemes including a scheme (scheme #1) that transmits a DM-RS through two existing symbols in a single subframe in the same manner as D2D (ProSe), through a higher layer signaling such as RRC or the like, and a scheme (scheme #2) that transmits a DM-RS through a total of three symbols in the single subframe.

35

Table 19

[0170]

40

[Table 19]

Parameter	PSBCH
Cyclic shift $n_{CS,\lambda}$	$\lfloor N_{ID}^{SL}/3 \rfloor \bmod 8$
Orthogonal sequence $[w^{\lambda}(0) \ w^{\lambda}(1) \ w^{\lambda}(2)]$	$[+1 \ +1 \ +1]$ if $N_{ID}^{SL} \bmod 3 = 0$ $[+1 \ e^{j2\pi/3} \ e^{j4\pi/3}]$ if $N_{ID}^{SL} \bmod 3 = 1$ $[+1 \ e^{j4\pi/3} \ e^{j2\pi/3}]$ if $N_{ID}^{SL} \bmod 3 = 2$

45

50

[0171] As shown in Table 19, $[+1 \ +1 \ +1]$, $[+1 \ e^{j2\pi/3} \ e^{j4\pi/3}]$, and $[+1 \ e^{j4\pi/3} \ e^{j2\pi/3}]$ may be used as three types of length 3 OCCs by taking into consideration the DM-RS transmission of a DM-RS that links with a PSBCH through a total of 3 symbols in a single subframe.

55

[0172] Each of the three length 3OCCs, that is, $[+1 \ +1 \ +1]$, $[+1 \ e^{j2\pi/3} \ e^{j4\pi/3}]$, and $[+1 \ e^{j4\pi/3} \ e^{j2\pi/3}]$ may be used respectively for the cases having remainders of 0, 1, and 2 that are obtained by dividing n_{ID}^{SL} by 3 in the case of a DM-RS that links with a PSBCH.

[0173] Further, in association with a cyclic shift, in the case of a DM-RS that links with a PSBCH in D2D (ProSe), one out of 8 cyclic shift values is determined by executing modulo8 (mod 8) on a value obtained by dividing n_{ID}^{SL} by 2,

whereas, in the case of a DM-RS that links with a PSBCH in V2X, one out of 8 cyclic shift values is determined by executing modulo8 (mod 8) on a value obtained by dividing $n_{SL_ID}^{SL}$ by 3. Further, in the case of a DM-RS that links with a PSBCH in D2D (ProSe), one out of 30 sequence shift patterns f_{ss} in group hopping is determined by executing modulo30 (mod 30) on a value obtained by dividing $n_{SL_ID}^{SL}$ by 16, whereas, in the case of a DM-RS that links with a PSBCH in V2X, one out of 30 sequence shift patterns f_{ss} in group hopping is determined by executing modulo 30 (mod 30) on a value obtained by dividing $n_{SL_ID}^{SL}$ by 24.

[0174] Here, each embodiment may be applied differently to each channel (PSCCH, PSSCH, PSDCH, and PSBCH) that links with a DM-RS. For example, in the case of a DM-RS that links with a PSCCH and a PSSCH, one of Method 4 for an orthogonal sequence (OCC) and a cyclic shift, Method 5 for an orthogonal sequence (OCC) and a cyclic shift, and Method 6 for an orthogonal sequence (OCC) and a cyclic shift may be used. However, in the case of a DM-RS that links with a PSBCH, one of Method 7 for an orthogonal sequence (OCC) and a cyclic shift, Method 8 for an orthogonal sequence (OCC) and a cyclic shift, and Method 9 for an orthogonal sequence (OCC) and a cyclic shift may be used.

[0175] In addition, $n_{CS,\lambda}$ is determined to be one out of a total of eight values which are determined through Table 6 to Table 19. In this instance, only $0, \pi/6, \pi/3, \pi/2, 4\pi/6, 5\pi/6, \pi, \text{ and } 7\pi/6$ are used based on $\alpha_\lambda = 2\pi n_{CS,\lambda}/12$ and thus, the value is not equally allocated with respect to 360 degrees, which is a drawback. Therefore, through Table 20 or 21, one out of a total of 8 cyclic shift values which are determined through Table 6 to Table 19 may indicate $n_{DMRS}^{(1)}$ or $n_{DMRS,\lambda}^{(2)}$ in Equation 5 below, as opposed to indicating $n_{CS,\lambda}$. That is, when a total of 8 cyclic shifts determined through Table 6 to Table 19 are 0, 1, 2, 3, 4, 5, 6, and 7, respectively, this indicates that $n_{CS,\lambda}$ value is 0, 1, 2, 3, 4, 5, 6, and 7, respectively. According to Table 20 in the present disclosure, when a total of 8 cyclic shifts determined through Table 6 to Table 19 are 0, 1, 2, 3, 4, 5, 6, and 7, respectively, this indicates that $n_{DMRS}^{(1)}$ value is 0, 2, 3, 4, 6, 8, 9, and 10, respectively. According to Table 21 in the present disclosure, when a total of 8 cyclic shifts determined through Table 6 to Table 19 are 0, 1, 2, 3, 4, 5, 6, and 7, respectively, this indicates that $n_{DMRS,\lambda}^{(2)}$ value is 0, 6, 3, 4, 2, 8, 10, and 9, respectively.

MathFigure 5

[Math.5]

$$n_{CS,\lambda} = \left(n_{DMRS}^{(1)} + n_{DMRS,\lambda}^{(2)} + n_{PN}(n_s) \right) \text{ mod } 12$$

Table 20

[0176]

[Table 20]

cyclicShift	$n_{DMRS}^{(1)}$
0	0
1	2
2	3
3	4
4	6
5	8
6	9
7	10

Table 21

[0177]

[Table 21]

cyclicShift	$n_{DMRS,\lambda}^{(2)}$
0 (000)	0
1 (001)	6
2 (010)	3
3 (011)	4
4 (100)	2
5 (101)	8
6 (110)	10
7 (111)	9

[0178] Here, the following cases are possible with respect to $n_{CS,\lambda}$ that is determined by adding three values, that is, $n_{DMRS}^{(1)}$, $n_{DMRS,\lambda}^{(2)}$, and n_{PN} , as shown in Equation 5, and executing modulo12 (mod 12) on the sum.

1) the case #1 in which a total of 8 cyclic shifts determined through Table 6 to Table 19 indicate $n_{DMRS}^{(1)}$

- determine $n_{DMRS}^{(1)}$ based on Table 20
- $n_{DMRS,\lambda}^{(2)}=0$
- $n_{PN}=0$

2) the case #1 in which a total of 8 cyclic shifts determined through Table 6 to Table 19 indicate $n_{DMRS,\lambda}^{(2)}$

- $n_{DMRS}^{(1)}=0$
- determine $n_{DMRS,\lambda}^{(2)}$ based on Table 21
- $n_{PN}=0$

3) the case #1 in which a total of 8 cyclic shifts determined through Table 6 to Table 19 indicate $n_{DMRS}^{(1)}$ and $n_{DMRS,\lambda}^{(2)}$

- determine $n_{DMRS}^{(1)}$ based on Table 20
- determine $n_{DMRS,\lambda}^{(2)}$ based on Table 21
- $n_{PN}=0$

4) the case #2 in which a total of 8 cyclic shifts determined through Table 6 to Table 19 indicate $n_{DMRS}^{(1)}$

- determine $n_{DMRS}^{(1)}$ based on Table 20
- $n_{DMRS,\lambda}^{(2)}=0$
- n_{PN} is generated to be different for each DM-RS transmission symbol

5) the case #2 in which a total of 8 cyclic shifts determined through Table 6 to Table 19 indicate $n_{DMRS,\lambda}^{(2)}$

- $n_{DMRS}^{(1)}=0$
- determine $n_{DMRS,\lambda}^{(2)}$ based on Table 21
- n_{PN} is generated to be different for each DM-RS transmission symbol

6) the case #2 in which a total of 8 cyclic shifts determined through Table 6 to Table 19 indicate $n_{DMRS}^{(1)}$ and $n_{DMRS,\lambda}^{(2)}$

- determine $n_{DMRS}^{(1)}$ based on Table 20
- determine $n_{DMRS,\lambda}^{(2)}$ based on Table 21
- n_{PN} is generated to be different for each DM-RS transmission symbol

[0179] Among the above described cases, the case that generates n_{PN} to be different for each DM-RS transmission symbol is to avoid an identical DM-RS that is transmitted by a terminal in a single subframe (or transmission time interval

(TTI)) by taking into consideration the environment where a terminal has a very quick movement speed. Methods to achieve the above will be described as follows.

[0180] Hereinafter, method 1 for generating n_{PN} to be different for each DM-RS transmission symbol is defined.

[0181] According to method 1, as shown in Equation 6 below, by taking into consideration the case in which a DM-RS is generated in two symbols in a single slot which corresponds to the part (a) of FIG. 5 or the part (b) of FIG. 5, two different n_{PN} values are defined in each slot, irrespective of the locations of the two symbols. One n_{PN} value is applied to a first symbol through which a DM-RS is transmitted in the slot, and the other n_{PN} value is applied to a second symbol through which the DM-RS is transmitted in the slot. In this instance, $l'=0$ or 1.

MathFigure 6

[Math.6]

$$n_{PN}(n_s) = \sum_{i=0}^7 c(16n_{ss}^{PSSCH} + 8l' + i) \cdot 2^i$$

[0182] Here, $c(i)$ denotes a pseudo-random sequence defined as Gold sequence having a degree of 31.

[0183] A pseudo-random sequence generator for the pseudo-random sequence is initialized to

$$c_{init} = \left\lfloor \frac{n_{ID}^{SA}}{30} \right\rfloor \cdot 2^5 + (n_{ID}^{SA} \bmod 30)$$

at the beginning of each slot that satisfies $n_{ss}^{PSSCH}=0$.

[0184] In this instance, n_{ss}^{PSSCH} denotes a current slot number in the subframe pool for a sidelink. Here, n_{ID}^{SA} may be a sidelink group destination identity.

[0185] Hereinafter, method 2 for generating n_{PN} to be different for each DM-RS transmission symbol is defined.

[0186] According to method 2, as shown in Equation 7 below, by taking into consideration the case in which a DM-RS is generated in three symbols in a single subframe which corresponds to the part (c) of FIG. 5, three different n_{PN} values are defined in each subframe, irrespective of the locations of the three symbols. A first n_{PN} value is applied to a first symbol through which a DM-RS is transmitted in the subframe, a second n_{PN} value is applied to a second symbol through which the DM-RS is transmitted in the subframe, and a third n_{PN} value is applied to a third symbol through which the DM-RS is transmitted in the subframe. In this instance, $l'=0, 1, \text{ or } 2$.

MathFigure 7

[Math.7]

$$n_{PN}(n_s) = \sum_{i=0}^7 c(8l' + i) \cdot 2^i$$

[0187] Also, $c(i)$ denotes a pseudo-random sequence defined as Gold sequence having a degree of 31.

[0188] The pseudo-random sequence generator for the pseudo-random sequence is initialized

$$c_{init} = \left\lfloor \frac{N_{ID}^{SL}}{30} \right\rfloor \cdot 2^5 + (N_{ID}^{SL} \bmod 30)$$

or

$$c_{\text{init}} = \left\lfloor \frac{\lfloor N_{\text{ID}}^{\text{SL}} / 24 \rfloor}{30} \right\rfloor \cdot 2^5 + \left(\lfloor N_{\text{ID}}^{\text{SL}} / 24 \rfloor \bmod 30 \right)$$

, at the beginning of each PSBCH subframe (a subframe that transmits a PSBCH).

[0189] Here, $N_{\text{ID}}^{\text{SL}}$ may be a physical layer sidelink synchronization identity.

[0190] Hereinafter, method 3 for generating n_{PN} to be different for each DM-RS transmission symbol is defined.

[0191] Method 3 defines a different n_{PN} value for each of the symbols in a single slot, and a corresponding symbol through which a DM-RS is transmitted applies a corresponding n_{PN} value of the symbol, as shown in Equation 8 below. This may be applied to all of the cases illustrated through FIGs. 5A, 5B, and 5C. In this instance, $N_{\text{symbol}}^{\text{SL}}$ denotes the number of symbols in a single slot in a sidelink (SL) (7 in the case of a normal CP, and 6 in the case of an extended CP), and $l=0,1,\dots,N_{\text{symbol}}^{\text{SL}}$ denotes a symbol index in a single slot.

MathFigure 8

[Math.8]

$$n_{\text{PN}}(n_s) = \sum_{i=0}^7 c(8 \cdot N_{\text{symbol}}^{\text{SL}} \cdot n_s + 8l + i) \cdot 2^i$$

[0192] In Equation 8, n_s is $n_{\text{ss}}^{\text{PSSCH}}$ when the equation is applied to a DM-RS for a PSSCH.

[0193] However, in Equation 8, n_s may have two values, that is, 0 or 1, when the equation is applied to a DM-RS for a PSBCH.

[0194] Also, $c(i)$ denotes a pseudo-random sequence defined as Gold sequence having a degree of 31.

[0195] When Equation 8 is applied to the DM-RS for the PSSCH, the pseudo-random sequence generator for the pseudo-random sequence may be initialized to

$$c_{\text{init}} = \left\lfloor \frac{n_{\text{ID}}^{\text{SA}}}{30} \right\rfloor \cdot 2^5 + \left(n_{\text{ID}}^{\text{SA}} \bmod 30 \right)$$

at the beginning of each slot that satisfies $n_{\text{ss}}^{\text{PSSCH}}=0$.

[0196] When Equation 8 is applied to the DM-RS for the PSBCH, the pseudo-random sequence generator for the pseudo-random sequence may be initialized

$$c_{\text{init}} = \left\lfloor \frac{N_{\text{ID}}^{\text{SL}}}{30} \right\rfloor \cdot 2^5 + \left(N_{\text{ID}}^{\text{SL}} \bmod 30 \right)$$

or

$$c_{\text{init}} = \left\lfloor \frac{\lfloor N_{\text{ID}}^{\text{SL}} / 24 \rfloor}{30} \right\rfloor \cdot 2^5 + \left(\lfloor N_{\text{ID}}^{\text{SL}} / 24 \rfloor \bmod 30 \right)$$

, at the beginning of every PSBCH subframe (a subframe that transmits a PSBCH).

[0197] In this instance, n_{ss}^{PSSCH} denotes a current slot number in the subframe pool for a sidelink. Here, n_{ID}^{SA} is a sidelink group destination identity, and n_{ID}^{SL} is a physical layer sidelink synchronization identity.

[0198] FIG. 6 is a block diagram illustrating a wireless communication system according to an embodiment of the present invention.

[0199] Referring to FIG. 6, the UE 300 includes a processor 310, a radio frequency (RF) module 305 and a memory 315. The memory 315 is connected to the processor 310 and stores various pieces of information to drive the processor 310. The RF module 305 is connected to the processor 310 and transmits and/or receives a radio signal. For example, the RF module 305 receives an upper-layer message, such as a RRC (connection reconfiguration) message, and a SIB message from the BS 350. Also the RF module 305 transmits an uplink signal according to an embodiment of the present invention. The processor 310 implements functions, processes and/or methods of the UE suggested in FIGS. 2 to 5 in the present specification. The memory 315 may store with various values calculated by using equations and tables, and provide inputs to the processor 310 based on request of order from the processor 310.

[0200] The BS 350 includes a processor 355, a Radio Frequency (RF) module 365, and a memory 360. The memory 360 is connected to the processor 355, and stores various pieces of information for driving the processor 355. The RF module 365 is connected with the processor 355, and transmits and/or receives a wireless signal. The processor 355 implements functions, processes and/or methods of the BS suggested in FIGS. 2 to 5 in the present specification.

[0201] Exemplary embodiments of the present invention may be implemented by hardware, software or a combination thereof. In a hardware configuration, the above-described functions and operations may be performed by one or more processors, such as a microprocessor, a controller, a microcontroller, or an ASIC (Application Specific Integrated Circuit), a DSP (Digital Signal Processor), a PLD (Programmable logic device), a FPGA (Field Programmable Gate Array), and/or combinations thereof configured to perform the functions and operations. In a software configuration, software or program codes to perform the functions and operations may be implemented as modules. Software may be stored in one or more memory units and may be executed by the one or more processors. It will be apparent to those of ordinary skill in the art from the description of the present invention to design, develop and implement the memory units or the processors.

[0202] A processor according to an embodiment of the present disclosure may determine group hopping, an orthogonal sequence (OCC), and a cyclic shift value based on the methods described through Table 6 to Table 19.

[0203] Also, in the case of a cyclic shift $n_{CS,\lambda}$ value in Table 6 to Table 19, the processor may execute a control so that at least one out of $n_{DMRS}^{(1)}$ or $n_{DMRS,\lambda}^{(2)}$ in Equation 5 is indicated through Table 20 or Table 21 as opposed to applying the methods described through Table 6 to Table 19 as they are, by taking into consideration a drawback in that only 0, $\pi/6$, $\pi/3$, $\pi/2$, $4\pi/6$, $5\pi/6$, π , and $7\pi/6$ are used based on

$$\alpha_{\lambda} = 2\pi n_{CS,\lambda} / 12$$

and the value is not equally allocated with respect to 360 degrees.

[0204] According to one or more exemplary embodiments, an apparatus and method for transmitting Demodulation-Reference Signal (DM-RS) configured for Vehicle-to-everything (V2X) communication are provided. An apparatus may be equipped in a vehicle, such as a car, motorcycle, and the like. However, the apparatus may be equipped in other devices configured for a V2X communication.

[0205] The apparatus may include a processor, a memory, and a wireless transceiver including an RF module and an antenna. The processor may generate a first DM-RS for V2X communication and a second DM-RS for V2X communication, the first DM-RS for V2X communication being mapped in a first symbol in a first slot of a subframe, the second DM-RS for V2X communication being mapped in a second symbol in the first slot of the subframe, and generate a third DM-RS for V2X communication and a fourth DM-RS for V2X communication, the third DM-RS for V2X communication being mapped in a first symbol in a second slot of the subframe, the fourth DM-RS for V2X communication being mapped in a second symbol in the second slot of the subframe. The mapping processes may be performed by the processor.

[0206] The processor may control a wireless transceiver to transmit the first DM-RS for V2X communication, the second DM-RS for V2X communication, the third DM-RS for V2X communication, and the fourth DM-RS for V2X communication to another device through a V2X communication. If a group-hopping is enabled, the first DM-RS for V2X communication may be generated based on a first group-hopping and the second DM-RS for V2X communication may be generated based on a second group-hopping.

[0207] The first group-hopping may be associated with a first equation,

$$\left(\sum_{i=0}^7 c(16n_{ss}^{PSSCH} + i) \cdot 2^i \right) \text{mod} 30$$

, and the second group-hopping may be associated with a second equation,

$$\left(\sum_{i=0}^7 c(16n_{SS}^{PSSCH} + 8 + i) \cdot 2^i\right) \bmod 30$$

5 . Here, $c(x)$ for the first equation and the second equation denotes a pseudo-random sequence that is defined as a length-31 Gold sequence and n_{SS}^{PSSCH} denotes a current slot number in a subframe pool for a sidelink. $n_{SS}^{PSSCH}=k$ for the first slot of the subframe and $n_{SS}^{PSSCH}=k+1$ for the second slot of the subframe, where k is a non-negative integer. The number k may be one of 0, 2, 4, ..., 18 of ten subframes arranged in the subframe pool.

10 **[0208]** The processor may apply first orthogonal sequence [+1 +1 +1 +1] or second orthogonal sequence [+1 -1 +1 -1] in association with the first, second, third, and fourth DM-RSs for V2X communication. The first orthogonal sequence [+1 +1 +1 +1] may be configured to be applied when a modulo-2 operation of an identifier is equal to zero, and the second orthogonal sequence [+1 -1 +1 -1] may be configured to be applied when the modulo-2 operation of the identifier is equal to one.

15 **[0209]** According to one or more exemplary embodiments, each of the first slot and the second slot consists of seven symbols, respectively (normal cyclic prefix). As shown in the part (b) of FIG. 5, when the first slot precedes the second slot in a time axis, the first symbol in the first slot is symbol #2 and the second symbol in the first slot is symbol #5 if the seven symbols in the first slot are arranged from symbol #0 to symbol #6, and the first symbol in the second slot is symbol #1 and the second symbol in the second slot is symbol #4 if the seven symbols in the second slot are arranged from symbol #0 to symbol #6.

20 **[0210]** According to one or more exemplary embodiments, an apparatus and method for receiving Demodulation-Reference Signal (DM-RS) configured for Vehicle-to-everything (V2X) communication are provided. An apparatus may be equipped in a vehicle, such as a car, motorcycle, and the like. However, the apparatus may be equipped in other devices configured for a V2X communication.

25 **[0211]** The apparatus may include a processor, a memory, and a wireless transceiver including an RF module and an antenna. The processor may receive and decode a first DM-RS for V2X communication and a second DM-RS for V2X communication, the first DM-RS for V2X communication being mapped in a first symbol in a first slot of a subframe, the second DM-RS for V2X communication being mapped in a second symbol in the first slot of the subframe, and receive and decode a third DM-RS for V2X communication and a fourth DM-RS for V2X communication, the third DM-RS for V2X communication being mapped in a first symbol in a second slot of the subframe, the fourth DM-RS for V2X communication being mapped in a second symbol in the second slot of the subframe.

30 **[0212]** The processor may generate a first DM-RS for comparison, a second DM-RS for comparison, a third DM-RS for comparison, and a fourth DM-RS for comparison. The generation may be performed based on the equations described above. The processor may compare the first DM-RS for V2X communication with the first DM-RS for comparison, compare the second DM-RS for V2X communication with the second DM-RS for comparison, compare the third DM-RS for V2X communication with the third DM-RS for comparison, and compare the fourth DM-RS for V2X communication with the fourth DM-RS for comparison. The first DM-RS for comparison may be generated based on the first group-hopping, and the second DM-RS for comparison may be generated based on the second group-hopping.

35 **[0213]** While the present invention has been shown and described in connection with the embodiments, it will be apparent to those skilled in the art that modifications and variations can be made. Thus, the present invention is not limited to the foregoing embodiments and may include all the embodiments within the scope of the appended claims.

Industrial Applicability

45 **[0214]** For example, various exemplary embodiments have been described with respect to 3GPP LTE or LTE-A systems; however, aspects of the illustrated embodiments may be applied to other mobile communication systems.

[0215] The subject matter of the following paragraphs [274] to [286] that are numbered for allowing references forms part of the disclosure of the present document. It is emphasized that the following paragraphs are part of the description and shall not be confused with the claims that are defining the scope of protection. The claims start on the page labeled "claims".

Claims

55 1. A user equipment, UE (300, 350), comprising:

a processor (310, 355) configured to:

determine, based on a first group-hopping pattern

$$\left(\sum_{i=0}^7 c(16n_{ss}^{PSSCH} + i) \cdot 2^i\right) \bmod 30,$$

a first group-hopping for a first symbol in a slot of a physical sidelink shared channel, PSSCH, where

n_{ss}^{PSSCH} is a number of the slot,

$c(x)$ denotes a pseudo-random sequence that is defined by a length-31 Gold sequence and initialized

at the beginning of a slot of the PSSCH that satisfies $n_{ss}^{PSSCH} = 0$, and

$x = 16n_{ss}^{PSSCH} + i$ for the first symbol in the slot; and

determine, based on a second group-hopping pattern

$$\left(\sum_{i=0}^7 c(16n_{ss}^{PSSCH} + 8 + i) \cdot 2^i\right) \bmod 30,$$

a second group-hopping for a second symbol in the slot, where

$c(x)$ denotes the pseudo-random sequence, and

$x = 16n_{ss}^{PSSCH} + 8 + i$ for the second symbol in the slot; and

a wireless transceiver comprising an antenna and configured to:

transmit, based on the first group-hopping for the first symbol in the slot, a first demodulation reference signal, DM-RS, for vehicle-to-everything, V2X, communication; and
transmit, based on the second group-hopping for the second symbol in the slot, a second DM-RS for V2X communication.

2. The UE (300, 350) of claim 1, wherein the processor (310, 355) is configured to:

determine, based on a third group-hopping pattern

$$\left(\sum_{i=0}^7 c(16n_{ss}'^{PSSCH} + i) \cdot 2^i\right) \bmod 30,$$

a third group-hopping for a first symbol in a second slot of the PSSCH, where $n_{ss}'^{PSSCH}$ is a number of the second slot; and

determine, based on a fourth group-hopping pattern

$$\left(\sum_{i=0}^7 c(16n_{ss}'^{PSSCH} + 8 + i) \cdot 2^i\right) \bmod 30,$$

a fourth group-hopping for a second symbol in the second slot; and wherein the wireless transceiver comprising an antenna and configured to:

transmit, based on the third group-hopping for the first symbol in the second slot, a third DM-RS for V2X communication; and

transmit, based on the fourth group-hopping for the second symbol in the second slot, a fourth DM-RS for V2X communication.

3. The UE (300, 350) of claim 2,

wherein the n_{ss}^{PSSCH} denotes a slot number of the slot in a subframe pool for sidelink, and
 wherein the n'_{ss}^{PSSCH} denotes a slot number of the second slot in the subframe pool for sidelink.

4. The UE (300, 350) of any one of claims 2 or 3,

wherein a subframe comprises the slot and the second slot, and
 wherein $n_{ss}^{PSSCH} = k$ for the slot in the subframe, and $n'_{ss}^{PSSCH} = k + 1$ for the second slot in the subframe,
 where k is a non-negative integer.

5. The UE (300, 350) of any one of claims 2 to 4, wherein:
 each of the first DM-RS, the second DM-RS, the third DM-RS, and the fourth DM-RS is associated with first orthogonal
 sequence [+1 +1 +1 +1] or second orthogonal sequence [+1 -1 +1 -1].

6. The UE (300, 350) of claim 5,

wherein the first orthogonal sequence [+1 +1 +1 +1] is configured to be applied if a modulo-2 operation of an
 identifier is equal to zero, or
 wherein the second orthogonal sequence [+1 -1 +1 -1] is configured to be applied if the modulo-2 operation of
 the identifier is equal to one.

7. The UE (300, 350) of any one of claims 2 to 6,

wherein each of the slot and the second slot consists of seven symbols, respectively, and
 wherein the slot precedes the second slot in a time axis.

8. The UE (300, 350) of claim 7, wherein the first symbol in the slot is symbol #2 and the second symbol in the slot is
 symbol #5 if the seven symbols in the slot are arranged from symbol #0 to symbol #6.

9. The UE (300, 350) of any one of claims 7 or 8, wherein the first symbol in the second slot is symbol #1 and the
 second symbol in the second slot is symbol #4 if the seven symbols in the second slot are arranged from symbol
 #0 to symbol #6.

10. The UE (300, 350) of any one of claims 2 to 9, wherein the processor (310, 355) is configured to:

determine to transmit demodulation reference signals, DM-RSs, associated with a V2X channel; and
 determine, for mapping the first, second, third, and fourth DM-RSs, a plurality of symbols in the slot and the
 second slot.

11. The UE (300, 350) of any one of claims 1 to 10, wherein the processor (310, 355) is configured to:
 determine whether a group-hopping for DM-RSs associated with a V2X channel is enabled.

12. The UE (300, 350) of any one of claims 1 to 11,

wherein the wireless transceiver comprises a radio frequency unit coupled to the processor (310, 355), and
 wherein the radio frequency unit is configured to transmit an uplink signal to a base station.

13. The UE (300, 350) of claim 12,
 wherein the radio frequency unit is configured to receive at least one of:

a radio resource control, RRC, connection reconfiguration message; or
 a system information block, SIB.

14. A system comprising:

the UE (300, 350) according to any one of claims 1 to 13;
 a base station configured to communicate a message with the UE (300, 350);
 a second UE (300, 350) configured to receive at least one of:

5 the first DM-RS for V2X communication; or
 the second DM-RS for V2X communication.

10 **Patentansprüche**

1. Benutzergerät, UE (300, 350), aufweisend:

einen Prozessor (310, 355), der ausgebildet ist

15 zum Bestimmen eines ersten Group-Hoppings für ein erstes Symbol in einem Slot eines Physical Sidelink Shared Channel, PSSCH, auf der Grundlage eines ersten Group-Hopping Patterns

$$\left(\sum_{i=0}^7 c(16n_{ss}^{PSSCH} + i) \cdot 2^i\right) \bmod 30,$$

20 wobei

n_{ss}^{PSSCH} eine Slot-Nummer ist,

25 $c(x)$ eine Pseudozufallsfolge bezeichnet, die durch eine Länge-31-Gold-Folge definiert ist, und zu Beginn eines Slots desjenigen PSSCH initialisiert wird, welcher $n_{ss}^{PSSCH} = 0$ erfüllt, und

für das erste Symbol im Slot gilt: $x = 16n_{ss}^{PSSCH} + i$; und

30 zum Bestimmen eines zweiten Group-Hoppings für ein zweites Symbol in dem Slot auf der Grundlage eines zweiten Group-Hopping Patterns

$$\left(\sum_{i=0}^7 c(16n_{ss}^{PSSCH} + 8 + i) \cdot 2^i\right) \bmod 30,$$

35 wobei

$c(x)$ die Pseudozufallsfolge bezeichnet, und

40 für das zweite Symbol im Slot gilt: $x = 16n_{ss}^{PSSCH} + 8 + i$; und

einen Drahtlos-Transceiver, der eine Antenne aufweist und ausgebildet ist zum

45 Übertragen eines ersten Demodulations-Referenzsignals, DM-RS, für Fahrzeug-zu-Allem-, V2X-, -Kommunikation, auf der Grundlage des ersten Group-Hoppings für das erste Symbol in dem Slot; und Übertragen eines zweiten DM-RS für V2X-Kommunikation, auf der Grundlage des zweiten Group-Hoppings für das zweite Symbol in dem Slot.

2. Das UE (300, 350) nach Anspruch 1, wobei der Prozessor (310, 355) weiter ausgebildet ist

50 zum Bestimmen eines dritten Group-Hoppings für ein erstes Symbol in einem zweiten Slot des PSSCH, auf der Grundlage eines dritten Group-Hopping Patterns

$$\left(\sum_{i=0}^7 c(16n_{ss}'^{PSSCH} + i) \cdot 2^i\right) \bmod 30,$$

55 wobei

$n_{ss}^{\prime PSSCH}$ eine Nummer des zweiten Slots ist; und
zum Bestimmen eines vierten Group-Hoppings für ein zweites Symbol in dem zweiten Slot auf der Grundlage eines vierten Group-Hopping Patterns

$$\left(\sum_{i=0}^7 c(16n_{ss}^{\prime PSSCH} + 8 + i) \cdot 2^i\right) \bmod 30;$$

und

wobei der Drahtlos-Transceiver eine Antenne aufweist und ausgebildet ist

zum Übertragen eines dritten DM-RS für V2X-Kommunikation, auf der Grundlage des dritten Group-Hoppings für das erste Symbol im zweiten Slot; und

zum Übertragen eines vierten DM-RS für V2X-Kommunikation, auf der Grundlage des vierten Group-Hoppings für das zweite Symbol im zweiten Slot.

3. Das UE (300, 350) nach Anspruch 2,

wobei n_{ss}^{PSSCH} eine Slot-Nummer des Slot in einem Subframe Pool für Sidelink bezeichnet, und

wobei $n_{ss}^{\prime PSSCH}$ eine Slot-Nummer des zweiten Slot in dem Subframe Pool für Sidelink bezeichnet.

4. Das UE (300, 350) nach Anspruch 2 oder 3,

wobei ein Subframe den Slot und den zweiten Slot aufweist, und

wobei für den Slot in dem Subframe $n_{ss}^{PSSCH} = k$ und für den zweiten Slot in dem Subframe

$n_{ss}^{\prime PSSCH} = k + 1$ gilt, wobei k eine nicht-negative ganze Zahl ist.

5. Das UE (300, 350) nach einem der Ansprüche 2 bis 4, wobei

das erste DM-RS, das zweite DM-RS, das dritte DM-RS und das vierte DM-RS alle mit einer ersten orthogonalen Sequenz [+1 +1 +1 +1] oder einer zweiten orthogonalen Sequenz [+1 -1 +1 -1] verknüpft ist.

6. Das UE (300, 350) nach Anspruch 5,

wobei die erste orthogonale Sequenz [+1 +1 +1 +1] ausgebildet ist, angewendet zu werden, wenn eine Modulo-2-Operation eines Identifizierers gleich Null ist, oder

wobei die zweite orthogonale Sequenz [+1 -1 +1 -1] ausgebildet ist, angewendet zu werden, wenn die Modulo-2-Operation des Identifizierers gleich Eins ist.

7. Das UE (300, 350) nach einem der Ansprüche 2 bis 6,

wobei sowohl der Slot als auch der zweite Slot jeweils aus sieben Symbolen bestehen, und wobei der Slot dem zweiten Slot in einer Zeitachse vorausgeht.

8. Das UE (300, 350) nach Anspruch 7, wobei das erste Symbol in dem Slot das Symbol # 2 und das zweite Symbol in dem Slot das Symbol # 5 ist, wenn die sieben Symbole in dem Slot von Symbol # 0 bis Symbol # 6 angeordnet sind.

9. Das UE (300, 350) nach einem der Ansprüche 7 oder 8, wobei das erste Symbol in dem zweiten Slot das Symbol # 1 und das zweite Symbol in dem zweiten Slot das Symbol # 4 ist, wenn die sieben Symbole in dem zweiten Slot von Symbol # 0 bis Symbol # 6 angeordnet sind.

10. Das UE (300, 350) nach einem der Ansprüche 2 bis 9, wobei der Prozessor (310, 355) ausgebildet ist

zum Bestimmen, einem V2X-Kanal zugeordnete Demodulations-Referenzsignale DM-RSs, zu übertragen; und zum Bestimmen, mehrere Symbole im Slot und dem zweiten Slot für das erste, zweite, dritte und vierte DM-

RS zu mappen.

11. Das UE (300, 350) nach einem der Ansprüche 1 bis 10, wobei der Prozessor (310, 355) ausgebildet ist zum Bestimmen, ob ein Group-Hopping für ein einem V2X-Kanal zugeordnetes DM-RSs aktiviert ist.

12. Das UE (300, 350) nach einem der Ansprüche 1 bis 11,

wobei der Drahtlos-Transceiver eine mit dem Prozessor (310, 355) gekoppelte Radio-Frequenz-Einheit aufweist, und

wobei die Radio-Frequenz-Einheit ausgebildet ist, an eine Base-Station ein Uplink-Signal zu übertragen.

13. Das UE (300, 350) nach Anspruch 12, wobei die Radio-Frequenz-Einheit ausgebildet ist um wenigstens eines der folgenden zu empfangen:

eine Radio-Resource-Control-, RRC, -Verbindungs-Rekonfigurations-Nachricht; oder einen System-Information-Block, SIB.

14. System aufweisend:

das UE (300, 350) nach einem der Ansprüche 1 bis 13; eine Base-Station, die ausgebildet ist, eine Nachricht an das UE (300, 350) zu kommunizieren; ein zweites UE (300, 350), das dazu ausgebildet ist, mindestens eines der folgenden zu empfangen:

das erste DM-RS für V2X-Kommunikation; oder das zweite DM-RS für V2X-Kommunikation.

Revendications

1. Équipement utilisateur, UE (300, 350), comprenant: un processeur (310, 355) configuré pour :

déterminer, sur la base d'un premier motif de saut de groupe

$$\left(\sum_{i=0}^7 c(16n_{SS}^{PSSCH} + i) \cdot 2^i \right) \text{mod}30,$$

un premier saut de groupe pour un premier symbole dans une fente d'un canal partagé de liaison latérale physique, PSSCH,

où

n_{SS}^{PSSCH} est un numéro de la fente,

$c(x)$ désigne une séquence pseudo-aléatoire qui est définie par une séquence de Gold de longueur 31 et

initialisée au début d'une fente du PSSCH qui satisfait $n_{SS}^{PSSCH} = 0$, et

$x = 16n_{SS}^{PSSCH} + i$ pour le premier symbole dans la fente ; et

déterminer, sur la base d'un deuxième motif de saut de groupe

$$\left(\sum_{i=0}^7 c(16n_{SS}^{PSSCH} + 8 + i) \cdot 2^i \right) \text{mod}30,$$

un deuxième saut de groupe pour un deuxième symbole dans la fente,

où

$c(x)$ désigne la séquence pseudo-aléatoire, et

$x = 16n_{SS}^{PSSCH} + 8 + i$ pour le deuxième symbole dans la fente ; et

EP 3 767 906 B1

un émetteur-récepteur sans fil comprenant une antenne et configuré pour :

transmettre, sur la base du premier saut de groupe pour le premier symbole dans la fente, un premier signal de référence de démodulation, DM-RS, pour une communication de véhicule-à-tout, V2X ; et
 transmettre, sur la base du deuxième saut de groupe pour le deuxième symbole dans la fente, un deuxième DM-RS pour une communication V2X.

2. UE (300, 350) selon la revendication 1, dans lequel le processeur (310, 355) est configuré pour :

déterminer, sur la base d'un troisième motif de saut de groupe

$$\left(\sum_{i=0}^7 c(16n_{SS}^{iPSSCH} + i) \cdot 2^i \right) \bmod 30,$$

un troisième saut de groupe pour un premier symbole dans une seconde fente du PSSCH,

où n_{SS}^{iPSSCH} est un numéro de la seconde fente ; et

déterminer, sur la base d'un quatrième motif de saut de groupe

$$\left(\sum_{i=0}^7 c(16n_{SS}^{iPSSCH} + 8 + i) \cdot 2^i \right) \bmod 30,$$

un quatrième saut de groupe pour un deuxième symbole dans la seconde fente ; et dans lequel l'émetteur-récepteur sans fil comprend une antenne et est configuré pour :

transmettre, sur la base du troisième saut de groupe pour le premier symbole dans la seconde fente, un troisième DM-RS pour une communication V2X ; et

transmettre, sur la base du quatrième saut de groupe pour le deuxième symbole dans la seconde fente, un quatrième DM-RS pour une communication V2X.

3. UE (300, 350) selon la revendication 2,

dans lequel le n_{SS}^{PSSCH} désigne un numéro de fente de la fente dans un groupe de sous-trames pour la liaison latérale, et

dans lequel le n_{SS}^{iPSSCH} désigne un numéro de fente de la seconde fente dans le groupe de sous-trames pour la liaison latérale.

4. UE (300, 350) selon l'une quelconque des revendications 2 ou 3,

dans lequel une sous-trame comprend la fente et la seconde fente, et

dans lequel $n_{SS}^{PSSCH} = k$ pour la fente dans la sous-trame, et $n_{SS}^{iPSSCH} = k + 1$ pour la seconde fente dans la sous-trame, où k est un entier non négatif.

5. UE (300, 350) selon l'une quelconque des revendications 2 à 4, dans lequel : chacun parmi le premier DM-RS, le deuxième DM-RS, le troisième DM-RS et le quatrième DM-RS est associé à une première séquence orthogonale [+1 +1 +1 +1] ou à une seconde séquence orthogonale [+1 -1 +1 -1].

6. UE (300, 350) selon la revendication 5,

dans lequel la première séquence orthogonale [+1 +1 +1 +1] est configurée pour être appliquée si une opération modulo-2 d'un identifiant est égale à zéro, ou dans lequel la seconde séquence orthogonale [+1 -1 +1 -1] est configurée pour être appliquée si l'opération modulo-2 de l'identifiant est égale à un.

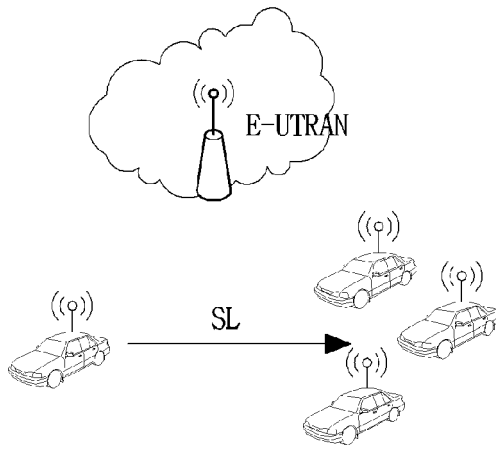
7. UE (300, 350) selon l'une quelconque des revendications 2 à 6,

EP 3 767 906 B1

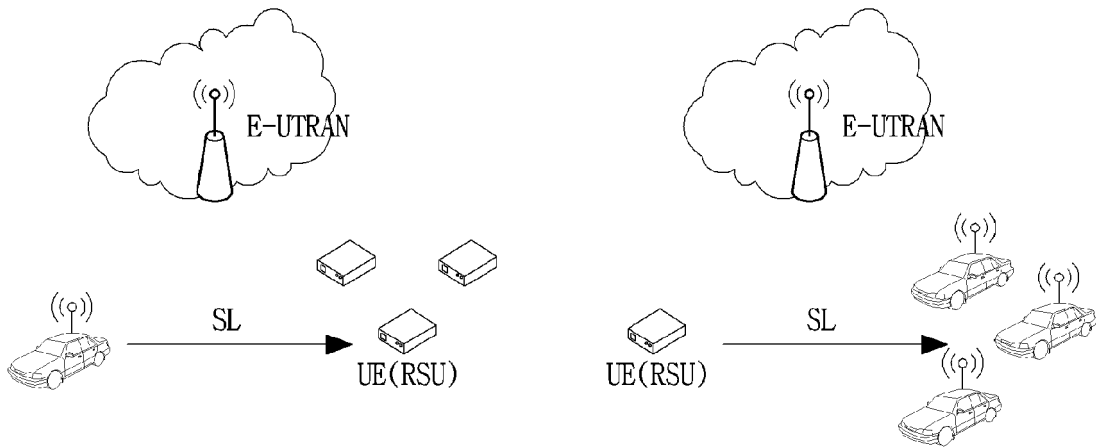
dans lequel chacune parmi la fente et la seconde fente est constituée de sept symboles, respectivement, et dans lequel la fente précède la seconde fente sur un axe temporel.

- 5
8. UE (300, 350) selon la revendication 7, dans lequel le premier symbole dans la fente est le symbole #2 et le deuxième symbole dans la fente est le symbole #5 si les sept symboles dans la fente sont agencés du symbole #0 au symbole #6.
- 10
9. UE (300, 350) selon l'une quelconque des revendications 7 ou 8, dans lequel le premier symbole dans la seconde fente est le symbole #1 et le deuxième symbole dans la seconde fente est le symbole #4 si les sept symboles dans la seconde fente sont agencés du symbole #0 au symbole #6.
- 15
10. UE (300, 350) selon l'une quelconque des revendications 2 à 9, dans lequel le processeur (310, 355) est configuré pour :
- déterminer le fait de transmettre des signaux de référence de démodulation, DM-RS, associés à un canal V2X ; et déterminer, pour la mise en correspondance des premier, deuxième, troisième et quatrième DM-RS, une pluralité de symboles dans la fente et la seconde fente.
- 20
11. UE (300, 350) selon l'une quelconque des revendications 1 à 10, dans lequel le processeur (310, 355) est configuré pour :
- déterminer si un saut de groupe pour des DM-RS associés à un canal V2X est activé.
- 25
12. UE (300, 350) selon l'une quelconque des revendications 1 à 11,
- dans lequel l'émetteur-récepteur sans fil comprend une unité de radiofréquence couplée au processeur (310, 355), et dans lequel l'unité de radiofréquence est configurée pour transmettre un signal de liaison montante à une station de base.
- 30
13. UE (300, 350) selon la revendication 12, dans lequel l'unité de radiofréquence est configurée pour recevoir :
- un message de reconfiguration de connexion de commande de ressources radio, RRC ; et/ou un bloc d'informations système, SIB.
- 35
14. Système comprenant :
- l'UE (300, 350) selon l'une quelconque des revendications 1 à 13 ;
une station de base configurée pour communiquer un message à l'UE (300, 350) ;
un second UE (300, 350) configuré pour recevoir :
- 40
- le premier DM-RS pour une communication V2X ; et/ou
le deuxième DM-RS pour une communication V2X.
- 45
- 50
- 55

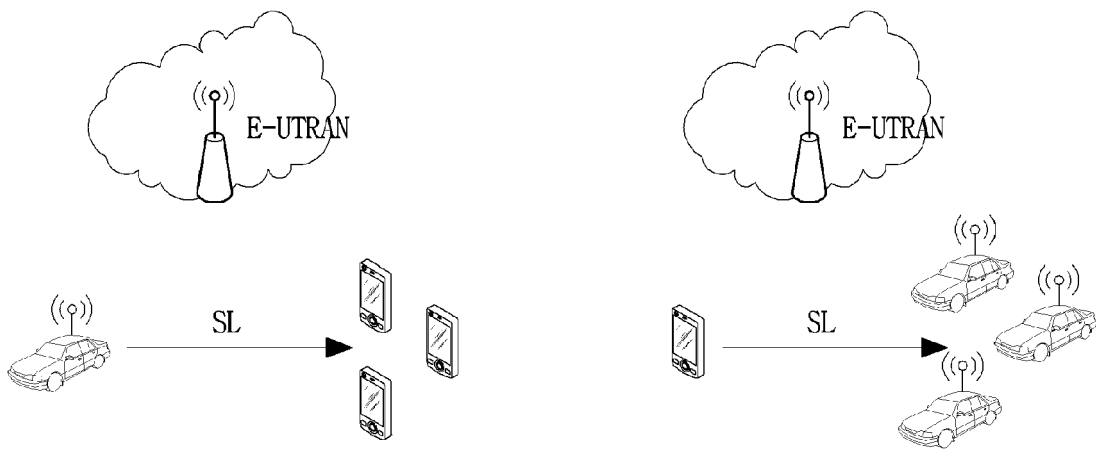
[Fig. 1]



(a) V2V OPERATION

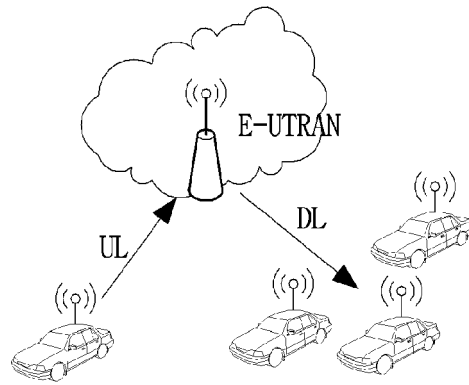


(b) V2I OPERATION

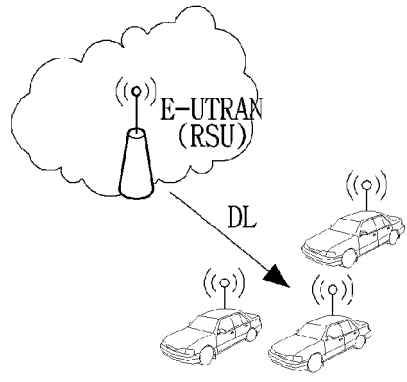
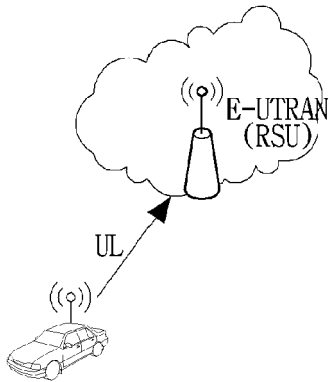


(c) V2P OPERATION

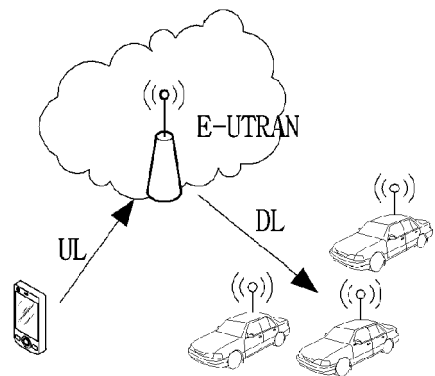
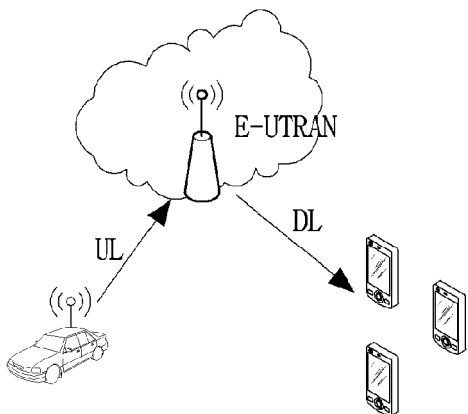
[Fig. 2]



(a) V2V OPERATION

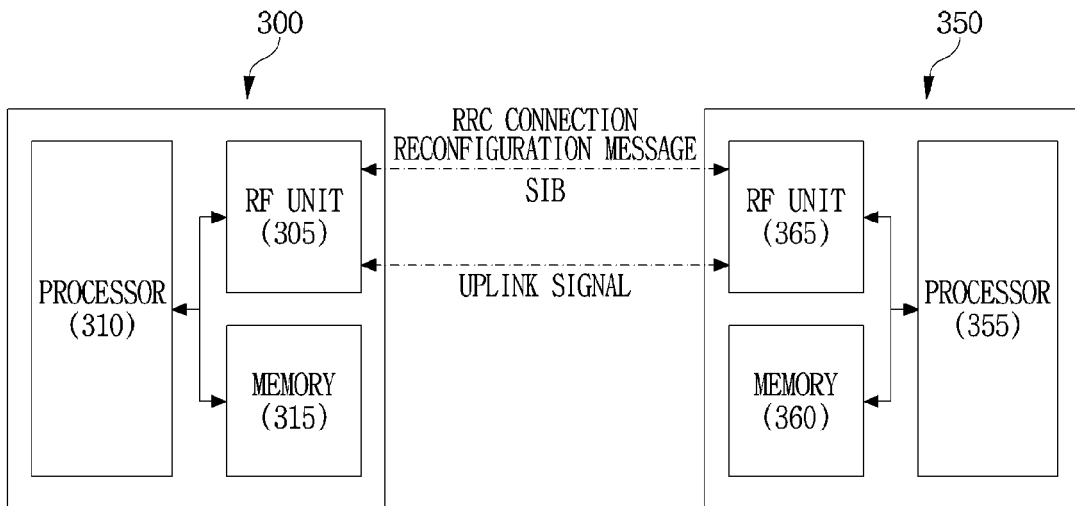


(b) V2I OPERATION



(c) V2P OPERATION

[Fig. 6]



REFERENCES CITED IN THE DESCRIPTION

This list of references cited by the applicant is for the reader's convenience only. It does not form part of the European patent document. Even though great care has been taken in compiling the references, errors or omissions cannot be excluded and the EPO disclaims all liability in this regard.

Patent documents cited in the description

- KR 1020150136017 [0002]
- KR 1020160058976 [0002]
- KR 1020160103277 [0002]
- WO 2015119478 A1 [0005]
- EP 2487853 A2 [0006]