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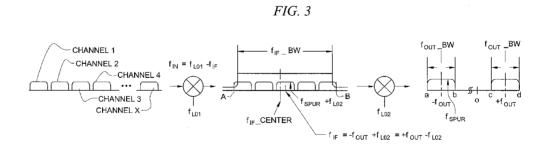
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(54) Title: SYSTEM AND METHOD OF ELIMINATING OR MINIMIZING LO-RELATED INTERFERENCE FROM TUNERS



(57) Abstract: Systems and methods of eliminating or reducing interference resulting from harmonics of local oscillator frequencies of mixers are shown. In one embodiment, a determination is made as to a zone or zones within an intermediate frequency band associated with undesired spurs. Inter-tuner spurs and intra-tuner spurs may be identified such that frequency information of the identified spurs may be utilized to define a plurality of exclusion zones. Local oscillator frequencies may subsequently be efficiently selected in view of the exclusion zone information. Embodiments may also determine a score for identified spurs which may be used to optimally select from within a set of spur-generating local oscillator frequencies.



WO 2008/148125 A1

SYSTEM AND METHOD OF ELIMINATING OR MINIMIZING LO-RELATED INTERFERENCE FROM TUNERS

CROSS-REFERENCE TO RELATED APPLICATIONS

[0001] The present application claims the benefit of priority to co-pending and commonly assigned United States provisional patent application serial number 60/941,213 entitled "System and Method of Eliminating or Minimizing Lo-Related Interference From Tuners," filed May 31, 2007, and is a continuation-in-part of co-pending, commonly assigned United States patent application serial number 10/952,185 entitled "System and Method of Eliminating or minimizing Lo-Related Interference From Tuners," filed September 28, 2004, the disclosures of which are hereby incorporated herein by reference. The present invention is related to co-pending and commonly assigned United States patent applications serial number 11/325,854 entitled "System and Method for Discovering Frequency Related Spurs in a Multi-Conversion Tuner," filed January 5, 2006, serial number 08/904,693, now patent number 6,725,463, entitled "Dual Mode Tuner for Co-Existing Digital and Analog Television Signals," filed August 1, 1997, and serial number 11/486,706 entitled "Broadband Integrated Tuner," filed June 29, 2006, the disclosures of which are hereby incorporated herein by reference.

TECHNICAL FIELD

[0002] The invention generally relates to tuner circuits and more particularly to the identification and/or elimination or reduction of spurious signals in multiple conversion tuners.

BACKGROUND OF THE INVENTION

[0003] In a tuner or frequency converter (such as, for example, a dual-conversion tuner), an incoming signal at frequency $f_{\rm IN}$ is mixed with a signal at frequency $f_{\rm LO1}$ from a local oscillator (LO) to produce a signal at an intermediate frequency $f_{\rm IF}$. This signal may then be mixed with a signal at frequency $f_{\rm LO2}$ from a second local oscillator signal to produce the desired output frequency $f_{\rm OUT}$, in a dual conversion tuner configuration. This process is illustrated in FIGURE 1A, which is a portion of one example of a tuner (shown as RF converter 10) showing how the $f_{\rm LO}$ signals (provided by LO 12 and 13) are mixed. Such a tuner is shown in U.S. Patent No. 5,737,035, issued April 7, 1998 hereby incorporated by reference herein. Typically, but not always, the frequency of first local oscillator, e.g., LO 12, is greater than that of second local oscillator, e.g., LO 13. That is, generally $f_{\rm LO1} > f_{\rm LO2}$. Accordingly, reference shall be made herein to equations in which it is assumed that $f_{\rm LO1} > f_{\rm LO2}$. However, it should be appreciated that the formulae herein are applicable to situations in which $f_{\rm LO2} > f_{\rm LO1}$, such as by replacing $f_{\rm LO1}$ with $f_{\rm LO2}$ and replacing $f_{\rm LO2}$ with $f_{\rm LO1}$ in situations where $f_{\rm LO2} > f_{\rm LO1}$.

[0004] FIGURE 1B shows a simplified diagram of two mixing stages with the filtering omitted. These filters ultimately determine final bandwidth ($f_{\rm BW}$) of the tuner, but since they do not contribute to the production of LO-related spurs, they are omitted from FIGURE 1B.

[0005] An adverse effect of the dual conversion process is the introduction of LO-related spurs into the tuned signal. These spurs are created by combinations of the harmonics of the LO frequencies used (f_{LO1} and f_{LO2}). To improve sensitivity and selectivity in modern tuners, there is a need to minimize the foregoing spurious frequency elements (spurs) and noise that can occur in the tuner output.

[0006] The frequency of each of the LO-related spurs can be calculated as:

$$(1) f_{SPUR} = n \times f_1 - m \times f_2$$

where n and m are integer numbers representing, respectively the harmonics of the high and low local oscillator frequencies, and f_1 and f_2 are the local oscillator frequencies (e.g., f_{LO1} and f_{LO2} , respectively where $f_{LO1} > f_{LO2}$). If any spur generated by a given combination of f_{LO1} and f_{LO2} falls within the output bandwidth (f_{BW}) of the converter/tuner, that spur can degrade the quality of the output signal. If a spur does exist within the desired output bandwidth, the LO frequencies can be adjusted to different values to avoid the spur falling within the output band. As manufacturing processes produce denser and faster IC's, the number of harmonics (n_{MAX}) that must be considered continues to increase. Since the number of LO frequency combinations that can possibly create spurs in n harmonics is n^2 , the amount of resources required to avoid the spurs increases dramatically as technology improves. As an example, at the time the circuit shown in FIGURE 1A was initially produced, the number of harmonics (n) that were typically taken into consideration was 5. Currently, the number of harmonics typically taken into consideration is on the order of 15.

[0007] One reason why it is important to avoid LO spurious products is that a spur which is generated by multiples of f_{LO1} and f_{LO2} in a double conversion system will often have a power level which is much greater than the actual RF signal. Therefore, if a spur caused by a product of f_{LO1} and f_{LO2} falls in the desired IF output pass band, its amplitude (power level) may be larger than the IF output level of the original desired signal, corrupting the performance of the mixer itself.

[0008] One of the fixes for this problem is that when it is known that a certain spur (such as a spur associated with two times the first LO and three times the second LO) will fall within the output pass band, the LO frequencies can be changed (up or down) a certain amount, which will, in effect, still allow the circuit to tune to the desired output frequency, but the spur will be moved up or down and outside of the output bandwidth of the tuner.

[0009] Accordingly, one method for identifying spurs falling within a particular band, such as the tuner output band, is to look at all the harmonics of the first LO, mixed with all the harmonics of the second LO and, one by one, check off each one. Thus, if a circuit designer is looking up to the 15^{th} harmonic of the first LO and the 15^{th} harmonic of the second LO, the designer checks one times f_{LO1} (first harmonic) and one

times f_{LO2} (first harmonic) to see if there is a spur of concern. If there is no spur of concern, then the designer continues with one times f_{LO1} (first harmonic) and two times f_{LO2} (second harmonic) to see if there is a spur of concern. If not, then the process continues with one times f_{LO1} (first harmonic) and three times f_{LO2} (third harmonic) to see if there is a spur of concern. Once all harmonics of f_{LO2} have been considered, the harmonic of the first LO frequency may be incremented and each harmonic of the second LO frequency again considered. That is, the designer continues with two times f_{LO1} (second harmonic) and one times f_{LO2} (first harmonic) to see if there is a spur of concern, and so on. This results in n^2 combinations being looked at. This is a time consuming method. Even assuming that the mathematics of how spurs are generated allows for the elimination of quite a few of the coefficients for the first and second LO, the operation remains essentially an n^2 operation.

[0010] It should be appreciated that spur identification and avoidance as discussed above is dependent on the circuit that is being used and which spurs might come through the chip more strongly than other spurs. It is also dependent on the input frequency and on all the specific channels that might be on the input frequency. That method is also specific to the first IF frequency and to the output frequency. Thus, for each application of a circuit the chip designer generally must employ a unique program for each channel input lineup in the desired frequency spectrum. This then implies that a different spur avoidance algorithm must be created for every customer application, i.e., each tuner implementation.

[0011] The foregoing spurs, resulting from harmonics of the local oscillator frequencies of a same tuner, are referred to herein as intra-tuner spurs. It should be appreciated, however, that various devices, such as Plug-and-PlayTM, TiVoTM, set-top boxes, and televisions with picture-in-picture, may utilize two or more tuners or frequency converters. Accordingly, spur types and sources, other than the aforementioned intra-tuner spurs, also may exist.

[0012] In the current state of the art, tuners are often separately packaged and engage separate portions of the device in order to minimize tuner interference. By maintaining some physical separation between tuners of such devices cross-talk and interference generated by the tuners can be minimized. However, the ability to maintain

useful separation between tuners has been limited as the size of such devices continues to diminish. Development is also being made to integrate two or more tuners on a single circuit board. One challenge of miniaturization of such devices is the elimination or minimization of multi-tuner interference. RF shielding and/or extensive RF filtering and circuitry is often needed to suppress tuner generated interference.

[0013] An example of a system including a plurality of double conversion tuners (shown as tuners 106 and 108) is shown as tuner system 100 in FIGURE 1C. In each of tuners 106 and 108, an incoming signal at frequency $f_{\rm IN}$ is mixed with a signal at frequency $f_{\rm LO1}$ from a local oscillator (LO) to produce a signal at an intermediate frequency $f_{\rm IF}$, and this signal is then mixed with a signal at frequency $f_{\rm LO2}$ from a second local oscillator to produce the desired output frequency $f_{\rm OUT}$, as discussed above. However, in dual tuner system 100, multiple LOs may be utilized to produce two desired output frequencies, $f_{\rm OUT,1}$ and $f_{\rm OUT,2}$. In the example of FIGURE 1, each tuner 106 and 108 includes a pair of mixers, shown here as mixers 110 and 112 and mixers 114 and 116, respectively, wherein the $f_{\rm LOi,j}$ signals are mixed to produce $f_{\rm OUT,1}$ and $f_{\rm OUT,2}$.

[0014] Single tuner systems can have intra-tuner spurs related to the frequency harmonics of one or more associated local oscillators. Multiple tuner systems can be affected by both intra-tuner spurs and inter-tuner spurs (spurs related to the frequency harmonics of one or more local oscillators of another tuner in the system). That is, as in a single tuner system, each tuner of a multiple tuner system may be affected by intra-tuner spurs related to the harmonics of its own local oscillator(s). Additionally, each tuner in a multiple tuner system may be affected by inter-tuner spurs which are spurs related to frequency harmonics of local oscillators of both the subject tuner and any neighboring tuner(s).

[0015] An efficient method of identifying and/or eliminating or minimizing the effects of spurs, whether intra-tuner spurs or inter-tuner spurs, would be desirable.

BRIEF SUMMARY OF THE INVENTION

[0016] The present invention is directed to systems and methods of identifying and/or eliminating or reducing interference resulting from harmonics of local oscillator frequencies of mixers in a tuner system. In one embodiment, a determination is made as to a zone or zones in which harmonics result in undesired spur generation. In preferred embodiments of the invention, one or more exclusion zones of local oscillator frequency combinations are identified within which spurs are generated. In some situations spurs in the tuner output are unavoidable. For example, the opportunity to adjust local oscillators may be limited to a range of frequencies within which one or more spurs always exist. As some spurs are more significant (e.g., cause greater interference) than other spurs, preferred embodiments of the invention may also determine a score for identified spurs which may be used to optimally select from within a set of spur-generating local oscillator frequencies.

[0017] In one embodiment, a method of the present invention identifies inter-tuner spurs and intra-tuner spurs and utilizes frequency information of the identified spurs to define a plurality of exclusion zones. LO frequencies may subsequently be efficiently selected in view of the exclusion zone information. In a preferred embodiment, the selection of local oscillator frequencies hinges on the selection of a preferred intermediate frequency, IF, of the tuner. As described herein, a preferred IF may be identified with knowledge of the boundaries of the exclusion zone.

advantages of the present invention in order that the detailed description of the invention that follows may be better understood. Additional features and advantages of the invention will be described hereinafter which form the subject of the claims of the invention. It should be appreciated by those skilled in the art that the conception and specific embodiment disclosed may be readily utilized as a basis for modifying or designing other structures for carrying out the same purposes of the present invention. It should also be realized by those skilled in the art that such equivalent constructions do not depart from the spirit and scope of the invention as set forth in the appended claims. The novel features which are believed to be characteristic of the invention, both as to its organization and method of operation, together with further objects and advantages will

be better understood from the following description when considered in connection with the accompanying figures. It is to be expressly understood, however, that each of the figures is provided for the purpose of illustration and description only and is not intended as a definition of the limits of the present invention.

BRIEF DESCRIPTION OF THE DRAWINGS

- [0019] For a more complete understanding of the present invention, reference is now made to the following descriptions taken in conjunction with the accompanying drawing, in which:
 - [0020] FIGURE 1A is a prior art dual conversion tuner system;
- [0021] FIGURE 1B is a simplified diagram of two mixing stages, with filtering omitted, of a prior art dual conversion tuner;
- [0022] FIGURE 1C is a simplified diagram of a multiple tuner system of the prior art, wherein each tuner has a pair of mixing stages;
- [0023] FIGURE 2 shows one embodiment of a system using the concepts of the present invention;
- [0024] FIGURE 3 illustrates a double conversion tuner having a spurious signal in an output band of the tuner;
- [0025] FIGURE 4 illustrates the double conversion tuner of FIGURE 3 wherein the spurious signal has been shifted out of the output band of the tuner and into an adjacent channel;
- [0026] FIGURE 5 shows the relationship of spur frequencies in the output bands to intermediate frequencies; and
- [0027] FIGURE 6 shows one embodiment of a method of practicing concepts of the present invention.

DETAILED DESCRIPTION OF THE INVENTION

[0028] One of the known approaches to spur elimination is to change the LO frequencies of the tuner in order to shift the spur outside of an output bandwidth or an output band of interest. For a given spur (such as a spur associated with two times the first LO and three times the second LO) that falls within the output pass band, the LO frequencies can be changed (up or down) a certain amount, which will, in effect, still allow the circuit to tune to the desired output frequency, but the spur will be shifted up or down and outside of the output bandwidth of the tuner. The invention disclosed herein is directed, in one aspect, toward efficient selection of LO frequencies in order to minimize or eliminate one or more spurs from an output pass band. The concepts of the present invention may be applied to eliminate or minimize LO-related interference in single tuner systems (e.g., intra-tuner spurs in multi-conversion tuners) as well as to eliminate or minimize LO-related interference in multiple tuner systems (e.g., intra-tuner spurs in multi-conversion tuners).

[0029] Efficient identification of spurs is desirable. One method for identifying spurs falling within a particular band, such as the tuner output band or other band of interest, is to look at all the harmonics of the first LO, mixed with all the harmonics of the second LO and, one by one, check off each one. Thus, if a circuit designer is looking up to the 15th harmonic of the first LO and the 15th harmonic of the second LO, the designer checks one times $f_{\rm LO1}$ (first harmonic) and one times $f_{\rm LO2}$ (first harmonic) to see if there is a spur of concern. If there is no spur of concern, then the designer continues with one times f_{LO1} (first harmonic) and two times f_{LO2} (second harmonic) to see if there is a spur of concern. If not, then the process continues with one times f_{LO1} (first harmonic) and three times f_{LO2} (third harmonic) to see if there is a spur of concern. Once all harmonics of f_{LO2} have been considered, the harmonic of the first LO frequency may be incremented and each harmonic of the second LO frequency again considered. That is, the designer continues with two times f_{LO1} (second harmonic) and one times f_{LO2} (first harmonic) to see if there is a spur of concern, and so on. This results in n^2 combinations being looked at. This is a time consuming method. Even assuming that the mathematics of how spurs are generated allows for the elimination of quite a few of the coefficients for the first and second LO, the operation remains essentially an n^2 operation.

referenced patent application entitled "System and Method for Discovering Frequency Related Spurs in a Multi-Conversion Tuner." In a disclosed embodiment, a determination is made as to a band or bands in which harmonics could possibly result in interference and determining which combination of LO frequencies result in harmonics not falling within the determined band or bands. Preferred embodiments leverage the fact that harmonics of a particular frequency are evenly spaced to avoid examining all of the possible harmonics. For example, rather than calculate every harmonic and check that each calculated harmonic does not fall within the determined band or bands, embodiments of that invention determine the smallest harmonics that are greater than each edge of the determined band or bands. An interfering spur, a difference of the LO harmonics falling within the band or bands, may be determined to exist where the smallest harmonic difference for a particular LO harmonic that is greater than a first edge of a determined band is not equal to the smallest harmonic difference for the particular LO harmonic that is greater than a second edge of the determined band.

[0031] Once spurs have been identified, they can be eliminated by selecting different LO frequencies. Such LO frequency selection can be via a random process, e.g., a new set of LO frequencies can be randomly selected and the spur calculations again performed to determine whether a spur exists within the band of interest. However, a more efficient method for selecting alternative LO frequencies would be desirable.

[0032] FIGURE 2 shows a simplified block diagram of multiple tuner system 200 having a pair of double conversion tuners, shown here as tuners 210 and 220. Examples of devices comprising such a system include a set-top cable box, cable modem, Plug-and-PlayTM device, TiVoTM device, and a television with picture-in-picture capability. RF signals are input to multiple tuner system 200. Although the illustrated embodiment shows RF signals being provided by cable system 230 and antenna 231, RF signals may be received from any number of sources, such as a satellite system, or other signal source.

[0033] In the illustrated embodiment of multiple tuner system 200, tuners 210 and 220 are double conversion tuners. However, embodiments of the present

invention may be utilized with respect to systems in which one or more tuners provide frequency conversion in a number of stages different than that illustrated, e.g., single conversion tuners, triple conversion tuners, quadruple conversion tuners, etcetera. Moreover, embodiments of the present invention may be utilized with respect to tuner systems having a number of tuners different than that illustrated, e.g., single tuner systems, triple tuner systems, quadruple tuner systems, etcetera. The exemplary embodiment, however, is shown with respect to multiple tuners, each having multiple frequency conversion stages, in order to concisely present concepts of the present invention.

[0034] A first mixer of tuner 210, mixer 211, is connected to the RF input signal, f_{IN} , and the output, $f_{\text{LO1,1}}$, of LO 212. Mixer 211 receives both the RF input signal, f_{IN} , and the first LO signal produced by LO 212, $f_{\text{LO1,1}}$, and generates an output signal, which may be called the first IF, shown as $f_{\text{IF,1}}$. The frequency of the signal produced by LO 212 is controlled by a tuning a phase locked loop or other local oscillator control circuit, shown as circuit 213, which is, in turn, controlled by system controller 250 through a control interface.

[0035] The first IF signal, generated by mixer 211, is connected through IF filter 214, which attenuates undesired signals. The output of IF filter 214 is connected to a second mixer of tuner 210, mixer 215. Once the first IF signal generated by mixer 211 has been filtered, it is mixed with a second local oscillator signal, $f_{LO2,1}$, generated by local oscillator 216, whose output is connected to mixer 215. Mixer 215 operates to generate an output signal, $f_{OUT,1}$. The frequency of the signal produced by LO 216 is controlled by tuning a phase locked loop or other local oscillator control circuit, shown as circuit 217, which is, in turn, controlled by system controller 250 through a control interface.

[0036] In a similar manner, tuner 220 has mixers 221 and 225, LOs 222 and 226, circuits 223 and 227, and IF filter 224. Tuner 220 of embodiments operates as described above with respect to tuner 210, although LOs 222 and 227 may be controlled independently of LOs 212 and 217 to provide a different signal (e.g., channel) as an output signal, $f_{OUT,2}$, of tuner 220.

[0037] The output signals of tuners 210 and 220 of the illustrated embodiment are provided to using device 240, such as may comprise a set-top cable box, cable modem, Plug-and-PlayTM device, TiVoTM device, a television with picture-in-picture capability, or the like. Using device 240 may comprise various circuits, such as demodulator 241, processor 242, and memory 243, utilized in further processing the signals output from tuners 210 and 222.

or more new channels are selected), one or more LO frequencies of tuners 210 and/or 220 are adjusted by operation of controller 250. LO frequencies should be carefully chosen to avoid spurious signals appearing in the output band of interest associated with tuners 210 and 220. Although a number of LO frequencies may provide conversion of a signal from a particular RF carrier frequency to a particular output frequency, many such LO frequency combinations will have spurs associated therewith which also fall within the IF frequency bandwidths and/or output frequency bandwidths of either or both of tuners 210 and 220. Accordingly, before implementation of a particular LO frequency combination for tuning to a desired signal by tuners 210 and 220, the LO-related spurs are analyzed according to embodiments of the invention for undesired spurs. In device 200 of the illustrated embodiment, selection of LO frequencies and the associated spur analysis is done dynamically, such as at the time of channel selection. Accordingly, concepts of the present invention can be employed to minimize delay in tuning to selected channels.

[0039] One advantage of systems and methods of the present invention is time savings for alignment, when a tuner is used over a wide range of frequencies. In such a situation it is important to find the LO spurs quickly with as few calculations as possible as these calculations are made every time a channel is changed. Controller 250 of embodiments, which may be implemented in software, hardware or both, enables the first IF generated by the first mixer to be varied dynamically in order to solve the problem of spurious signal generation at certain channel values. Efficient selection of LO frequencies to avoid or minimize spurs is one object of an embodiment of the present invention.

[0040] A discussion of two types of spurs follows. Referring still to FIGURE 2, two double conversion tuners with a single RF input are provided. Each of the tuners has a separate output which may be a different (or the same) channel. Each tuner may have certain spurs, referred herein as "intra-tuner" spurs, which are related to the local oscillators of the associated tuner. The frequency of each of the intra-tuner LO-related spurs can be calculated according to equation (1) set forth above, which has been repeated below for convenience:

(1)
$$f_{SPUR} = n \times f_1 - m \times f_2$$

where n and m are integer numbers representing, respectively the harmonics of the high and low local oscillator frequencies, and f_1 and f_2 are the local oscillator frequencies (e.g., f_{LO1} and f_{LO2} , respectively where $f_{\text{LO1}} > f_{\text{LO2}}$). If any spur generated by a given combination of f_{LO1} and f_{LO2} falls within the output bandwidth (f_{BW}) of the tuner, that spur can degrade the quality of the output signal.

[0041] Each tuner may also have "inter-tuner" spurs related to local oscillators of one or more neighboring tuners. Generation of these output spurs is dependent on the particular local oscillator frequencies of the neighbor tuner.

[0042] Inter-tuner spurs can be calculated as:

(2)
$$f_{SPUR.1} = n \times f_{LO1.1} + m \times f_{LO1.2} - f_{LO2.1}$$

(3)
$$f_{SPUR,2} = n \times f_{LO1,1} + m \times f_{LO1,2} - f_{LO2,2}$$

where n and m are integers and $|n| \le \max_{i=1}^n |m_i| \le \max_{i=$

 $f_{LO2,i}$ = tuner *i*'s second LO frequency. For the tuners 210 and 220 of FIGURE 2, equation (2) defines spurs falling within the output band of tuner 210, while equation (3) defines spurs falling within the output band of a tuner 220.

[0043] Therefore, intra-tuner and inter-tuner spur equations for multiple tuner systems are as follows:

(4)
$$f_{SPUR,i} = n \times f_{LO1,i} - m \times f_{LO2,i}$$

for (5)
$$i = 2 \rightarrow n$$
, $f_{SPUR.1} = n \times f_{LO1.1} + m \times f_{LO1.i} - f_{LO2.1}$

and;

for (6)
$$i = 2 \rightarrow n$$
, $f_{SPUR,i} = n \times f_{LO1,1} + m \times f_{LO1,i} - f_{LO2,i}$

where equation (4) is an equation for intra-tuner spurs, equation (5) is an equation for inter-tuner spurs in a first tuner of a multiple tuner system, and equation (6) is an equation for inter-tuner spurs in tuners 2 through n in a multiple tuner system.

[0044] Equations (1) through (6) permit identification of two types of spurs in the tuner output band of interest. Other spurs may exist and could also be determined. As described herein, if a spur does exist within the desired output bandwidth, the LO frequencies may be adjusted to different values to avoid the spur falling within the output band.

[0045] The characteristic movement of spurs in response to different LO frequencies can be used to determine a particular range or continuum of first IF values yielding a spur in the output bandwidth. According to embodiments of the invention, this particular range of first IF values defines an exclusion zone of first IF values associated with a particular spur. First IF values within the exclusion zone yield a spur in the tuner output bandwidth. First IF values outside of the exclusion zone yield a tuner

output which is free of the particular spur. In this manner, by selecting a particular IF value a known spur may be rejected at the output. At other times, a spur-free tuner output may not be possible and a choice between known spurs may be desired in order to minimize the detrimental effect on tuner performance. As described herein in further detail, a scoring system can be utilized to rank particular spurs and to facilitate selection of local oscillator frequencies yielding a spur with minimal detrimental effect on tuner performance.

[0046] The frequency of a spur within the output bandwidth is a function of associated local oscillator frequencies. In multiple tuner devices a spur frequency of one tuner can be a function of the frequencies of local oscillators of another tuner (inter-tuner spurs). By varying the frequencies of associated local oscillators, a tuner spur may be shifted out of the tuner output bandwidth.

[0047] FIGURE 3 illustrates a tuner having an input signal, f_{IN} , and a local oscillator signal, f_{LOI} , connected to a first mixer (e.g., mixer 211 of tuner 210 or mixer 221 of tuner 220 shown in FIGURE 2). The first mixer generates an output signal within a first IF bandwidth, f_{IF_BW} . The output signal generated by the first mixer, is connected to a second mixer (e.g., mixer 215 of tuner 210 or mixer 225 of tuner 220 shown in FIGURE 2), where it is mixed with a second local oscillator signal, f_{LO2} . The second mixer generates an output signal and its image, $-f_{OUT}$ ($-f_{OUT} = f_{IF} - f_{LO2}$) and $+f_{OUT}$ ($+f_{OUT} = f_{IF} + f_{LO2}$), each having bandwith f_{OUT_BW} .

[0048] Any spur, f_{SPUR} , that falls within the output band or its image is of concern and, therefore, is most likely undesirable. The illustrated embodiment shows spur f_{SPUR} in the output bandwidth of $-f_{OUT}$. For simplification in explaining the concepts of the present invention, spur f_{SPUR} will be assumed to be an intra-tuner spur, as can be represented by equation (1) given above as $f_{SPUR} = n \times f_{LO1} - m \times f_{LO2}$. Of course, spur f_{SPUR} may comprise an inter-tuner spur, as can be represented by either equation (2), given as $f_{SPUR,1} = n \times f_{LO1,1} + m \times f_{LO1,2} - f_{LO2,1}$ above, or equation (3), given as $f_{SPUR,2} = n \times f_{LO1,1} + m \times f_{LO1,2} - f_{LO2,2}$ above.

[0049] FIGURE 3 also shows the location of a predetermined first intermediate frequency (IF), $(f_{IF} = f_{OUT} + f_{LO2})$. The first IF in the illustrated embodiment is different than the center frequency of the IF filter. In other embodiments, the first IF can be equal to the center frequency of the IF filter.

[0050] FIGURE 4 graphically illustrates the movement of the output spur from the location in FIGURE 3 to a new location at $f_{SPUR'}$ which is outside of the tuner's output bandwidth. The position of the spur at $f_{SPUR'}$ shifted as a result of a change in f_{IF} by an amount equal to Δf_{IF} . As intermediate frequency, f_{IF} , is equal to $f_{LO2} + f_{OUT}$, and since it is preferred that f_{OUT} remain fixed, the change in f_{IF} results from a change in the frequency of LO2. For comparison, the location of the spur from FIGURE 3 is shown in phantom lines.

[0051] The relationship of the IF bandwidth to the output bandwidth of the system of FIGURES 3 and 4 is provided in FIGURE 5. FIGURE 5 illustrates the range of intermediate frequency spurs which result in spurs at the edges of the output bands. Specifically, a spur located at point "a" in the output bandwidth will appear in the range of intermediate frequencies from a' to a" in the intermediate frequency bandwidth. Similarly, a spur located at point "b" in the output bandwidth will appear in the range of intermediate frequencies from b' to b", a spur located at point "c" in the output bandwidth will appear in the range of intermediate frequencies from c' to c", and a spur located at point "d" in the output bandwidth will appear in the range of intermediate frequencies from d' to d". Accordingly, FIGURE 5 shows that there is a definable range of IF frequencies within which a spur will remain in the output channel, and that this range is bounded by c', b', and a', d' at which point the spur transitions out of the output channel and into an adjacent channel. Equations of interest include:

(7)
$$f_{SPUR} = n \times f_{LO1} - m \times f_{LO2}$$

(8)
$$f_{out} - \frac{1}{2} f_{out-BW} \le f_{spur} \le f_{out} + \frac{1}{2} f_{out-BW}$$

(9)
$$f_{SPUR} 1 = n \times f_{LO1} - (m-1) \times f_{LO2}$$

(10)
$$m = \frac{nxf_{L01} - f_{spur}' + 1}{f_{L02}}$$

Where f_{spur} is the spur location in the output bandwidth and f_{spur} is the spur location in the intermediate frequency bandwidth.

[0052] As mentioned above, a spur such as f_{SPUR} appearing in either the output frequency or its image, $+f_{\text{OUT}}$ and $-f_{\text{OUT}}$, bandwidth is undesirable. Accordingly, a spur appearing at a frequency between $-f_{\text{OUT}} - f_{\text{OUT}_{\text{BW}}}$ and $-f_{\text{OUT}} + f_{\text{OUT}_{\text{BW}}}$ (the frequency range associated with $f_{\text{OUT}_{\text{BW}}}$ at $-f_{\text{OUT}}$) or between $+f_{\text{OUT}} - f_{\text{OUT}_{\text{BW}}}$ and $+f_{\text{OUT}} + f_{\text{OUT}_{\text{BW}}}$ and $+f_{\text{OUT}_{\text{BW}}} + f_{\text{OUT}_{\text{BW}}}$ at $+f_{\text{OUT}_{\text{BW}}} + f_{\text{OUT}_{\text{BW}}} + f_$

[0053] Directing attention again to FIGURE 3, points between which frequencies of spurs translated to the first intermediate frequency range are likely to be of interest are shown as points A and B (corresponding to points b', c' and a', d' in FIGURE 5). In the illustrated embodiment, points A and B are associated with an edge of the outermost channels passed, or substantially passed, by the intermediate frequency bandwidth, $f_{\text{IF_BW}}$, as may be defined by an intermediate frequency filter or filters. That is, point A is associated with a lowest frequency edge of a lowest frequency channel passed, or substantially passed, by the intermediate frequency bandwidth and point B is

associated with a highest frequency edge of a highest frequency channel passed, or substantially passed, by the intermediate frequency bandwidth. These points delineate the upper and lower limits of wherein an output channel may appear in the intermediate frequency bandwidth. By translating potential output spur frequencies to this intermediate frequency bandwidth, by adding the second local oscillator frequency (f_{LO2}) thereto, analysis with respect to whether the translated spur frequency falls between 2 points (A and B) may be utilized to identify spurs of interest. Moreover, the computations are simplified by avoiding negative numbers. In operation according to a preferred embodiment, all spurs that could appear within the intermediate frequency bandwidth are identified in one step of a process and are thereafter used to identify intermediate frequency exclusion zones.

[0054] Embodiments of the present invention operate to analyze spurs utilizing spur formulae translated to the intermediate frequency to determine if they fall between points A and B demarcating the minimum and maximum IF frequencies which may appear in the output bandwidth. It should be appreciated that the output bandwidth $(f_{\text{OUT_BW}})$ is a frequency range that translates to a corresponding frequency range $(f_{\text{IF OUT BW}})$ within the intermediate frequency bandwidth, where:

(11)
$$f_{IF \text{ OUT BW}} = (f_{OUT \text{ BW}})/(m-n)$$

Accordingly, in deriving formulae for use in identifying spurs of interest, an embodiment of the present invention selects one frequency within the output bandwidth, arbitrarily selected to represent a spur (f_{SPUR}) and translates the selected frequency to the intermediate frequency as an IF spur representation (f_{IF_SPUR}). The IF spur representation may be expanded by the appropriate translated bandwidth ($f_{IF_OUT_BW}$) to identify a spur exclusion zone (f_{SPUR_EX}) for avoiding spurs in the output bandwidth, where:

$$(12) f_{\text{SPUR EX}} = f_{\text{IF SPUR}} \pm f_{\text{IF OUT BW}}/2$$

That is, a spur associated with a given m, n, f_{IN} , and f_{OUT} falling within the output

frequency band must be altered in frequency, such as through adjustment of the first and/or second LO frequencies, a sufficient amount to fall outside of the output bandwidth. Thus an exclusion zone corresponding to the output frequency band and appropriately placed with respect to a spur identifies a range of frequencies in which spurs which would appear in the output frequency band may fall.

[0055] Preferably, a representative spur (f_{SPUR}) is selected in each of the output frequency bandwidth and its image for the above translation to the intermediate frequency. In a preferred embodiment of the invention, the representative spur is selected as the center frequency of each such output frequency bandwidth ($+f_{OUT}$ and $-f_{OUT}$) to simplify expansion of the IF spur representation (f_{SPUR}) by the translated output frequency range ($f_{IF_OUT_BW}$) through symmetry. Of course, other representative spurs may be selected for use according to embodiments of the invention.

[0056] Selecting as a spur of interest for a given m, n, f_{IN} , and f_{OUT} as the output center frequency ($f_{SPUR} = +f_{OUT}$), and using the spur equation given above as equation (1), we have:

$$(13) + f_{OUT} = n \times f_{LO1} - m \times f_{LO2}$$

It should be appreciated that equation (13) has substituted $+f_{OUT}$ for f_{SPUR} of equation (1) because in the assumption $f_{SPUR} = +f_{OUT}$. To translate frequencies at the output frequency bandwidth to the intermediate frequency bandwidth, the second local oscillator frequency may be added to an output frequency to translate that frequency to an intermediate frequency. Therefore, translating the frequencies of equation (13) to the first IF gives:

(14)
$$f_{LO2} + f_{OUT} = n \times f_{LO1} - m \times f_{LO2} + f_{LO2}$$

The LO frequencies, f_{LO1} and f_{LO2} , in equation (14) may be restated in terms of the IF (f_{IF}) to find the IF where the spur is at the output center frequency $(f_{\text{SPUR}} = +f_{\text{OUT}})$. Restating f_{LO1} and f_{LO2} in terms of f_{IF} gives:

(15)
$$f_{IF} = n(f_{IN} + f_{IF}) - (m-1)(f_{IF} - f_{OUT})$$

Solving for f_{IF} in equation (15) gives:

(16)
$$f_{IF} = (n \times f_{IN} + (m-1)f_{OUT})/(m-n)$$

[0057] Similarly, selecting as a spur of interest for a given m, n, f_{IN} , and f_{OUT} as the output image center frequency ($f_{SPUR} = -f_{OUT}$), and using the spur equation given above as equation (1), we have:

(17)
$$-f_{OUT} = n \times f_{LO1} - m \times f_{LO2}$$

Equation (17), similar to equation (13) discussed above, has substituted $-f_{OUT}$ for f_{SPUR} of equation (1) because in the assumption $f_{SPUR} = -f_{OUT}$. To translate frequencies at the output frequency bandwidth to the intermediate frequency bandwidth, the second local oscillator frequency may be added to an output frequency to translate that frequency to an intermediate frequency. Therefore, translating the frequencies of equation (17) to the first IF gives:

$$(18) f_{LO2} - f_{OUT} = n \times f_{LO1} - m \times f_{LO2} + f_{LO2}$$

The LO frequencies (f_{LO1} and f_{LO2}) in equation (18) may be restated in terms of the IF (f_{IF}) to find the IF where the spur is at the output center frequency ($f_{SPUR} = -f_{OUT}$). Restating f_{LO1} and f_{LO2} in terms of f_{IF} gives:

(19)
$$f_{IF} = n(f_{IN} + f_{IF}) - m(f_{IF} - f_{OUT}) + f_{IF} - f_{OUT} + 2f_{OUT}$$

or:

(20)
$$f_{IF}(m-n) = n \times f_{IN} + m \times f_{OUT}$$

Solving for f_{IF} in equation (20) gives:

(21)
$$f_{IF} = (n \times f_{IN} + (m+1)f_{OUT})/(m-n)$$

[0058] It should be appreciated that equations (16) and (21) above provide translation of a spur (f_{SPUR}) to the intermediate frequency, and thus each provide an IF spur representation (f_{IF_SPUR}) . That is, equation (16) provides an IF spur representation for a spur appearing at $+f_{OUT}$ $(f_{IF_SPUR}+)$ and equation (21) provides an IF spur representation for a spur appearing at $-f_{OUT}$ $(f_{IF_SPUR}-)$. Accordingly, replacing f_{IF} in equations (16) and (21) with the appropriate spur designator gives:

(22)
$$f_{IF SPUR+} = (n \times f_{IN} + (m-1)f_{OUT})/(m-n)$$

(23)
$$f_{IF SPUR} = (n \times f_{IN} + (m+1)f_{OUT})/(m-n)$$

[0059] The IF spur representations represented in equations (22) and (23) may be expanded by the translated bandwidth ($f_{\text{IF_OUT_BW}}$) according to embodiments of the invention to identify spur exclusion zones ($f_{\text{SPUR_EX}}$). That is:

(24)
$$f_{\text{SPUR EX}} = f_{\text{IF SPUR}} \pm f_{\text{IF OUT BW}}/2$$

Such spur exclusion zones identify a range in which local oscillator frequency adjustment is insufficient to move the identified spur out of the output frequency band or its image.

[0060] An embodiment of an algorithm operable according to the present invention determines if a spur is indicated in the output frequency band or its image by

analyzing local oscillator harmonic changes within a band of the intermediate frequency (e.g., between points A and B). If it is determined that there is a harmonic change within the band, indicating that a harmonic falls somewhere in the band, the location of the spur (as translated to the intermediate frequency) is preferably determined (e.g., using equation (22) and/or (23)). A spur exclusion zone is then preferably identified in association with the spur intermediate frequency location. Having identified various spur locations (perhaps scored, such as with respect to potential output signal degradation associated therewith), and their associated exclusion zones, operation of the algorithm may select local oscillator frequencies which avoid some or all identified spurs.

[0061] Directing attention to FIGURE 6, details with respect to one method operable in accordance with the above for eliminating or minimizing the effect of spurs in an output of a tuner system, such as tuner system 200 of FIGURE 2, is shown. It should be appreciated that the algorithm of FIGURE 6 can be implemented by a general purpose digital computer. Alternatively, the processes may be executed by a dedicated, special purpose processor. In one embodiment of the present invention, the processor may be on the same board as the tuner(s), associated memory, and other discrete electronics. In another embodiment, the processor and tuner(s) may be on a single integrated circuit. In another embodiment, the processor may be remotely disposed and in communication with the tuner(s). According to one embodiment, the processes of the illustrated method are performed by controller 250 shown in FIGURE 2.

[0062] At block 601, the band edges of the intermediate frequency bandwidth or the range of intermediate frequencies likely to appear in the output frequency or its image bandwidth are determined. That is, upper and lower intermediate frequency limits of interest, identified as points A (minimum $f_{\rm IF}$ of interest) and B (maximum $f_{\rm IF}$ of interest) in the embodiment illustrated in FIGURE 3, are determined. These points may be represented as:

(25)
$$A = f_{IF CENTER} - f_{IF BW}/2 - f_{OUT BW}/2$$

(26) B =
$$f_{IF CENTER} + f_{IF BW}/2 + f_{OUT BW}/2$$

[0063] At block 602, the local oscillator frequencies associated with the band edges identified above are determined. These local oscillator frequencies may be determined as follows:

(27)
$$f_{LO1, MIN} = A + f_{IN}$$

$$(28) f_{LO2, MIN} = A - f_{OUT}$$

$$(29) f_{\text{LO1, MAX}} = B + f_{\text{IN}}$$

$$(30) f_{LO2, MAX} = B - f_{OUT}$$

[0064] At block 603, the first LO1 harmonic of interest, n_0 , is calculated. According to a preferred embodiment, the first harmonic of interest is the first LO1 harmonic that could possibly result in a spur, translated to the intermediate frequency, which falls within the identified intermediate frequency band edges. The first harmonic of interest for LO1 may be determined as follows:

(31)
$$n_o = \left[\frac{f_{LO2,MIN} - A}{f_{LO1MIN} - f_{LO2MIN}} \right]$$

[0065] As the first pass through iterative processes of the method illustrated in FIGURE 6, n (representing a selected harmonic of LO1) is set to n_0 (representing the lowest LO1 harmonic analyzed).

[0066] At block 604, the smallest harmonic of LO2, m_{A1} , associated with a spur at point A for $+f_{OUT}$ and the smallest harmonic of LO2, m_{B1} , associated with a spur

spur at point B for $+f_{OUT}$ is calculated for a given n (e.g., n_0 in a first iteration). The smallest harmonics at points A and B for LO2 for a given LO1 harmonic may be determined as follows:

(32)
$$m_{A1} = \left[\frac{nx f_{LO1MIN} - f_{OUT}}{f_{LO2MIN}} \right]$$

(33)
$$m_{\text{B1}} = \left| \frac{nx f_{LO1MAX} - f_{\text{OUT}}}{f_{LO2MAX}} \right|$$

[0067] A determination is made at block 605 as to whether the smallest harmonic of LO2 associated with a spur at point A, m_{A1} , for $+f_{OUT}$ and the smallest harmonic of LO2 associated with a spur at point B, m_{B1} , for $+f_{OUT}$ are equal. If the harmonics are equal, no spur falls between. That is, if the harmonic does not change in integer value between point A (a lowest IF frequency of interest) and point B (a highest IF frequency of interest) there is no spur associated with the frequencies within this range.

[0068] If it is determined that $m_{A1} \neq m_{B1}$ at block 605, the illustrated method proceeds to block 606 wherein the location of the spur, f_{SPUR} , is calculated and the spur exclusion zone, f_{SPUR_EX} , is determined. The spur may be calculated for the identified n and m using equation (22). That is:

(34)
$$f_{IF SPUR+} = (n \times f_{IN} + (m_{A1} - 1)f_{OUT})/(m_{A1} - n)$$

From equation (24) above it is known that $f_{SPUR_EX} = f_{IF_SPUR} \pm f_{IF_OUT_BW}/2$. Accordingly, the exclusion zone for the identified spur can be calculated as follows:

$$(35) f_{\text{SPUR EX}} = ((n \times f_{\text{IN}} + (m_{\text{A1}} - 1) f_{\text{OUT}}) / (m_{\text{A1}} - n)) \pm f_{\text{IF_OUT_BW}} / 2$$

In addition to determining the exclusion zone, embodiments of the present invention further operate to score the spur with respect to the spur's likelihood, or predicted

severity, of causing undesired results with respect to the output frequency band. For example, the values of n and/or m (the harmonic degree) may be used in scoring a spur (e.g., the larger the harmonic degree, the smaller in magnitude the spur and thus the less likely the spur is to cause undesired results in the output frequency band).

[0069] If, however, it is determined that $m_{A1} = m_{B1}$ at block 605, the illustrated method proceeds to block 607. At block 607, the smallest harmonic of LO2, m_{A2} , associated with a spur at point A for $-f_{OUT}$ and the smallest harmonic of LO2, m_{B2} , associated with a spur at point B for $-f_{OUT}$ is calculated for a given n (e.g., n_0 in a first iteration). The smallest harmonics at points A and B for LO2 for a given LO1 harmonic may be determined as follows:

(36)
$$m_{A2} = \left[\frac{nx f_{LO1MIN} + f_{OUT}}{f_{LO2MIN}} \right]$$

(37)
$$m_{\text{B2}} = \left[\frac{nx f_{LO1\text{MAX}} + f_{\text{OUT}}}{f_{LO2\text{MAX}}} \right]$$

[0070] A determination is made at block 608 as to whether the smallest harmonic of LO2 associated with a spur at point A, m_{A2} , for $-f_{OUT}$ and the smallest harmonic of LO2 associated with a spur at point B, m_{B2} , for $-f_{OUT}$ are equal. If the harmonics are equal, no spur falls between. That is, if the harmonic does not change in integer value between point A (a lowest IF frequency of interest) and point B (a highest IF frequency of interest) there is no spur associated with the frequencies within this range.

[0071] If it is determined that $m_{A2} \neq m_{B2}$ at block 608, the illustrated method proceeds to block 609 wherein the location of the spur, f_{SPUR} , is calculated and the spur exclusion zone, f_{SPUR_EX} , is determined. The spur may be calculated for the identified n and m using equation (23). That is:

(38)
$$f_{IF}$$
 SPUR- = $(n \times f_{IN} + (m_{A2} + 1)f_{OUT})/(m_{A2} - n)$

From equation (24) above it is known that $f_{SPUR_EX} = f_{IF_SPUR} \pm f_{IF_OUT_BW}/2$. Accordingly, the exclusion zone for the identified spur can be calculated as follows:

(39)
$$f_{\text{SPUR EX}} = ((n \times f_{\text{IN}} + (m_{\text{A2}} + 1)f_{\text{OUT}})/(m_{\text{A2}} - n)) \pm f_{\text{IF OUT BW}}/2$$

In addition to determining the exclusion zone, embodiments of the present invention further operate to score the spur with respect to the spur's likelihood, or predicted severity, of causing undesired results with respect to the output frequency band. For example, the values of n and/or m (the harmonic degree) may be used in scoring a spur (e.g., the larger the harmonic degree, the smaller in magnitude the spur and thus the less likely the spur is to cause undesired results in the output frequency band).

[0072] A determination is made at block 610 as to whether the smallest harmonic of LO2 associated with a spur at point B (e.g., $m_{\rm B1}$) analyzed above is greater than a selected harmonic of LO1 (e.g., $n_{\rm MAX}$). The maximum harmonic of LO1 used with respect to the foregoing determination may be selected from an order of harmonic degrees (e.g., 15) likely to cause undesired results in the output frequency band.

[0073] If it is determined at block 610 that the identified harmonic of LO2 is not greater than a selected maximum harmonic of LO1, processing according to the illustrated embodiment proceeds to block 611 wherein the LO1 harmonic is incremented (n = n + 1) for another iteration of processing according to blocks 604-610. However, if it is determined at block 610 that the identified harmonic of LO2 is greater than a selected maximum harmonic of LO1, a desired degree of harmonics have been processed according to blocks 604-610 and thus processing according to the illustrated embodiment proceeds to block 612.

[0074] At block 612, an intermediate frequency, $f_{\rm IF}$, is selected based upon the exclusion zones identified through operation of the processes of blocks 604-610. According to a preferred embodiment, an intermediate frequency which is outside of all identified exclusion zones, and preferably which is closest to the intermediate frequency

band center frequency, is selected as the intermediate frequency for tuning a given f_{IN} to a given f_{OUT} . It should be appreciated that the aforementioned exclusion zones may substantially cover the intermediate frequency bandwidth. Likewise, the exclusion zones may substantially exclude only otherwise undesirable intermediate frequencies within the intermediate frequency bandwidth (e.g., intermediate frequencies at the extreme edges of the intermediate frequency bandwidth). Accordingly, embodiments of the invention may operate to select an intermediate frequency that falls within an exclusion zone. For example, using the aforementioned spur scoring, an embodiment of the invention may select an intermediate frequency having a spur in the output frequency bandwith, or its image, with a score indicative of that spur resulting in little undesired effects.

[0075] At block 613, the local oscillator frequencies, LO1 and LO2, are selected based upon the selected intermediate frequency. For example, knowing $f_{\rm IF}$, LO1 may be selected for a given $f_{\rm IN}$ to result in the signal of $f_{\rm IN}$ being converted to $f_{\rm IF}$. Likewise, knowing $f_{\rm IF}$, LO2 may be selected for a given $f_{\rm OUT}$ to result in the signal of $f_{\rm IF}$ being converted to $f_{\rm OUT}$. The foregoing local oscillator frequencies may be provided by a controller, such as controller 250 of FIGURE 2, to local oscillator control circuitry, such as local oscillator control circuits 213 and 217 and/or 223 and 227, for tuning a tuner, such as tuner 210 and/or 220, to a desired channel.

[0076] The inventions disclosed herein promote efficient selection of IF values without resorting to the large tables of local oscillator frequencies used to avoid spurs in the prior art. Tuner flexibility and efficiency is improved since large LO frequency tables need not be calculated. Tuner proximity in multiple tuner devices can be minimized by utilizing the spur avoidance techniques of the present invention. The present invention finds particular utility with respect to multiple conversion tuners and/or multiple tuners on a single circuit board or within a single integrated circuit wherein the distance between tuners can be minimized.

[0077] Note that while the embodiments discuss local oscillator frequencies, the inventive concepts are applicable to any frequency interference sensitive circuit or system where the harmonics of frequencies could add spurs (or extraneous frequencies) into a circuit at specific frequencies. Also, note that the inventions disclosed herein can be applied to tuners having more than one intermediate frequency,

such as triple or quadruple conversions tuners. Additionally, the inventions disclosed herein could be applied to combinations of single and multiple conversion tuners and to systems having any number of tuners.

described in detail, it should be understood that various changes, substitutions and alterations can be made herein without departing from the spirit and scope of the invention as defined by the appended claims. Moreover, the scope of the present application is not intended to be limited to the particular embodiments of the process, machine, manufacture, composition of matter, means, methods and steps described in the specification. As one of ordinary skill in the art will readily appreciate from the disclosure of the present invention, processes, machines, manufacture, compositions of matter, means, methods, or steps, presently existing or later to be developed that perform substantially the same function or achieve substantially the same result as the corresponding embodiments described herein may be utilized according to the present invention. Accordingly, the appended claims are intended to include within their scope such processes, machines, manufacture, compositions of matter, means, methods, or steps.

CLAIMS

What is claimed is:

1. A method of avoiding interfering signals in an electronic system, said method comprising:

determining an output band of interest for a tuner;

determining an intermediate frequency band associated with said output band; identifying exclusion zones within said intermediate frequency band associated with corresponding spurs in said output band by analyzing harmonics associated with first and second local oscillators;

selecting an intermediate frequency for use in frequency conversion of an input signal to said output band based at least in part on said exclusion zones; and

selecting at least one of a first local oscillator frequency and a second local oscillator frequency as a function of said intermediate frequency.

2. The method of claim 1, further comprising:

determining an image of said output band of interest for said tuner, wherein said identifying exclusion zones includes identifying exclusion zones within said intermediate frequency band associated with corresponding spurs in said output band image by analyzing harmonics of first and second local oscillator frequencies.

- 3. The method of claim 1, wherein said analyzing harmonics comprises: identifying a first harmonic of interest; and incrementing the first harmonic of interest for iterative analysis of said harmonics.
- 4. The method of claim 3, wherein said first harmonic of interest comprises a first harmonic associated with said first local oscillator that could possibly result in an undesired spur.
- 5. The method of claim 3, wherein said incrementing is repeated until a desired harmonic degree has been analyzed.

6. The method of claim 1, wherein said exclusion zones provide information with respect to intermediate frequency values resulting in a spur being within the output band.

- 7. The method of claim 1, wherein said first local oscillator frequency and said second local oscillator frequency are utilized with respect to a multiple conversion tuner.
- 8. The method of claim 1, wherein said first local oscillator frequency is utilized with respect to a first tuner and said second local oscillator frequency is utilized with respect to a second tuner.
 - 9. The method of claim 1, further comprising: scoring said spurs corresponding to said exclusion zones.
- 10. The method of claim 9, wherein said scoring is based at least in part on a level of effect upon a signal output in said output band associated with said spurs.
- 11. The method of claim 9, wherein said selecting said intermediate frequency comprises:

selecting an intermediate frequency within said intermediate frequency band falling within an exclusion zone associated with a spur having an acceptably low score.

12. The method of claim 1, wherein said selecting said intermediate frequency comprises:

selecting an intermediate frequency within said intermediate frequency band falling outside of all exclusion zones.

13. The method of claim 12, wherein said selecting said intermediate frequency further comprises:

selecting said intermediate frequency to be as near a center frequency of said intermediate frequency band.

14. A method of avoiding interfering signals in an electronic system, said method comprising:

determining an output band of interest for a tuner;
determining an image of said output band of interest for said tuner;
selecting an intermediate frequency band associated with said output band;
iteratively analyzing harmonics associated with a first local oscillator and a

second local oscillator to determine whether particular combinations of said harmonics associated with said first local oscillator and said second local oscillator result in a spur within said output band or said output band image;

identifying exclusion zones within said intermediate frequency band associated with said spurs; and

selecting an intermediate frequency through reference to said exclusion zones.

15. The method of claim 14, further comprising:

selecting at least one of a frequency of said first local oscillator and a frequency of said second local oscillator for frequency conversion of an input signal to said output band as a function of said intermediate frequency.

16. The method of claim 14, wherein said iteratively analyzing harmonics comprises:

for a particular harmonic associated with said first local oscillator frequency, determining if an integer value of a harmonic of said second local oscillator frequency changes between a frequency of a lower limit of said intermediate frequency band and a frequency of an upper limit of said intermediate frequency band.

17. The method of claim 14, wherein said iteratively analyzing harmonics comprises:

identifying a first harmonic of interest with respect to said first local oscillator; and

incrementing the first harmonic of interest for iterative analysis of said harmonics.

18. The method of claim 17, wherein said iteratively analyzing harmonics comprises:

analyzing a range of harmonics of interest with respect to said second local oscillator prior to incrementing said first harmonic of interest.

- 19. The method of claim 17, wherein said first harmonic of interest comprises a first harmonic associated with said first local oscillator that could possibly result in an undesired spur, and, wherein said incrementing is repeated until a desired harmonic degree has been analyzed.
- 20. The method of claim 14, wherein said exclusion zones provide information with respect to intermediate frequency values resulting in a spur being within the output band.
 - 21. The method of claim 14, further comprising: scoring said spurs corresponding to said exclusion zones.
- 22. The method of claim 21, wherein said selecting said intermediate frequency comprises:

selecting an intermediate frequency within said intermediate frequency band falling within an exclusion zone associated with a spur having an acceptably low score.

23. The method of claim 14, wherein said selecting said intermediate frequency comprises:

selecting an intermediate frequency within said intermediate frequency band falling outside of all exclusion zones.

24. The method of claim 23, wherein said selecting said intermediate frequency further comprises:

selecting said intermediate frequency to be as near a center frequency of said intermediate frequency band.

25. A tuner system comprising:

a first mixer which accepts an input signal and a first local oscillator signal to generate a first intermediate frequency signal within an intermediate frequency band, said intermediate frequency band being associated with an output frequency band;

a second mixer which accepts a second local oscillator signal;

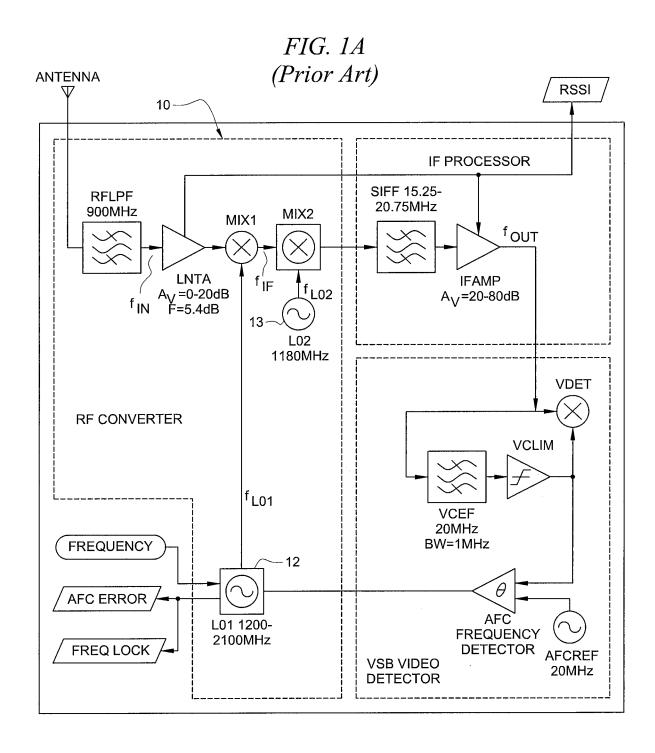
means for identifying exclusion zones within said intermediate frequency band associated with corresponding spurs in said output band; and

means for selecting a frequency of said first intermediate frequency signal through reference to said exclusion zones.

- 26. The system of claim 25, wherein said means for identifying exclusion zones identifies said exclusion zones through analysis of harmonics of said first local oscillator signal and said second local oscillator signal.
- 27. The system of claim 25, further comprising: means for determining a frequency of said first local oscillator signal as a function of said first intermediate frequency signal frequency.
- 28. The method of claim 25, wherein said means for identifying exclusion zones comprises:

means for determining, for a particular harmonic associated with said first local oscillator frequency, if an integer value of a harmonic of said second local oscillator frequency changes between a frequency of a lower limit of said intermediate frequency band and a frequency of an upper limit of said intermediate frequency band.

- 29. The method of claim 25, wherein said first mixer and said second mixer are part of a multiple conversion tuner.
- 30. The method of claim 25, wherein said first mixer is part of a first tuner and said second mixer is part of a second tuner.



2/7

FIG. 1B (Prior Art)

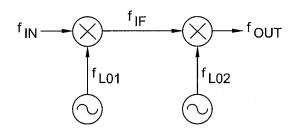
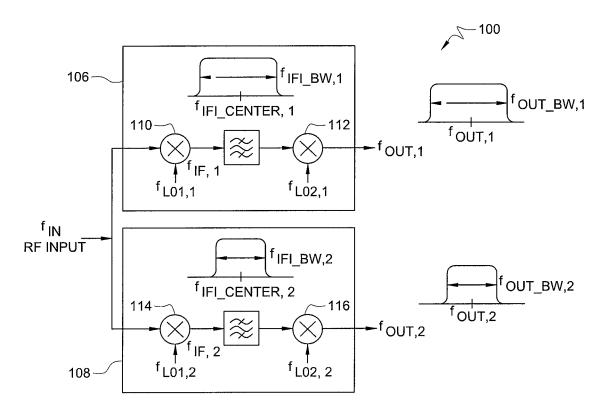
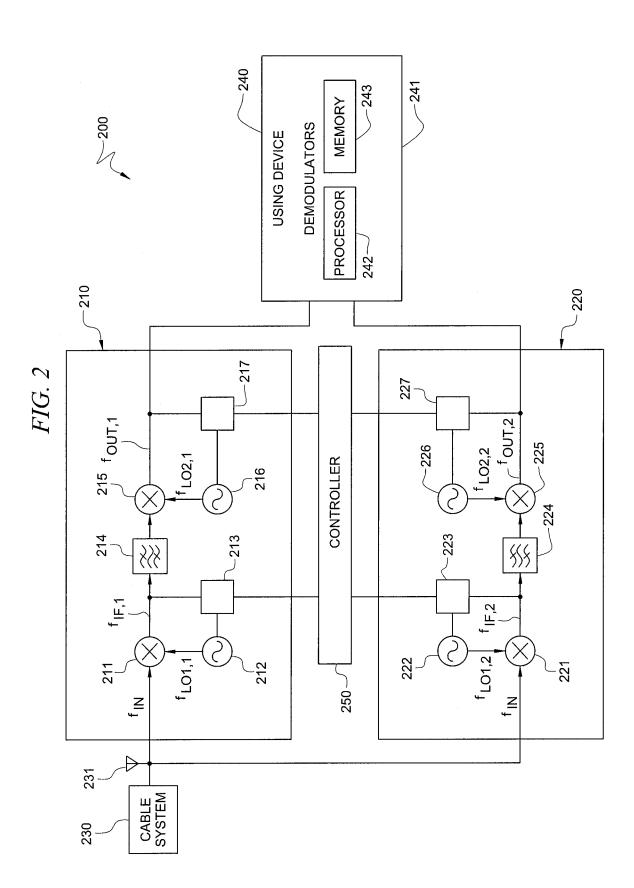


FIG. 1C (Prior Art)





4/7

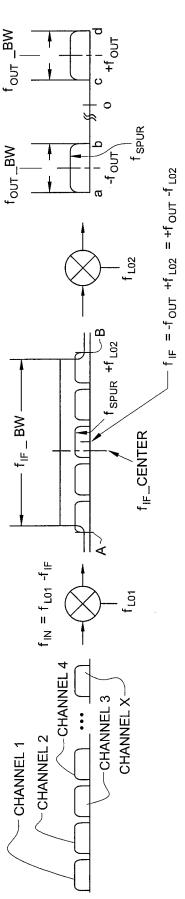
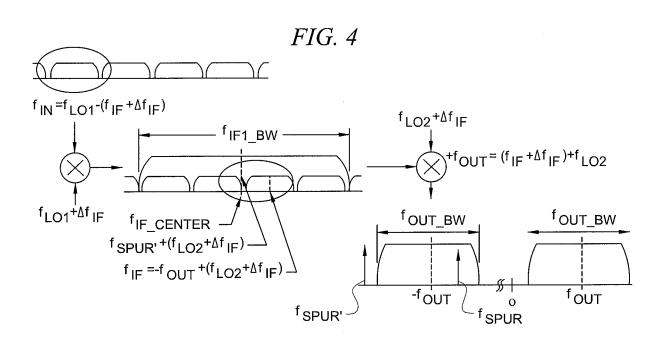
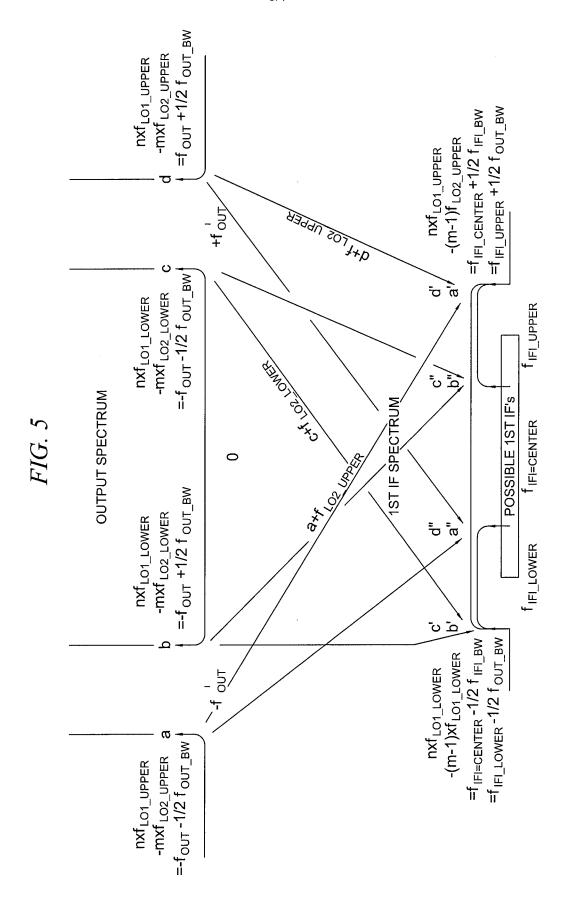
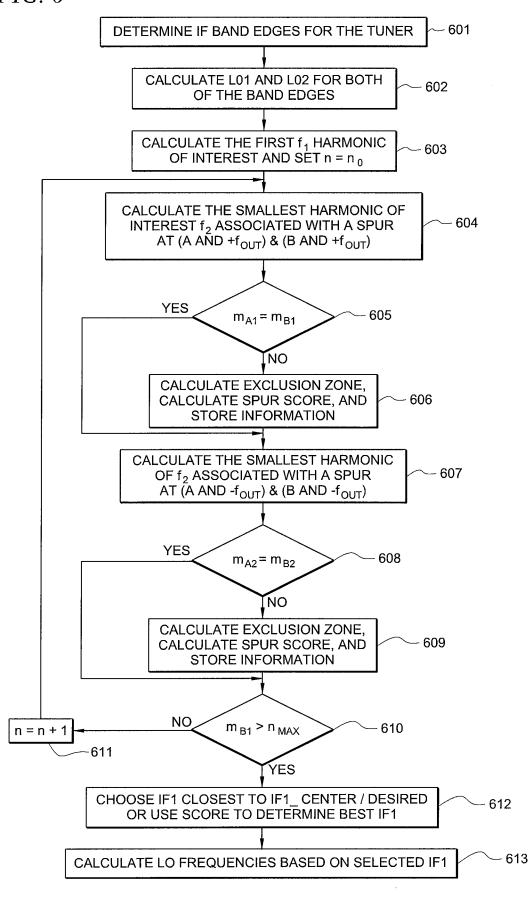


FIG. 3









INTERNATIONAL SEARCH REPORT

International application No. PCT/US 08/65069

A. CLASSIFICATION OF SUBJECT MATTER IPC(8) - H04B 1/00 (2008.04) USPC - 455/63.1 According to International Patent Classification (IPC) or to both national classification and IPC				
B. FIELDS SEARCHED				
Minimum documentation searched (classification system followed by classification symbols) USPC - 455/63.1				
Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched USPC - 455/63.1, 283, 296 text search, see search terms below				
Electronic data base consulted during the international search (name of data base and, where practicable, search terms used) PubWEST(PGPB,USPT,USOC,EPAB,JPAB); Google Scholar Search Terms Used: conversion, converter, tuner, spurious, harmonic, identif, eliminat, reduc, minimi, interference, local, oscillator, exclud, exlusion, free of, area, region, band, zone, tuner, converter, score, scoring, metric, select, prefer, frequency, mixing, mixer				
C. DOCUMENTS CONSIDERED TO BE RELEVANT				
Category*	Citation of document, with indication, where ap	propriate, of the relevant passages	Relevant to claim No.	
Y	US 6,567,654 B1 (CORONEL ARREDONDO et al.) 20 May 2003 (20.05.2003), especially Figs 1, 2, 4, 5; col 2, ln 16-19; col 3, ln 40-42, 54-61; col 4, ln 67; col 5, ln 1-47; col 6, ln 22-23, 25-26; col 7, ln 33-43, 67.		1-30	
Υ	US 2006/0068708 A1 (DESSERT et al.) 30 March 2006 (30.03.2006), especially Figs 2, 9; para [0011]-[0012], [0034], [0036]-[0038], [0042], [0045], [0049]-[0050], [0053].		1-30	
Α	US 6,876,839 B2 (HARRIS) 05 April 2005 (05.04.2005), entire document.		1-30	
Α	US 6,898,420 B2 (BLACK et al.) 24 May 2005 (24.05.2005), entire document.		1-30	
Α	US 6,785,527 B2 (EARLS) 31 August 2004 (31.08.2004), entire document.		1-30	
Further documents are listed in the continuation of Box C.				
* Special categories of cited documents: "T" later document published after the international filing date or priority				
"A" document defining the general state of the art which is not considered to be of particular relevance date and not in conflict with the application but cited to understand the principle or theory underlying the invention				
"E" earlier application or patent but published on or after the international filing date "L" document which may throw doubts on priority claim(s) or which is document which may throw doubts on priority claim(s) or which is				
cited to establish the publication date of another citation or other special reason (as specified) "O" document referring to an oral disclosure, use, exhibition or other document referring to an oral disclosure, use, exhibition or other document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination				
means being obvious to a person skilled in the art "P" document published prior to the international filing date but later than "&" document member of the same patent family the priority date claimed				
Date of the actual completion of the international search Date of mailing of the international search report				
23 July 2008 (23.07.2008) 28 JUL 2008				
Name and mailing address of the ISA/US Authorized officer:				
	CT, Attn: ISA/US, Commissioner for Patents 50, Alexandria, Virginia 22313-1450	Lee W. Young		
	Facsimile No. 571-273-3201 PCT Helpdesk: 571-272-4300 PCT OSP: 571-272-7774			