

12 **EUROPEAN PATENT APPLICATION**

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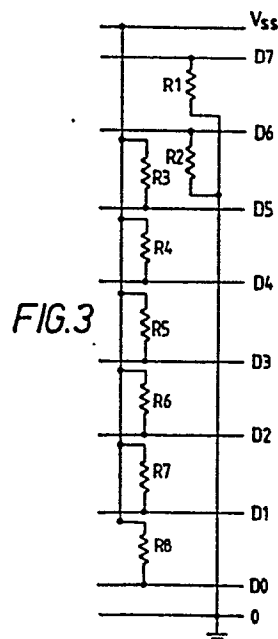
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54 **Microprocessor-based control system.**

57 In order to prevent indeterminate operation of a microprocessor-based control system when a nonexistent address is called, the data lines are strapped to high level or low level lines by impedance elements whose impedance is high in comparison with the load impedance of the lines and low in comparison with the non-loaded impedance.





DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int. Cl. 3)
Y	IBM TECHNICAL DISCLOSURE BULLETIN, vol. 12, no. 8, January 1970, pages 1250-1251, New York, US; R. BELLIEZI: "I/O device selection and verification" * Page 1251 *	1,2	G 06 F 13/00
Y	--- US-A-4 030 080 (BURKETT et al.) * Figure 5; abstract; column 9, lines 9-16 *	1,2	
Y	--- EDN ELECTRICAL DESIGN NEWS, vol. 25, no. 3, 5th February 1980, pages 161-165, Denver, US; R.B. BONNEY: "For muP-system data entry, thumbwheel switches excel" * Figures 1,2; page 162, paragraph "Choose among several switch-isolation schemes", left-hand column - right-hand column, permultimate line"	1,2	
P,X	--- EDN MAGAZINE, vol. 26, no. 3, February 1981, page 88, Boston, US; N.D. MACKINTOSH: "Interrogation tells muP which boards are present" * Whole document *	1,2	
The present search report has been drawn up for all claims			
Place of search THE HAGUE		Date of completion of the search 29-06-1984	Examiner LEDROUT P.
CATEGORY OF CITED DOCUMENTS X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document			