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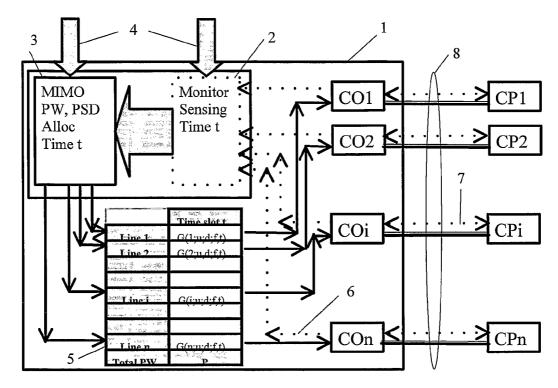
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(54) Title: MIMO DYNAMIC PSD ALLOCATION FOR DSL NETWORKS



(57) Abstract: A system and method for multiple inputs, multiple outputs (MIMO) PSD allocation in a DSL system is disclosed. In some embodiments, the method may comprise monitoring system performance by performing a multi-ended line test (MELT), processing the MELT and allocating PSD based on at least one of system coupling power and system traffic.

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MIMO DYNAMIC PSD ALLOCATION FOR DSL NETWORKS

BACKGROUND OF THE INVENTION

[0001] Systems and methods for dynamically allocating power and power spectral densities (PSD) at a remote device located at a digital subscriber line access multiplexer (DSLAM) for each digital subscriber line (DSL) connected service are disclosed.

[0002] In typical DSL systems, power and PSD are allocated on a pair-to-pair basis. Thus, power and PSD are typically allocated in a single input, single output (SISO) fashion. Accordingly, in SISO systems it is not possible to reallocate power or PSD to compensate for increased (or decreased) demand on one or more of the systems. This can lead to system inefficiencies. Other drawbacks also exist.

SUMMARY OF THE INVENTION

[0003] Accordingly, there is provided a system and method for multiple inputs, multiple outputs (MIMO) PSD allocation in a DSL system. In some embodiments, the method may comprise monitoring system performance by performing a multi-ended line test (MELT), processing the MELT and allocating PSD based on at least one of system coupling power and system traffic.

[0004] In some embodiments, the method of performing a MELT may further comprise dynamically determining the near end cross talk (NEXT)/ECHO couplings at a customer premises (CP) location and a central office (CO) location. The method of performing a MELT may also comprise dynamically determining the far end cross talk (FEXT) couplings at a CP location and a CO location.

[0005] Embodiments of the invention may process the MELT by a disruptive method. Other embodiments may process the MELT by a non-disruptive method. Non-disruptive methods may comprise passive or active methods. Other methods are also possible.

[0006] In some embodiments, the allocation of PSD based upon system coupling power may comprise implementing a full mask control scheme. Other embodiments may allocate PSD by implementing a selective bit control scheme or by implementing a power swap scheme. Other techniques are also possible.

[0007] Embodiments of the invention also provide a system for dynamically monitoring and allocating upstream and downstream power spectral density (PSD) of a transceiver set. In some embodiments the system may comprise a monitor for performing MELTs, a controller,

responsive to the monitor, for dynamically allocating upstream and downstream PSD and a table of upstream PSD and downstream PSD for each time (t) and each line. In accordance with other aspects of the invention, the monitor and the controller may be receptive to a priori information from other system levels.

[0008] The present invention provides techniques that dynamically allocate power and spectral densities to a downstream (DS) network in a centralized manner. A remote device located at a DSL access multiplexer (DSLAM) regularly (i.e., at time slot t) senses the budget demand of each DSL connected service. To illustrate, if one system is idle there typically is no need to transmit any power until the demand resumes. This available power may be used to provide improved service to remote customer premises (CPs). If one CP equipment (CPE) is near the DSLAM and one is remote, delivering a minimum rate to each does not require the same power. The centralized system then may allocate power/power spectral density (PSD) to each DSL system in accordance to its demands or needs in such a way that the total power delivered to the series of connected DSL systems is substantially fixed to a certain fixed value P. The dynamically centralized allocation procedure also may handle the spectral compatibility among the different DSL systems. The centralized device ensures spectral compatibility. Another exemplary technique of the present invention is to allocate power/PSD in a multiple input, multiple output (MIMO) manner instead of a single input, single output (SISO) manner. This approach makes better use of the fixed global power available for the whole DSLAM.

[0009] Other systems and methods are also possible.

BRIEF DESCRIPTION OF THE DRAWINGS

[0010] Figure 1 is a schematic illustration of the dynamic power allocation system according to some embodiments of the invention.

[0011] Figure 2 is a schematic flow chart illustrating a method for dynamic power allocation in accordance with embodiments of the invention.

DETAILED DESCRIPTION OF THE DRAWING FIGURES

[0012] Exemplary systems and techniques of various embodiments of the invention are now described in detail. The following description is intended to convey a thorough understanding of the invention by providing a number of specific embodiments and details involving DSL-based systems. It is understood, however, that the present invention is not limited to these specific embodiments and details, which are exemplary only. It is further

understood that one possessing ordinary skill in the art, in light of known systems and methods, would appreciate the use of the invention for its intended purposes and benefits in any number of alternative embodiments, depending upon specific design and other needs.

[0013] Figure 1 is a schematic diagram of a MIMO dynamic PSD allocation system according to some embodiments of the invention. As shown in Fig. 1, the system may comprise a centralized part located at the CO.

[0014] Embodiments of the system may also comprise a monitor 2 for monitoring the system. For example, monitor 2 may perform a MELT or other line test. The monitoring may occur at any suitable time. For example, monitor 2 may sense at any number of predetermined times (t).

[0015] Monitor 2 may communicate with a controller 3. In some embodiments, controller 3 may dynamically allocate, at every time (t), the upstream and downstream PSD.

[0016] In some embodiments, monitor 2 and controller 3 may receive *a priori* information 4 from other parts of the system. For example, *a priori* information 4 may comprise Local network information, sources of cross talk, bundle distribution, system traffic information, or other sources of information. In some embodiments, this *a priori* information 4 is dynamically updated.

[0017] In some embodiments, the system may comprise a table 5 of upstream and downstream PSDs for each time (t). Table 5 may comprise any suitable table, such as a software table, firm ware table, or other suitable format. As shown in Fig. 1, table 5 may contain a table of lines (e.g., line 1, ..., line n) and corresponding time slot (t) PSD upstream and downstream values (e.g., G(1;u;d;f;t), ..., G(n;u;d;f;t;)).

[0018] As indicated in Fig. 1 information to monitor 2 may come from various parts of the system. For example, as indicated by the broken line information 6 may come from the CO or CP. In addition, information and parameters may be exchanged between the CO and CP as indicated at 7. The bundle 8 indicates the grouping of monitored lines (e.g., lines 1 - n).

[0019] In some embodiments, monitor 2 may perform MELT. The aim of the monitoring piece is to dynamically perform MELT. In some embodiments, the MELT procedure should dynamically provide the following set of parameters.

[0020] The first parameter may be NEXT/ECHO couplings at the CPs:

 $H_{i\to j}^{CP}(f,t)$, $1 \le i, j \le n$ at each frequency f and at each time instant t. All these parameters are collected in a Matrix $\mathbf{H}^{CP}(f,t)$ at each frequency f and at each time instant t.

[0021] Another parameter may be NEXT/ECHO Couplings at the COs:

 $H_{i\to j}^{CO}(f,t)$, $1 \le i, j \le n$ at each frequency f and at each time instant t. All these parameters are collected in a Matrix $\mathbf{H}^{CO}(f,t)$ at each frequency f and at each time instant t.

[0022] Another parameter may be FEXT Couplings from the COs to the CPs:

 $H_{i\to j}^{CO\to CP}(f,t)$, $1 \le i,j \le n$ at each frequency f and at each time instant t. All these parameters are collected in a Matrix $\mathbf{H}^{CO\to CP}(f,t)$ at each frequency f and at each time instant t.

[0023] Still another parameter may be FEXT Couplings from the CPs to the COs: $H_{i\to j}^{CP\to CO}(f,t)$, $1\leq i,j\leq n$ at each frequency f and at each time instant t. All these parameters are collected in a Matrix $\mathbf{H}^{CP\to CO}(f,t)$ at each frequency f and at each time instant t.

[0024] In some embodiments, symmetries may simplify the structure of each matrices and the relationship between matrices. In what follows $\mathbf{H}_{prior}^{x}(f,t)$ will denote the a priori information coming from another level, identified as 4 in Fig. 1, irrespective of what x represents.

[0025] In different embodiments there may exist different methods to process MELT. For example, there may exist disruptive and non disruptive methods.

[0026] For some embodiments, disruptive methods may require to re-start at least one of the monitored modems. These methods can be processed at the very first installation of the system based on a vectorial start up. Similar to round robin tests, each modem will transmit a particular sequence and all the others will listen on both sides to have access to the matrices described above. Disruptive methods are active, in the sense they make each modem actively transmit a signal to sense the whole system.

[0027] For some embodiments, non-disruptive methods may not require to re-start any of the monitored modems. For some embodiments, two families of non-disruptive methods, active and passive, may exist.

[0028] For some embodiments, non-disruptive, active methods can, for example, increase the gi's of one particular system and check which other system will fall in SRA/Bit swap mode to update its bit loading. Similarly, for some embodiments, non-disruptive, passive methods do not generate extra-signals/power and may use the cross correlation between the residual error at each frequency and the NEXT/FEXT source.

[0029] In the following, the operation of some embodiments of the Control/Information exchange (e.g., items 3, 5 and 7 in Fig. 1) is explained.

[0030] In some embodiments, controller 3 may function based on coupling/power. For example, controller 3 may implement a full mask control scheme. In some embodiments, a full mask control scheme is a scheme in which the CO communicates to the CP the PSD mask to use to recompute on-the-fly a new bit-loading and using the SRA procedure to establish the new configuration in a non-disruptive way (as described in BIS).

[0031] In some embodiments, selective bit control may be implemented. Selective bit control may comprise a similar scheme as the full mask control described above but the CO communicates and/or a region in which the CP will re-compute the bit loading without changing the power allocation but reduced the bit/bin. This is an equivalent to having a margin per bin.

[0032] In some embodiments, another control scheme based on coupling/power is a power swap scheme. A power swap scheme may comprise a scheme in which the CO using the above schemes (e.g., full mask, selective bit, etc.) to re-distribute more power or PSD to some port or a scheme in which the power does not change in all ports but the information that each port carry will change on the fly. There are few scenarios to support one in which a power in a particular region is moved to anther port or a scheme in which an additional power is provided to anther port and only the margin in other ports which are influenced will be reduced.

[0033] In some embodiments, controller 3 may implement a control scheme based on traffic. For example, a traffic based power swap scheme may be implemented. A traffic based power swap scheme may comprise a scheme in which the CO monitors the data traffic, payload activity, on all the ports and based on the activities it re-allocates the power/margin to the ports which requires the additional payload.

[0034] Another traffic based control scheme may comprise an anticipated power swap scheme. In some embodiments, the anticipated power swap scheme may operate similar to the traffic based power swap scheme with a difference being that the anticipated power swap scheme may include short term as well as long term predictions.

[0035] Yet another traffic based control scheme may be selective power swap. This scheme may operate similar to the above schemes, but may additionally be based on the service the customer is subscribed to.

[0036] Still another traffic based control scheme may comprise a MIMO channel driven power swap scheme. This too may operate similar to the above, except it may also include consideration of the crosstalk table from above.

[0037] Figure 2 is a schematic flow chart illustrating a method for dynamic power allocation in accordance with embodiments of the invention. As indicated in Fig. 2, the process may begin with monitoring 202. Monitoring may comprise a MELT or other monitoring scheme. At 204, the MELT or other monitoring scheme is processed as described above. For example, using a disruptive or non-disruptive method. Finally, at 206 PSD may be allocated according to a suitable scheme as described above.

[0038] While the invention has been described in conjunction with the preferred embodiments, it should be understood that modifications will become apparent to those of ordinary skill in the art and that such modifications are intended to be included within the scope of the invention and the following claims.

CLAIMS

What is claimed is:

1. A method for multiple inputs, multiple outputs (MIMO) power spectral density (PSD) allocation in a digital subscriber line (DSL) system, the method comprising:

monitoring system performance by performing a multi-ended line test (MELT); processing the MELT and,

allocating PSD based on at least one of system coupling power and system traffic.

- The method of claim 1 wherein performing a MELT further comprises:
 dynamically determining the near end cross talk (NEXT)/ECHO couplings at a customer premises (CP) location.
- 3. The method of claim 1 wherein performing a MELT further comprises: dynamically determining the near end cross talk (NEXT)/ECHO couplings at a central office (CO) location.
- 4. The method of claim 1 wherein performing a MELT further comprises:

 dynamically determining the far end cross talk (FEXT) couplings at a customer premises (CP) location.
- 5. The method of claim 1 wherein performing a MELT further comprises:dynamically determining the far end cross talk (FEXT) couplings at a central office (CO) location.
- 6. The method of claim 1 wherein processing the MELT further comprises processing the MELT by a disruptive method.
- 7. The method of claim 1 wherein processing the MELT further comprises processing the MELT by a non-disruptive method.
- 8. The method of claim 7 wherein the non-disruptive method further comprises an active method.

9. The method of clam 7 wherein the non-disruptive method further comprises a passive method.

- 10. The method of claim 1 wherein the allocation of PSD based upon system coupling power further comprises implementing a full mask control scheme.
- 11. The method of claim 1 wherein the allocation of PSD based upon system coupling power further comprises implementing a selective bit control scheme.
- 12. The method of claim 1 wherein the allocation of PSD based upon system coupling power further comprises implementing a power swap scheme.
- 13. A system for dynamically monitoring and allocating upstream and downstream power spectral density (PSD) of a transceiver set, the system comprising:
 - a monitor for performing multi-ended line tests (MELT);
 - a controller, responsive to the monitor, for dynamically allocating upstream and downstream PSD; and
 - a table of upstream PSD and downstream PSD for each time (t) and each line.
- 14. The system of claim 13, wherein the monitor is receptive to *a priori* information from other system levels.
- 15. The system of claim 13, wherein the controller is receptive to *a priori* information from other system levels.

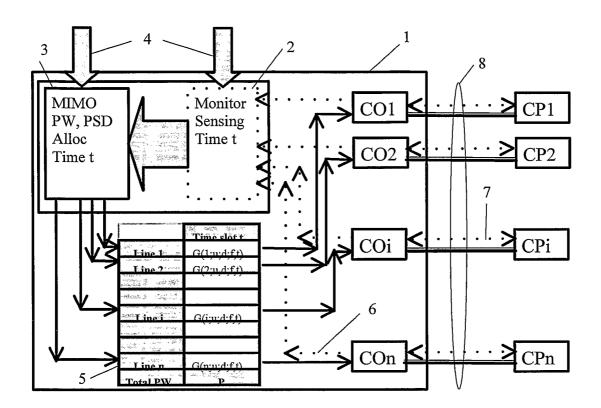


Figure 1

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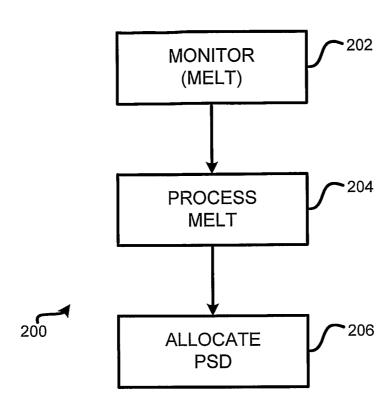


FIGURE 2