

(19) World Intellectual Property Organization
International Bureau



(43) International Publication Date
23 March 2006 (23.03.2006)

PCT

(10) International Publication Number
WO 2006/031411 A2

(51) International Patent Classification:
G03F 7/09 (2006.01) **G03F 7/038** (2006.01)

(21) International Application Number:
PCT/US2005/030407

(22) International Filing Date: 26 August 2005 (26.08.2005)

(25) Filing Language: English

(26) Publication Language: English

(30) Priority Data:
10/938,161 9 September 2004 (09.09.2004) US

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(81) Designated States (unless otherwise indicated, for every kind of national protection available): AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BW, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KM, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NA, NG, NI, NO, NZ, OM, PG, PH, PL, PT, RO, RU, SC, SD, SE, SG, SK, SL, SM, SY, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, YU, ZA, ZM, ZW.

(84) Designated States (unless otherwise indicated, for every kind of regional protection available): ARIPO (BW, GH, GM, KE, LS, MW, MZ, NA, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HU, IE, IS, IT, LT, LU, LV, MC, NL, PL, PT, RO, SE, SI, SK, TR), OAPI (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

Published:

— without international search report and to be republished upon receipt of that report

For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.

(54) Title: A CONDUCTIVE LITHOGRAPHIC POLYMER AND METHOD OF MAKING DEVICES USING SAME

(57) Abstract: A conductive photolithographic film and method of forming a device using the conductive photolithographic film. The method includes depositing a conductive photolithographic film on a top surface of a substrate; and patterning the conductive photolithographic film to create a desired circuit pattern using a lithographic process. The conductive photolithographic film comprising about 50% to about 60% of a mixture of epoxy acrylate, a thermal curing agent, and a conductive polymer; about 20% to about 30% of a lithographic reactive component; about 10% to about 15% of a photo-active material; and about 3% to about 5% of additives that enhance conductivity of the conductive photolithographic polymer.



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A CONDUCTIVE LITHOGRAPHIC POLYMER AND METHOD OF MAKING DEVICES USING SAME

BACKGROUND

[0001] Embodiments of the present invention relate to a conductive lithographic polymer
5 and method of making interconnection and conductive features for devices incorporating
the conductive lithographic polymer.

[0002] An integrated circuit is an interconnected ensemble of devices formed within a
semiconductor material and within a dielectric material that overlies a surface of the
semiconductor. Devices which may be formed within the semiconductor include
10 transistors, bipolar transistors, diodes and diffused resistors, to name a few. Devices
which may be formed within the dielectric include thin-film resistors and capacitors.
Silicon or silicon comprising material is typically used as the substrate for these devices.

[0003] Typically, more than 100 integrated circuit die (IC chips) are constructed on a
single 8-inch diameter silicon wafer. The devices are interconnected by conductor paths
15 (also referred to as metalization layer) formed within the dielectric. Typically, two or
more levels of conductor paths, with successive levels separated by a dielectric layer, are
employed as interconnections. In current practice, metalization layers, typically made of
copper, are formed on dielectric layers to establish the conductor paths. Examples of
processing methods to form the metalization layers include chemical vapor deposition
20 (CVD), physical vapor deposition (PVD), and electrochemical deposition.

Electrochemical deposition of copper has been found to provide the most cost-effective
manner in which to deposit a copper metalization layer. In addition to being economically

viable, such deposition techniques provide substantially conformal copper films that are mechanically and electrically suitable for interconnect structures.

[0004] An example of an electrochemical deposition is described (**Figures 1A-1E**). First, an electroless copper plated layer 102 is formed on an insulating layer 104 of a substrate
5 106, which may include thereon conductive features or devices 108 (**Figure 1A**). The insulating layer 104 includes through holes or vias 110 to enable connection to the conductive features 108. The insulating layer 104 is typically an insulating layer which is interposed between conductor patterns formed on multiple layers for the purpose of ensuring the electrical insulation between the conductor patterns.

10 [0005] Next, a layer of photoresist layer 112 is patterned on the electroless copper plated layer 102 as shown in **Figure 1B**. Next, an electrolytic copper plated layer 114 is formed on the exposed electroless copper plated layer 102 as shown in **Figure 1C**. The electroless copper plated layer 102 is used as an electrical feed or seed layer for the electrolytic plating layer 114.

15 [0006] Next, the photoresist layer 112 is removed as shown in **Figure 1D**. Next, the exposed electroless copper plated layer 102 is removed as shown in **Figure 1E** by using a copper etching solution. Typically, an alkali etching solution is used as the etching solution. Due to the foregoing, a conductor pattern 116, in which the electrolytic copper plated layer 114 is laminated on the electroless copper plated layer 102, can be formed on
20 the insulating layer 104. This process is typically repeated over and over for a formation of a multilayered device.

[0007] The current practice causes devices (e.g., printed circuit boards) to be fabricated with a long process throughput time because of the time consuming for film lamination to

complete the patterning of the conductive layers. Layers of photoresist need to be used, layers of electroless plating and electrolytic plating have to be used, and removed at each step. In addition, various control systems are needed for chemical solutions to maintain line stability, for instance, in electroless plating process there is a need to control

5 accurately and carefully the amount to be deposited and as well as controlling the thickness of the electroless plating. The fabrication process for devices thus becomes time consuming and costly.

BRIEF DESCRIPTION OF THE DRAWINGS

10 [0008] The embodiments of the present invention are illustrated by way of example and not by way of limitation in the figures of the accompanying drawings in which like references indicate similar elements. The invention may best be understood by referring to the following description and accompanying drawings that are used to illustrate

15 embodiments of the invention. It should be noted that references to “an” or “one” embodiment of the invention in this disclosure are not necessarily to the same embodiment, and they mean at least one. In the drawings:

[0009] **Figures 1A-1E** illustrate an example of a current practice of forming a conductor path for a device using electrochemical process;

[0010] **Figures 2A-2E** illustrate an exemplary method of forming a conductor path in

20 accordance to the embodiments of the present invention; and

[0011] **Figures 3A-3D** illustrate another exemplary method of forming a conductor path in accordance to the embodiments of the present invention;

DETAILED DESCRIPTION

[0012] Exemplary embodiments are described with reference to specific configurations and techniques. Those of ordinary skill in the art will appreciate the various changes and
5 modifications to be made while remaining within the scope of the appended claims. Additionally, well known elements, devices, components, circuits, process steps and the like are not set forth in detail.

[0013] Exemplary embodiments of the present invention pertain to a conductive lithographic polymer and method of fabricating devices (e.g., a printed circuit board) with
10 interconnection and conductive features incorporating the conductive lithographic polymer. Conductive traces and conductive paths are used throughout fabrication processes of devices. For examples, devices are formed on substrates and are interconnected to one another through insulation layers and conductive paths or metalization layers. As previously discussed, electrochemical is one of the well-known
15 methods that are widely practiced for making such conductive paths. The conventional methods commonly require multiple etching and lithographic steps in fabricating the conductive paths for the semiconductor devices. The currently practice requires long throughput time and incurs extensive cost for making the devices.

[0014] Exemplary embodiments of the present invention disclose a conductive
—20— lithographic polymer that can be used to fabricate conductive paths, metalization layers or other conductive features for the semiconductor devices. As used herein, a conductive lithographic polymer can be a synthetic material that includes an intrinsically conductive polymer and a polymer having optical properties. A conductive lithographic polymer can

also be a material that includes a lithographic polymer and a conducting polymer that includes a physical mixture of a nonconductive polymer with a conducting material such as a metal or carbon power. A conductive lithographic polymer can also be a material that includes a lithographic polymer and a conducting polymer that includes a physical mixture
5 of a conductive polymer with additional conducting material such as a metal or carbon power added to the mixture to enhance the conductivity of the conductive polymer. The conductive lithographic polymer can be used as a photoresist material due to its optical properties. Normal lithographic methods can be used to pattern the conductive lithographic polymer. The lines, traces, or paths formed using the conductive lithographic
10 polymer can then be used as conductive traces or paths, or metalization layers due to its conductive properties. The conductive lithographic polymer thus may replace the ordinary photoresist layer plus the electroless plating layer previously mentioned or may replace the ordinary photoresist layer, the electroless plating layer, and the electrolytic plating layer previously mentioned. The conductive lithographic polymer may also be used to form
15 other conductive traces and/or metalization layers currently being formed using metals such as copper.

[0015] In one embodiment, the conductive lithographic polymer is a mixture that comprises of (1) about 50% to about 60% (weight percentage) of a mixture of epoxy acrylate, a thermal curing agent, and a conductive polymer; (2) about 20% to about 30%
20 (weight percentage) of a lithographic reactive component; (3) about 10% to about 15% (weight percentage) of a photo-active material; and (4) about 3% to about 5% (weight percentage) of additives that enhance or serve other function conductivity of the conductive photolithographic polymer.

[0016] The ratio of epoxy acrylate, a thermal curing agent, and a conductive polymer may be varied depending on the desired level of conductivity of the conductive lithographic polymer and the conductivity of the conductive polymer used for the mixture.

[0017] In one embodiment, the epoxy acrylate is a mixture of acid functional monomers and non-acid functional monomer. The epoxy acrylate may be selected from a group consisting of acrylic acid, methacrylic acid, maleic acid, fumaric acid, citraconic acid, 2-acrylamido-2-methylpropanesulfonic acid, 2-hydroxyethyl acryloyl phosphate, 2-hydroxypropyl acryloyl phosphate, 2-hydroxy-alpha-acryloyl phosphate, and methyl acrylate, 2-ethyl hexyl acrylate, n-butyl acrylate, n-hexyl acrylate, methyl methacrylate, hydroxy ethyl acrylate, butyl methacrylate, octyl acrylate, 2-ethoxy ethyl methacrylate, and t-butyl acrylate.

[0018] In one embodiment, the thermal curing agent is selected from a group consisting of imidazole, imidazole derivative, 2-methylimidazole, 2-ethylimidazole, 2-ethyl-4-methylimidazole, 2-phenylimidazole, 4-phenylimidazole, 1-cyanoethyl-2-phenylimidazole, 1-(2-cyanoethyl)-2-ethyl-4-methylimidazole, guanamine, acetoguanamine, benzoguanamine, amine, dicyandiamide, benzyldimethyl amine, 4-(dimethylamino)-N,N-dimethylbenzyl amine, 4-methoxy-N,N-dimethylbenzyl amine, 4-methyl-N,N-dimethylbenzyl amine, melamine, phenolic resin, phenol novolak resin, and cresol novolak resin.

[0019] In one embodiment, the conductive polymer is inherently or naturally conductive. In one embodiment, the conductive polymer is selected from a group consisting of polyaniline, polypyrrole, and polythiophene, polyphenylenevinylene, polydialkylfluorene, polyaniline derivatives, polypyrrole derivatives, polythiophene, derivatives, and a

nanocomposite polymer. The conductive material can also be Iodine (AsF₅), polyacetylene, or poly sulfur nitride with dopant.

[0020] In one embodiment, the lithographic reactive component is a monomer, a dimer, or a short chain oligomer having ethylenic unsaturation. The lithographic reactive component may be selected from a group consisting of styrene maleic anhydride copolymers, and similar anhydride-containing copolymers, wherein each of the styrene maleic anhydride copolymer, and the similar anhydride-containing copolymers is partially esterified with hydroxy-functional (meth)acrylic esters. In one embodiment, the hydroxy-functional (meth)acrylic esters is selected from a group consisting of hydroxyethyl acrylate, acrylic acid, methacrylic acid, maleic acid, fumaric acid, and citraconic acid functional monomers.

[0021] In one embodiment, the photo-active material is selected from a group consisting of 9-phenylacridine, n-phenyl glycine, aromatic ketones, N,N'-tetraethyl-4, 4'-diaminobenzophenone, and 4-methoxy-4'-dimethylaminobenzophenone. The aromatic ketones can be benzophenone, N, N'-tetramethyl-4, or 4'-diaminobenzophenone.

[0022] A surfactant may also be included in the additives. In one embodiment, the additives are selected from a group consisting of a color former, a surfactant, a catalyst, a filler, a plasticizer, and a metal powder. The additives can be component that will enhance the conductivity of the polymer. Materials that can be used as additives also include a conductive agents, metals, metal powders, and nanosize metal powders. The conductive materials can be copper, gold, titanium, chromium, aluminum, iron, nickel, cobalt, zinc, copper zinc, nickel-iron, cobalt-iron, silver, Graphite, or carbon black powder, etc.

[0023] A solvent is included in the mixture that makes the conductive lithographic polymer. The solvent can be selected from various types of suitable organic solvents including a hydrocarbon solvent or an alcohol. The organic solvent can also be selected from a group consisting of ketone, methyl ethyl ketone, cyclohexanone, aromatic hydrocarbon, toluene, xylene, tetramethyl benzene, glycol ether, diethylene glycol monoethyl ether, dipropylene glycol diethyl ether, ester, ethyl acetate, butyl acetate, butyl cellosolve acetate, carbitol acetate, aliphatic hydrocarbon, octane, decane, petroleum solvent, petroleum ether, petroleum naphtha, and solvent naphtha.

[0024] The conductive photolithographic polymer has a conductivity comparable to that of a conductive metal such as copper, iron, nickel, cobalt, zinc, copper zinc, nickel-iron, or cobalt-iron. The conductivity of the conductive photolithographic polymer can be brought up the conductivity of these conductive metals by varying the concentration of the additives or the metal powder. In one embodiment, the conductivity of the conductive photolithographic polymer ranges from about 1×10^{-10} to about 1×10^6 Siemens per centimeter. Generally, copper has a conductivity level of about 1×10^6 Siemens per centimeter and a semiconductor material has a conductivity level of about 1×10^{-8} Siemens per centimeter to about 1×10^2 Siemens per centimeter.

[0025] Films, patterns, or lines formed using the conductive lithographic polymer can be achieved using lithographic techniques currently used in device fabrication. Standard lithographic printing technology can be used to fabricate films, lines, or patterns of the conductive lithographic polymer on various substrates. After the fabrication, these films, lines, or patterns of the conductive lithographic polymer may form conductive features or metalization for various semiconductor or electronic devices. The conductive lithographic polymer can be formed on a substrate from an ink, a solution, or a dry film with a

deposition, printing, or lamination process. The conductive lithographic polymer can be used in the fabrication of an electronic device, a micro-electronic device, a microprocessor, a chipset, an electrical controller, a printed circuit board, an electrical appliance, an optical coupler, an optoelectronic component, a display component, a liquid crystal display, or a flat panel display, etc., which may all use the conductive lithographic polymer in their circuitry.

[0026] In one embodiment, an electronic device such as a printed circuit board is formed using a conductive lithographic polymer such as those previously described. It is to be noted that the exemplary mixtures of the conductive lithographic polymer previously mentioned are not the only components that can be used for the conductive lithographic polymer used in making the electronic device. The fabrication of an electronic device that may benefit from the conductive photolithographic polymer will be described with reference to **Figures 2A-2D**.

[0027] First, an insulating layer 250 having an opening 240 as a via or a through hole is formed on each of both (top and bottom) surfaces of a core substrate 200 (**Figure 2A**). The substrate 200 can be any desired substrate such as an organic material, ceramic, glass, or semiconductor material such as silicon, silicon containing material, silicon on insulation material, silicon germanium material. The substrate 200 may include microelectronic structures such as transistors or integrated circuits (not shown) formed thereon or therein.

[0028] The substrate 200 also includes conductive features 202. The conductive features 202 can be formed on the top surface, side surfaces, and bottom surface of the substrate 200. The conductive features 202 can be a conductive contact or contacts or metalization layers for the devices (not shown) that have been formed in the substrate 200 such as a

transistor or an integrated circuit using methods known in the art. The conductive contacts can be made of copper, titanium, aluminum, chromium, or other suitable conductive material.

[0029] The insulating layer 250 is a conventional dielectric or insulating material typically used in semiconductor devices. The insulating layer 250 is formed on the substrate 200 using conventional methods as is known in the art. Vias or through holes 240 are created into the insulating layer 250 so that electrical contacts can be established to the conductive features 202. The insulating layer 250 can be formed on both top and bottom surfaces of the substrate 200 depending on applications and devices.

[0030] Next, a conductive photolithographic film 230 is formed on a surface of the insulating layer 250 (**Figure 2A**). In one embodiment, the conductive photolithographic film 230 is formed on both top and bottom surfaces of the insulating layer 250. In one embodiment, the conductive photolithographic film 230 is formed from an ink solution where the conductive photolithographic material is printed or laid down on the surface of the insulating layer 250. The printed conductive photolithographic material is then allowed to dry or cure to form the conductive photolithographic film 230. Alternatively, the conductive photolithographic film 230 is formed by laminating a dry film down onto the surface of the insulating layer 250. Pressure and temperature may be applied to cause the conductive photolithographic film 230 to flow into the vias or through holes 240 to contact the conductive features 202.

[0031] Next, the conductive photolithographic film 230 is masked (**Figure 2B** with a mask 241) according to a desired circuit pattern for the film 230 using methods known in the art similar to masking a photoresist film. The conductive photolithographic film 230 is

then exposed to light 245, for instance, at about 50-150 mJ/cm² as typically done in exposing a photoresist film. Then, the light exposed conductive photolithographic film 230 is developed to create the desired pattern of conductive photolithographic film. The unmasked portion of the conductive photolithographic film 230 is removed upon the development of the film 230 leaving the masked portion of the conductive photolithographic film 232 as shown in **Figure 2C**. The developing solution can be a conventional developing solution used to develop a photoresist film as is known in the art, e.g., a 0.7-1.0% solution of sodium.

[0032] The conductive photolithographic film 232 may have a thickness similar to that of a typical electroless plating film used in conventional process for forming conductor paths as previously described. In one embodiment, the conductive photolithographic film 232 has a thickness between about 0.5 μ m to about 10 μ m.

[0033] The traces of the conductive photolithographic film 232 is also designed or configured such that all traces are connected to external poles (not shown) for electrolytic copper plating. The connecting traces are later on etched away after the electrolytic copper plating.

[0034] Next, an electrolytic plating film 234 is formed on the conductive photolithographic film 232 (**Figure 2D**) using techniques known in the art. For example, the electrolytic plating film 234 can be formed by dipping the substrate 200 into an electrolytic plating liquid and flowing an electric current through the conductive photolithographic film 232. The electrolytic plating film 234 will be formed on the conductive photolithographic film 232. The electrolytic plating film 234 may have a thickness of about 5-30 μ m. The electrolytic plating film 234 is typically an electrolytic

plating copper film. In one embodiment, a solution comprising sulfuric acid of about 180 g/L and copper sulfate of about 80 g/L is used for the electrolytic plating process to form the electrolytic plating film 234. Together, the electrolytic plating film 234 and the conductive photolithographic film 232 form conductive patterns or circuit patterns 236 of the semiconductor device. In the embodiments where the conductive photolithographic film 232 is formed on the top and bottom surfaces of the substrate 200, the electrolytic plating film 234 may also be formed on both top and bottoms surfaces of the substrate 200 as shown in **Figure 2D**. After the electrolytic plating, the traces connected to external poles used for the electrolytic plating are etched away using a conventional method.

10 [0035] The process illustrated **Figures 2A-2D** can be repeated as many times as necessary for forming additional layers of conducting patterns for a multilayer semiconductor devices. For instance, another insulation layer 247 can be formed on top of the substrate 200 and over the conductive patterns 236 as shown in **Figure 2E**. Vias/holes 249 are created through the insulation layer to allow for connection to the conductive patterns 236 or other conductive features on the substrate 200. Then, the process described for forming the conductive photolithographic film and the electrolytic film previously described can be likewise repeated to create another desired circuit pattern.

[0036] In one embodiment, the conductive features 202 shown above can also be made from a conductive lithographic polymer. In the present embodiment, a conductive lithographic polymer such as those previously described (in the form of an ink or a dry film) is formed, deposited, or laminated on the substrate 200 to form a conductive lithographic polymer film. The conductive lithographic polymer film is then masked, exposed, and developed similar to previously described to form a desired circuit pattern for the conductive features 202. After the conductive lithographic polymer film is

patterned, the conductive elements 202 are formed on the substrate 250. In the embodiment where the conductive lithographic polymer is laid down as a film, pressure and temperature may be applied to cause the conductive lithographic polymer film to flow into crevices, openings, trenches, or vias (not shown) on the substrate 200.

5 [0037] In the embodiments above, the conductive lithographic polymer is used to replace the electroless copper process and the photoresist process as used in the conventional method to form conductive patterns. In some other embodiments, a conductive lithographic polymer is used to replace the electroless copper plating, the photoresist, as well as the electrolytic copper plating as used in conventional methods of forming
10 conducting patterns. **Figures 3A-3C** illustrate such embodiments. In these embodiments, a conductive lithographic polymer film is first formed on a surface of a substrate. The conductive lithographic polymer film is then masked, exposed, and developed to form a conducting pattern. The conductive lithographic polymer film is formed sufficiently thick and with sufficient conductivity so that the electrolytic plating is not necessary.

15 [0038] In **Figure 3A**, an insulating layer 350 having an opening 340 as a via or a through hole is formed on a surface (as shown herein, each of the top and bottom surfaces) of a core substrate 300. The substrate 300 can be any desired substrate such as an organic material, ceramic, glass, or semiconductor material such as silicon, silicon containing material, silicon on insulation material, silicon germanium material. The substrate 300
20 may include microelectronic structures such as transistors or integrated circuits (not shown) formed thereon or therein. The substrate 300 also includes conductive features 302. The conductive features 302 can be formed on the top surface, side surfaces, and bottom surface of the substrate 300. The conductive features 302 can be a conductive contact or contacts or metalization layers for the devices (not shown) that have been

formed in the substrate 300 such as a transistor or an integrated circuit using methods known in the art. The conductive contacts can be made of copper, titanium, aluminum, chromium, or other suitable conductive material.

[0039] Next, a conductive photolithographic film 330 is formed on a surface of the
5 insulating layer 350. In one embodiment, the conductive photolithographic film 330 is formed on both top and bottom surfaces of the insulating layer 350. In one embodiment, the conductive photolithographic film 330 is formed from an ink solution where the conductive photolithographic material is printed or laid down on the surface of the insulating layer 350. The printed conductive photolithographic material is then allowed to
10 dry or cure to form the conductive photolithographic film 330. Alternatively, the conductive photolithographic film 330 is formed by laminating a dry film down onto the surface of the insulating layer 350. Pressure and temperature may be applied to cause the conductive photolithographic film 330 to flow into the vias or through holes 340 to contact the conductive features 302. The conductive photolithographic material may fill into the
15 openings, vias, trenches, or crevices after being formed on the insulating layer as shown in Figures 3A-3C.

[0040] Next, the conductive photolithographic film 330 is masked (**Figure 3B**) with a mask 362 according to a desired circuit pattern for the film 330 using methods known in the art similar to masking a photoresist film. The conductive photolithographic film 330 is
20 then exposed to light 360, for instance, at about 50-150 mJ/cm² as typically done in exposing a photoresist film. Then, the light exposed conductive photolithographic film 330 is developed to create the desired pattern of conductive photolithographic film. The unmasked portion of the conductive photolithographic film 330 is removed upon the development of the film 330 leaving the masked portion of the conductive

photolithographic film 332 as shown in **Figure 3C**. The developing solution can be a conventional developing solution used to develop a photoresist film as is known in the art, e.g., a 0.7-1.0% solution of sodium.

5 [0041] The conductive photolithographic film 332 may have a thickness similar to that of a typical electroless plating film plus a typical electroplating film used in conventional processes for forming conductor path as previously described. In one embodiment, the conductive photolithographic film 332 has a thickness between about 10 μm to about 100 μm .

10 [0042] The process illustrated in 232 **Figures 3A-3C** can be repeated as many times as necessary for forming additional layers of conducting patterns for a multilayer semiconductor devices. For instance, as shown in **Figure 3D**, another insulation layer 347 can be formed on top of the substrate 300 and over the conductive patterns 332. Vias/holes 349 are created through the insulation layer 347 to allow for connection to the conductive patterns 332 or other conductive features on the substrate 300. Then, the process described for forming the conductive photolithographic film can be likewise repeated to create another desired circuit pattern.

20 [0043] In one embodiment, the conductive features 302 show above are made from a conductive lithographic polymer. In the present embodiment, a conductive lithographic polymer such as those previously described (in the form of an ink or a dry film) is formed, deposited, or laminated on the substrate 300 to form a conductive lithographic polymer film. The conductive lithographic polymer film is then masked, exposed, and developed similar to previously described to form a desired circuit pattern for the conductive features 302. After the conductive lithographic polymer film is patterned, the conductive elements

302 are formed on the substrate 300. In the embodiment where the conductive lithographic polymer is laid down as a film, pressure and temperature may be applied to cause the conductive lithographic polymer film to flow into crevices, openings, trenches, or vias (not shown) on the substrate 300.

- 5 [0044] Embodiments of the present invention may be used to reduce the long process throughput time for semiconductor device fabrication caused by electroless and electrolytic processes. Additionally, the embodiments may allow for reducing of materials used in fabrication intermediate steps (e.g., photoresist material and electroless plating materials) typically used for coating and etching processes.
- 10 [0045] While the invention has been described in terms of several embodiments, those of ordinary skill in the art will recognize that the invention is not limited to the embodiments described. The method and apparatus of the invention, but can be practiced with modification and alteration within the spirit and scope of the appended claims. The description is thus to be regarded as illustrative instead of limiting.
- 15 [0046] Having disclosed exemplary embodiments, modifications and variations may be made to the disclosed embodiments while remaining within the spirit and scope of the invention as defined by the appended claims.

We claim:

1. A conductive photolithographic polymer comprising:
 - about 50% to about 60% of a mixture of epoxy acrylate, a thermal curing agent, and a conductive polymer;
 - 5 about 20% to about 30% of a lithographic reactive component;
 - about 10% to about 15% of a photo-active material; and
 - about 3% to about 5% of additives that enhance conductivity of the conductive photolithographic polymer.
- 10 2. The conductive photolithographic polymer of claim 1 wherein the conductivity of the conductive photolithographic polymer ranges from about 1×10^{-10} to about 1×10^6 Siemens per centimeter.
- 15 3. The conductive photolithographic polymer of claim 1 wherein the epoxy acrylate is a mixture of acid functional monomers and non-acid functional monomer.
- 20 4. The conductive photolithographic polymer of claim 1 wherein the epoxy acrylate is selected from a group consisting of acrylic acid, methacrylic acid, maleic acid, fumaric acid, citraconic acid, 2-acrylamido-2-methylpropanesulfonic acid, 2-hydroxyethyl acryloyl phosphate, 2-hydroxypropyl acryloyl phosphate, 2-hydroxy-alpha-acryloyl phosphate, methyl acrylate, 2-ethyl hexyl acrylate, n-butyl acrylate, n-hexyl acrylate, methyl methacrylate, hydroxy ethyl acrylate, butyl methacrylate, octyl acrylate, 2-ethoxy ethyl methacrylate, and t-butyl acrylate.

5. The conductive photolithographic polymer of claim 1 wherein the lithographic reactive component is a monomer, a dimer, or a short chain oligomer having ethylenic unsaturation.

5 6. The conductive photolithographic polymer of claim 1 wherein the lithographic reactive component is selected from a group consisting of styrene maleic anhydride copolymers, and similar anhydride-containing copolymers, wherein each of the styrene maleic anhydride copolymers, and the similar anhydride-containing copolymers is partially esterified with hydroxy-functional (meth)acrylic esters.

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7. The conductive photolithographic polymer of claim 6 wherein the hydroxy-functional (meth)acrylic esters is selected from a group consisting of hydroxyethyl acrylate, acrylic acid, methacrylic acid, maleic acid, fumaric acid, and citraconic acid functional monomers.

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8. The conductive photolithographic polymer of claim 1 wherein the photo-active material is selected from a group consisting of 9-phenylacridine, n-phenyl glycine, aromatic ketones, N,N'-tetraethyl-4, 4'-diaminobenzophenone, and 4-methoxy-4'-dimethylaminobenzophenone.

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9. The conductive photolithographic polymer of claim 8 wherein the aromatic ketones include benzophenone, N, N'-tetramethyl-4, and 4'-diaminobenzopnenone.

10. The conductive photolithographic polymer of claim 1 wherein the additives are selected from a group consisting of a color former, a surfactant, a catalyst, a filler, a plasticizer, and a metal powder.

5 11. The conductive photolithographic polymer of claim 1 wherein the conductive polymer is selected from a group consisting of polyaniline, polypyrrole, and polythiophene, polyphenylenevinylene, polydialkylfluorene, polyaniline derivatives, polypyrrole derivatives, and polythiophene, derivatives, and a nanocomposite polymer.

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12. The conductive photolithographic polymer of claim 1 wherein the photolithographic polymer is made from an ink comprising an organic solvent, the organic solvent selected from a group consisting of ketone, methyl ethyl ketone, cyclohexanone, aromatic hydrocarbon, toluene, xylene, tetramethyl benzene, glycol ether, diethylene glycol monoethyl ether, dipropylene glycol diethyl ether, ester, ethyl acetate, butyl acetate, butyl cellosolve acetate, carbitol acetate, aliphatic hydrocarbon, octane, decane, petroleum solvent, petroleum ether, petroleum naphtha, and solvent naphtha.

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20 13. The conductive photolithographic polymer of claim 1 wherein the thermal curing agent is selected from a group consisting of imidazole, imidazole derivative, 2-methylimidazole, 2-ethylimidazole, 2-ethyl-4-methylimidazole, 2-phenylimidazole, 4-phenylimidazole, 1-cyanoethyl-2-phenylimidazole, 1-(2-cyanoethyl)-2-ethyl-4-methylimidazole, guanamine, acetoguanamine, benzoguanamine, amine, dicyandiamide, benzyldimethyl amine, 4-

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(dimethylamino)-N,N-dimethylbenzyl amine, 4-methoxy-N,N-dimethylbenzyl amine, 4-methyl-N,N-dimethylbenzyl amine, melamine, phenolic resin, phenol novolak resin, and cresol novolak resin.

5 14. A method of fabricating a device comprising:

depositing a conductive photolithographic film on a top surface of a substrate; and

patterning the conductive photolithographic film to create a desired circuit pattern using a lithographic process.

10

15. The method of claim 14 further comprising:

forming a dielectric layer having one or more vias on the substrate prior to depositing the conductive photolithographic film, wherein the dielectric layer insulates the substrate from the conductive photolithographic film, and wherein the conductive photolithographic film is formed on top of the dielectric layer.

15

16. The method of claim 15 wherein the substrate further includes one or more features having conductive elements formed therein and wherein the vias enable electrical interconnection to the conductive elements.

20

17. The method of claim 16 further comprising:

interconnecting the conductive photolithographic film to the conductive elements.

18. The method of claim 17 further comprising:

forming an additional conductive film on the desired circuit pattern of the photolithographic film; and

5 interconnecting the additional conductive film to the conductive elements through the vias.

19. The method of claim 18 wherein forming the additional conductive film includes using an electrolytic process.

10

20. The method of claim 16 further comprising:

depositing a second conductive photolithographic film on a bottom surface of the substrate; and

15 patterning the second conductive photolithographic film to create a second desired circuit pattern using the lithographic process.

21. The method of claim 20 further comprising:

20 - forming a second dielectric layer having one or more vias on the bottom surface of the substrate prior to depositing the second conductive photolithographic film, wherein the second dielectric layer insulates the bottom surface of the substrate from the second conductive photolithographic film, and wherein the second conductive photolithographic film is formed on top of the second dielectric layer.

22. The method of claim 21 wherein the substrate further includes one or more features having conductive elements formed therein and wherein the vias enable electrical interconnection to the conductive elements.

5

23. The method of claim 22 further comprising:

interconnecting the second conductive photolithographic film to the conductive elements.

10

24. The method of claim 23 further comprising:

forming an additional conductive film on the desired circuit pattern of the second photolithographic film; and

interconnecting the additional conductive film to the conductive elements through the vias.

15

25. The method of claim 24 wherein forming the additional conductive film includes using an electrolytic process.

26. An electronic device comprising:

20

a substrate;

a dielectric layer formed on the substrate;

a conductive photolithographic film formed on top of the dielectric layer, wherein the conductive photolithographic film is patterned according to a desired circuit pattern using a lithographic process.

5 27. The electronic device of claim 26 wherein the conductive photolithographic film further comprising:

about 50% to about 60% of a mixture of epoxy acrylate, a thermal curing agent, and a conductive polymer;

about 20% to about 30% of a lithographic reactive component;

10 about 10% to about 15% of a photo-active material; and

about 3% to about 5% of additives that enhance conductivity of the conductive photolithographic polymer.

15 28. The electronic device of claim 26 wherein the substrate further includes one or more features having conductive elements formed therein and wherein the dielectric layer has vias created therethrough, the vias enable electrical interconnection to the conductive elements, and wherein the conductive photolithographic film is electrically interconnected to the conductive elements through the vias.

20

29. The electronic device of claim 26 further comprising:

an additional conductive film formed on the desired circuit pattern of the conductive photolithographic film, wherein the additional conductive film is interconnected to the conductive elements through the vias.

30. The electronic device of claim 26 wherein the conductive photolithographic film includes an inherently conductive polymer.

5 31. A method of fabricating a device comprising:

depositing a conductive photolithographic film on a top surface of an insulation layer, the insulation layer formed on a substrate;

masking, the conductive photolithographic film according to a desired circuit pattern;

10 exposing the conductive photolithographic film to light; and

developing the conductive photolithographic film and removing unmasked portions of the conductive photolithographic film.

32. The method of claim 32 further comprising:

15 forming a second insulation layer on the substrate and over the conductive photolithographic film after the masking, exposing, and developing;

depositing a second conductive photolithographic film on a top surface of the second insulation layer;

masking, the second conductive photolithographic film according to a desired circuit pattern;

20 exposing the second conductive photolithographic film to light; and

developing the second conductive photolithographic film and removing unmasked portions of the second conductive photolithographic film.

33. The method of claim 31 further comprising:

forming an electrolytic plating film on the conductive photolithographic film after the masking, exposing, and developing.

5 34. The method of claim 33 further comprising:

forming a second insulation layer on the substrate and over the electrolytic plating film;

depositing a second conductive photolithographic film on a top surface of the second insulation layer;

10 masking, the second conductive photolithographic film according to a desired circuit pattern;

exposing the second conductive photolithographic film to light; and

developing the second conductive photolithographic film and removing unmasked portions of the second conductive photolithographic film.

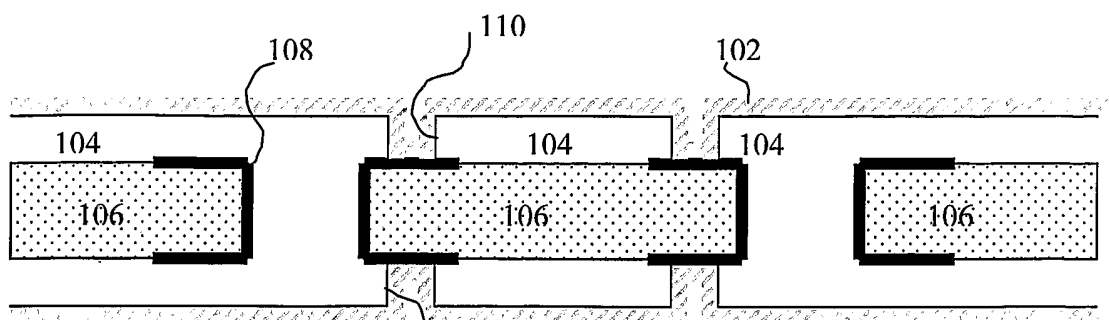
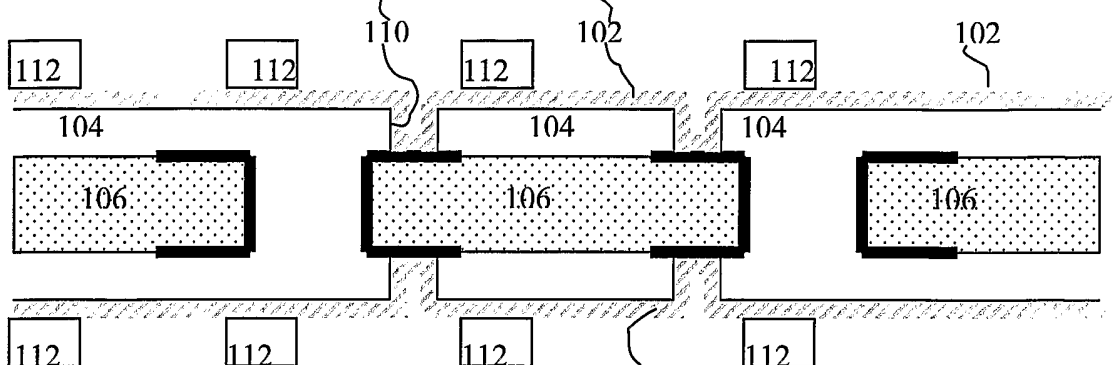
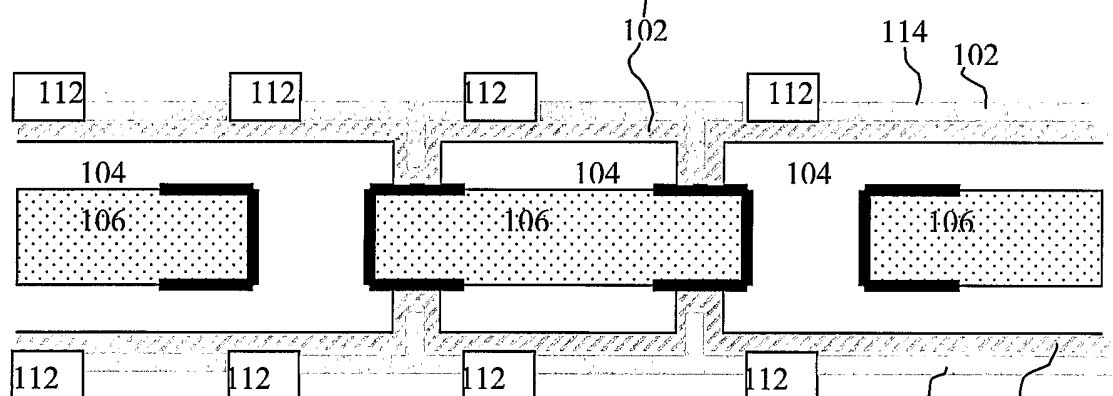
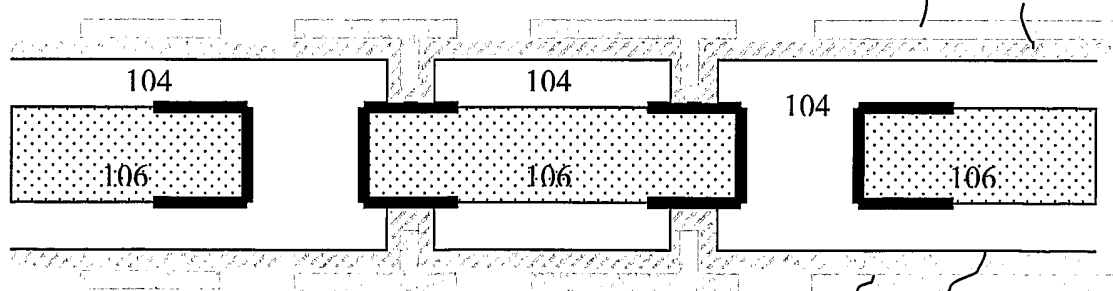
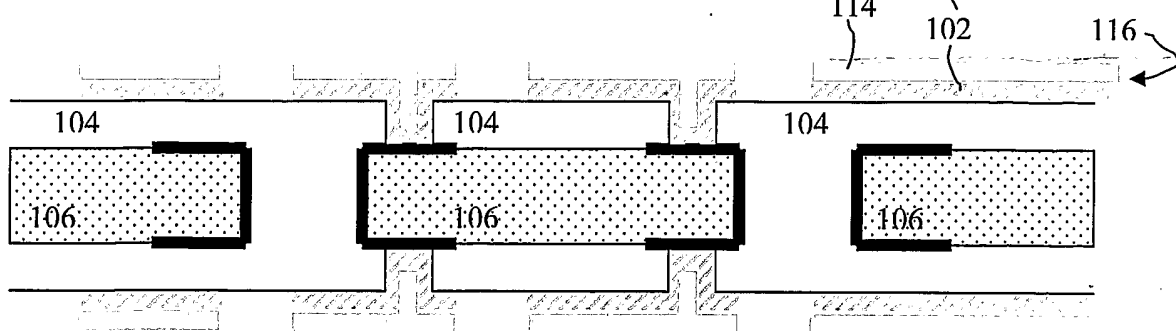
PRIOR ART**Figure 1A****Figure 1B****Figure 1C****Figure 1D****Figure 1E**

Figure 2A

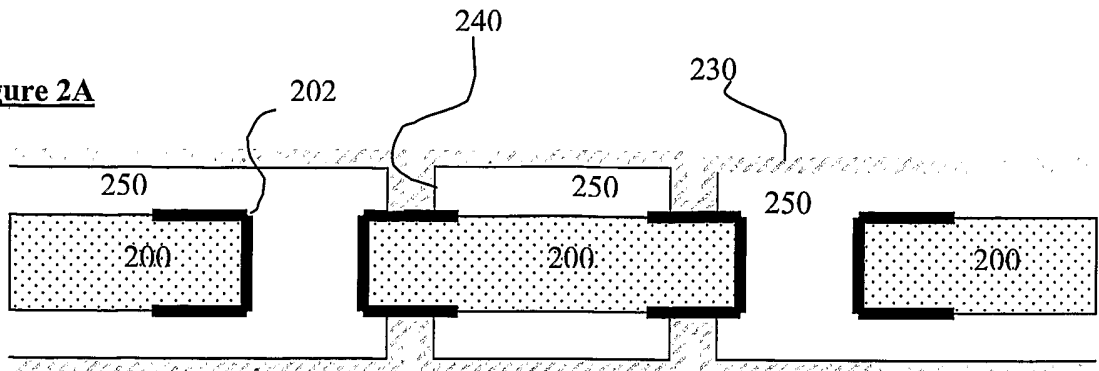


Figure 2B

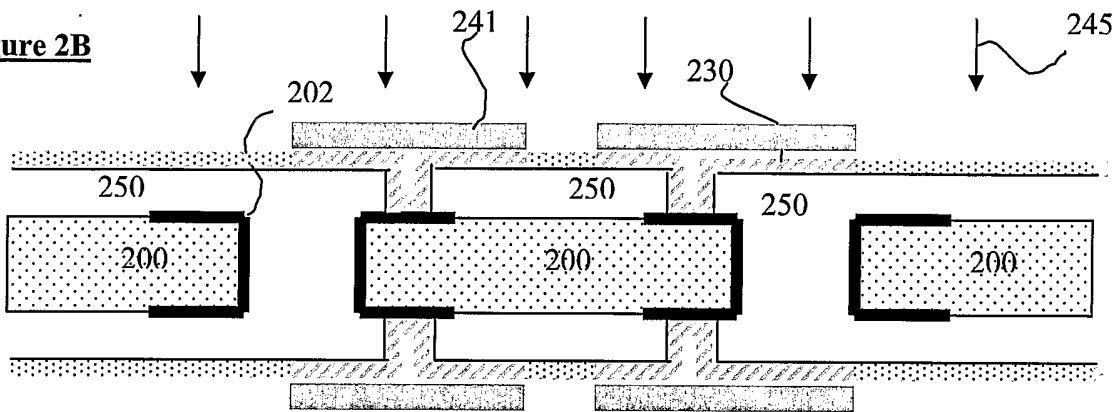


Figure 2C

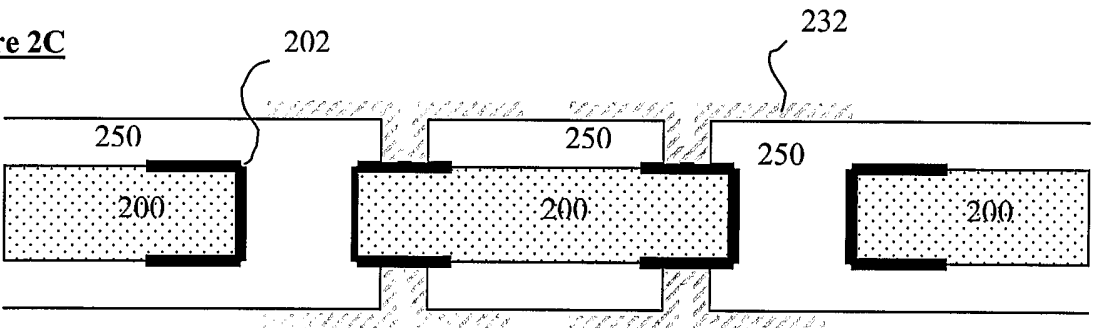


Figure 2D

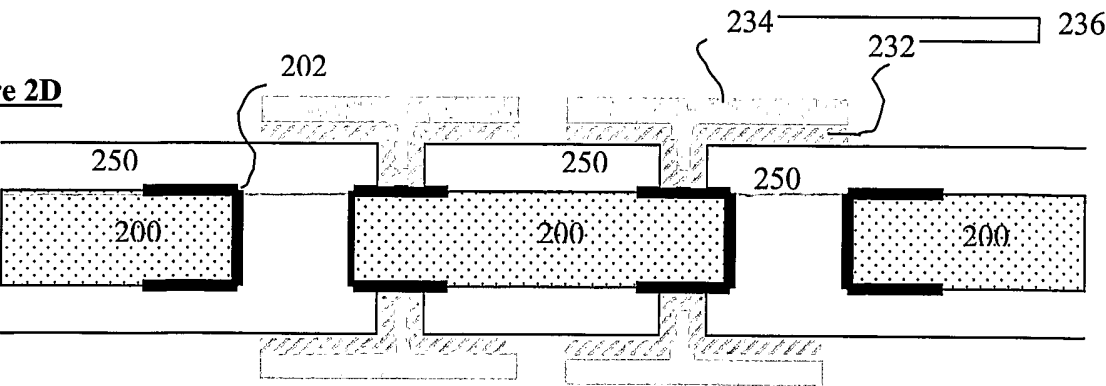


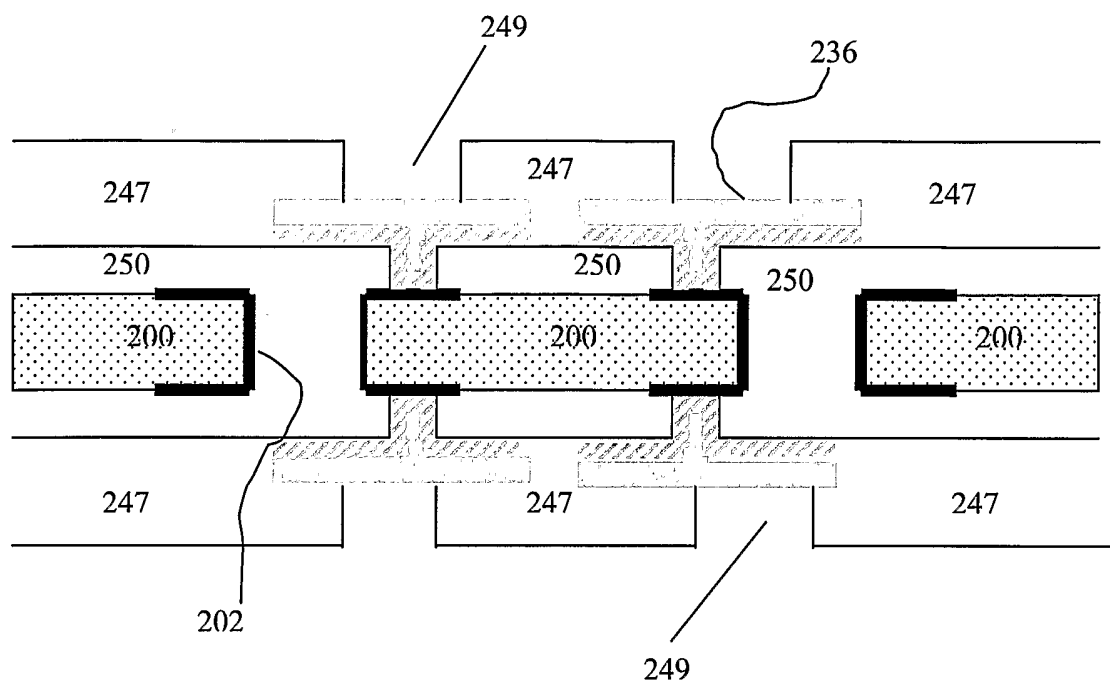
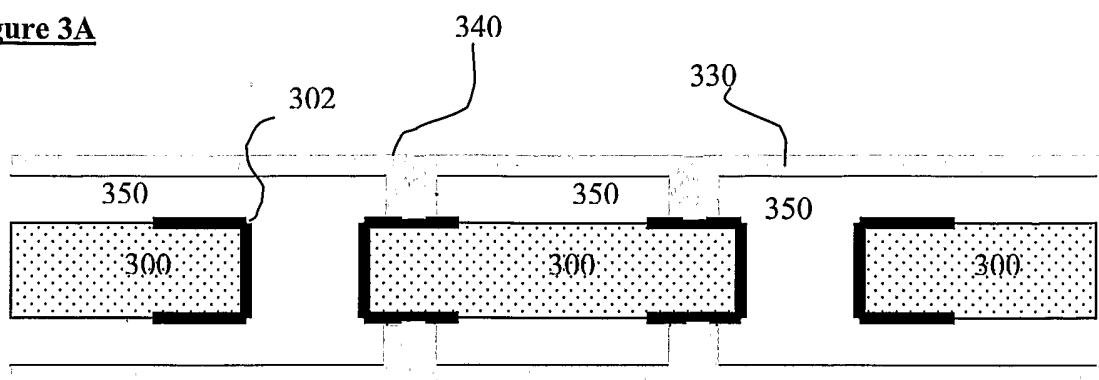
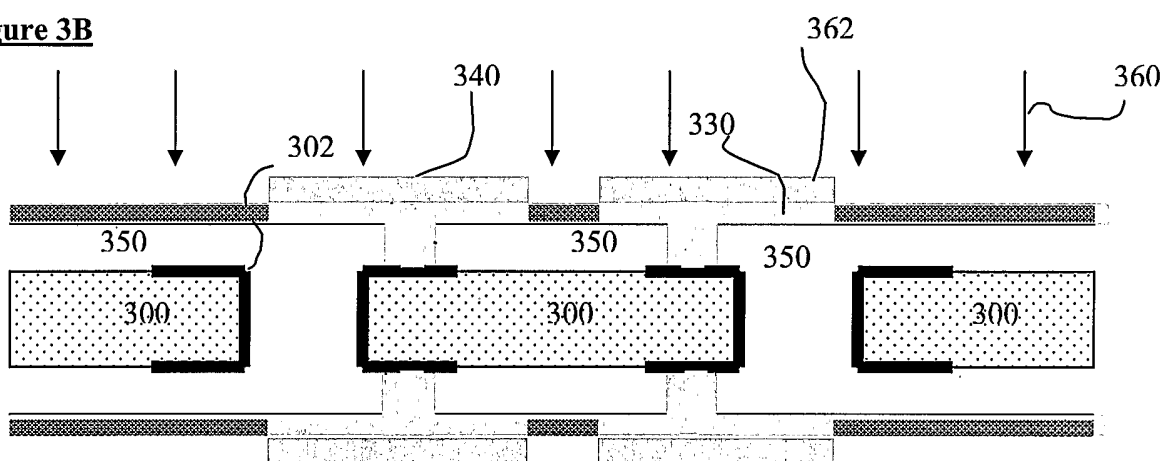
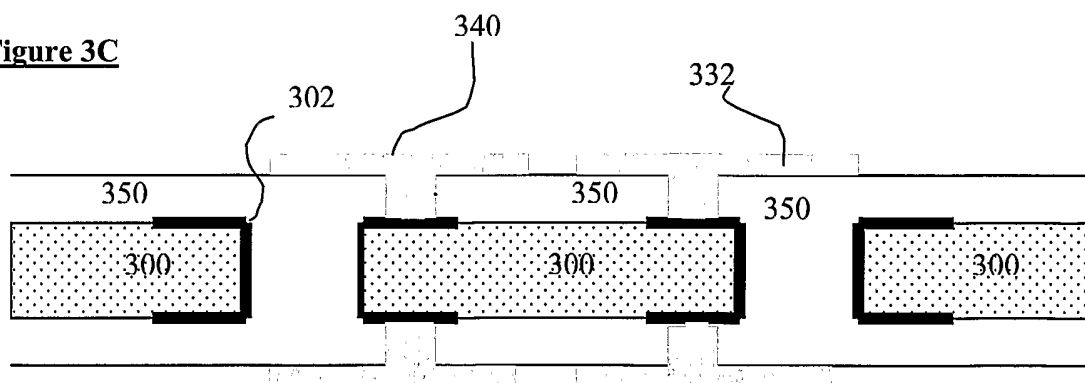
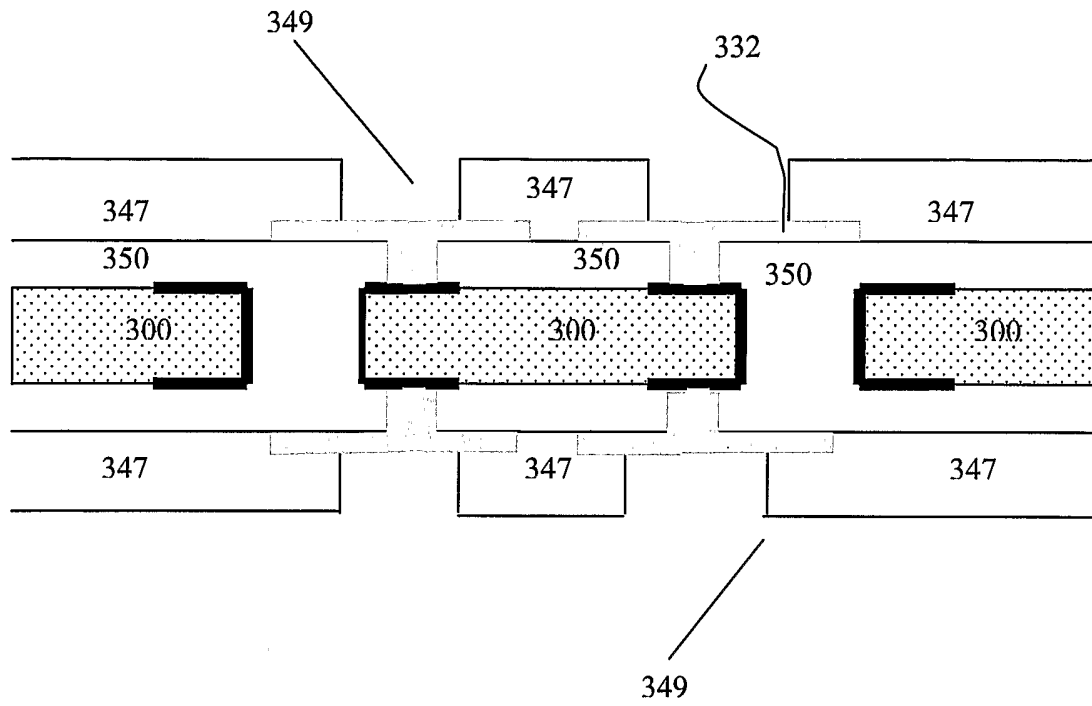
Figure 2E

Figure 3A**Figure 3B****Figure 3C**

**Figure 3D**