FORM 2

THE PATENTS ACT, 1970
(39 of 1970)
&
THE PATENTS RULES, 2003

COMPLETE SPECIFICATION
(See section 10, rule 13)

"INTEGRATED CIRCUITS WITH CACHE-COHERENCY"

QUALCOMM TECHNOLOGIES, INC., a Company incorporated in the State of Delaware, United States of America of Attn: International IP Administration, 5775 Morehouse Drive, San Diego, California 92121-1714, United States of America

The following specification particularly describes the invention and the manner in which it is to be performed.

24. The system of claim 17 wherein the plurality of coherent agents are connected through at least one coherency controller.

25. A method for accessing data stored in a target within a cache coherent system comprising, the method comprising the steps of:

requesting appropriate ownership of the data for the type of desired access;

directly accessing the data from the target that serves as a data backing store; and

relinquishing ownership of the data.

RECTIFIED SHEET (RULE 91)

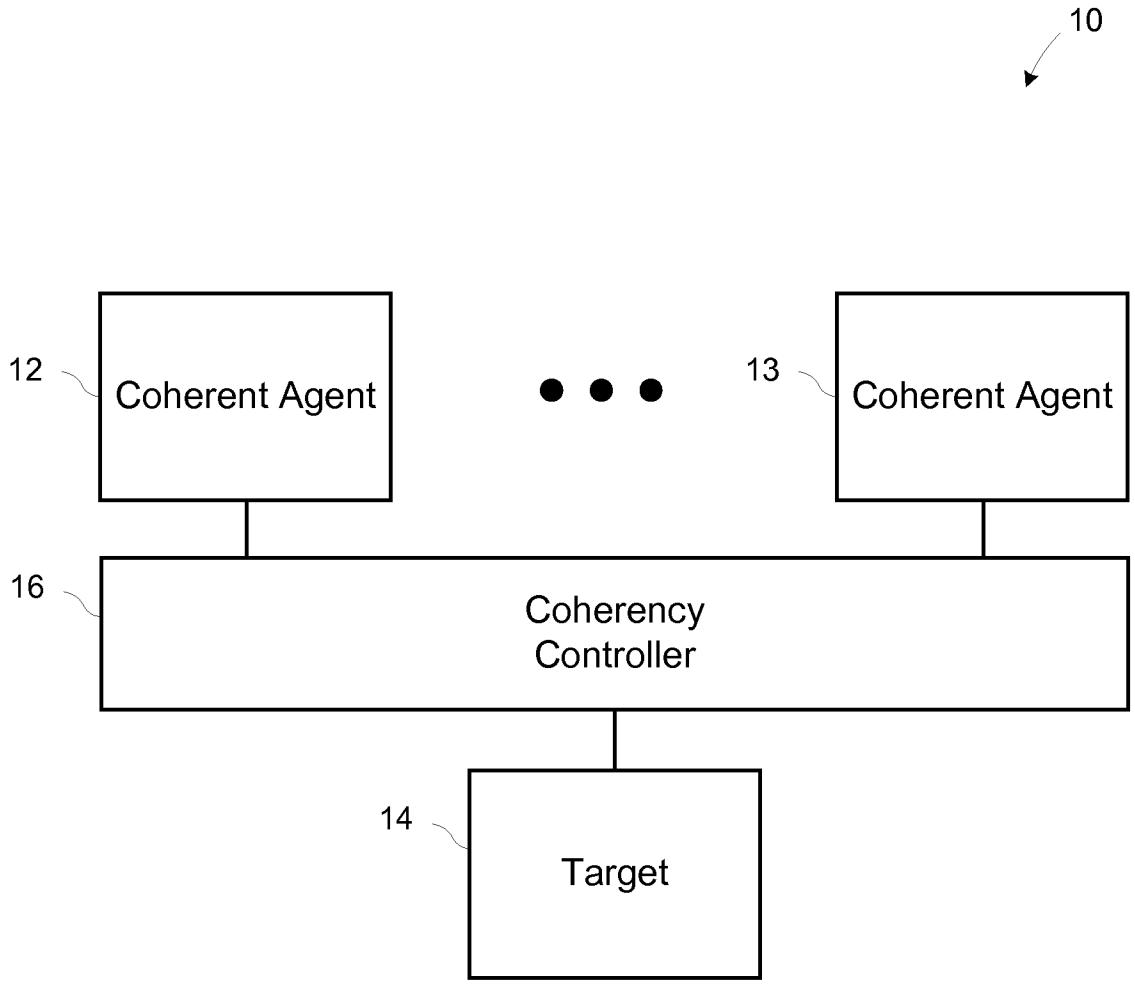

10
12
Coherent Agent
13
Coherent Agent
16
Coherency
Controller
14
Target

FIG. 1

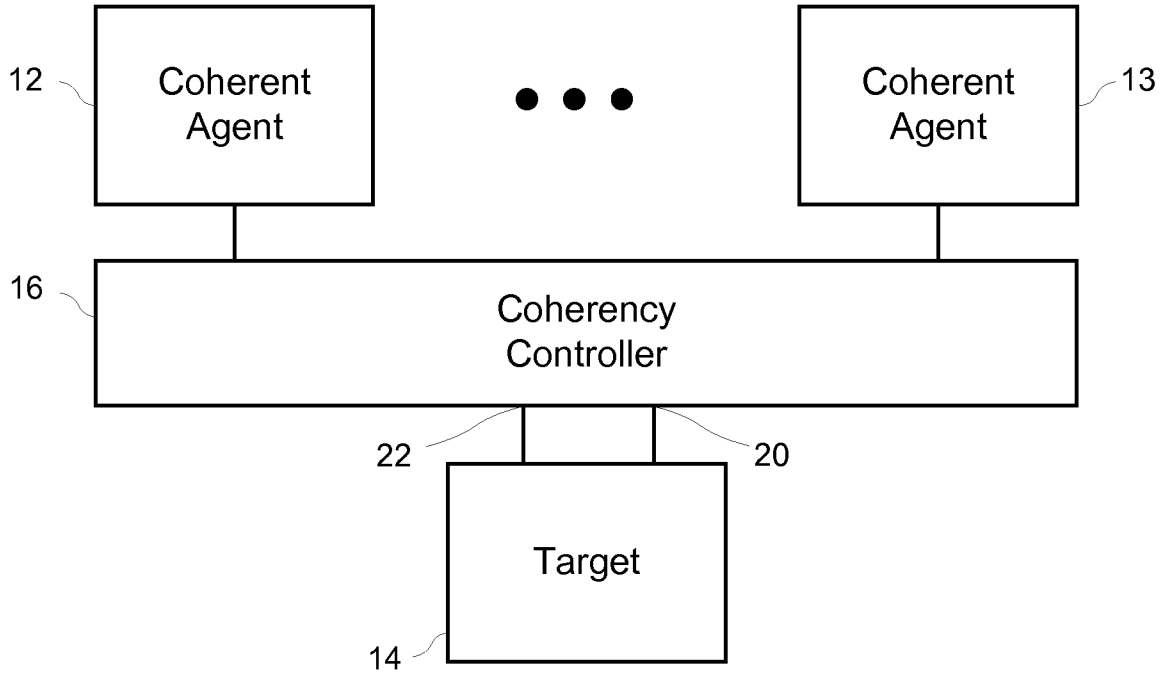

12
Coherent Agent
Coherent Agent
13
16
Coherency Controller
22
20
Target
14

FIG. 2

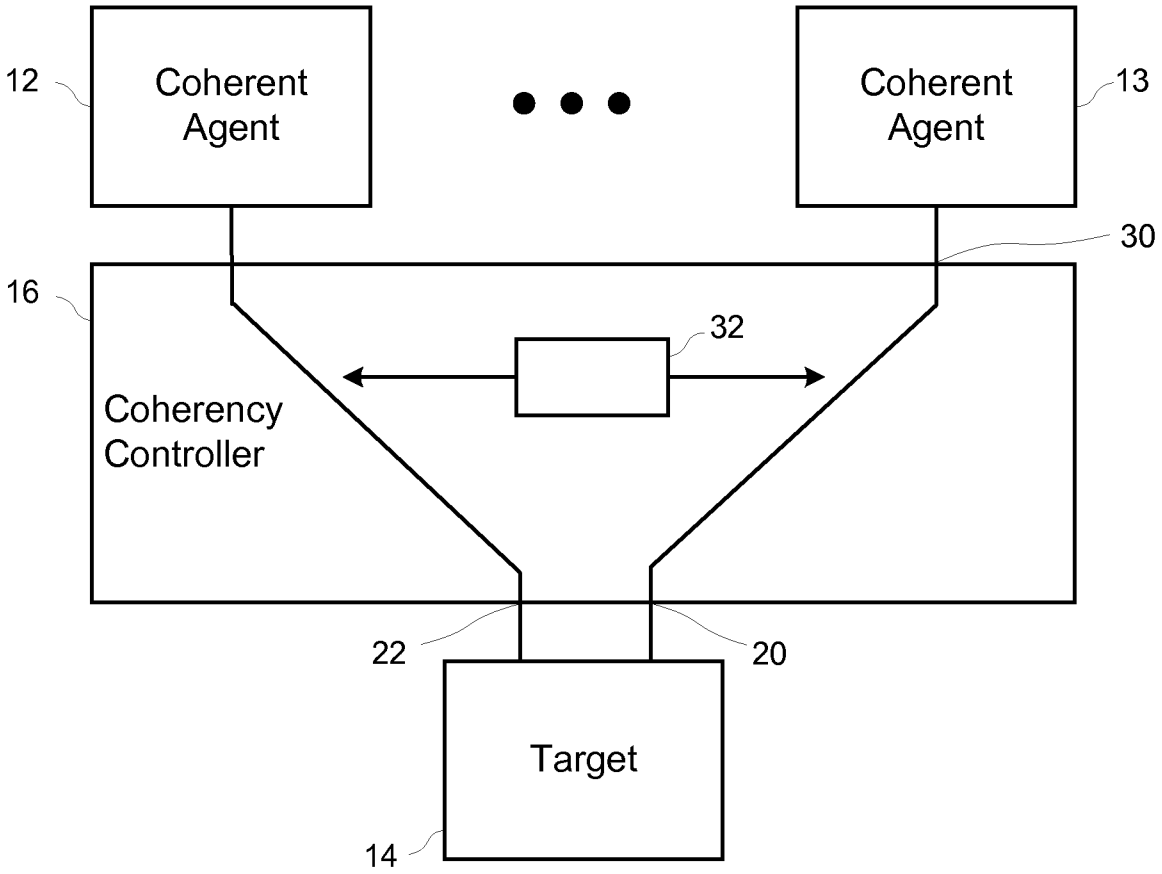

12
Coherent Agent
Coherent Agent
13
30
16
32
Coherency Controller
22
20
Target
14

FIG. 3

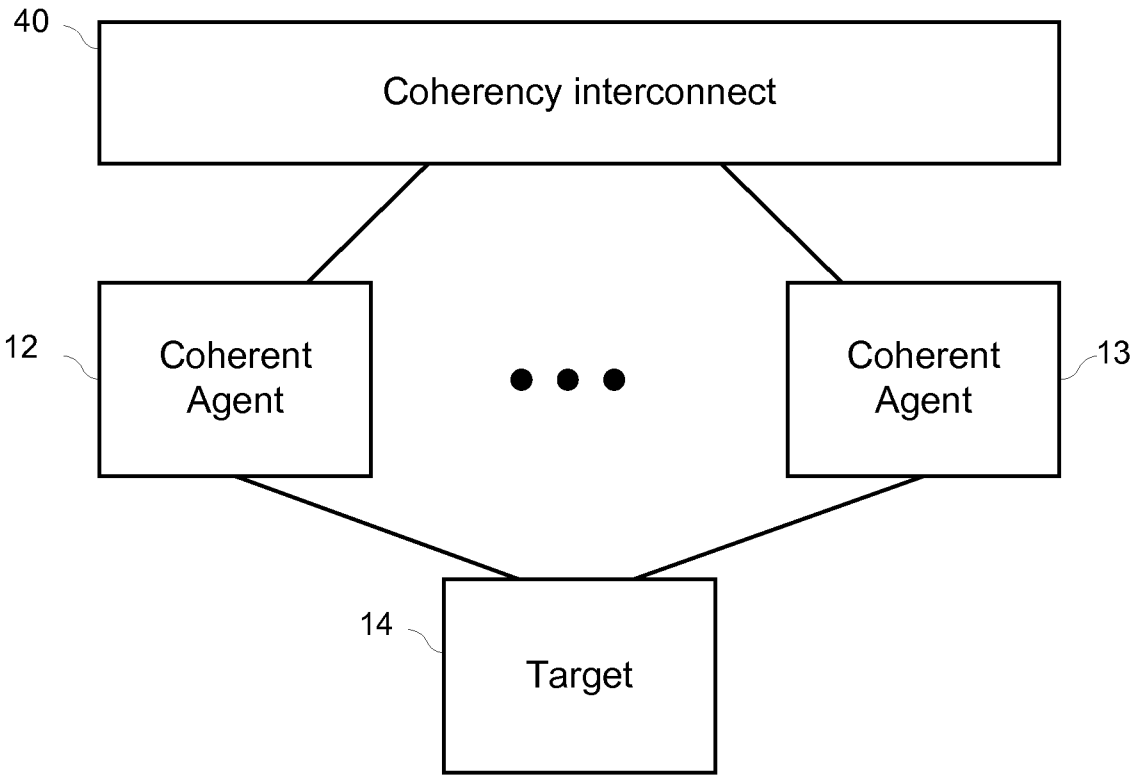

40
Coherency interconnect
12
Coherent Agent
Coherent Agent
13
14
Target

FIG. 4

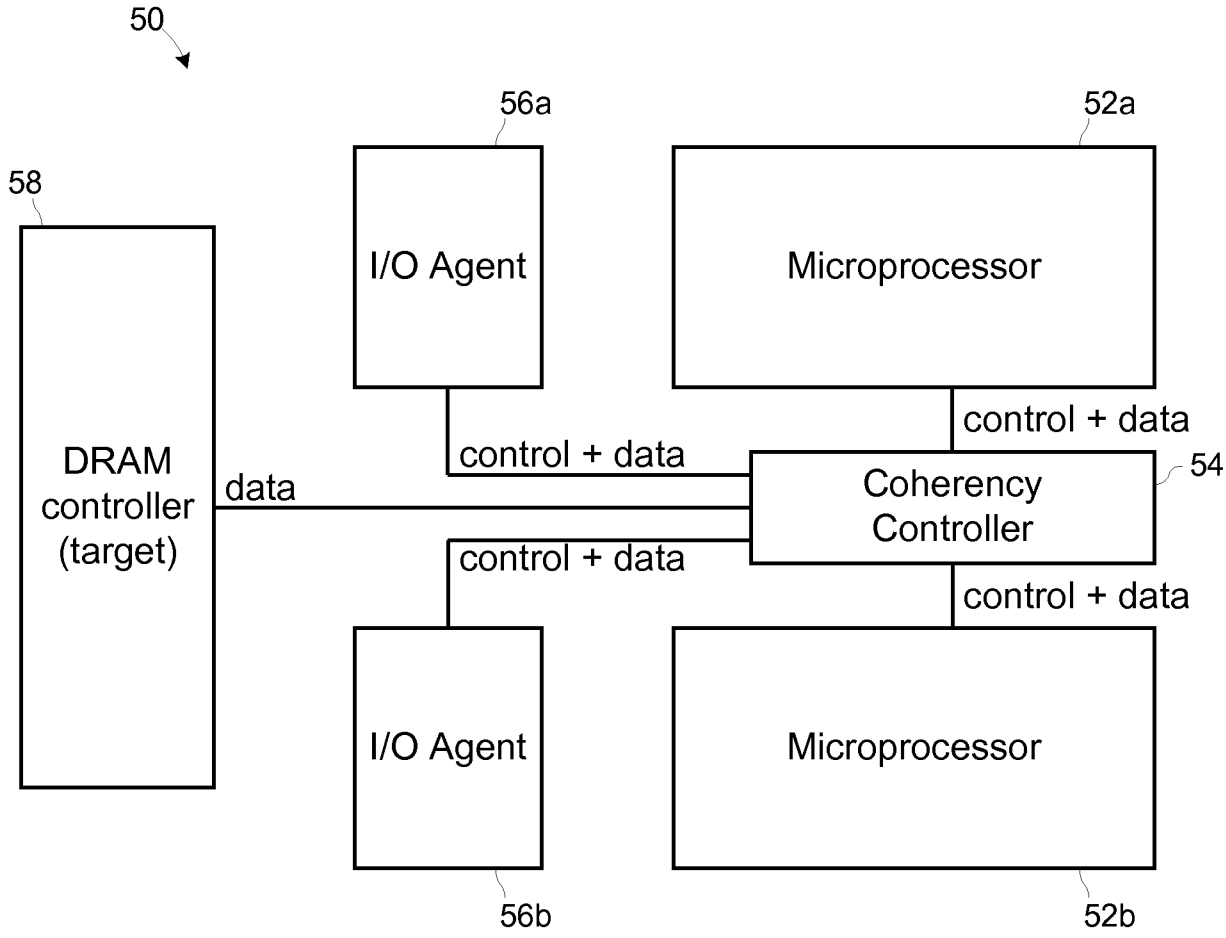

50
56a
52a
58
I/O Agent
Microprocessor
control + data
DRAM
controller
(target)
data
control + data
Coherency
Controller
54
control + data
control + data
I/O Agent
Microprocessor
56b
52b

FIG. 5

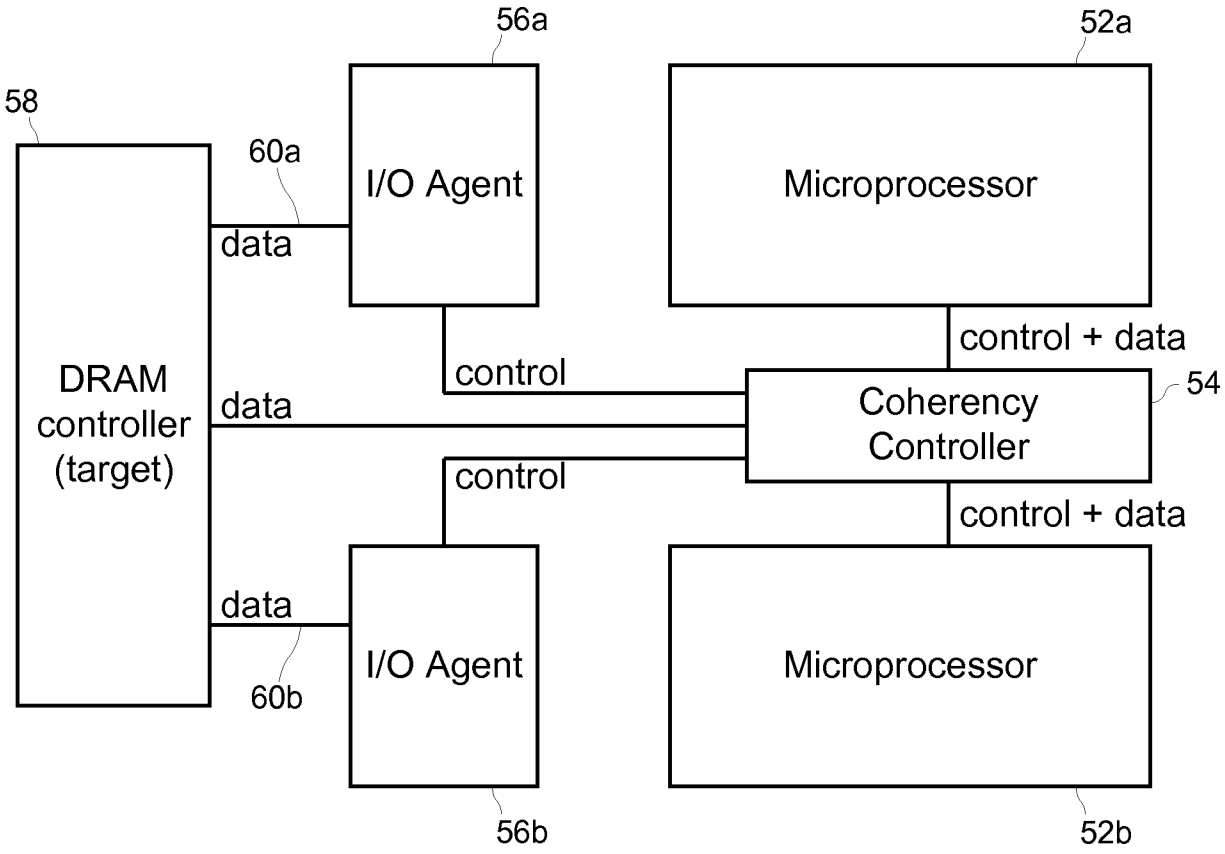

56a
52a
58
60a
I/O Agent
Microprocessor
data
control + data
DRAM
controller
(target)
control
Coherency
Controller
54
data
control
control + data
data
I/O Agent
Microprocessor
60b
56b
52b

FIG. 6