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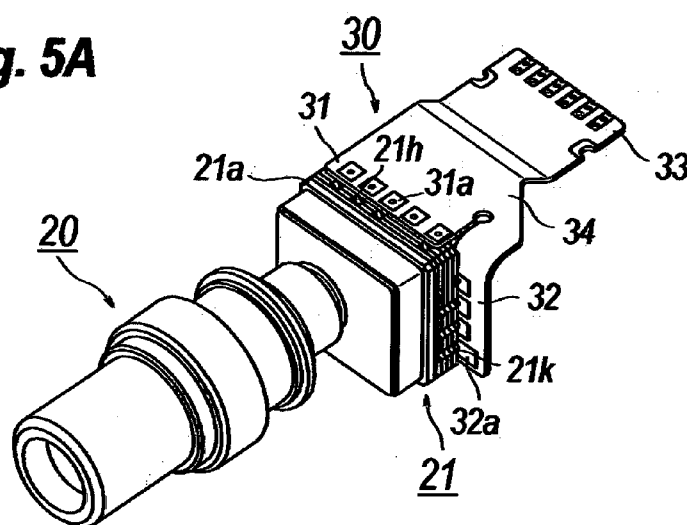
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[Continued on next page]

(54) **Title:** OPTICAL TRANSCEIVER IMPLEMENTING WITH FLEXIBLE PRINTED CIRCUIT CONNECTING OPTICAL SUBASSEMBLY TO CIRCUIT BOARD

Fig. 5A



(57) **Abstract:** An optical transceiver is disclosed, where the optical transceiver includes an optical subassembly (OSA) with a bottom plate for dissipating heat and connected to an electronic circuit with a flexible printed circuit (FPC). The FPC is soldered with the side electrodes of the OSA as forming a solder fillet in the plane electrode, or the FPC is soldered with the plane electrodes of the OSA as forming the solder fillet in the side electrodes, and leaving a limited room for receiving the curved FPC in peripheries of the OSA.



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DESCRIPTION

Title of Invention

OPTICAL TRANSCEIVER IMPLEMENTING WITH FLEXIBLE
PRINTED CIRCUIT CONNECTING OPTICAL SUBASSEMBLY TO
CIRCUIT BOARD

Technical Field

[0001] An embodiment of the present invention relates to an optical transceiver implementing with a flexible printed circuit connecting an optical subassembly (hereafter denoted as OSA) to an electronic circuit board.

Background Art

[0002] An optical transceiver generally implements an OSA that performs conversion between an electrical signal and an optical signal, and an electronic circuit for processing the electrical signal. **Figs. 11A and 11B** show an example of the OSA **201** that provides an optical device **202** and a coupling unit **203**. The former element **202** may include a semiconductor laser diode (hereafter denoted as LD), in a case of a transmitter optical sub-assembly (TOSA), for converting the electrical signal to the optical signal. The LD is installed within a housing **202a** made of multi-layered ceramics having a plurality of electrodes **202b** to transmit signals therein or to provide electrical power thereto. Those electrodes **202b** are connected to the circuit board via the flexible printed circuit (hereafter denoted as FPC) **210**.

[0003] The FPC **210** is connected with the optical device **202** as shown in **Fig. 11B**. Specifically, facing the pads of the FPC against the bottom surface of the ceramic package **202a** and bending the FPC at a portion **211**

neighbor to the pads, those pads are soldered with the electrodes of the package **202a**. Solder to fix the pads on the FPC **210** to the electrodes of the transmitter device **202** creeps up to the side portion of the package **202a**, which is often called as a castellation, to form a solder fillet F. A conventional assembly of the FPC **210** and the optical device **202** may reinforce a portion of the solder fillet F by a support.

[0004] The FPC **210** is to be bent at a vicinity of the optical device **202** to be in contact with the circuit board placed behind the optical device **202**. However, because of the support **220** or the solder fillet F, an excess length of the FPC **210** is necessary just beside of the package **202a** of the optical device **202**. This excess length sometimes becomes 1.5 mm or more, which means that the optical transceiver installing the optical transceiver **201** is necessary to provide a room to receive the excess length of the FPC **210**, and becomes hard to make the housing thereof small.

Summary of Invention

[0005] One aspect of the present invention relates to an optical transceiver that comprises an OSA, a circuit board and an FPC. The OSA has a bottom plate and a plurality of electrodes. Each of electrodes has a plane portion and a side portion and are arranged so as to avoid the bottom plate. The circuit board mounts an electronic circuit to communicate with the OSA. The FPC connects the OSA with the circuit board. The FPC has first to third areas. The first and second areas are connected to the OSA, while, the third area is connected to the circuit board. A feature of an embodiment according to the invention is that the FPC is soldered to one of the side portion and the plane portion of the electrodes in the OSA as leaving a solder fillet on the other of the side portion and the plane portion.

Brief Description of Drawings

[0006] The invention may be more completely understood in consideration of the following detailed description of various embodiments of the invention in connection with the accompanying drawings, in which:

5 [0007] **Fig. 1** is an outer shape of an optical transceiver according to an embodiment of the present invention;

[0008] **Fig. 2** shows an inside of the optical transceiver shown in **Fig. 1**, which omits an upper housing to show the inside thereof;

10 [0009] **Fig. 3** is a partially cut view of a TOSA installed in the optical transceiver shown in **Figs. 1** and **2**;

[0010] **Fig. 4** is a rear view of the TOSA shown in **Fig. 3** to show an arrangement of the electrodes provided in the bottom and the side of the package;

15 [0011] **Figs. 5A** and **5B** show a TOSA shown in **Fig. 4** attached with an FPC, and **Fig. 5C** is a side view of the TOSA;

[0012] **Fig. 6** is a plan view of the FPC to be assembled with the OSA as shown in **Figs. 5A** and **5B**;

20 [0013] **Figs. 7A** and **7B** are perspective views of a TOSA according to another embodiment of the invention, the TOSA having a modified arrangement of the FPC attached thereto;

[0014] **Figs. 8A** and **8B** are perspective views of a TOSA according to still another embodiment of the invention, where the TOSA assembles an FPC with another arrangement to be fixed to the TOSA, and **Fig. 8C** is a side view of the TOSA shown in **Figs. 8A** and **8B**;

25 [0015] **Figs. 9A** to **9C** are perspective views of the FPC assembled with the TOSA shown in **Figs. 8A** and **8B**, while, **Fig. 9D** is a plan view of

the FPC assembled with the TOSA;

[0016] **Figs. 10A and 10B** are perspective views of still another arrangement to assemble the FPC with the TOSA according to the fourth embodiment of the invention; and

5 [0017] **Fig. 11A** is a perspective view of a conventional arrangement of the FPC assembled with the TOSA, and **Fig. 11B** is a side view of the arrangement shown in **Fig. 11A**.

Description of Embodiments

10 [0018] Next, some embodiments according to the present invention will be described as referring to drawings. An optical module according to an embodiment of the invention may be applicable to, for instance, an optical transceiver illustrated in **Figs. 1 and 2**. The optical transceiver **10** shown in the figures comprises an optical sub-assemblies (OSA) that installs an optical device that performs conversion between an optical signal and an
15 electrical signal, for instance, a semiconductor laser diode (LD) for a transmitter optical subassembly (TOSA) or a semiconductor photodiode (PD) for a receiver optical subassembly; and an electronic circuit electrically communicating with the OSAs and mounted on a circuit board. These elements, the OSAs and the circuit board, are enclosed within a housing
20 comprised of a top cover **11** and a bottom cover **12**. The optical subassembly according to the present embodiment provides a ceramic package to enclose the LD or the PD therein.

[0019] The housing, specifically, the top cover **11** and the bottom cover **12**, may be made of metal to have functions of not only electrically
25 shielding the circuit enclosed therein but effectively dissipating heat generated by the electronic circuit outside the housing. The housing, **11**

and 12, may provide an optical receptacle 14 to receive an external optical connector therein. The optical receptacle 14 provides, in the rear end thereof, a flange 13 to set the optical transceiver 10 in a host system. The rear end of the housing, 11 and 12, exposes an end of the circuit board, where an electrical plug 15a is formed, to communicate electrically with the host system. Specifically, inserting the electrical plug 15a into an electrical connector provided in the host system, an electrically communicating path for the host system may be secured. In the explanation above and presented below, contexts such as “front”, “rear”, “top”, “bottom”, and so on are used only for the explanation sake; and not always reflect the practical conditions.

[0020] **Fig. 2** exposes the inside of the optical transceiver 10 by omitting the top cover 11. The bottom cover 12 mounts the circuit board 15, and the circuit board 15 mounts circuits communicating with OSAs, 20 and 40, and including active devices 19 such as an LD-driver, an amplifier and so on, and some passive devices. The bottom cover 12 also mounts the receiver optical subassembly (ROSA) 40 and the transmitter optical subassembly (TOSA) 20 in a front thereof, which are electrically connected with the circuit board 15 via the flexible printed circuit (FPC) board, 30 and 50, respectively. Heat generated by the TOSA 20 may be conducted to the top cover 11 via a thermal block 16.

[0021] **Figs. 3 and 4** illustrate the TOSA 20 according to an embodiment of the invention. **Fig. 3** is a partially cut view, while, **Fig. 4** is a rear view of the TOSA 20. The TOSA 20 of the embodiment, as shown in **Fig. 3**, includes a transmitter device 21 enclosing a semiconductor light-emitting device and a coupling unit 29 to couple the semiconductor

light-emitting device with an optical fiber. The transmitter device **21** provides a ceramic package **21a** whose side walls are formed by stacking a plurality of rectangular ceramic films, a top wall **21c** and a ceiling **21t** with a cylindrical chimney **21d**. The coupling unit **29** includes a joint sleeve **22**, a sleeve **23a**, a stub **23b** with a coupling fiber **23c** in a center thereof, and a sleeve cover **23d**. These elements, **23a** to **23d**, constitute a sleeve member assembled with the cylindrical chimney **21d** of the transmitter device **21** via the joint sleeve (J-sleeve) **22**.

[0022] The ceramic package **21a** provides a bottom plate **24** on which a thermo-electric-cooler (TEC) **25** is mounted. The TEC may cool down or heat up a top plate thereof; concurrently, the bottom plate thereof is heated up or cooled down to keep the thermal equilibrium between two plates. The LD as the semiconductor light-emitting device may be mounted on the top plate of the TEC **25** to control a temperature thereof. The LD generates heat driven by a current, which increases the temperature of the LD and shifts a wavelength of light emitted from the LD. Accordingly, it is generally necessary to keep the operating temperature of the LD in constant, or usually to cool down the temperature thereof, by mounting the LD on the TEC **25**.

[0023] In order to cool down the bottom plate of the TEC **25**, which is usually heated up by cooling the top plate thereof down, the bottom plate of the TEC is necessary to conduct heat efficiently to a substance outside of the package **21a**. That is, the bottom plate **24** of the ceramic package **21a** is inevitably to be physically in contact in a whole surface thereof with an external substance. Then, the FPC **30** connected to the transmitter device **21** is required to escape from the bottom plate **24** of the ceramic package

21a.

[0024] As described, the J-sleeve **22** may optically couple the transmitter device **21** with the optical fiber to be set within the sleeve **23a**, which is not shown in the figures. Adjusting an insertion depth of the J-sleeve **22** into the cylindrical chimney **21d**, the optical alignment along the optical axis, Z-direction, may be carried out; while, sliding the sleeve cover **23d** on the ceiling of the J-sleeve **22**, the alignment in the plane perpendicular to the optical axis, XY-direction, may be performed.

[0025] The coupling unit **29** may receive an external optical fiber, which is not explicitly illustrated in the figures. The coupling unit **29** includes the sleeve **23a**, the stub **23b** and the sleeve cover **23d**, where these elements have a cylindrical shape. The stub **23b** has the coupling fiber **23c** in a center thereof. Inserting the external fiber, in particular, a cylindrical ferrule securing the external fiber in a tip thereof, into the sleeve **23a** and making the tip of the external fiber abut against the tip of the coupling fiber **23c**, the physical contact between the external fiber and the coupling fiber **23c** may be realized without causing the substantial reflection thereat. On the other hand, the light emitted from the LD is focused on the other end of the coupling fiber **23c** by the lens in the transmitter device **21**. Thus, the light generated by the LD in the transmitter device **21** may enter the external fiber set in the sleeve **23a**.

[0026] The bottom plate **24** of the ceramic package **21a** and the FPC **30** attached to the OSA will be described. As shown in **Fig. 4**, the bottom plate **24** of the ceramic package **21a** having a substantial thickness is stuck out from the multi-layered ceramics **21b**. Moreover, the bottom plate **24** forms rooms along two edges neighbor to each other to form electrodes

thereat. That is, the first room **21e** of the package **21a** includes a plurality of electrodes each having a plane portion **21g** and a side portion **21h** extending from the plane portion **21g** along the side wall of the multi-layered ceramics **21b**. The second room **21f** also provides the electrodes each having the plane portion **21j** and the side portion **21k** extending in the side surface of the ceramic layers **21b**. The side electrodes, **21h** and **21k**, have a semi circular cross section, which is often called as the castellation. The FPC **30** is connected to those pads, **21g** to **21k**.

[0027] **Figs. 5A to 5C** schematically illustrate the TOSA **20** assembled with the FPC **30**; while, **Fig. 6** is a plan view of the FPC **30**. The FPC **30**, as shown in **Fig. 6**, provides the first area **31** for radio frequency (RF) pads **31a**, the second area **32** for low frequency (LF) pads **32a**, and the third area **33** for board pads **33a**. The RF pads **31a** in the first area **31** are arranged in a line; while, the LF pads **32a** in the second area **32** are also arranged in a line substantially perpendicular to the line of the RF pads **31a**. The RF pads **31a** in the first area **31** are connected with the board pads **33a** in the third area in the shortest, while, the LF pads **32a** in the second area is detoured, or offset from the line connecting the RF pads **31a** with the board pads **33a**.

[0028] These two lines of the arrays surround the bottom plate **24** of the package **21a**; specifically, two lines are running in parallel to respective edges of the bottom plate **24**. Moreover, the FPC **31** provides a slit **35** between the first and second areas, **31** and **32**, which facilitates the FPC **30**, in particular, the second area **32** thereof, to be bent from the first and third areas, **31** and **33**. When the FPC **30** has no slit between the first and

second areas, **31** and **32**, a radius to bend the second area **32** is forced to be small, which may cause a breaking of wiring **34** on the FPC **30**. The slit **35** provided between two areas, **31** and **32**, enables the bent radius of the FPC **30** to be large to enhance the reliability of the wiring.

5 [0029] Referring to **Figs. 5A to 5C** again, the RF pads **31a** in the first area **31** of the FPC **30** are connected to the side portion **21h** of the electrodes in the first room **21e** as leaving a solder fillet on the plane portion **21g** of the electrode without being bent at the first area **31**; while, the LF pads **32a** in the second area **32** are connected to the plane portion **21j** of the electrodes in
10 the second room **21f** as leaving the solder fillet in the side portion **21k** after being bent at a root portion of the second area **32** by about a right angle. Because the RF pads **31a** are connected with the side portion **21h** without any bending the first area **31**, the wiring **34** formed therein, which transmits high frequency signals, may be protected from the breakage.

15 [0030] Moreover, the RF pads **31a** not only covers the side portion **21h** of the electrodes in the first room **21e** but directly comes in contact thereto without bending the first area **31** of the FPC **30**, as shown in **Fig. 5C**. Thus, an excess space to receive a bent FPC becomes unnecessary in an arrangement of the FPC **30** according to the embodiment of the invention.
20 The arrangement of the embodiment may further escape the FPC **30** from coming in contact with the bottom housing **12** in a bent portion thereof, which not only makes the housing compact but reduces the degradation of the signals with high frequency components propagating on the wiring **34** in the FPC **30** because the wiring **34** is not in contact with the bottom housing
25 **12**.

[0031] The FPC **30** without being bent in the first area **31** thereof may

shorten the wiring running thereat, which may also suppress the degradation of the high frequency signals propagating thereon and enhance the reliability. Although the second area 32 of the FPC 30 is bent to connect to the plane portion 21j in the second room 21f, these electrodes 21j are connected to the LF pads 32a that carries signals with relatively lower frequencies; accordingly, the length of the second area 32 and the presence of bent portions does not affect the quality of the signals.

[0032] (Second Embodiment)

[0033] Figs. 7A and 7B illustrates another arrangement of the FPC 30A assembled with the transmitter device 21. The FPC 30A of the embodiment has another second area 32A extending from the first area 31 but, in contrast to the arrangement of the aforementioned FPC 30, in a direction perpendicular to a direction connecting the first area 31 and the third area 33. That is, the second area 32A of the embodiment laterally extends with respect to the first area 31, and is bent at a root portion thereof. The LF pads 32a of the second area 32A are soldered not only to the plane portion 21j but to leave the solder fillet on the side portion 21k in the second room 21f. Thus, the arrangement of the FPC 30A shown in Figs. 7A and 7B, may save a space for receiving a bent FPC outside of the second area 32A of the FPC 30A, which may make the housing of the optical transceiver 10 further compact along the lateral direction.

[0034] (Third embodiment)

[0035] Figs. 8A to 8C illustrate an assembly of a transmitter optical device 20B according to the third embodiment of the invention, where Figs. 8A and 8B are perspective views of the assembly; while, Fig. 8C is a side view. The FPC 30B of the present embodiment also provides, similar to

those of aforementioned embodiments, the first area **31B** and the second area **32B**, the former of which is connected to the electrodes in the first room **21e**, while, the latter area **32B** is connected to the electrodes in the second room **21f**. The FPC **30B** further provides the third area **33** substantially identical with the third area of the former embodiments. Wires **34** are also formed on the FPC **30B**.

[0036] As shown in the figures, the FPC **30B** is connected with the plane portion **21g** in the first room **21e** such that it is extended along the edge of the bottom plate **24**, namely in parallel to the normal of the bottom plate **24**, bent at the deep corner between the bottom plate **24** and the first room **24e**, and extended outwardly. The pads **31a** in the first area **31B** are soldered not only with the plane portion **21g** but leave the solder fillet F on the side portion **21h**.

[0037] The second area **32B** of the FPC **30B** is bent along the other edge of the bottom plate **24**, and extends toward a direction opposite to the first area **31B** to cover the plane portion **21j** of the electrodes in the second room **21f**. An edge of the second area **32B** slightly sticks outwardly to form other solder fillets in the side portion **21k**.

[0038] In the arrangement of the FPC **30B** according to the embodiment, although the FPC **30B** is bent to be in contact with the plane portion, **21g** and **21j**, of the electrodes; the side extension H stuck out from the edge of the package **21a** may be less than about 1.0 mm, because the FPC **30B** is bent at a portion within the periphery of the package **21a**. Moreover, the FPC **30B** extends in substantially linear after the bend above described to be in contact with the circuit board **15**. Thus, the arrangement of the FPC **30B** and the transmitter device **21** according to the embodiment

is unnecessary to provide a room in a side of the package **21a** to receive a bent FPC.

[0039] **Figs. 9A to 9D** show the FPC **30C** modified from the FPC **30B** shown in **Fig. 8**, where **Figs. 9A to 9D** are perspective views by omitting wirings **34** on the FPC, while **Fig. 9D** is a developed view thereof. The FPC **30C** of the present embodiment shown in **Figs. 9A to 9C**, provides the first pads **31a** in the first area **31C**, the second pads in the second area **32C**, and third pads **33a** in the third area **33**. The FPC **30C** also provides a slit **35C** between the first and second areas, **31C** and **32C**.

[0040] The slit **35C** between the first and second areas, **31C** and **32C**, may facilitate the bend of the areas. The FPC **30C** could not be connected in the plane portion, **21g** and **21j**, at the same time. Bending the first area **31C** outwardly in an FPC with no slit, the second area **32** is inevitably bent outwardly to make the pads **32a** in the second area **32C** separate from the plane portion **21j** of the electrode in the second room **21f**. The slit **35C** makes it possible for the first area **31C** and the second area **32C** to be bent oppositely.

[0041] **Figs. 10A and 10B** show still another embodiment of the FPC according to the present invention. The FPC **30D** has a feature that the second area **32D** thereof has substantially same bending arrangement as that of the first area **31D**. That is, the second area **32D** is once bent at a side of the first area **31D**, then bent outwardly along the edge of the bottom plate **24** to form the shape same as those of the first area **31D**. The end of the second area **32D** also sticks out from the edge of the package **21a**, which enables to form the solder fillet to be in contact with the side portion **21k** of the electrode, namely the castellation.

[0042] In the foregoing detailed description, the method and apparatus of the present invention have been described with reference to specific exemplary embodiments thereof. It will, however, be evident that various modifications and changes may be made thereto without departing from the broader spirit and scope of the present invention. The present specification and figures are accordingly to be regarded as illustrative rather than restrictive.

CLAIMS

1. An optical transceiver, comprising:

an optical subassembly (OSA) having a bottom plate and a plurality of electrodes each including a plane portion and a side portion, the electrodes being arranged to avoid the bottom plate;

a circuit board for mounting an electronic circuit communicating with the optical subassembly; and

a flexible printed circuit (FPC) for connecting the optical subassembly to the circuit board, the flexible printed circuit having a first area and a second area connected to the OSA, and a third area connected to the circuit board, the FPC being soldered to one of the side portion and the plane portion of the electrodes of the OSA as leaving a solder fillet in the other of the side portion and the plane portion of the electrodes.

2. The optical transceiver of claim 1,

wherein the OSA has a rectangular housing with the bottom plate attached to the rectangular housing to provide a first room and second room extending along two respective sides of the bottom plate neighbor to each other and providing the electrodes.

3. The optical transceiver of claim 2,

wherein the first area of the FPC is attached to the side portion of the electrodes in the first room as leaving the solder fillet in the side portion of the electrodes in the first room.

4. The optical transceiver of claim 3,

wherein the second area of the FPC is attached to the side portion of the electrodes in the second room as leaving the solder fillet in the side portion of the electrodes in the second room.

5

5. The optical transceiver of claim 2,

wherein the first area of the FPC is bent along an edge of the bottom plate facing the first room of the housing to be attached to the plane portion of the electrodes in the first room as leaving the solder fillet in the side portion of the electrodes in the first room.

10

6. The optical transceiver of claim 5,

wherein the second area of the FPC is bend along another edge of the bottom plate facing the second room of the housing to be attached to the plane portion of the electrodes in the second room as leaving the solder fillet in the side portion of the electrodes in the second room.

15

7. The optical transceiver of claim 1,

wherein the FPC has a slit between the first area and the second area.

20

8. The optical transceiver of claim 1,

wherein the first area of the FPC faces the third area of the FPC to connect the first area and the second area with each other in substantially shortest, and

25

wherein the second area of the FPC is offset from a line connecting the first area to the third area.

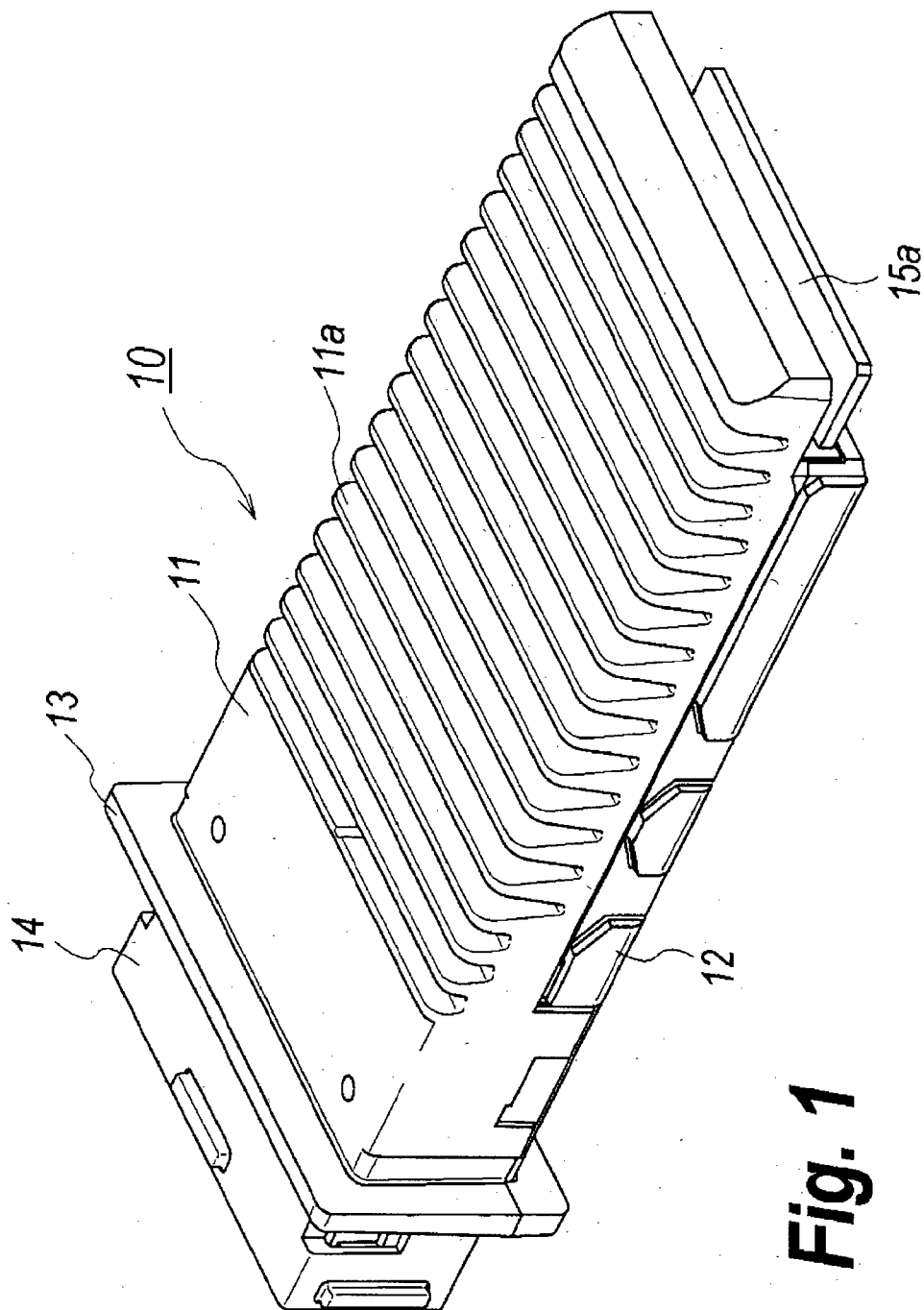
9. The optical transceiver of claim 8,
wherein the FPC extends from the OSA to the circuit board
substantially in flat.

5

10. The optical transceiver of claim 1,
wherein the first area provides a plurality of pads for signals
with high frequency components, and the second area provides a plurality of
pads for signals with low frequency components.

10

11. The optical transceiver of claim 10,
wherein the bottom plate mounts a thermoelectric controller.



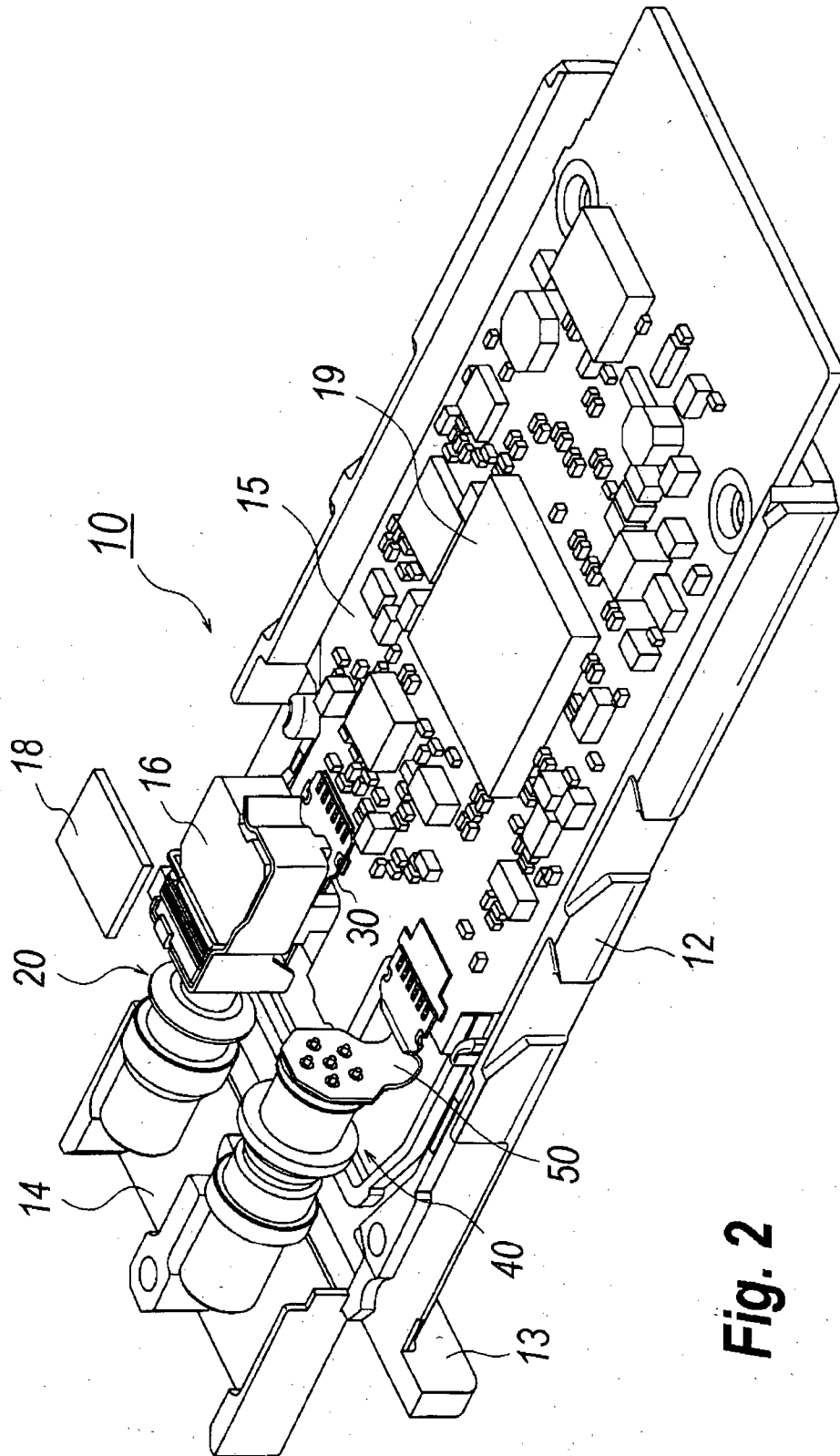


Fig. 2

Fig. 3

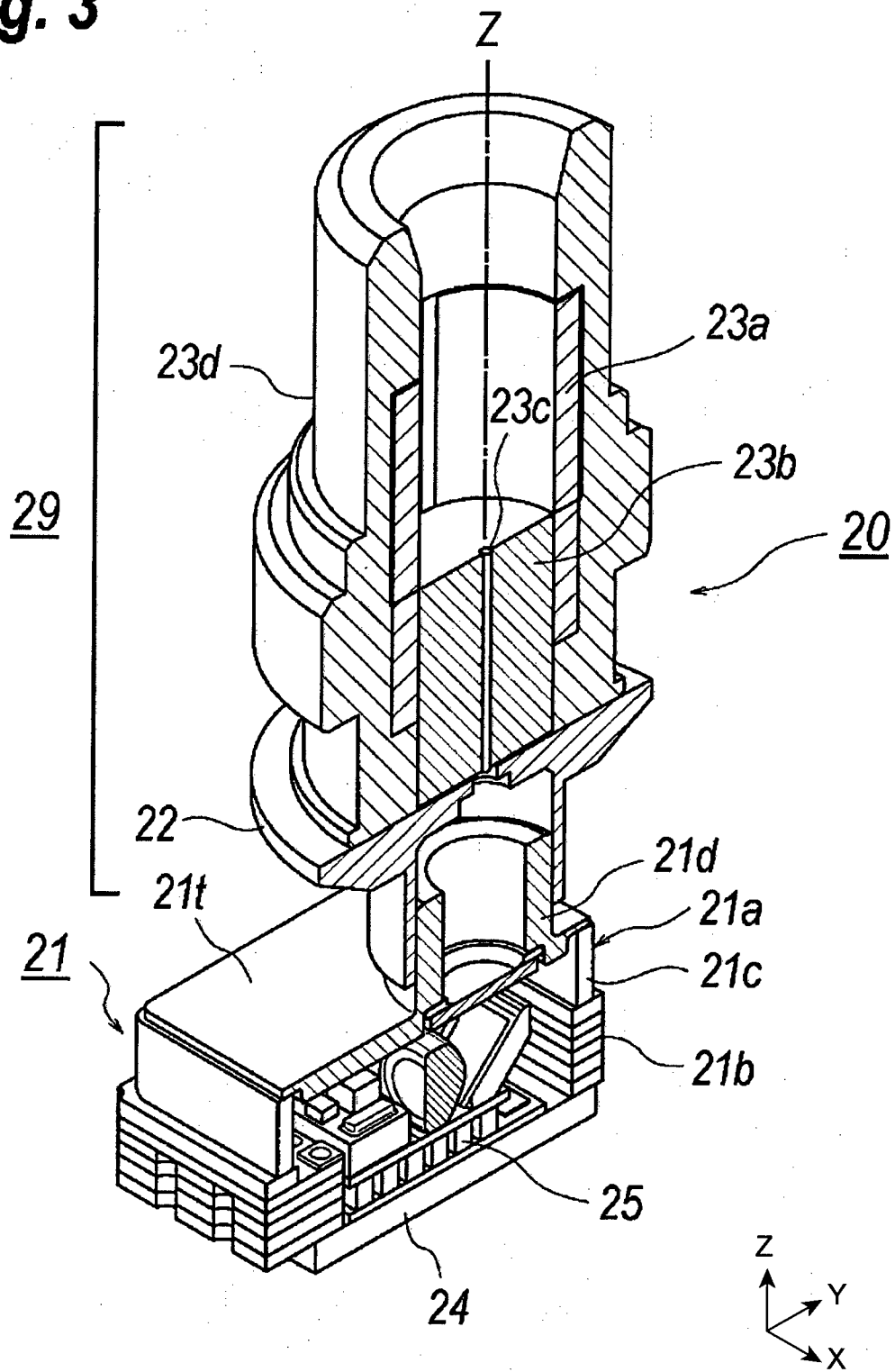


Fig. 4

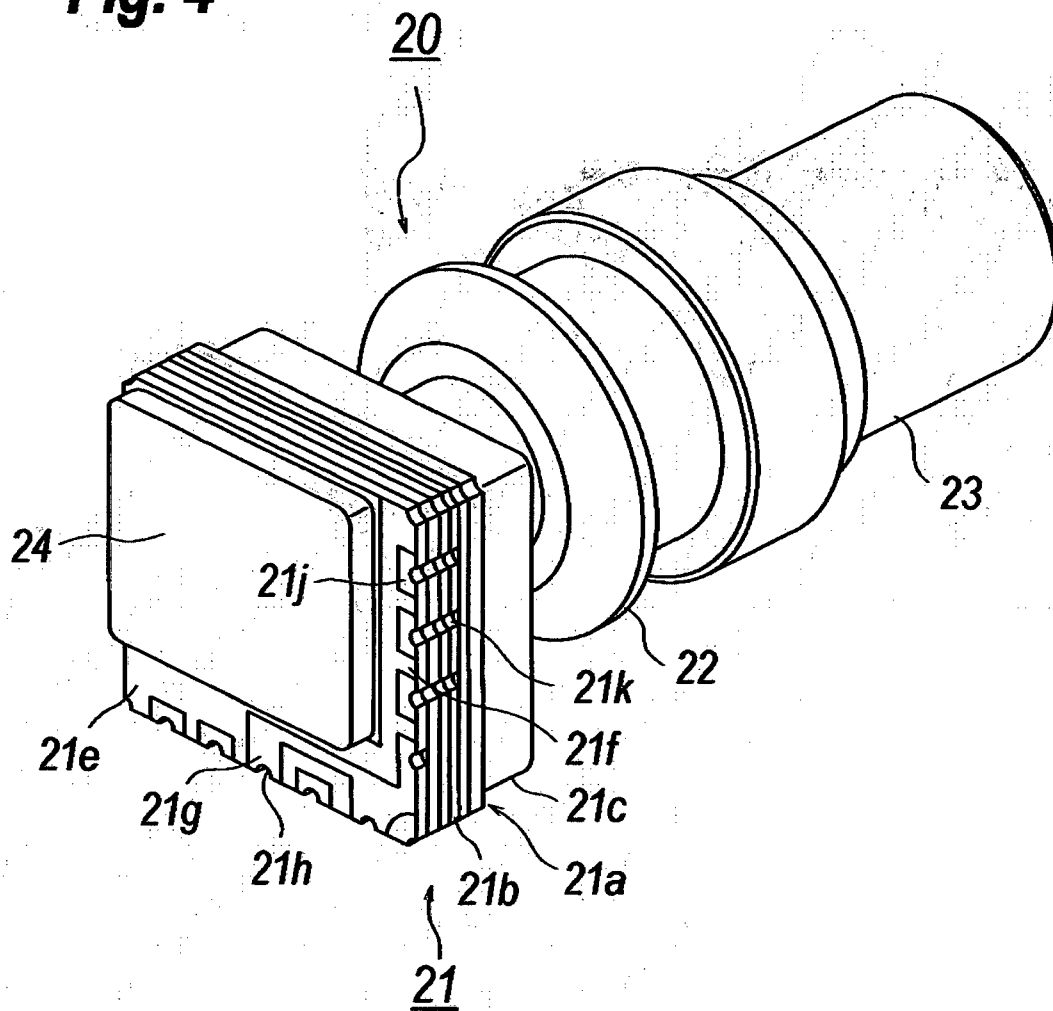


Fig. 5A

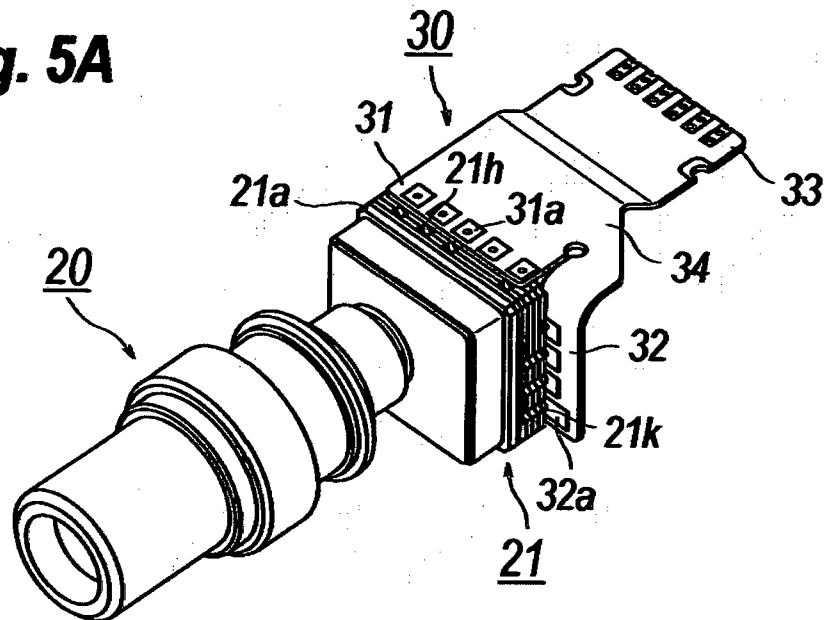


Fig. 5B

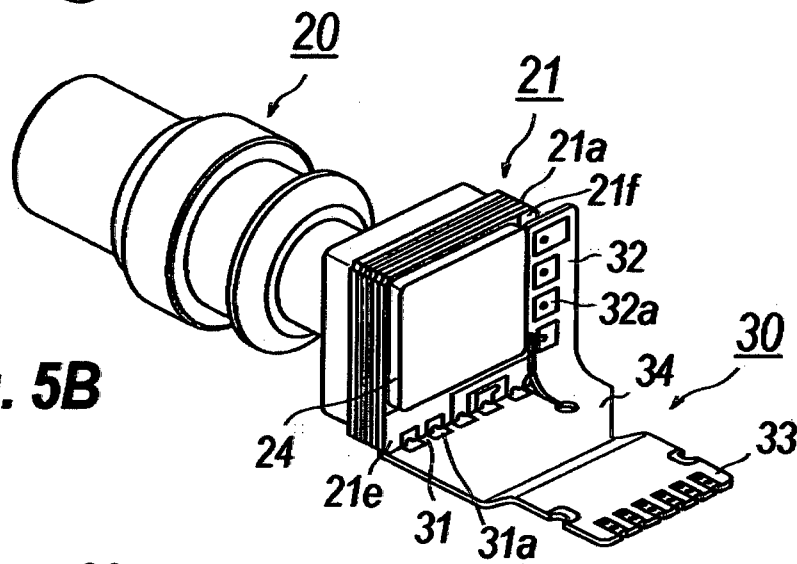


Fig. 5C

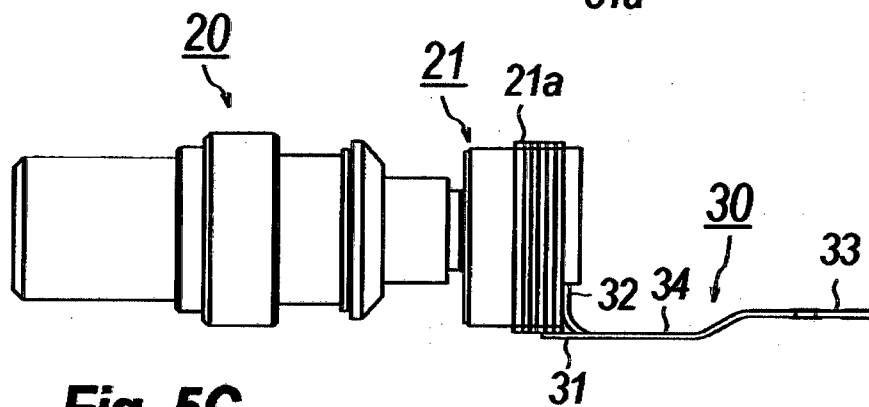


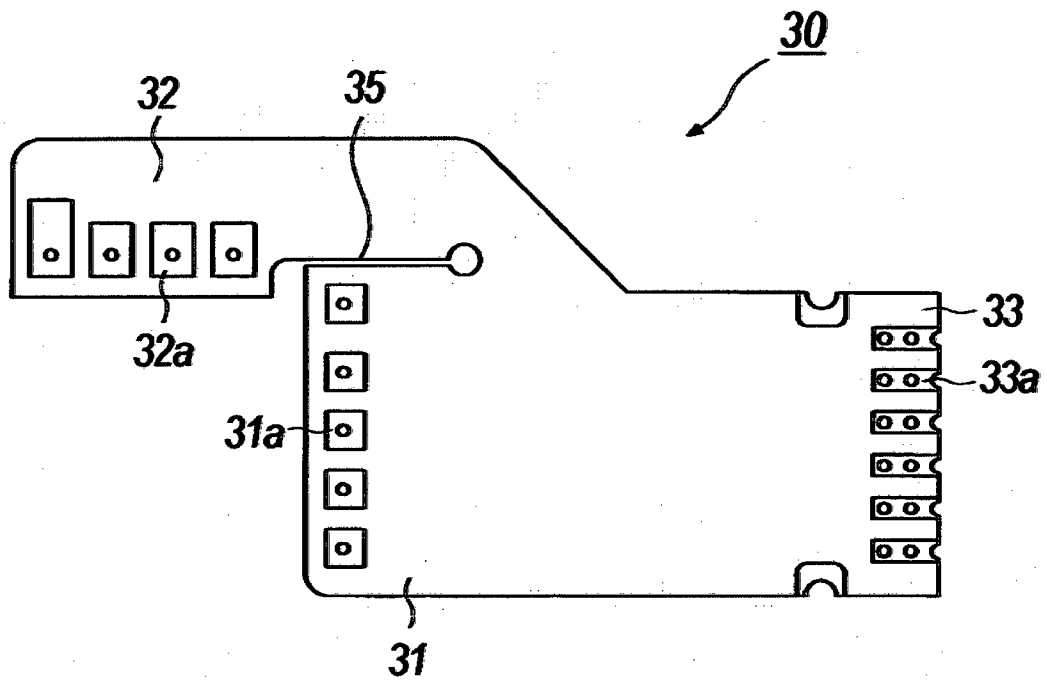
Fig. 6

Fig. 7A

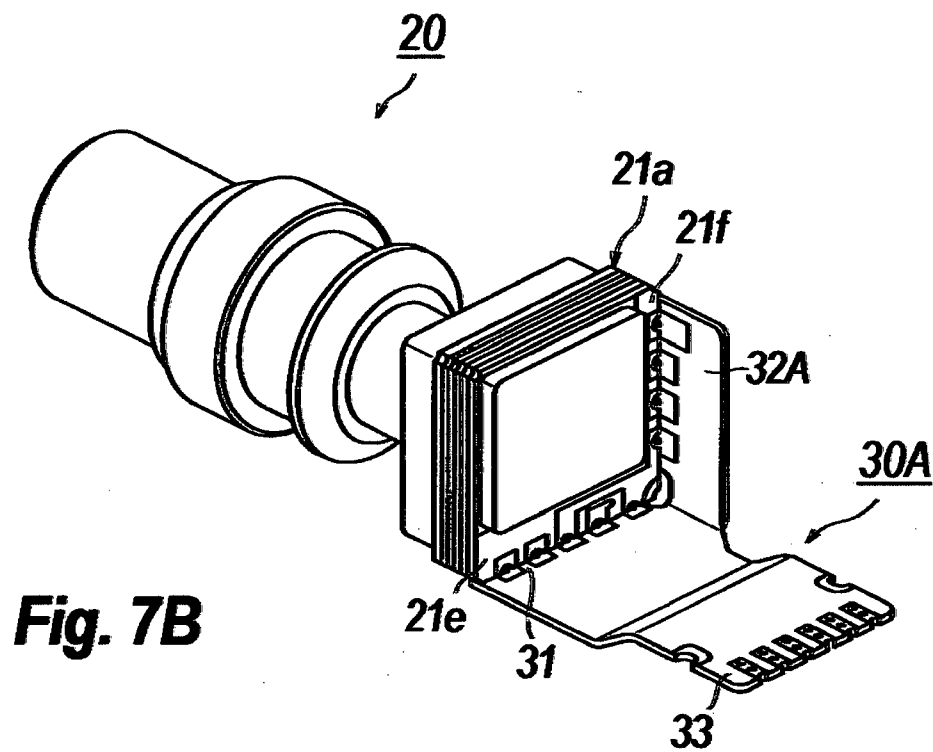
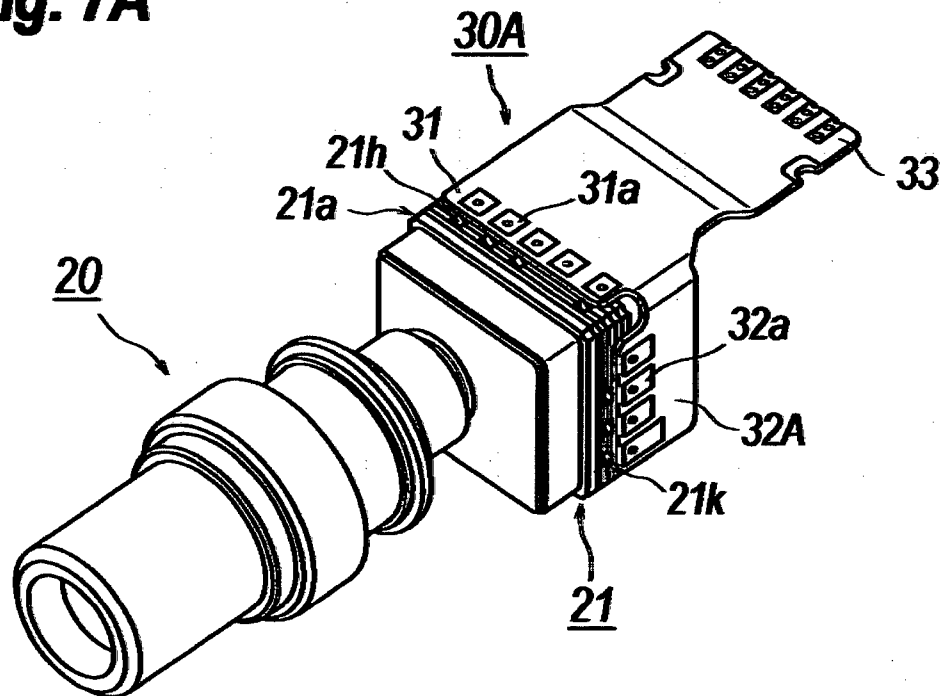


Fig. 7B

Fig. 8A

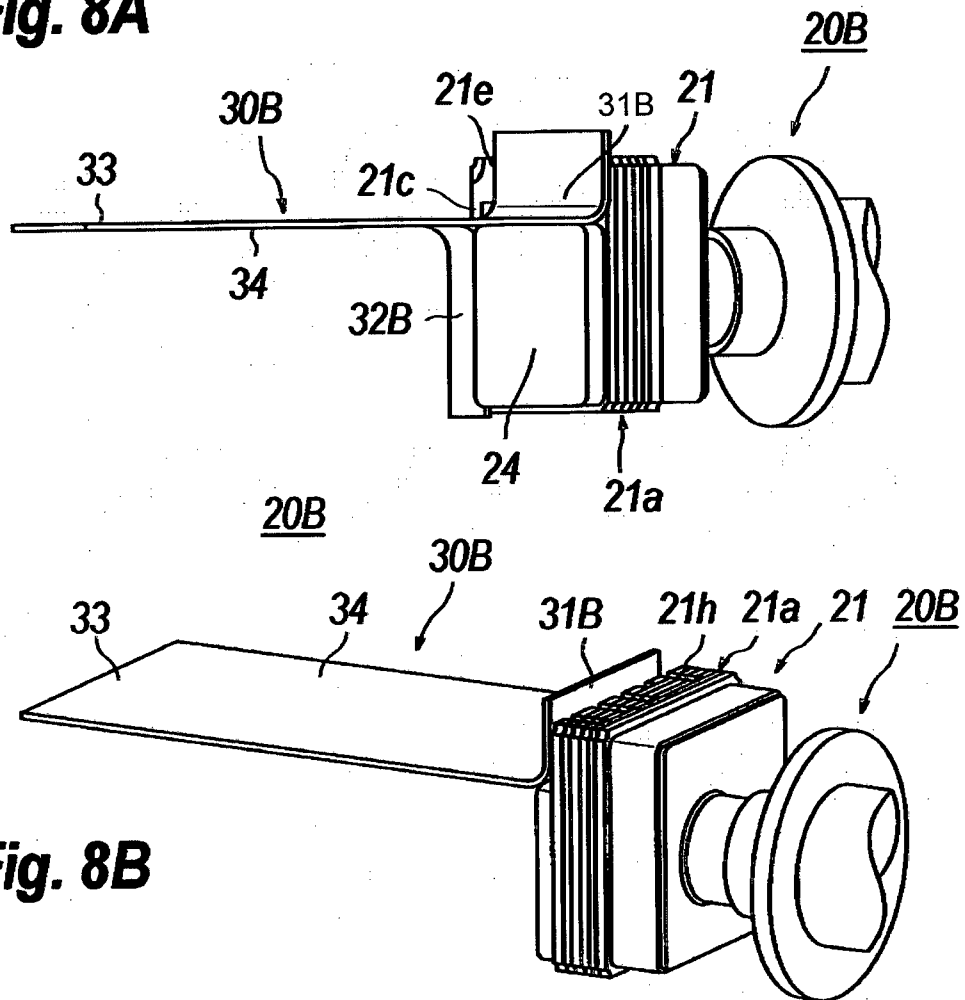


Fig. 8B

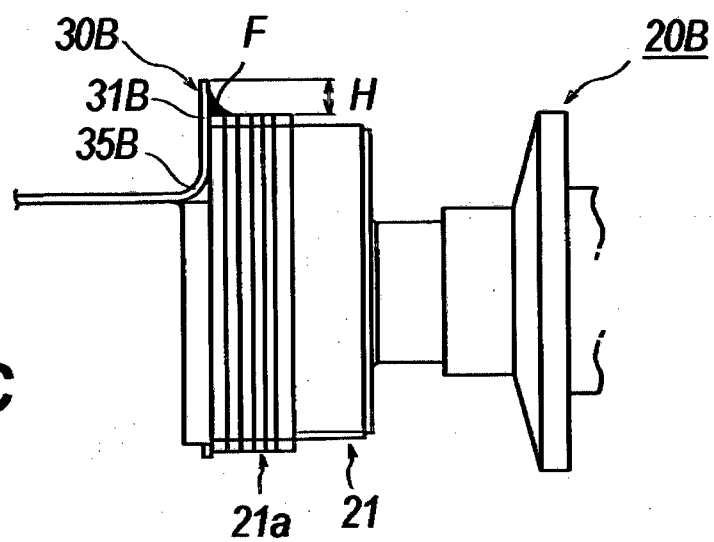


Fig. 8C

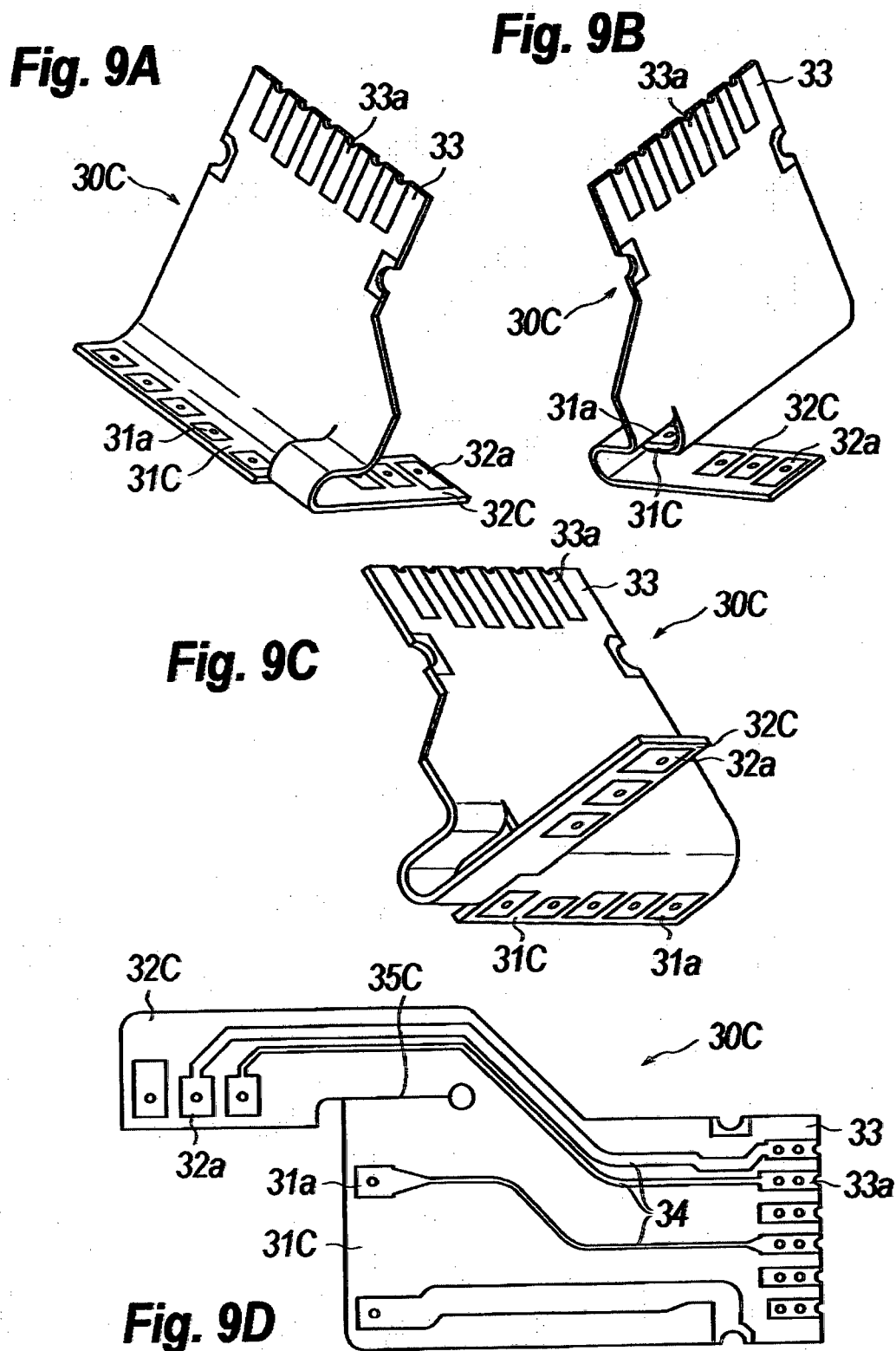


Fig. 10A

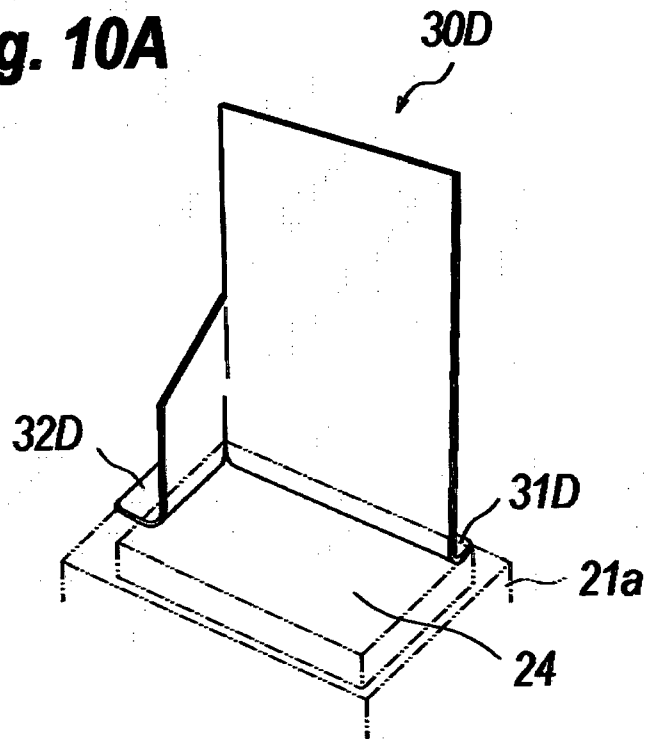


Fig. 10B

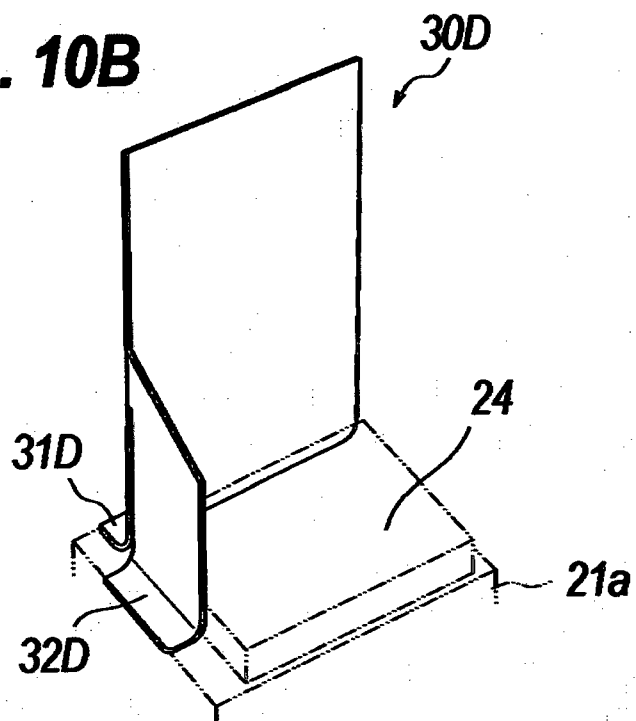
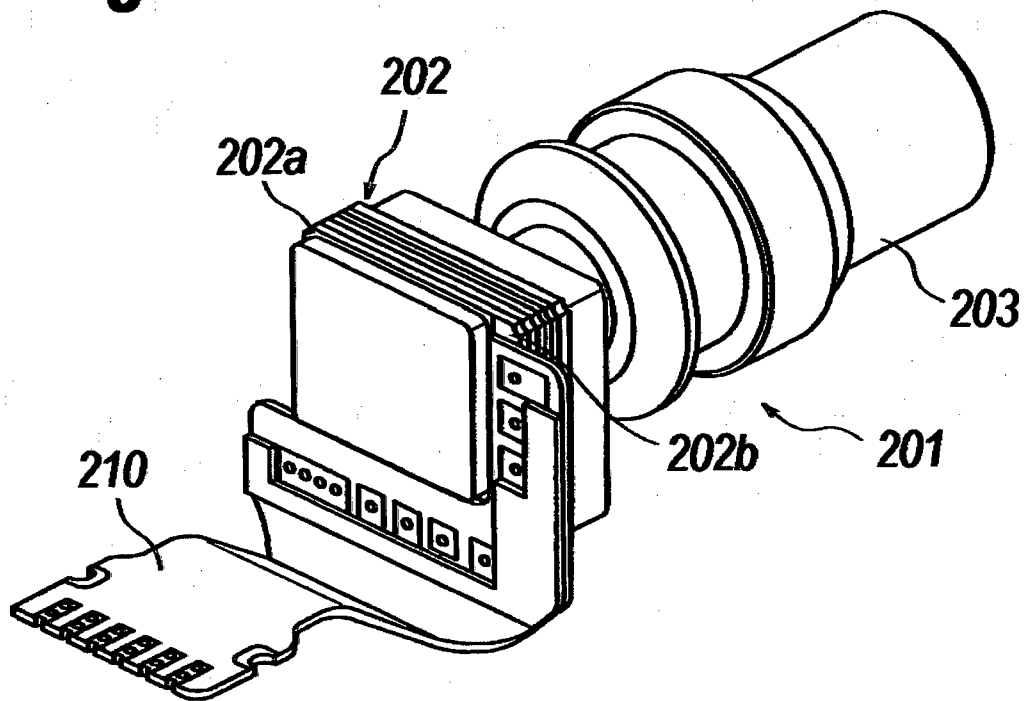
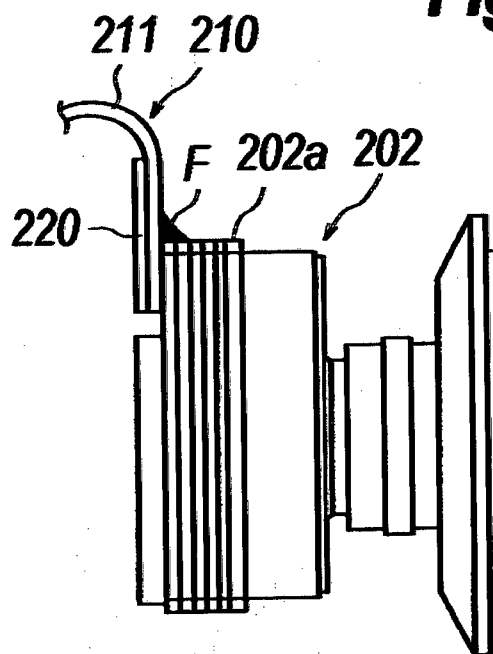


Fig. 11A**Fig. 11B**

INTERNATIONAL SEARCH REPORT

International application No

PCT/JP2012/060393

A. CLASSIFICATION OF SUBJECT MATTER
INV. G02B6/42
ADD.

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)
G02B

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

EPO-Internal, WPI Data

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	US 2004/120660 A1 (GO HISAO [JP] ET AL) 24 June 2004 (2004-06-24) figure 7	1-11
A	----- US 2009/010653 A1 (AOKI SHIN-ICHI [JP]) 8 January 2009 (2009-01-08) figure 1A	1-11
A	----- WO 2005/054921 A1 (BOOKHAM TECHNOLOGY PLC [GB]; AYLIFFE MICHAEL H [US]) 16 June 2005 (2005-06-16) figure 1	1-11
A	----- WO 2007/037364 A1 (NIPPON TELEGRAPH & TELEPHONE [JP]; NTT ELECTRONICS CORP [JP]; OHYAMA T) 5 April 2007 (2007-04-05) figure 1	1-11
	----- -/-	



Further documents are listed in the continuation of Box C.



See patent family annex.

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Date of the actual completion of the international search

4 July 2012

Date of mailing of the international search report

19/07/2012

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INTERNATIONAL SEARCH REPORT

International application No

PCT/JP2012/060393

C(Continuation). DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	US 2010/322569 A1 (OHYAMA TAKAHARU [JP] ET AL) 23 December 2010 (2010-12-23) figures 14-18 -----	1-11

INTERNATIONAL SEARCH REPORT

Information on patent family members

International application No

PCT/JP2012/060393

Patent document cited in search report	Publication date	Patent family member(s)	Publication date
US 2004120660 A1	24-06-2004	JP 3861864 B2	27-12-2006
		JP 2004151686 A	27-05-2004
		US 2004120660 A1	24-06-2004
US 2009010653 A1	08-01-2009	JP 2009016682 A	22-01-2009
		US 2009010653 A1	08-01-2009
WO 2005054921 A1	16-06-2005	CN 1906515 A	31-01-2007
		GB 2439596 A	02-01-2008
		US 2005135443 A1	23-06-2005
		WO 2005054921 A1	16-06-2005
WO 2007037364 A1	05-04-2007	JP 4699155 B2	08-06-2011
		JP 2007094145 A	12-04-2007
		WO 2007037364 A1	05-04-2007
US 2010322569 A1	23-12-2010	US 2010322569 A1	23-12-2010
		WO 2009001958 A1	31-12-2008