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For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.

(54) Title: CONTROL PROCEDURE SELECTION

(57) Abstract: In one embodiment of the present invention, a process that uses a first processor is provided. The process includes the operation of blocking by the first processor of completion by a second processor of a configuration cycle. The process also includes the operation of selecting by the first processor of one procedure from a plurality of procedures that are associated with respective types of circuitry that may be used to permit the first processor to control a device. The one procedure is associated with one of the respective types of circuitry that is actually available to be used by the first processor.



WO 2003/077115 A3

INTERNATIONAL SEARCH REPORT

International Application No
PCT/US 03/04641

A. CLASSIFICATION OF SUBJECT MATTER

IPC 7 G06F9/445

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC 7 G06F

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal, WPI Data, PAJ

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
Y	US 6 260 094 B1 (DAVIS BARRY R ET AL) 10 July 2001 (2001-07-10) column 13, line 18 - line 67; figure 1	1-36
Y	R. RUSSELL: "BIOS Basics" Online! 5 June 2001 (2001-06-05), pages 1-1, XP002266574 Retrieved from the Internet: URL: http://web.archive.org/web/20010605201959/peripherals.about.com/compute/peripherals/library/weekly/aa052201a.htm 'retrieved on 2004-01-12! page 1, line 20 - line 34	1-36, 39, 40

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☒ Further documents are listed in the continuation of box C.

☒ Patent family members are listed in annex.

* Special categories of cited documents:

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"P" document published prior to the international filing date but later than the priority date claimed

"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention

"X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone

"Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art.

"&" document member of the same patent family

Date of the actual completion of the international search

30 March 2004

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INTERNATIONAL SEARCH REPORT

II International Application No
PCT/US 03/04641

C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	A. TISCHENKO: "Zero-Channel RAID. The economic approach" COMPUTER REVIEW, 'Online! 30 January 2002 (2002-01-30), pages 1-4, XP002266763 Retrieved from the Internet: URL: http://www.itc.ua/article.phtml?ID=8820 > 'retrieved on 2004-01-02! page 2, paragraph 1	1-36
X	INTEL CORP.: "DK440LX Motherboard. Technical Product Specification" 'Online! October 1997 (1997-10), INTEL CORPORATION , INTEL CORPORATION , XP002274571 Retrieved from the Internet: URL: ftp://download.intel.com/support/motherboards/desktop/dk440lx/68271701.PDF > 'retrieved on 2004-03-22! page 12 - page 52; figure 1	37,38
Y	INTEL CORP.: "MROMB Design Considerations Using the Intel 80303 I/O Processor. Application Note." September 2001 (2001-09), INTEL CORPORATION , XP002274842 page 7 - page 13; figures 2,3	39,40

INTERNATIONAL SEARCH REPORT

International application No.
PCT/US 03/04641

Box I Observations where certain claims were found unsearchable (Continuation of Item 1 of first sheet)

This International Search Report has not been established in respect of certain claims under Article 17(2)(a) for the following reasons:

1. ☐ Claims Nos.:
because they relate to subject matter not required to be searched by this Authority, namely:
2. ☐ Claims Nos.:
because they relate to parts of the International Application that do not comply with the prescribed requirements to such an extent that no meaningful International Search can be carried out, specifically:
3. ☐ Claims Nos.:
because they are dependent claims and are not drafted in accordance with the second and third sentences of Rule 6.4(a).

Box II Observations where unity of invention is lacking (Continuation of item 2 of first sheet)

This International Searching Authority found multiple inventions in this International application, as follows:

see additional sheet

1. ☒ As all required additional search fees were timely paid by the applicant, this International Search Report covers all searchable claims.
2. ☐ As all searchable claims could be searched without effort justifying an additional fee, this Authority did not invite payment of any additional fee.
3. ☐ As only some of the required additional search fees were timely paid by the applicant, this International Search Report covers only those claims for which fees were paid, specifically claims Nos.:
4. ☐ No required additional search fees were timely paid by the applicant. Consequently, this International Search Report is restricted to the invention first mentioned in the claims; it is covered by claims Nos.:

Remark on Protest

- ☐ The additional search fees were accompanied by the applicant's protest.
- ☐ No protest accompanied the payment of additional search fees.

FURTHER INFORMATION CONTINUED FROM PCT/ISA/ 210

This International Searching Authority found multiple (groups of) inventions in this international application, as follows:

1. claims: 1-36

Blocking of configuration cycle performed by the host processor and selecting a procedure to control a device

2. claims: 37,38

A motherboard

3. claims: 39,40

Hardware to implement a MROMB technique

INTERNATIONAL SEARCH REPORT

International Application No

PCT/US 03/04641

Patent document cited in search report	Publication date	Patent family member(s)	Publication date
US 6260094	B1	10-07-2001	
		US 5838935 A	17-11-1998
		US 5734847 A	31-03-1998
		US 6324609 B1	27-11-2001
		AU 1953397 A	26-08-1998
		WO 9835295 A1	13-08-1998
		US 5848249 A	08-12-1998