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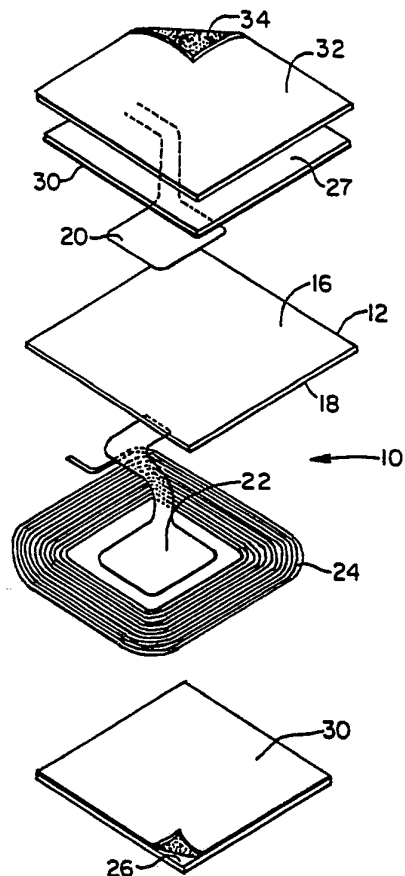
<p>(51) International Patent Classification <sup>5</sup> : <b>G08B 13/14, B32B 31/00 H01F 5/00</b></p>	<p><b>A1</b></p>	<p>(11) International Publication Number: <b>WO 92/21113</b> (43) International Publication Date: 26 November 1992 (26.11.92)</p>
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<p>(21) International Application Number: PCT/US92/04230 (22) International Filing Date: 18 May 1992 (18.05.92) (30) Priority data: 703,932 22 May 1991 (22.05.91) US 877,494 30 April 1992 (30.04.92) US (71) Applicant: CHECKPOINT SYSTEMS, INC. [US/US]; 550 Grove Road, Thorofare, NJ 08086 (US). (72) Inventors: APPALUCCI, Lawrence, C. ; 503 Ridgeview Lane, Villanova, PA 19085 (US). ORTIZ, Luis, Felipe ; Urbanizacion Santa Teresita, Calle 13-A5-16, Ponce, Puerto Rico 00731 (US). MAZOKI, Gary, T. ; 5 Pluto Drive, Seawell, NJ 08080 (US).</p>	<p>(74) Agents: SCHWARZE, William, W. et al.; Panitch Schwarze Jacobs &amp; Nadel, 1601 Market Street, 36th Floor, Philadelphia, PA 19103 (US). (81) Designated States: AT (European patent), AU, BE (European patent), CA, CH (European patent), DE (European patent), DK (European patent), ES (European patent), FI, FR (European patent), GB (European patent), GR (European patent), IT (European patent), JP, KR, LU (European patent), MC (European patent), NL (European patent), NO, SE (European patent). <b>Published</b> <i>With international search report.</i></p>
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(54) Title: STABILIZED RESONANT TAG CIRCUIT AND DEACTIVATOR

(57) Abstract

The present invention is a stabilized resonant tag circuit (10) for use as an electronic article surveillance tag. The tag has a flexible substantially planar dielectric substrate (12) having conductors (20, 22) positioned on either side, at least one of the conductors including an inductor (24). The tag is stabilized by a flexible, substantially planar, tear-resistant, substantially vapor impermeable polymeric film (26, 27) adhered to and covering at least one of the conductors and the substrate. The film provides a vapor barrier which minimizes the effects of body and article detuning on the circuit and promotes the secured integrity of the tag. The tag circuit may include an intended portion (36) in at least one of the conductors which permits the tag to be deactivated in response to an electromagnetic field of sufficient energy to destroy the resonant properties of the circuit.



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STABILIZED RESONANT TAG CIRCUIT AND DEACTIVATOR

5                    Field of the Invention

The invention relates to resonant tag circuits for electronic article surveillance systems and, more particularly, to stabilization of such tags.

10                   Background of the Invention

Electronic article surveillance (EAS) systems help to deter the unauthorized removal of articles from a surveillance area. One aspect of this deterrence results from the visual observance  
15 of a component of an EAS system attached to an article to be protected, namely a resonant tag circuit attached to a particular article. Typical EAS systems involve passing articles having EAS tags attached thereto through an electromagnetic  
20 field of predetermined frequency generated in a controlled area. The tag circuit resonates at the frequency of the electromagnetic field, enabling the receiving component of an EAS system to detect the presence of the tag in the controlled area and  
25 indicate unauthorized removal of the tagged article from the protected premises. The legitimate removal of tagged articles from the premises may be accomplished by removal or deactivation of the tag circuit before the article is passed through the  
30 electromagnetic field.

-2-

Typical resonant tag circuits comprise a central dielectric layer and conductive layers on either side of the dielectric layer. For example, U.S. Patent No. 4,598,276 discloses an EAS marker  
5 having a central dielectric layer and conductive spirals on opposite surfaces of the dielectric layer, including an inductive component, the resulting circuit resonating upon exposure to an electromagnetic field of a predetermined frequency.

10 In addition to the basic component layers of EAS tags described above, various outer or covering layers may be provided over the conductive layers. For example, U.S. Patent Nos. 4,717,438 and 4,843,404 discloses tags having additional  
15 outer layers of an unspecified material and a release sheet attached by means of an adhesive; U.S. Patent No. 4,792,790 discloses a tag having outer layers of a light-transparent carrier material; and U.S. Patent No. 4,413,254 discloses a  
20 tag having outer cover layers of paper or another insulating material.

Further, U.S. Patent No. 4,369,557 discloses a tag having outer layers formed of an insulative substrate web having a low dissipation  
25 factor at a given frequency and a stable dielectric constant, such as polyethylene, polypropylene, polyester (Mylar), or polyisobutylene. The preferred web material disclosed is polyethylene because of its low cost and ease of bonding with  
30 aluminum foil.

Still further examples include U.S. Patent No. 4,864,280 which discloses a tag having outer layers, one layer being an adhesive film and detachable cover sheet and the opposite layer being  
35 a paper film or film of synthetic plastics

-3-

material. The possibility of printing on the latter layer is also discussed. U.S. Patent No. 4,783,646 discloses a tag having outer polyester films for use as an insulating covering material  
5 which is less expensive than polyimide and facilitates the transmission of radio waves.

The prior art fails to recognize many unexpected advantages arising from the selection of a polymeric material as an outer layer for an EAS  
10 tag, such as those advantages associated with a process including indenting or dimpling to fabricate deactivatable tags.

Deactivatable EAS tags are disclosed in U.S. Patent No. 4,498,076, which is hereby  
15 incorporated by reference. This patent discloses deactivatable resonant tag circuits in which a portion of one conductor is indented into the central substrate layer such that the conductors on either side of the substrate are closer to each  
20 other at the indented portion than the remainder of the conductors. The tag is deactivated upon exposure to an electromagnetic field of a predetermined frequency, which causes an arc discharge between the conductors through the  
25 substrate at the indented portion. The electric arc vaporizes a portion of the conductors near the indented portion, thereby destroying the conductive path and deactivating the circuit. Alternatively, the arc forms a plasma with deposited metal between  
30 the conductors which permanently short-circuits the conductors and destroys the resonant properties of the circuit.

Other examples of deactivatable tags are disclosed in the prior art, such as U.S. Patent No.  
35 4,021,705, which discloses a deactivatable tag

-4-

having a central layer of electrically insulative material with layers on either side of conductive material and at least one fusible link which can be fused by application of an energizing field to  
5 activate or alter the characteristics of the resonant circuit.

Further, U.S. Patent Nos. 4,778,552, 4,802,944, 4,818,312, 4,846,922, 4,910,499 and 4,954,814 disclose deactivatable tags having an  
10 internal non-conductive layer between two conductive layers and outer layers and a release sheet releasably adhered by an adhesive to one of the outer layers.

In some situations, prior art tag  
15 circuits may be detuned by contact between the tag and the article to which the tag is secured. For example, meat is typically packaged in a breathable polymeric stretch film which permits liquid or gas, such as oxygen, to pass therethrough. The oxygen  
20 which penetrates the film causes the meat packaged therein to have a bright red color associated with freshness. However, the tag circuit may be detuned by capacitance coupling of the inductor with the article. There is a need in the art for a  
25 stabilized resonant tag circuit and deactivator in which detuning of the tag circuit by contact with the article or moisture on the article or packaging surface is minimized.

#### Summary of the Invention

30 According to the present invention, a resonant tag circuit is provided for use as an electronic article surveillance tag, having a flexible substantially planar dielectric substrate. A first conductor is positioned on the first side

-5-

of the dielectric substrate, and a second conductor is positioned on the second side of the dielectric substrate, while at least one of the conductors comprises an inductor. A flexible, substantially planar, tear resistant, polymeric film is adhered to and covers one of the conductors and the substrate on the side of the substrate opposite the side which is to be adhered to an article subject to surveillance. The film provides a vapor barrier for the tag, minimizing effects of body detuning on the circuit and promoting the secured integrity of the tag while maintaining the flexibility thereof.

Another aspect of the invention is a deactivatable resonant tag circuit having a predetermined resonant frequency for use as an electronic article surveillance tag, which includes means for deactivating the resonant tag circuit in response to an electromagnetic field of sufficient energy to destroy the resonant properties of the circuit. The deactivating means includes a portion of at least one of the conductors which is indented in the substrate such that the conductors are closer to each other at the indented portion than at the remainder of the conductor. A flexible, substantially planar, tear resistant, polymeric film is adhered to and covers the conductor and the substrate on the side of the substrate opposite to the side which is to be adhered to an article subject to surveillance. The film provides a vapor barrier for the tag minimizing effects of body detuning on the circuit, and promoting the secured integrity of the tag while maintaining the flexibility thereof. In addition, as described below, the film provides unexpected advantages in the formation of the deactivation means in the tag.

-6-

Yet another aspect of the present invention is a resonant tag circuit provided for use as an electronic article surveillance tag, having a flexible substantially planar dielectric substrate. A first conductor is positioned on the first side of the dielectric substrate, and a second conductor is positioned on the second side of the dielectric substrate, while at least one of the conductors comprises an inductor. A flexible, substantially planar, tear resistant, substantially vapor impermeable polymeric film is adhered to and covers each of the conductors and the substrate on the side of each conductor opposite to the side which is positioned on the dielectric substrate.

The film provides a substantially impermeable vapor barrier for the tag, whereby effects of body and article detuning on the circuit are minimized and the secured integrity of the tag is promoted while maintaining the flexibility thereof.

In addition, the resonant tag circuit may include means for deactivating the circuit in response to an electromagnetic field of sufficient energy to destroy the resonant properties of the circuit. The deactivating means includes a portion of at least one of the conductors which is indented into the substrate such that the conductors are closer to each other at the indented portion than at the remainder of the conductor. In addition, as described below, the polymeric film provides unexpected advantages in the formation of the deactivation means in the tag.

#### Brief Description of the Drawings

The foregoing summary, as well as the following detailed description of the preferred



-7-

embodiments, will be better understood when read in conjunction with the appended drawings. For the purpose of illustrating the invention, there are shown in the drawings embodiments which are  
5 presently preferred, it being understood, however, that the invention is not limited to the specific arrangements and instrumentalities disclosed. In the drawings:

Fig. 1 is an exploded perspective view  
10 of a preferred embodiment of an electronic article surveillance tag in accordance with the present invention;

Fig. 2 is a cross-sectional view of a portion of a preferred embodiment of an electronic  
15 article surveillance tag in accordance with the present invention;

Fig. 3 is a cross-sectional view of a portion of a preferred embodiment of a deactivatable electronic article surveillance tag  
20 in accordance with the present invention; and

Fig. 4 is a cross-sectional view of a portion of another preferred embodiment of a deactivatable electronic article surveillance tag in accordance with the present invention.

#### 25 Detailed Description of the Preferred Embodiments

Referring to the drawings, wherein like numerals indicate like elements throughout, there is shown in Figs. 1 and 2 a preferred embodiment of a resonant tag circuit, generally designated 10,  
30 for use as an electronic article surveillance tag in accordance with the present invention.

Referring now to Fig. 1, the resonant tag circuit 10 includes a dielectric substrate 12. In the present embodiment, the dielectric substrate 12

-8-

is preferably both flexible and substantially planar. The aforementioned characteristics of the dielectric substrate are particularly useful in fabrication of the resonant tag circuit 10, enabling a series of tags 10 to be fabricated from a continuous web, as is well-known in the art. Flexibility also allows the resonant tag circuit 10 to be attached to a variety of articles (not shown) or bodies (not shown) having non-planar surfaces.

10 In the present embodiment, the dielectric substrate 12 is made from a polymeric material, preferably polyethylene. However, it will be recognized by those skilled in the art that the dielectric substrate 12 may be made from a variety of polymeric materials, including polyvinyl  
15 chloride, polystyrene, and other engineering thermoplastics which will be evident to those skilled in the art.

As best shown in Fig. 1, the dielectric  
20 substrate 12 includes a first side 16 and a second side 18. A first conductor 20 is positioned on the first side 16 of the dielectric substrate 12. The resonant tag circuit 10 further includes a second conductor 22 positioned on the second side 18 of  
25 the dielectric substrate 12. At least one of the conductors 20, 22 further includes an inductor 24. The first and second conductors 20, 22 together form an inductive-capacitance circuit.

In the present embodiment, it is  
30 preferred that each of the conductors 20, 22 be substantially planar and flexible. The conductors 20, 22 are preferably made from aluminum, specifically aluminum foil. However one of ordinary skill in the art would understand that  
35 other conductive materials, such as copper or

-9-

nickel, may be used for the conductors 20, 22. Further, one of ordinary skill in the art would understand that the conductors 20, 22 may be made from different conductive materials. The preferred  
5 thickness of the conductors 20, 22 is approximately 3 mils, although the conductors 20, 22 may be of any thickness in keeping with the spirit and scope of the present invention.

It is preferred that the inductor 24 be  
10 generally formed in a spiral path surrounding the conductive portion of the conductor 22, as best shown in Fig. 1, although the inductor 24 may be formed in other configurations.

The conductors 20, 22 are preferably  
15 formed on the first and second sides 16, 18 of the dielectric substrate 12 by an extrusion coating process (not shown). One of ordinary skill in the art would understand that the process of forming the basic tag structure comprising the conductors  
20 20, 22 and the inductor 24 on the dielectric substrate 12 is not specifically pertinent to an understanding of the present invention and is well known in the prior art. Further description of this part of the fabrication process is not  
25 believed to be necessary nor is it limiting.

As best shown in Fig. 1, the resonant tag circuit 10 of the invention includes polymeric films 27, 26 adhered to and covering each conductor 20, 22. The first polymeric film 27 is adhered to  
30 and covers the conductor 20 and side 16 of dielectric substrate 12. The second polymeric film 26 is adhered to and covers the conductor 22, inductor 24 and side 18 of dielectric substrate 12.

In an alternative preferred embodiment  
35 best shown in Fig. 4, the resonant tag circuit 10

-10-

includes polymeric film 26 which is adhered to and covers the conductor 22, inductor 24 and side 18 of dielectric substrate 12, i.e., the side of the substrate opposite to the side which is to be  
5 adhered to an article (not shown) subject to surveillance. The resonant tag circuit 10 of this alternative preferred embodiment does not include polymeric film 27 (shown in Figs. 1-3) which is adhered to and covers the conductor 20 and side 16  
10 of dielectric substrate 12 of the other preferred embodiment previously discussed.

The polymeric films 27, 26 are preferably both flexible and substantially planar to facilitate fabrication of the resonant tag  
15 circuit 10 and adherence of the tag 10 to non-planar surfaces on a wide variety of articles and bodies (not shown) to be protected by an electronic article surveillance system. Preferably, the polymeric films 27, 26 are also tear resistant,  
20 thus promoting the secured integrity of the tag 10 while maintaining the flexibility thereof.

It is preferred that the polymeric films 27, 26 provide a vapor barrier for the adjacent conductor 20 or 22 and dielectric  
25 substrate 12, thus eliminating the need for a film overlamine. The polymeric films 27, 26 preferably have a moisture absorption less than 0.8% as measured in accordance with ASTM D570-63. The low moisture absorption is particularly  
30 advantageous in that a smooth, wrinkle-free printing surface and substantially consistent heat transfer properties are maintained which provide for easier fabrication of resonant tag circuits 28.

Another advantage of the present  
35 stabilized tag circuit is that it resists detuning

-11-

of the circuit which occurs as a result of capacitance coupling of the inductor with the article or an animal body. For example, as previously mentioned, meat is typically packaged in permeable polymeric film. Packaging film having water adsorbed therein has a lower impedance compared to an equivalent film without water. When a typical prior art tag is placed on the meat package, the tag circuit may be detuned by the meat (i.e., article detuning). Further, detuning may occur as a result of handling (body contact) of the tag circuit by persons applying the tag or persons inspecting the article (i.e., body detuning). In the present invention, the polymeric film provides a substantially impermeable barrier which inhibits both article and body detuning of the tag circuit.

Another example of a situation in which use of the present tag would be advantageous is in wrist or ankle bands typically used in hospitals to identify patients. For example, in a nursery, the present tag may be used in an identification band to inhibit removal of infants from a secured area. Typical prior art tags, when used in such applications, may suffer from circuit detuning because of a lack of a barrier between the circuit and the patient's skin. The polymeric film 27 of the present invention provides a barrier layer which minimizes circuit detuning to improve circuit performance.

It is also preferred that the polymeric films 27, 26 have a tensile strength greater than 26,000 psi in the machine direction as measured in accordance with ASTM D882-80. The high tensile strength is indicative of the properties inherent

-12-

to polymeric materials which contribute to the tear resistance of the polymeric films 27, 26 and promote the secured integrity of the resonant tag circuit 10 while maintaining the requisite flexibility thereof. High tensile strength also allows use of polymeric films 27, 26 having thicknesses of less than about 2 mils, i.e., less than the thickness of paper used as outer layers in tags disclosed in the prior art.

Those skilled in the art would recognize the prior art practice of adhering paper to both sides of the resonant tag circuit 10 to cover the conductors 20, 22 and stabilize the laminate. Typically, the thickness of such paper is on the order of 3.5 to 4 mils. In contrast, the polymeric films 27, 26 of the present invention may each have a thickness not greater than about 2 mils and preferably less than about 1 mil. By using outer layers of polymeric film 27, 26 on both sides of the tag 10 instead of paper layers, the total thickness of the tag may be reduced to less than about 12 mils. In the alternative preferred embodiment in which polymeric film 27 is omitted, by using an outer layer of polymeric film 26 on one side of the tag 10 instead of a paper layer, the total thickness of such a tag in contrast with a typical prior art tag may be reduced from about 9 mils to less than about 7 mils.

The polymeric films 27, 26 are preferably made from polyester, although one of ordinary skill in the art understands that other polymeric materials such as polyethylene, polyvinyl chloride, and other plastics may be used. The polymeric films 27, 26 may be made from the same or different polymeric materials, as desired. For example, the

-13-

polymeric film 26 may be made from polyethylene while the polymeric film 27 may be made from polyvinyl chloride. Preferably, the polymeric films 27, 26 are made from the same material.

5           Suitable polyester films 27, 26 may include, for example, Mylar, which is commercially available from E. I. DuPont de Nemours & Co., Inc. of Wilmington, Delaware. Mylar is available in a variety of colors. The fabrication of resonant tag  
10 circuits 10 using a variety of colored Mylars may be advantageous in that different colored Mylars could provide visual differentiation among tags which resonate at different frequencies. An outer layer of thermal printing paper (not shown) may be  
15 added to the layer of Mylar which allows the tags to be printed upon in thermal printers.

A preferred polyester film 27 or 26 is Melinex, which is commercially available from ICI Americas Inc. of Wilmington, Delaware. Melinex is  
20 preferred because it has adequate gas/liquid impermeability, as well as sufficient flexibility, and strength at a gauge thickness less than about 1 mil.

Use of the polymeric films 27, 26 in the  
25 tag 10 is advantageous in that the polymeric films 27, 26 resist warping or shrinkage, thereby providing a substantially smooth, wrinkle-free printing surface opposite the conductor 22. The smooth, wrinkle-free printing surfaces which result  
30 from use of the polymeric films 27, 26 facilitate printing of prices and other information on the tag 10. The use of polymeric films 27, 26 allows the tags 10 to be printed upon in thermal, laser, and other printers having minimal printing clearances  
35 in which it was previously not possible to print on

-14-

a paper-covered tag. The thinner, more pliable tag 10 also reduces the pressure on printer heads.

Where a non-thermal printer is to be used, the polymeric film may be coated with a layer 5 which accepts inks or toners for non-thermal printing. Such a printable film is the commercially available product 200 Dukote CITC (DK-393) of Dunmore Corp. of Newtown, Pennsylvania, which is a specially coated Melinex film.

10 The resonant tag circuit 10 further includes adhesive layers 30 which adhere each polymeric film 27, 26 to each respective conductor 20, 22 and side 16, 18 of the substrate 12. It is preferred that the adhesive layers 30 each have a 15 thickness not greater than about 1.5 mils, and preferably in the range of about 0.5 to 1.5 mils.

Preferably, the resonant tag circuit 10 further includes a release liner 32, such as paper or plastic film, which is releasably adhered by a 20 pressure sensitive adhesive 34 to the polymeric film 27 on the opposite side from the conductor 20 and substrate 12. In the alternative embodiment shown in Fig. 4, it is preferred that the release liner 32 be releasably adhered to the conductor 20 25 and dielectric substrate 12 on the opposite side 16 from the polymeric film 26. The release liner 32 is removed from the tag 10 prior to attachment of the tag 10 to the article to be protected. It is preferred that the release liner 32 have a 30 thickness not greater than about 3.5 mils, although a release liner having a thickness greater than 3.5 mils could be used in conjunction with the present tag 10.

Another aspect of the invention relates 35 to use of polymeric films 27, 26 as outer layers



-15-

for a deactivatable resonant tag circuit 28, best shown in Fig. 3. The deactivatable resonant tag circuit 28 is substantially similar to the resonant tag circuit 10, however the deactivatable tag 28 further includes means for deactivating the tag 28 in response to an electromagnetic field (not shown) of sufficient energy to destroy the resonant properties of the tag circuit 28. The deactivating means comprises an indented portion or portions 36 (sometimes referred to as "dimples") of the conductor 22 which is indented into the substrate 12 such that the conductors 20, 22 are closer to each other at the indented portion 36 than over the remainder of the conductor 22. One of ordinary skill in the art would understand that either or both of the conductors 20, 22 may be indented into the substrate 12 in keeping with the spirit and scope of the invention.

In the alternative preferred embodiment best shown in Fig. 4, it is preferred that the indented portion(s) 36 be located on the opposite side of the substrate 12 from the side bearing the polymeric film 26 such that portion(s) 36 of the conductor 20 are indented into the substrate 12.

The deactivatable resonant tag circuit 28 is destroyed by an arc discharge (not shown) between the conductors 20, 22 and through the substrate 12 at the indented portion 36. The electric arc vaporizes a portion (not shown) of the conductors 20, 22 proximate the indented portion 36, thereby destroying the conductive path and deactivating the circuit. Alternatively, the arc forms a plasma of deposited metal between the conductors causing a short-circuit and destroying the resonant capabilities of the tag 10. A process

-16-

by which the tag 28 may be deactivated is explained at length in U.S. Patent No. 4,498,076.

The preferred process for fabricating the deactivatable tags 28 is set forth generally in U.S. Patent No. 3,913,219 of G. Lichtblau, which is hereby incorporated by reference. One of ordinary skill in the art would understand that other similar processes may be used in keeping with the spirit and scope of the present invention.

10 Generally a roll of etched circuits which comprise a dielectric substrate covered on either side by a layer of conductors is joined with one or more polymeric films which are carried on matrix carriers. Prior to joining of the polymeric films and the etched circuits, adhesive is applied to the surface of the polymeric films to be joined to the etched circuits. The etched circuits and films are joined by pressure applied by a pressure roll. The joined etched circuits and films together form a stabilized resonant tag web.

To form a deactivatable tag, adhesive 30 and polymeric film 26 are joined to the etched circuits and passed through a dimpling (indenting) roll in order to form the indented portions 36 best shown in Fig. 3. In a preferred embodiment, the tag web is then coated with pressure sensitive adhesive 30 and joined to polymeric film 27, so that only the side of the tag having polymeric film 26, which is to be positioned away from the article, is dimpled.

In the alternative preferred embodiment, polymeric film 27 and adjacent adhesive 30 are omitted and the indented portions 36 are formed in conductor 20 and substrate 12 on the opposite side

-17-

of the substrate 12 from the side bearing the polymeric film 26.

The web and release liner 32 are passed between an anvil roll and a dye-cut cylinder in order to separate the matrix carriers from the polymeric films, the matrix carriers being collected on matrix rolls. The web is subsequently coated with a pressure sensitive adhesive 34 in order that a release liner 32 and optional thermal printing paper, if desired, may be joined to the web. The adhesive 34 may be of a pressure sensitive type, however one of ordinary skill in the art would understand that non-pressure sensitive adhesives may be used. The final web of resonant tag circuits may be stored on a roll.

The use of polymeric films 27, 26 is particularly advantageous in the present process because the polymeric films 27, 26 allow substantially consistent heat transfer in any portions of the tag fabrication process which may involve the application of heat. The consistent heat transfer characteristics are due in part to the uniformity in thickness obtainable by extrusion of the polymeric material. Other desirable advantages of using polymeric films 27, 26 include low moisture absorption which allows more consistent heat transfer, and high tensile strength which allows use of polymeric films 27, 26 each of approximately 1 mil in thickness.

From the foregoing description, it can be seen that the present invention comprises a stabilized resonant tag circuit for use as an electronic article surveillance tag. It will be appreciated by those skilled in the art that changes could be made to the embodiments described

-18-

above without departing from the broad inventive  
concept thereof. It is understood, therefore, that  
this invention is not limited to the particular  
embodiments disclosed, but it is intended to cover  
5 modifications which are within the spirit and scope  
of the invention as defined by the appended claims.

-19-

CLAIMS

1. A resonant tag circuit for use as an electronic article surveillance tag, comprising:  
a flexible substantially planar  
5 dielectric substrate having a first side and a second side;  
a first conductor positioned on said first side of said dielectric substrate;  
a second conductor positioned on  
10 said second side of said dielectric substrate, wherein at least one of said conductors comprises an inductor; and  
a flexible, substantially planar, tear resistant, substantially vapor impermeable  
15 polymeric film adhered to and covering one of said conductors and said substrate on the side of said substrate opposite to the side which is to be adhered to an article subject to surveillance, said film providing a substantially impermeable vapor  
20 barrier for said one conductor and said substrate whereby effects of body and article detuning on said circuit are minimized, and the secured integrity of said tag is promoted while maintaining the flexibility thereof.
- 25 2. A resonant tag circuit according to claim 1, whereby said polymeric film resists warping or shrinkage, thereby providing a substantially smooth, wrinkle-free printing surface opposite said conductor.
- 30 3. A resonant tag circuit according to claim 1, wherein said tag has a thickness not greater than about 7 mils.

-20-

4. A resonant tag circuit according to claim 1, wherein said dielectric substrate comprises a polymeric material.

5 claim 4, wherein said dielectric substrate comprises polyethylene.

6. A resonant tag circuit according to claim 1, wherein at least one of said conductors comprises aluminum.

10 7. A resonant tag circuit according to claim 1, wherein said polymeric film is a polyester.

8. A resonant tag circuit according to claim 1, wherein said polymeric film has a  
15 thickness not greater than about 2 mils.

9. A resonant tag circuit according to claim 1, wherein said polymeric film has a moisture absorption less than about 0.8 percent.

20 10. A resonant tag circuit according to claim 1, wherein said polymeric film has a tensile strength greater than about 26,000 psi.

25 11. A resonant tag circuit according to claim 1, wherein an adhesive means adheres said polymeric film to said one conductor and substrate, said adhesive means having a thickness not greater than about 1.5 mils.

30 12. A resonant tag circuit according to claim 1, wherein said tag further comprises a release liner releasably adhered to said conductor and substrate on the opposite side from said polymeric film.

35 13. A resonant tag circuit according to claim 1, having a predetermined resonant frequency for use as an electronic article surveillance tag, further comprising:

-21-

means for deactivating said resonant tag circuit in response to an electromagnetic field of sufficient energy to destroy the resonant properties of said circuit, said deactivating means comprising a portion of at least one of said conductors which is indented into said substrate such that said conductors are closer to each other at said indented portion than the remainder of the conductor.

10           14. A resonant tag circuit according to claim 1, further comprising:

                  a second flexible, substantially planar, tear resistant, substantially vapor impermeable polymeric film adhered to and covering the other of said conductors and said substrate on the side of said other conductor opposite to the side which is positioned on said dielectric substrate, said film providing a substantially impermeable vapor barrier for each of said conductors and said substrate, whereby effects of body and article detuning on said circuit are minimized, and the secured integrity of said tag is promoted while maintaining the flexibility thereof.

25           15. A resonant tag circuit according to claim 14, whereby said second polymeric film resists warping or shrinkage, thereby providing a substantially smooth, wrinkle-free printing surface, said printing surface being located on a side of said polymeric film opposite to a side which is positioned on said conductors and said substrate.

30           16. A resonant tag circuit according to claim 14, wherein said tag has a thickness not greater than about 12 mils.

-22-

17. A resonant tag circuit according to claim 14, wherein an adhesive means adheres said second polymeric film to each of said conductors and said substrate, said adhesive means having a  
5 thickness not greater than about 1.5 mils.

18. A resonant tag circuit according to claim 14, wherein said tag further comprises a release liner releasably adhered to said second polymeric film.

10 19. A resonant tag circuit according to claim 14, having a predetermined resonant frequency for use as an electronic article surveillance tag, further comprising:

means for deactivating said resonant  
15 tag circuit in response to an electromagnetic field of sufficient energy to destroy the resonant properties of said circuit, said deactivating means comprising a portion of at least one of said  
conductors which is indented into said substrate  
20 such that said conductors are closer to each other at said indented portion than the remainder of the conductor.



FIG. 1

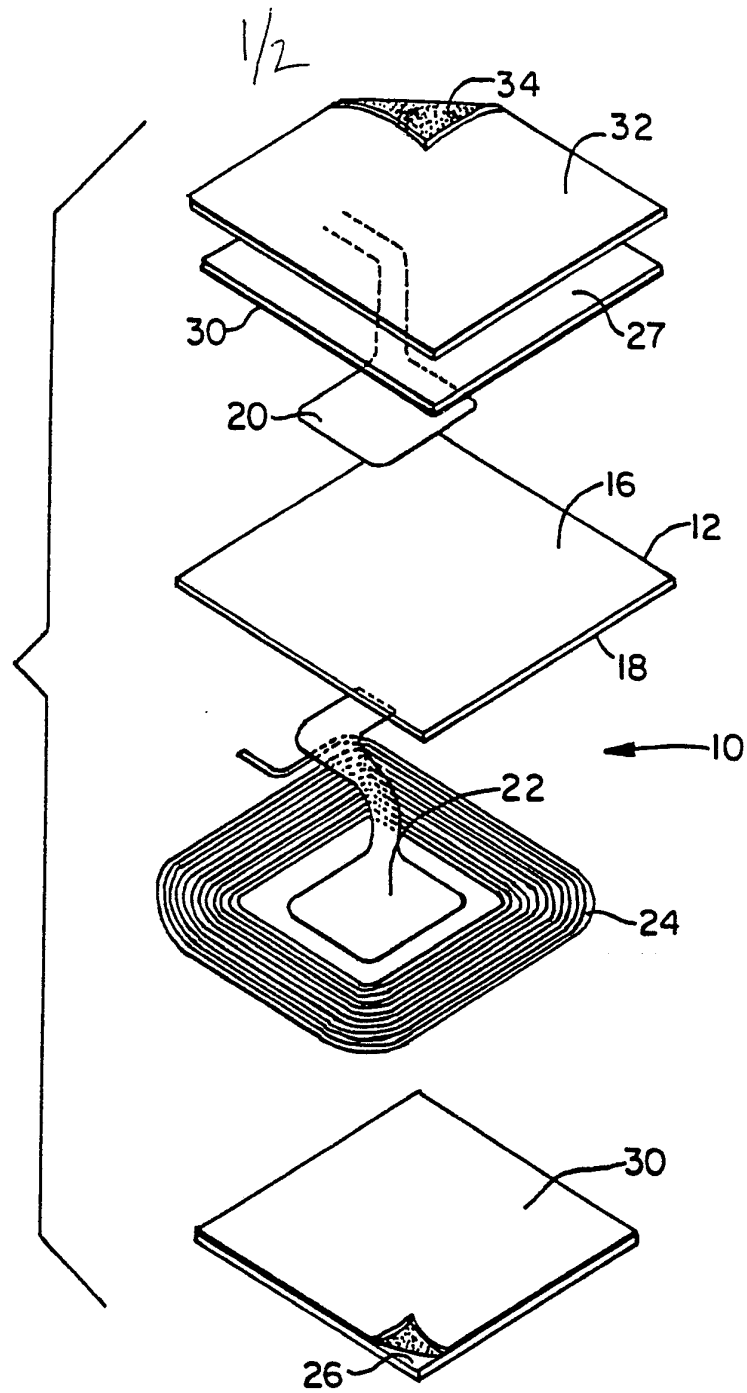


FIG. 2

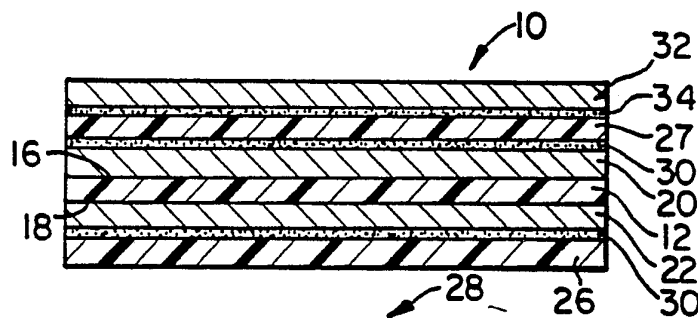
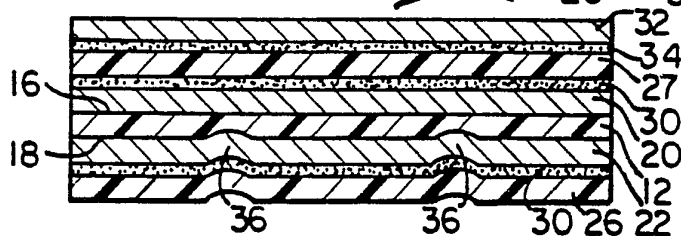


FIG. 3



2/2

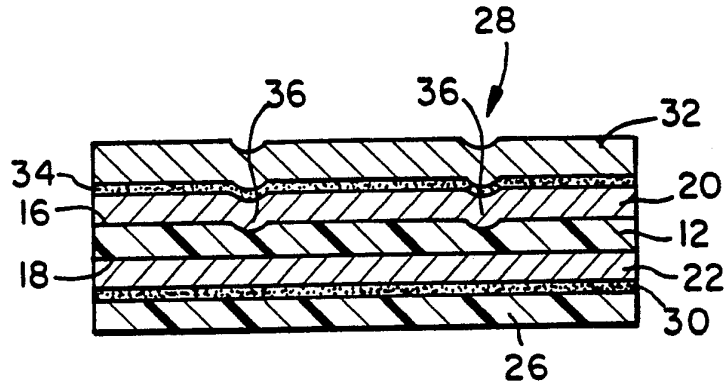


FIG. 4

INTERNATIONAL SEARCH REPORT

International application No.  
PCT/US92/04230

<p><b>A. CLASSIFICATION OF SUBJECT MATTER</b>                  IPC(5) :G08B 13/14; B32B 31/00; H01F 5/00                  US CL :340/572; 156/274.2; 336/200                  According to International Patent Classification (IPC) or to both national classification and IPC</p>														
<p><b>B. FIELDS SEARCHED</b></p> <p>Minimum documentation searched (classification system followed by classification symbols)                  U.S. : 29/846, 842, 831, 829, 825; 156/273.9, 272.2; 361/402, 395, 397-400                  427/96, 116, 117, 123; 333/175, 185; 343/895</p> <p>Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched</p> <p>Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)                  USPTO APS (resonant, dielectric, substrate, conductor, induct, polymer, film, vapor, flexible, vapor barrier, surveillance, eas)</p>														
<p><b>C. DOCUMENTS CONSIDERED TO BE RELEVANT</b></p> <table border="1"> <thead> <tr> <th>Category*</th> <th>Citation of document, with indication, where appropriate, of the relevant passages</th> <th>Relevant to claim No.</th> </tr> </thead> <tbody> <tr> <td>A</td> <td>US, A, 4,990,891 (REEB) 05 February 1991.</td> <td>1-19</td> </tr> <tr> <td>A</td> <td>US, A, 4,823,234 (KONISHI ET AL) 18 April 1989.</td> <td>1-19</td> </tr> <tr> <td>A</td> <td>US, A, 4,250,398 (ELLIS ET AL) 10 February 1981.</td> <td>1-19</td> </tr> </tbody> </table>			Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.	A	US, A, 4,990,891 (REEB) 05 February 1991.	1-19	A	US, A, 4,823,234 (KONISHI ET AL) 18 April 1989.	1-19	A	US, A, 4,250,398 (ELLIS ET AL) 10 February 1981.	1-19
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<p><input type="checkbox"/> Further documents are listed in the continuation of Box C. <input type="checkbox"/> See patent family annex.</p>														
<table border="0"> <tr> <td>* Special categories of cited documents:</td> <td>"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention</td> </tr> <tr> <td>"A" document defining the general state of the art which is not considered to be part of particular relevance</td> <td>"X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone</td> </tr> <tr> <td>"E" earlier document published on or after the international filing date</td> <td>"Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art</td> </tr> <tr> <td>"L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)</td> <td>"&amp;" document member of the same patent family</td> </tr> <tr> <td>"O" document referring to an oral disclosure, use, exhibition or other means</td> <td></td> </tr> <tr> <td>"P" document published prior to the international filing date but later than the priority date claimed</td> <td></td> </tr> </table>			* Special categories of cited documents:	"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention	"A" document defining the general state of the art which is not considered to be part of particular relevance	"X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone	"E" earlier document published on or after the international filing date	"Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art	"L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)	"&" document member of the same patent family	"O" document referring to an oral disclosure, use, exhibition or other means		"P" document published prior to the international filing date but later than the priority date claimed	
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<p>Date of the actual completion of the international search 07 JULY 1992</p>		<p>Date of mailing of the international search report <b>11 SEP 1992</b></p>												
<p>Name and mailing address of the ISA/ Commissioner of Patents and Trademarks Box PCT Washington, D.C. 20231</p>		<p>Authorized officer <i>Thomas J. Mullen, Jr.</i> THOMAS J. MULLEN, JR.</p>												
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