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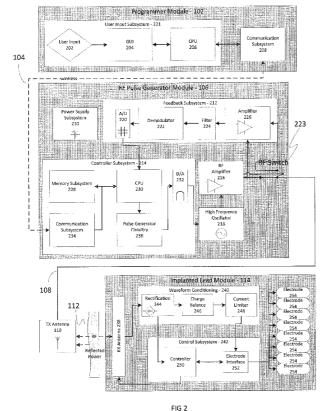
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(54) Title: NEURAL STIMULATOR SYSTEM



(57) Abstract: An implantable neural stimulator includes one or more electrodes, a first antenna, and one or more circuits. The one or more electrodes configured to apply one or more electrical pulses to neural tissue. The first antenna is a dipole antenna and is configured to receive an input signal containing electrical energy. The one or more circuits are configured to create one or more electrical pulses suitable using the electrical energy contained in the input signal; supply the one or more electrical pulses to the one or more electrodes such that the one or more electrodes apply the one or more electrical pulses to neural tissue; generate a stimulus feedback signal; and send the stimulus feedback signal to the dipole antenna such that the dipole antenna transmits the stimulus feedback signal to the second antenna through electrical radiative coupling.

SE, SG, SK, SL, SM, ST, SV, SY, TH, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, ZA, ZM, ZW.

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A. CLASSIFICATION OF SUBJECT MATTER IPC(8) - A61N 1/00 (2012.01) USPC - 607/60 According to International Patent Classification (IPC) or to both national classification and IPC			
B. FIELDS SEARCHED			
Minimum documentation searched (classification system followed by classification symbols) IPC(8) - A61N 1/00 (2012.01) USPC - 607/60			
Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched IPC(8) - A61N 1/00 (2012.01) (keyword limited - see terms below) USPC - 607/60, 68-72 (keyword limited - see terms below)			
Electronic data base consulted during the international search (name of data base and, where practicable, search terms used) PubWEST (PGPB, USPT, EPAB, JPAB), Google Scholar, Google Patents; Search Terms Used: neural stimulator, stimulat\$4, activat\$4, neural, organ, sensory, rectp\$3, impant\$5, medtronic, dipole, antenna, electrode, frequenc\$3, radiat\$4, energiz\$4, rectifier, capacitor, charge, encod\$3, phase, exceed\$3, surpass\$3, circuit, feedback signal, zero charge			
C. DOCUMENTS CONSIDERED TO BE RELEVANT			
Category*	Citation of document, with indication, where a	ppropriate, of the relevant passages	Relevant to claim No.
×	US 2007/0213783 A1 (Pless) 13 September 2007 (13.09.2007), entire document, especially; para. [0020], [0053], [0081], [0083], [0085], [0090], [0091], [0100], [0103], [0104], [0124], [0136], [0147], [0151], [0157], [0159], [0162], [0174], [0186], [0188], [0194], [0195], Fig. 2		1-7, 9, 11-26, 28, 30-39
Y			8, 10, 27, 29, 40
Y	US 6,415,184 B1 (Ishikawa et al.) 02 July 2002 (02.07 col. 14, In. 35-40, col. 8, In. 10 - 20).	.2002), entire document, especially;	8, 10, 27, 29, 40
A	US 2007/0288066 A1 (Christman et al.) 13 December 2007 (13.12.2007), entire document		1 - 40
Further documents are listed in the continuation of Box C.			
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