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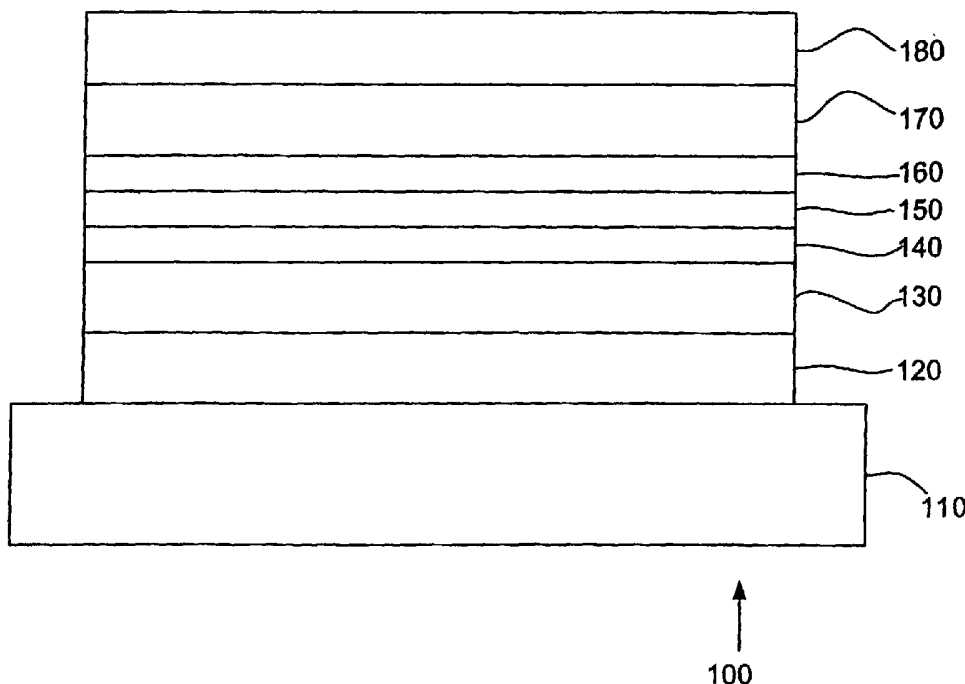
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(54) Title: VERY LOW VOLTAGE HIGH EFFICIENCY PHOLED IN A P-I-N STRUCTURE



(57) Abstract: An organic light emitting device (100) is provided, having a p-doped organic layer (130), an n-doped layer (170), and a phosphorescent emissive layer (150) disposed between the p-doped and n-doped layers. Blocking layers (140, 160) are used to confine electrons, holes, and excitons in the emissive layer. A device having a cathode (180) on the top is provided, as well as an "inverted" device having a cathode on the bottom.



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*For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.*

## VERY LOW VOLTAGE, HIGH EFFICIENCY PHOLED IN A P-I-N STRUCTURE

### Field of the Invention

[0001] The present invention relates to organic light emitting devices, and more specifically to the use of blocking layers to increase the efficiency of such devices.

### Background

[0002] Organic light emitting devices (OLEDs), which make use of thin films that emit light when excited by electric current, are becoming an increasingly recognized technology for applications such as flat panel displays. Popular OLED configurations include double heterostructure, single heterostructure, and single layer, as described in PCT Application WO 96/19792, which is incorporated herein by reference.

[0003] Until recently, OLED devices generally relied on intrinsic semiconductor materials. The hole transport, electron transport, and emissive layers were not doped for the purpose of controlling carrier concentration. An OLED having a p-i-n structure is described in Huang et al., *Low Voltage Organic Electroluminescent Devices Using pin Structures*, Applied Physics Letters, Vol. 80, No. 1, pp 139-141 (2002). In particular, the OLED has a p-doped layer, an intrinsic emissive layer, and an n-doped layer. Huang also describes the use of "blocking" layers on both sides of the organic emissive layer of a p-i-n OLED.

### Summary of the Invention

[0004] An organic light emitting device is provided, having a p-doped organic layer, an n-doped layer, and a phosphorescent emissive layer disposed between the p-doped and n-

doped layers. Blocking layers are used to confine electrons, holes, and excitons in the emissive layer. A device having a cathode on the top is provided, as well as an "inverted" device having a cathode on the bottom.

#### Brief Description of the Drawings

[0005] Fig. 1 shows a p-i-n organic light emitting device having a cathode on the top of the device.

[0006] Fig. 2 shows an n-i-p organic light emitting device having a cathode on the bottom of the device.

[0007] Fig. 3 shows the I-V characteristics of devices fabricated in accordance with an embodiment of the present invention;

[0008] Fig. 4 shows the quantum efficiency-voltage characteristics of devices fabricated in accordance with an embodiment of the present invention;

[0009] Fig. 5 shows the power efficiency-current density characteristics of devices fabricated in accordance with an embodiment of the present invention;

[0010] Fig. 6 shows the quantum efficiency-luminance characteristics of devices fabricated in accordance with an embodiment of the present invention;

[0011] Fig. 7 shows the electroluminescent (EL) intensity-voltage characteristics of devices fabricated in accordance with an embodiment of the present invention;

[0012] Fig. 8 shows the EL intensity-voltage characteristics for devices fabricated in accordance with an embodiment of the present invention;

[0013] Fig. 9 shows the quantum efficiency and the power efficiency of devices fabricated in accordance with an embodiment of the present invention; and

[0014] Fig. 10 shows the transmission-wavelength characteristics for devices fabricated in accordance with an embodiment of the present invention.

#### Detailed Description

[0015] An OLED having a p-i-n structure has an anode, a p-doped organic layer adapted to transport holes, an intrinsic organic emissive layer, an n-doped organic layer adapted to transport electrons, and a cathode. The device is referred to as a p-i-n device because, as one moves away from the substrate, there is a p-doped layer, an intrinsic layer, and an n-doped layer, in that order. When a current is applied between the anode and cathode, holes are injected from the anode into the p-doped layer, and subsequently into the

emissive layer. Electrons are injected from the cathode into the n-doped layer, and subsequently into the emissive layer. Electrons and holes may combine in the emissive layer to form an exciton, which may subsequently decay to emit light. In a theoretical 100% efficient OLED, all of the electrons and holes would combine in the emissive layer to form excitons and subsequently emit light. As used herein, the terms “doping” and “doped” refers to the addition of a second constituent to a base material, where the concentration of the second constituent may range from just over zero to almost 100%.

**[0016]** Embodiments of the present invention may be used with a doped emissive layer, even though the layer is described here as intrinsic. For example, the emissive layer may be doped with dyes to control the emissive properties. At high doping levels of dyes, the conductivity may also increase.

**[0017]** In a fluorescent emissive material, for example Alq<sub>3</sub>, spin states associated with an exciton disallow many of the excitons from emitting light, as described in Adachi, Baldo, Thompson, and Forrest, “Nearly 100% Internal Phosphorescent Efficiency In An Organic Light Emitting Device,” J. Appl. Phys., 90 5048 (2001), which is incorporated herein by reference. In contrast, spin states allow excitons to emit light in particular classes of phosphorescent emissive materials known to the art.

**[0018]** In addition, electrons injected from the n-doped layer into the emissive layer may travel across the emissive layer without combining with a hole, and pass into the p-doped layer. Similarly, a hole may travel across the emissive layer without combining with an electron, and pass into the n-doped layer. Once this happens, the electrons and holes in question are not available to form a light-emitting exciton, decreasing device efficiency.

**[0019]** Also, there are a number of ways that an exciton may decay without emitting light. An exciton may quench on an impurity in the emissive layer. Where a p-doped layer or an n-doped layer is used, any dopant that diffuses into the emissive layer from these transport layers may quench excitons. The use of undoped buffer layers to prevent such diffusion is described in Huang at 140.

**[0020]** In addition, excitons may diffuse out of the emissive layer into the surrounding layers, where they will not emit light. Such diffusion is not generally a problem in fluorescent devices, because the excitons have relatively short lifetimes and diffusion lengths, on the order of 1 to 10 nanoseconds and 1 to 5 nanometers. But, in a phosphorescent material, excitons may have much longer lifetimes and diffusion lengths, on the order of 100 to 1000 nanoseconds and 50 to 200 nanometers, and such diffusion may be more significant.

[0021] Blocking layers may be used to prevent electrons and holes from leaving the emissive layer. An electron blocking layer may be disposed between the emissive layer and the p-doped layer, to prevent electrons from passing into the p-doped layer. Preferably, the energy barrier is sufficiently great that even high energy electrons have a small probability of surmounting the barrier. As a result, the energy barrier is preferably significantly higher than the thermal energy.

[0022] Similarly, a hole blocking layer may be disposed between the emissive layer and the n-doped layer, to prevent holes from passing onto the n-doped layer. Preferably, the energy barrier is sufficiently great that even high energy holes have a small probability of surmounting the barrier. As a result, the energy barrier is preferably significantly higher than the thermal energy.

[0023] Blocking layers may also be used to prevent excitons from diffusing out of the emissive layer. An exciton, which is an electron that has been excited into the conduction band, paired with a hole located on the same organic semiconductor molecule, has an energy that is related to the band gap of the semiconductor. The exciton energy is actually less than the band gap due to Coulombic attraction of the bound electron-hole pair. A material having a particular exciton energy will block the entry of excitons from a material having a lower exciton energy.

[0024] Excitons in a material having a particular band gap (the difference between the HOMO and LUMO energy levels) generally have an energy level that is less than that of excitons in a material having a wider band gap. Accordingly, excitons generally may not diffuse from a material having a lower band gap into a material having a higher band gap, and a higher band gap material may be used to block excitons from leaving a lower band gap material.

[0025] Fig. 1 shows an organic light emitting device 100. The device includes a substrate 110, an anode 120, a p-doped layer 130, a first blocking layer 140, an emissive layer 150, a second blocking layer 160, an n-doped layer 170, and a cathode 180. Because layer 130 is p-doped, emissive layer 150 is intrinsic, and layer 170 is n-doped, device 100 may be referred to as a p-i-n device. Device 100 may be fabricated by depositing the layers described, in order.

[0026] Substrate 110 and anode 120 may be any suitable material or combination of materials known to the art, such that anode 120 is adapted to inject holes into p-doped layer 130. Anode 120 and substrate 110 may be sufficiently transparent to create a bottom emitting

device. A preferred substrate and anode combination, which is transparent, is commercially available ITO (anode) deposited on glass or plastic (substrate). Substrate 110 may be rigid or flexible. Preferred anode materials include conductive metal oxides and metals. A hole-injection enhancement layer may be used to increase the injection of holes from anode 120 into p-doped layer 130.

**[0027]** P-doped layer 130 may be a p-doped organic semiconductor material. For example, m-MTDATA:F4-TCNQ (50:1), which is m-MTDATA doped with F4-TCNQ at a molar ratio of 50:1, is a suitable p-doped organic semiconductor material for p-doped layer 130. Any of organic layers of the various embodiments may be deposited by methods known to the art, including thermal evaporation or organic vapor phase deposition (OVPD), such as described in U.S. Patent No. 6,337,102 to Forrest et al., which is incorporated by reference in its entirety.

**[0028]** First blocking layer 140 may be adapted to block electrons from moving out of emissive layer 150 into first blocking layer 140. This blocking may be accomplished by using a first blocking layer 140 having a LUMO (lowest unoccupied molecular orbital) energy level that is significantly higher than the LUMO energy level of emissive layer 150. A greater difference in LUMO energy levels results in better electron blocking properties. Suitable materials for use in first blocking layer 140 are dependent upon the material of emissive layer 150.

**[0029]** Emissive layer 150 may be any suitable organic emissive material. Preferably, emissive layer 150 is a phosphorescent emissive material, although fluorescent emissive materials may also be used. Phosphorescent materials are preferred because of the higher luminescent efficiencies associated with such materials. Many emissive materials have resistivity that is significant, so it is also preferable to minimize the thickness of emissive layer 150, while still having a thickness sufficient to ensure a contiguous layer.

**[0030]** Second blocking layer 160 may be adapted to block holes from moving out of emissive layer 150 into second blocking layer 160. This blocking may be accomplished by using a second blocking layer 160 having a HOMO (highest occupied molecular orbital) energy level that significantly higher than the HOMO energy level of emissive layer 150. A greater difference in HOMO energy levels results in better hole blocking properties. Suitable materials for use in second blocking layer 160 are dependent upon the material of emissive layer 150.

**[0031]** N-doped layer 170 may be an n-doped organic semiconductor material. For

example, BPhen\*Li (1:1), which is BPhen doped with Li at a molar ratio of 1:1, is a suitable n-doped organic semiconductor material for n-doped layer 170.

[0032] Cathode 180 may be any suitable material or combination of materials known to the art, such that cathode 180 is adapted to inject electrons into n-doped layer 170. For example, ITO, zinc-indium-tin oxide, and other materials known to the art may be used. Cathode 180 may be sufficiently transparent to create a top emitting device. Both cathode 180 and anode 120 may be transparent or partially transparent to create a transparent OLED. An electron-injection enhancement layer may be used to increase the injection of electrons from cathode 180 into n-doped layer 170.

[0033] Where emissive layer 150 is a phosphorescent material, first blocking layer 140 and second blocking layer 160 preferably have exciton energies higher than that of emissive layer 150. Generally, this may be accomplished by using materials for first blocking layer 140 and second blocking layer 160 that have wider band gaps than emissive layer 150.

[0034] Preferably, blocking layers 140 and 160 are not doped to enhance their conductivity. Doping these layers in such a manner may allow the dopant in question to diffuse into the emissive layer, where it may quench excitons and reduce device efficiency. In addition, blocking layers 140 and 160 are preferably sufficiently thick, and the process parameters are sufficiently controlled, that there is little or no diffusion of dopants from p-doped layer 130 and n-doped layer 170 into emissive layer 150. It may be desirable to enhance the stability of certain blocking layer materials, such as BPhen and BCP, by adding another constituent to these layers. Wakimoto, U.S. Patent Application Pub. 2001/0,043,044 at paragraph 40, and Wakimoto, U.S. Patent Application Pub. 2001/0,052,751 at paragraph 36, which are incorporated by reference in their entireties, describe the mixing of BCP with another constituent.

[0035] Because first and second blocking layers 140 and 160 may prevent the movement of electrons, holes, and excitons out of emissive layer 150, it may be possible to use a very thin emissive layer, on the order of 10 nm or less, and more preferably about 5 nm or less, in conjunction with the blocking layers. A thin emissive layer 150 advantageously reduces the resistance of the OLED. The use of such a thin emissive layer may not be feasible without the use of blocking layers, because electrons, holes, and excitons might readily move out of a thin emissive layer, reducing device efficiency.

[0036] Most preferably, two blocking layers, one on either side of the emissive layer, are used to maximize the number of charge carriers and excitons trapped in the emissive

layer. But, the use of a single blocking layer to prevent excitons and charge carriers from leaving one side of the emissive layer is also within the scope of the invention.

[0037] A first preferred embodiment uses the following materials and thicknesses:

substrate 110                      commercially available ITO coated (150 nm) substrate

and anode 120:

p-doped layer 130:    50 nm m-MTDATA:F4-TCNQ (50:1)

first blocking layer 140:    10 nm Ir(ppz)<sub>3</sub>

emissive layer 150:            5 nm CBP:Ir(ppy)<sub>3</sub> (13:1)

second blocking layer 160:    25 nm Bphen

p-doped layer 170:    35 nm BPhen\*Li (1:1)

cathode 180:                    100 nm Al

[0038] The quantum efficiency of the first preferred embodiment may be high for several reasons. Emissive layer 150 of this embodiment is a phosphorescent material, which results in a device having high quantum efficiency. First blocking layer 140 and second blocking layer 160 are undoped, so that there are no dopants to diffuse from those layers into emissive layer 150. First blocking layer 140 and second blocking layer 160 have higher band gaps, and higher exciton energies, than emissive layer 150. Consequently, excitons that form in emissive layer 150 may not diffuse out. In first blocking layer 140, the doping profile of F4-TCNQ in m-MTDATA may be well defined by controlled coevaporation, and the diffusion of F4-TCNQ at room temperature is minimal. Similarly, Li has a very low diffusion length in BPhen due to the closely packed structure of BPhen. As a result, there should be very little or no F4-TCNQ or Li diffusion into emissive layer 150 at room temperature, and very little or no exciton quenching due to such diffusion.

[0039] The first preferred embodiment may have a low operating voltage for several reasons. The injection of carriers into the highly doped transport layers is efficient, such that injection enhancement layers are not necessary in this embodiment. It is believed that the tunneling of electrons through an extremely thin depletion layer may play a role in the efficient injection of electrons from Al into Li-doped BPhen. With reference to Fig. 2, holes injected from the ITO anode 120 face a low series of barriers from ITO to m-MTDATA to Ir(ppz)<sub>3</sub> to Ir(ppy)<sub>3</sub>. Similarly, electrons injected from Al cathode 180 face a low series of barriers from Al to Li:BPhen to BPhen to Ir(ppy)<sub>3</sub>. The role of the HOMO and LUMO of CBP for carrier transport is unclear. The doped transport layers (n-doped layer 170 and p-doped layer 130) have high conductivity, and consequently low ohmic losses. The undoped

layers (first blocking layer 140, emissive layer 150, and second blocking layer 160) have a low total thickness, so the relatively lower conductivity does not lead to significant ohmic losses. Some Li diffusion into the undoped BPhen may further lower the thickness of the higher conductivity undoped region. In addition, undoped BPhen has a high electron mobility. Ir(ppy)<sub>3</sub> forms a trap in CBP for both electrons and holes, so the effective carrier mobilities are expected to be low. But, the low thickness of the CBP:Ir(ppy)<sub>3</sub> layer mitigates this low effective mobility.

**[0040]** Fig. 2 shows an organic light emitting device 200. The device includes a substrate 210, a cathode 220, an n-doped layer 230, a first blocking layer 240, an emissive layer 250, a second blocking layer 260, a p-doped layer 270, and an anode 280. Because OLEDs are generally fabricated with the anode on the bottom and the cathode on the top, and the device of Fig. 2 has cathode 220 on the bottom and anode 280 on the top, the device of Fig. 2 may be referred to as an “inverted” OLED. Device 200 may be fabricated by depositing the layers described, in order.

**[0041]** Substrate 210 and cathode 220 may be any suitable material or combination of materials known to the art, such that cathode 220 is adapted to inject electrons into n-doped layer 230. Cathode 220 and substrate 210 may be sufficiently transparent to create a bottom emitting device. Materials similar to those described for substrate 110 may be used. An electron-injection enhancement layer may be used to increase the injection of holes from cathode 220 into n-doped layer 230.

**[0042]** Because cathode 220 is on the bottom of the device, device 200 is particularly suitable for use with n-type transistors fabricated on the substrate. Some particularly desirable substrates, such as amorphous silicon, may allow for the fabrication of only n-type transistors. Cathodes are best controlled by an n-type transistor, and anodes are best controlled by p-type transistors. As a result, an inverted device such as device 200 favorably allows for the fabrication of OLEDs on an amorphous silicon substrate, and for the fabrication of an active matrix display of inverted OLEDs with a common top anode on an amorphous silicon substrate.

**[0043]** N-doped layer 230, first blocking layer 240, emissive layer 250, and second blocking layer 260 may be made of materials similar to n-doped layer 170, second blocking layer 160, emissive layer 150, and first blocking layer 140, respectively, of device 100, and have similar considerations.

**[0044]** P-doped layer 270 may be a p-doped organic semiconductor material, and may

be made of materials suitable for use in p-doped layer 130 of device 100. But, because device 200 has a sputtered top electrode, it is desirable to protect emissive layer 250 from damage during the deposition of top electrode 280. Consequently, it may be desirable to use a thick p-doped layer 270 to contribute to such protection.

[0045] Buffer layer 275 may be a p-doped organic semiconductor material, and may be made of any suitable material that transports holes from anode 280 to p-doped layer 270, and provides protection to the underlying organic layer during the deposition of anode 280. CuPc is known as a suitable protective buffer layer material, and CuPc:F4-TCNQ (50:1) is a suitable material for buffer layer 275. If p-doped layer 270 provides adequate protection to the underlying organic layers, and is able to form a good interface with sputter deposited ITO, buffer layer 275 may not be necessary.

[0046] Anode 280 may be any suitable material or combination of materials known to the art, such that anode 280 is adapted to inject electrons into n-doped layer 270 (or buffer layer 275, if present). Anode 280 may be sufficiently transparent to create a top emitting device. Both anode 280 and cathode 220 may be transparent or partially transparent to create a transparent OLED. A hole injection enhancement layer may be used to increase the injection of holes from cathode 180 into n-doped layer 270 (or buffer layer 275, if present).

[0047] The blocking properties of first blocking layer 240 and second blocking layer 260 are preferably similar to those of second blocking layer 160 and first blocking layer 140, respectively, of device 1, with respect to holes, electrons, and excitons.

[0048] A second preferred embodiment uses the following materials and thicknesses:  
substrate 210                      commercially available ITO coated (150 nm) substrate  
and cathode 220:

n-doped layer 230:    15 nm BPhen:Li (1:1)

first blocking layer 240:    20 nm BPhen

emissive layer 250:            10 nm CBP:Ir(ppy)<sub>3</sub> (13:1)

second blocking layer 260:    10 nm Ir(ppz)<sub>3</sub>

n-doped layer 270:    180 nm m-MTDATA:F4-TCNQ (50:1)

buffer layer 275                20 nm CuPc:F4-TCNQ (50:1)

anode 280:                      80 nm ITO

[0049] The second preferred embodiment has an energy level diagram similar to that of the first embodiment with respect to the blocking and emissive layers, except that it is inverted. The second preferred embodiment may have a high efficiency and low operating

voltage for reasons similar to those described with respect to the first preferred embodiment. Tunneling through thin depletion layers from the electrodes into the transport layers may contribute to the injection of carriers from the electrodes. The relatively thick p-doped layer 270 and buffer layer 275 protect emissive layer 250 from damage during the sputter deposition of anode 280, yet result in low ohmic losses to efficiency due to the doping and resultant high conductivity.

**[0050]** BA1q and BCP may be suitable substitutes for BPhen in any of the embodiments.

**[0051]** It is understood that the various embodiments described herein are by way of example only, and are not intended to limit the scope of the invention. For example, many of the materials described herein may be substituted with other materials without deviating from the spirit of the invention.

#### **Material Definitions:**

**[0052]** As used herein, abbreviations refer to materials as follows:

CBP:	4,4'-N,N'-dicarbazole-biphenyl
m-MTDATA	4,4',4''-tris(3-methylphenylphenylamino)triphenylamine
Alq3:	8-tris-hydroxyquinoline aluminum
Bphen:	4,7-diphenyl-1,10-phenanthroline
n-BPhen:	n-doped BPhen (doped with lithium)
F4-TCNQ:	tetrafluoro-tetracyano-quinodimethane
p-MTDATA:	p-doped m-MTDATA (doped with F4-TCNQ)
Ir(ppy)3:	fac-tris(2-phenylpyridine)-iridium
Ir(ppz)3:	fac-tris(1-phenylpyrazoloto,N,C(2'))iridium(III)
BCP:	2,9-dimethyl-4,7-diphenyl-1,10-phenanthroline
TAZ:	3-phenyl-4-(1'-naphthyl)-5-phenyl-1,2,4-triazole
CuPc:	copper phthalocyanine.
ITO:	indium tin oxide
NPD:	naphthyl-phenyl-diamine
TPD:	N,N'-bis(3-methylphenyl)-N,N'-bis-(phenyl)-benzidine
BA1q:	aluminum(III)bis(2-methyl-8-quinolinato)4-phenylphenolate

#### **Experimental:**

[0053] The devices described herein were fabricated using deposition techniques known to the art. The deposition of organic layers was by thermal deposition under a vacuum of at least about  $10^{-7}$  torr.

[0054] A first device was fabricated having the following layer sequence:

commercially available ITO (indium tin oxide) on a substrate

50 nm m-MTDATA:F4-TCNQ (50:1)

10 nm Ir(ppz)<sub>3</sub>

5 nm CBP:Ir(ppy)<sub>3</sub> (13:1)

40 nm BPhen

20 nm BPhen\*Li (1:1)

Al cathode

[0055] A second device was fabricated having the same layer sequence as the first device, except the 10 nm Ir(ppz)<sub>3</sub> was replaced by 10 nm NPD.

[0056] A third device was fabricated having the same layer sequence as the second device, except the thickness of the 5 nm CBP:Ir(ppy)<sub>3</sub> (13:1) was increased to 20 nm.

[0057] A fourth device was fabricated as described in Adachi, Baldo, Thompson, and Forrest, "High Efficiency Organic Devices With tris(2-phenylpyridine) Iridium Doped Into Electron Transporting Materials," J. Appl. Phys., 77, 904 (2000).

[0058] A fifth device was fabricated as described in Adachi, Baldo, Thompson, and Forrest, "Nearly 100% Internal Phosphorescent Efficiency In An Organic Light Emitting Device," J. Appl. Phys., 90, 5048 (2001).

[0059] A sixth device was fabricated having the same layer sequence as the second device, except the thickness of the 40 nm BPhen layer was decreased to 25 nm, and the thickness of the 20 nm BPhen layer was increased to 35 nm.

[0060] A seventh device was fabricated having the same layer sequence as the second device, except the thickness of the 40 nm BPhen layer was increased to 60 nm, and the 20 nm BPhen:Li layer was eliminated and replaced with a 1 nm layer of Li.

[0061] An eighth device was fabricated having the following layer sequence:

commercially available ITO (indium tin oxide) on a substrate

50 nm NPD

5 nm CBP:Ir(ppy)<sub>3</sub> (13:1)

10 nm BCP

40 nm Alq<sub>3</sub>

LiF:Al cathode

[0062] Fig. 3 is a graph depicting the I-V characteristics of device 1, device 2 and device 3. Plot 310 illustrates the current density (mA/cm<sup>2</sup>) for device 1 plotted against bias voltage (V). Plot 320 illustrates the current density (mA/cm<sup>2</sup>) for device 2 plotted against bias voltage (V). Plot 330 illustrates the current density (mA/cm<sup>2</sup>) for device 3 plotted against bias voltage (V). The current density for all devices 1-3 demonstrates a sharp increase between two and three volts, such that the operating voltage is a remarkably low 2-9 volts. Device 1, with an Ir(ppz)<sub>3</sub> blocking layer, is able to generate a larger current density at relevant voltages when compared to device 2 and device 3, which has an NPD blocking layer, particularly between three and seven volts.

[0063] Fig. 4 is a graph depicting the quantum efficiency-voltage characteristics of device 1, device 2 and device 3. Plot 410 illustrates the quantum efficiency (%) for device 1 plotted against current density (mA/cm<sup>2</sup>). Plot 420 illustrates the quantum efficiency (%) for device 2 plotted against current density (mA/cm<sup>2</sup>). Plot 430 illustrates the quantum efficiency (%) for device 3 plotted against current density (mA/cm<sup>2</sup>). Device 1 shows a higher quantum efficiency for current densities above about 0.5 mA/cm<sup>2</sup>.

[0064] Fig. 5 is a graph depicting the power efficiency-current density characteristics of device 1, device 4 and device 5. Plot 510 illustrates the power efficiency (lumens per watt or lm/W) of device 1 plotted against the current density (mA/cm<sup>2</sup>). Plot 520 illustrates the power efficiency (lm/W) of device 4 plotted against the current density (mA/cm<sup>2</sup>). Plot 530 illustrates the power efficiency (lm/W) of device 5 plotted against the current density (mA/cm<sup>2</sup>). Similar to Fig. 4, device 1 shows a higher power efficiency for current densities above about 0.5 mA/cm<sup>2</sup>.

[0065] Fig. 5 shows that device 1, fabricated in accordance with an embodiment of the present invention, has a power efficiency of 27 lm/W at an intensity of 1000 cd/m<sup>2</sup>. Device 1 has a CBP:Ir(ppz)<sub>3</sub> (13:1) emissive layer, which is adapted to emit green light. It is believed that this power efficiency at this intensity, for green light, is greater than any previously attained for an organic light emitting device. It is therefore possible to fabricate organic devices adapted to emit green light having a power efficiency greater than about 20 lm/W at an intensity of 1000 cd/m<sup>2</sup>, which is also believed to be superior to any previously attained results.

[0066] As used herein, the term "blue" light refers to an emission spectrum having a

peak wavelength less than or equal to about 495 nm, the term “green” light refers to an emission spectrum having a peak wavelength greater than about 495 nm and less than or equal to about 580 nm, and the term “red” light refers to an emission spectrum having a peak wavelength greater than about 580 nm.

[0067] It is generally known that blue and red OLEDs have lower power efficiencies than green OLEDs. Based on the results achieved for the green OLED, the structure of device 1 may be adapted to emit either red or blue light having a power efficiency greater than about 7 lm/W at an intensity of about 1000 cd/m<sup>2</sup> by adjusting the composition of the layers.

[0068] Fig. 6 is a graph depicting the quantum efficiency-luminance characteristics of device 2, device 6, device 7 and device 8. Plot 610 illustrates the quantum efficiency (%) of device 6 plotted against the luminance (cd/m<sup>2</sup>). Plot 620 illustrates the quantum efficiency (%) of device 7 plotted against the luminance (cd/m<sup>2</sup>). Plot 630 illustrates the quantum efficiency (%) of device 2 plotted against the luminance (cd/m<sup>2</sup>). Plot 640 illustrates the quantum efficiency (%) of device 8 plotted against the luminance (cd/m<sup>2</sup>). Fig. 6 shows that devices fabricated in accordance with embodiments of the invention have a relatively high quantum efficiency at a high luminance. High quantum efficiency at high luminance is a desirable characteristic for display devices and light sources.

[0069] Fig. 7 is a graph depicting the electroluminescent (EL) intensity-voltage characteristics of device 2, device 6, device 7 and device 8. Plot 710 illustrates the EL intensity (cd/m<sup>2</sup>) of device 6 plotted against the voltage (V). Plot 720 illustrates the EL intensity (cd/m<sup>2</sup>) of device 2 plotted against the voltage (V). Plot 730 illustrates the EL intensity (cd/m<sup>2</sup>) of device 7 plotted against the voltage (V). Plot 740 illustrates the EL intensity (cd/m<sup>2</sup>) of device 8 plotted against the voltage (V). Fig. 7 shows that devices fabricated in accordance with embodiments of the invention show a dramatic rise in emission between about 2.5 and 3.5 volts. Such devices reach an intensity of 1000 cd/m<sup>2</sup> at about 3 V, with 9% quantum efficiency, and an intensity of about 10,000 cd/m<sup>2</sup> at about 4 V, with about 7% quantum efficiency. These are desirably high intensities at low voltages.

### **Inverted Devices**

[0070] A ninth device was fabricated having the following layer sequence:  
commercially available ITO on a substrate

3 nm Alq<sub>3</sub>

15 nm n-BPhen  
20 nm BPhen  
10 nm CBP:Ir(ppy)<sub>3</sub>  
10 nm Ir(ppz)<sub>3</sub>  
180 nm p-MTDATA  
20 nm p-CuPc  
ITO

[0071] A tenth device was fabricated having the same layer sequence as the ninth device, but omitting the 3 nm Alq<sub>3</sub>.

[0072] An eleventh device was fabricated having the same layer sequence as the tenth device, but omitting the top ITO layer. Because it does not have a top electrode, the eleventh device is not a functional device, but is useful for characterizing light transmission properties.

[0073] Fig. 8 is a graph depicting the EL intensity-voltage characteristics for device 9 and device 10, with light intensity measured through the bottom electrode. Plot 810 illustrates the EL intensity (cd/m<sup>2</sup>) of device 9 plotted against the voltage (V). Plot 820 illustrates the EL intensity (cd/m<sup>2</sup>) of device 10 plotted against the voltage (V). Typically, conventional inverted devices have an operating voltage of 20 volts. Remarkably, as this graph illustrates, the operating voltage for inverted devices 9 and 10, in accordance with embodiments of the present invention, ranges between 2.5 and 7 volts, which is substantially less than that of conventional inverted devices using intrinsic transport layers.

[0074] Fig. 8 shows that the drive voltage of device 10 is 2.85 V at 100 cd/m<sup>2</sup>, 3.4 V at 1000 cd/m<sup>2</sup>, and 5.6 V at 10000 cd/m<sup>2</sup>. One factor that contributes to drive voltage is the energy of photons emitted by the emissive layer (the "photon energy"), which is about 2.4 eV for the CBP:Ir(ppy)<sub>3</sub> emissive layer of devices 9 and 10. All of the other factors that contribute to drive voltage result in an addition to the photon energy. For device 10, this additional voltage is 0.45 V at 100 cd/m<sup>2</sup>, 1 V at 1000 cd/m<sup>2</sup>, and 3.2 V at 10000 cd/m<sup>2</sup>. These additions should be consistent for devices having different photon energies. The present invention may therefore be used to fabricate n-i-p devices having a drive voltage at 100 cd/m<sup>2</sup> that is not more than about 1.5 volts higher than the photon energy of the emissive layer. N-i-p devices having a drive voltage at 1,000 cd/m<sup>2</sup> that is not more than about 2 volts higher than the photon energy of the emissive layer may also be fabricated. N-i-p devices having a drive voltage at 10,000 cd/m<sup>2</sup> that is not more than about 4 volts higher than the photon energy of the emissive layer may also be fabricated.

[0075] It is possible to fabricate a phosphorescent OLED adapted to emit white light by adding one or more components to the emissive layer of devices 9 or 10. Generally, white emitting devices have a drive voltage that is similar to that for green emitting devices, but slightly higher. Based on the measurements reported in Fig. 8 for devices 9 and 10, such a device would have a drive voltage of not greater than about 4 V at 100 cd/m<sup>2</sup>, not greater than about 4.5 V at 1000 cd/m<sup>2</sup>, and not greater than about 6.5 V at 10000 cd/m<sup>2</sup>.

[0076] Fig. 9 is a graph shows the quantum efficiency and the power efficiency of device 10. Plot 910 illustrates the quantum efficiency (%) of device 10 plotted against the luminance (cd/m<sup>2</sup>). Plot 920 illustrates the power efficiency (%) of device 10 plotted against the luminance (cd/m<sup>2</sup>) for the same device.

[0077] Fig. 10 is a graph depicting the transmission-wavelength characteristics for device 10 and device 11. Plot 1010 illustrates the transmission (%) of device 11 plotted against the wavelength (nm). Plot 1020 illustrates the transmission (%) of device 10 plotted against the wavelength (nm). As is illustrated in Fig. 10, the inverted device 10 has sufficient transparency in the visible range for practical purposes.

**What Is Claimed Is:**

1. An organic light emitting device, comprising:
  - (a) an anode disposed over a substrate;
  - (b) a p-doped organic layer disposed over and electrically connected to the anode;
  - (c) a phosphorescent organic emissive layer disposed over and electrically connected to the p-doped organic layer;
  - (d) an n-doped organic layer disposed over and electrically connected to the phosphorescent organic emissive layer; and
  - (e) a cathode disposed over and electrically connected to the n-doped organic layer,
  - (f) a first blocking layer disposed between and electrically connected to the p-doped organic layer and the emissive layer, the first blocking layer adapted to block electrons and excitons from entering the p-doped organic layer;
  - (g) a second blocking layer disposed between and electrically connected to the n-doped organic layer and the emissive layer, the second blocking layer adapted to block holes and excitons from entering the n-doped layer.
2. The device of claim 1, wherein the first and second blocking layers are not doped.
3. The device of claim 1, wherein the emissive layer is an intrinsic semiconductor.
4. The device of claim 1, wherein :  
the first blocking layer comprises Ir(ppz)<sub>3</sub>;  
the emissive layer comprises CBP:Ir(ppy)<sub>3</sub> (13:1); and  
the second blocking layer comprises BPhen.
5. An organic light emitting device, comprising:
  - (a) an anode disposed over a substrate;
  - (b) a p-doped organic layer disposed over and electrically connected to the anode;
  - (c) a phosphorescent organic emissive layer disposed over and electrically connected to the p-doped organic layer;
  - (d) an n-doped organic layer disposed over and electrically connected to the phosphorescent organic emissive layer; and
  - (e) a cathode disposed over and electrically connected to the n-doped organic layer,

- (f) a blocking layer disposed between and electrically connected to the p-doped organic layer and the emissive layer, the blocking layer adapted to block electrons and excitons from entering the p-doped organic layer.
6. The device of claim 5, wherein the blocking layer is not doped.
7. An organic light emitting device, comprising:
- (a) an anode disposed over a substrate;
  - (b) a p-doped organic layer disposed over and electrically connected to the anode;
  - (c) a phosphorescent organic emissive layer disposed over and electrically connected to the p-doped organic layer;
  - (d) an n-doped organic layer disposed over and electrically connected to the phosphorescent organic emissive layer; and
  - (e) a cathode disposed over and electrically connected to the n-doped organic layer; and
  - (f) a blocking layer disposed between and electrically connected to the n-doped organic layer and the emissive layer, the blocking layer adapted to block holes and excitons from entering the n-doped layer.
8. The device of claim 7, wherein the blocking layer is not doped.
9. An organic light emitting device, comprising:
- (a) an organic phosphorescent emissive layer having first and second surfaces;
  - (b) a first blocking layer disposed adjacent to and electrically connected to the first surface of the emissive layer; the first blocking layer being adapted to inject electrons into the emissive layer and to block holes and excitons from entering the first blocking layer;
  - (c) a second blocking layer disposed adjacent to and electrically connected to the second surface of the emissive layer; the second blocking layer being adapted to inject holes into the emissive layer and to block electrons and excitons from entering the second blocking layer.
10. The device of claim 9, wherein the first and second blocking layers are not doped.

11. The device of claim 9, wherein :  
the first blocking layer comprises Ir(ppz)<sub>3</sub>;  
the emissive layer comprises CBP:Ir(ppy)<sub>3</sub> (13:1); and  
the second blocking layer comprises BPhen.
  
12. An organic light emitting device, comprising:
  - (a) a cathode disposed over a substrate;
  - (b) an n-doped organic layer disposed over and electrically connected to the cathode;
  - (c) an organic emissive layer disposed over and electrically connected to the first blocking layer;
  - (d) a p-doped organic layer disposed over and electrically connected to the second blocking layer; and
  - (e) an anode disposed over and electrically connected to the p-doped layer.
  
13. The device of claim 12, further comprising:  
a blocking layer disposed between and electrically connected to the n-doped layer and the emissive layer, the blocking layer adapted to block holes and excitons from entering the n-doped layer;
  
14. The device of claim 12, further comprising:  
a blocking layer disposed between and electrically connected to the p-doped layer and the emissive layer, the blocking layer adapted to block electrons and excitons from entering the p-doped layer.
  
15. The device of claim 12, further comprising:  
a first blocking layer disposed between and electrically connected to the n-doped layer and the emissive layer, the first blocking layer adapted to block holes and excitons from entering the n-doped layer; and  
a second blocking layer disposed between and electrically connected to the p-doped layer and the emissive layer, the second blocking layer adapted to block electrons and excitons from entering the p-doped layer.
  
16. The device of claim 12, wherein the organic emissive layer is a fluorescent emissive

layer.

17. The device of claim 12, wherein the organic emissive layer is a phosphorescent emissive layer.
18. The device of claim 17, further comprising:  
a blocking layer disposed between and electrically connected to the n-doped layer and the emissive layer, the blocking layer adapted to block holes and excitons from entering the n-doped layer;
19. The device of claim 17, further comprising:  
a blocking layer disposed between and electrically connected to the p-doped layer and the emissive layer, the blocking layer adapted to block electrons and excitons from entering the p-doped layer.
20. The device of claim 17, further comprising:  
a first blocking layer disposed between and electrically connected to the n-doped layer and the emissive layer, the first blocking layer adapted to block holes and excitons from entering the n-doped layer; and  
a second blocking layer disposed between and electrically connected to the p-doped layer and the emissive layer, the second blocking layer adapted to block electrons and excitons from entering the p-doped layer.
21. The device of claim 12, wherein the first and second blocking layers are not doped.
22. The device of claim 20, wherein :  
the first blocking layer comprises BPhen;  
the emissive layer comprises CBP:Ir(ppy)<sub>3</sub> (13:1); and  
the second blocking layer comprises Ir(ppz)<sub>3</sub>.
23. An organic light emitting device, comprising:
  - (a) an anode;
  - (b) a p-doped layer disposed over and electrically connected to the anode, wherein the p-

- doped layer comprises m-MTDATA:F4-TCNQ (50:1);
- (c) a first blocking layer disposed over and electrically connected to the p-doped layer, wherein the first blocking layer comprises Ir(ppz)<sub>3</sub>;
  - (d) a phosphorescent emissive layer disposed over and electrically connected to the first blocking layer, wherein the emissive layer comprises CBP:Ir(ppy)<sub>3</sub> (13:1);
  - (e) a second blocking layer disposed over and electrically connected to the emissive layer, wherein the second blocking layer comprises BPhen;
  - (f) an n-doped layer disposed over and electrically connected to the second blocking layer, wherein the n-doped layer comprises BPhen\*Li (1:1); and
  - (g) a cathode disposed over and electrically connected to the n-doped layer.
24. The device of claim 23, wherein:
- (a) a thickness of the first blocking layer is at most about 100 Angstroms;
  - (b) a thickness of the emissive layer is at most about 50 Angstroms; and
  - (c) a thickness of the second blocking layer is at most about 250 Angstroms.
25. An organic light emitting device, comprising:
- (a) a cathode;
  - (b) an n-doped layer disposed over and electrically connected to the cathode, wherein the n-doped layer comprises BPhen\*Li (1:1);
  - (c) a first blocking layer disposed over and electrically connected to the n-doped layer, wherein the first blocking layer comprises BPhen;
  - (d) an emissive layer disposed over and electrically connected to the first blocking layer, wherein the emissive layer comprises CBP:Ir(ppy)<sub>3</sub> (13:1);
  - (e) a second blocking layer disposed over and electrically connected to the emissive layer, wherein the second blocking layer comprises Ir(ppz)<sub>3</sub>;
  - (f) a p-doped layer disposed over and electrically connected to the second blocking layer, wherein the p-doped layer comprises m\_MTDATA:F4-TCNQ (50:1); and
  - (g) an anode disposed over and electrically connected to the p-doped layer.
26. The device of claim 25, wherein:
- (a) a thickness of the first blocking layer is at most about 200 Angstroms;
  - (b) a thickness of the emissive layer is at most about 100 Angstroms; and

- (c) a thickness of the second blocking layer is at most about 100 Angstroms.
27. An organic light emitting device made by the process of:
- (a) providing an anode on a substrate;
  - (b) depositing a layer of m-MTDATA:F4-TCNQ (50:1) over the anode;
  - (c) depositing a layer of Ir(ppz)<sub>3</sub> over the layer of m-MTDATA:F4-TCNQ (50:1);
  - (d) depositing a layer of CBP:Ir(ppy)<sub>3</sub> (13:1) over the layer of Ir(ppz)<sub>3</sub>;
  - (e) depositing a layer of BPhen over the layer of Ir(ppy)<sub>3</sub>;
  - (f) depositing a layer of BPhen\*Li (1:1) over the layer of BPhen; and
  - (g) depositing a cathode over the layer of BPhen\*Li (1:1).
28. The device of claim 27, wherein:
- (a) the layer of Ir(ppz)<sub>3</sub> is deposited to a thickness of at most about 100 Angstroms;
  - (b) the layer of CBP:Ir(ppy)<sub>3</sub> (13:1) is deposited to a thickness of at most about 50 Angstroms; and
  - (c) the layer of BPhen is deposited to a thickness of at most about 250 Angstroms.
29. An organic light emitting device made by the process of:
- (a) providing a cathode on a substrate;
  - (b) depositing a layer of BPhen\*Li (1:1) over the cathode;
  - (c) depositing a layer of BPhen over the layer of BPhen\*Li (1:1);
  - (d) depositing a layer of CBP:Ir(ppy)<sub>3</sub> (13:1) over the layer of BPhen;
  - (e) depositing a layer of Ir(ppz)<sub>3</sub> over the layer of CBP:Ir(ppy)<sub>3</sub> (13:1);
  - (f) depositing a layer of m-MTDATA:F4-TCNQ (50:1) over the layer of Ir(ppz)<sub>3</sub>; and
  - (g) depositing an anode over the layer of m-MTDATA:F4-TCNQ (50:1).
30. The device of claim 29, wherein:
- (a) the layer of BPhen is deposited to a thickness of at most about 200 Angstroms;
  - (b) the layer of CBP:Ir(ppy)<sub>3</sub> (13:1) is deposited to a thickness of at most about 100 Angstroms; and
  - (c) the layer of Ir(ppz)<sub>3</sub> is deposited to a thickness of at most about 100 Angstroms.
31. An organic light emitting device, comprising:

- (a) a cathode disposed over a substrate;
- (b) an n-doped organic layer disposed over and electrically connected to the cathode;
- (c) a phosphorescent organic emissive layer disposed over and electrically connected to the first blocking layer;
- (d) a p-doped organic layer disposed over and electrically connected to the second blocking layer; and
- (e) an anode disposed over and electrically connected to the p-doped layer.

32. The device of claim 31, wherein the drive voltage at 100 cd/m<sup>2</sup> is not more than about 1.5 volts higher than the photon energy of the emissive layer.

33. The device of claim 31, wherein the drive voltage at 1,000 cd/m<sup>2</sup> is not more than about 2 volts higher than the photon energy of the emissive layer.

34. The device of claim 31, wherein the drive voltage at 10,000 cd/m<sup>2</sup> is not more than about 4 volts higher than the photon energy of the emissive layer.

35. An organic light emitting device, comprising:

- (a) an anode disposed over a substrate;
- (b) a p-doped organic layer disposed over and electrically connected to the anode;
- (c) a phosphorescent organic emissive layer disposed over and electrically connected to the p-doped organic layer;
- (d) an n-doped organic layer disposed over and electrically connected to the phosphorescent organic emissive layer; and
- (e) a cathode disposed over and electrically connected to the n-doped organic layer.

36. The device of claim 35, wherein the phosphorescent emissive layer is adapted to emit light having a peak wavelength less than or equal to about 495 nm, and wherein the power efficiency of the device is greater than about 7 lumens per watt at an intensity of about 1000 cd/m<sup>2</sup>.

37. The device of claim 35, wherein the phosphorescent emissive layer is adapted to emit light having a peak wavelength greater than about 495 nm and less than or equal to about 580

nm, and wherein the power efficiency of the device is greater than about 20 lumens per watt at an intensity of about 1000 cd/m<sup>2</sup>.

38. The device of claim 35, wherein the phosphorescent emissive layer is adapted to emit light having a peak wavelength greater than about 580 nm, and wherein the power efficiency of the device is greater than about 7 lumens per watt at an intensity of about 1000 cd/m<sup>2</sup>.

39. A phosphorescent OLED adapted to emit substantially white light having an intensity of at least about 100 cd/m<sup>2</sup> at a drive voltage of not greater than about 4 volts.

40. A phosphorescent OLED adapted to emit substantially white light having an intensity of at least about 1,000 cd/m<sup>2</sup> at a drive voltage of not greater than about 4.5 volts.

41. A phosphorescent OLED adapted to emit substantially white light having an intensity of at least about 100 cd/m<sup>2</sup> at a drive voltage of not greater than about 6.5 volts.

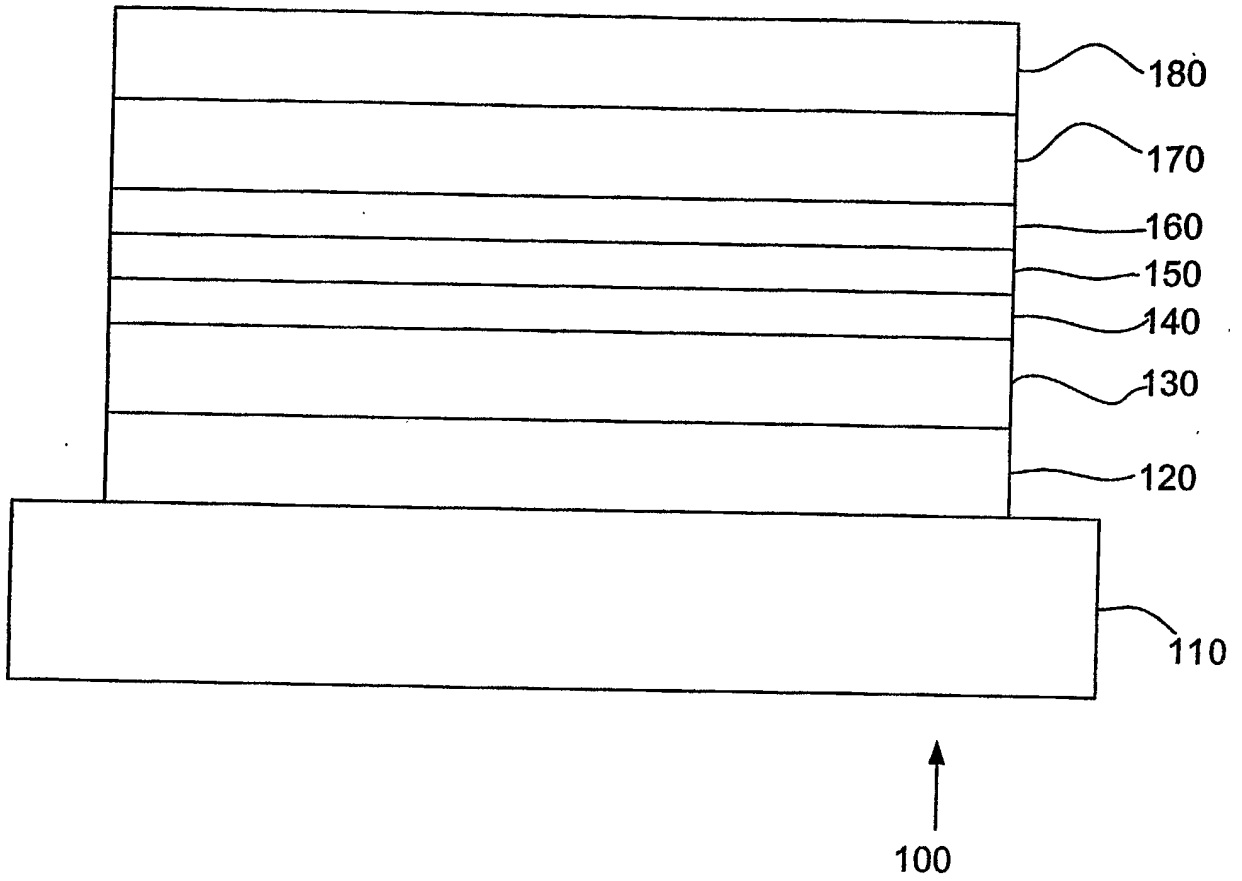


Figure 1

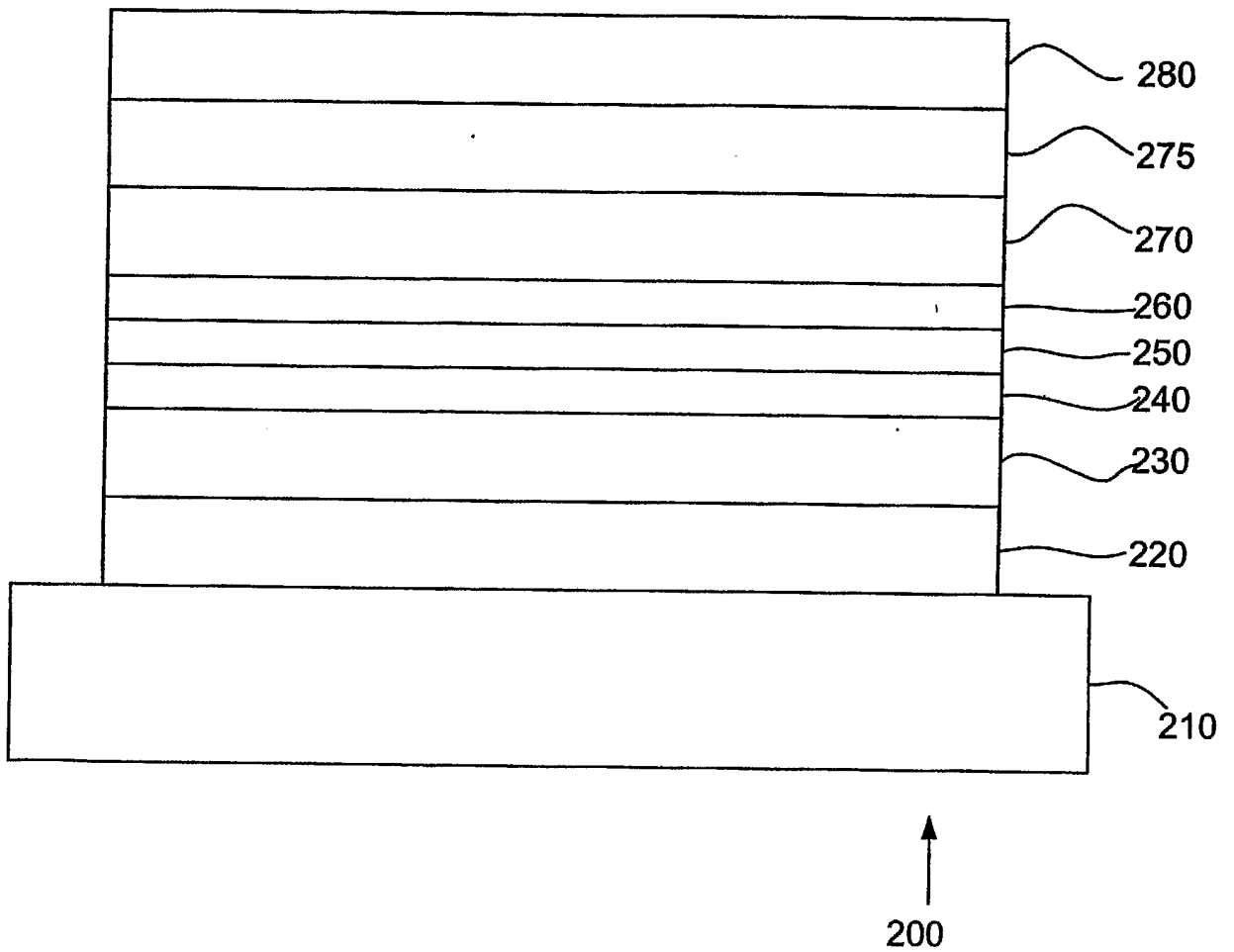


Figure 2

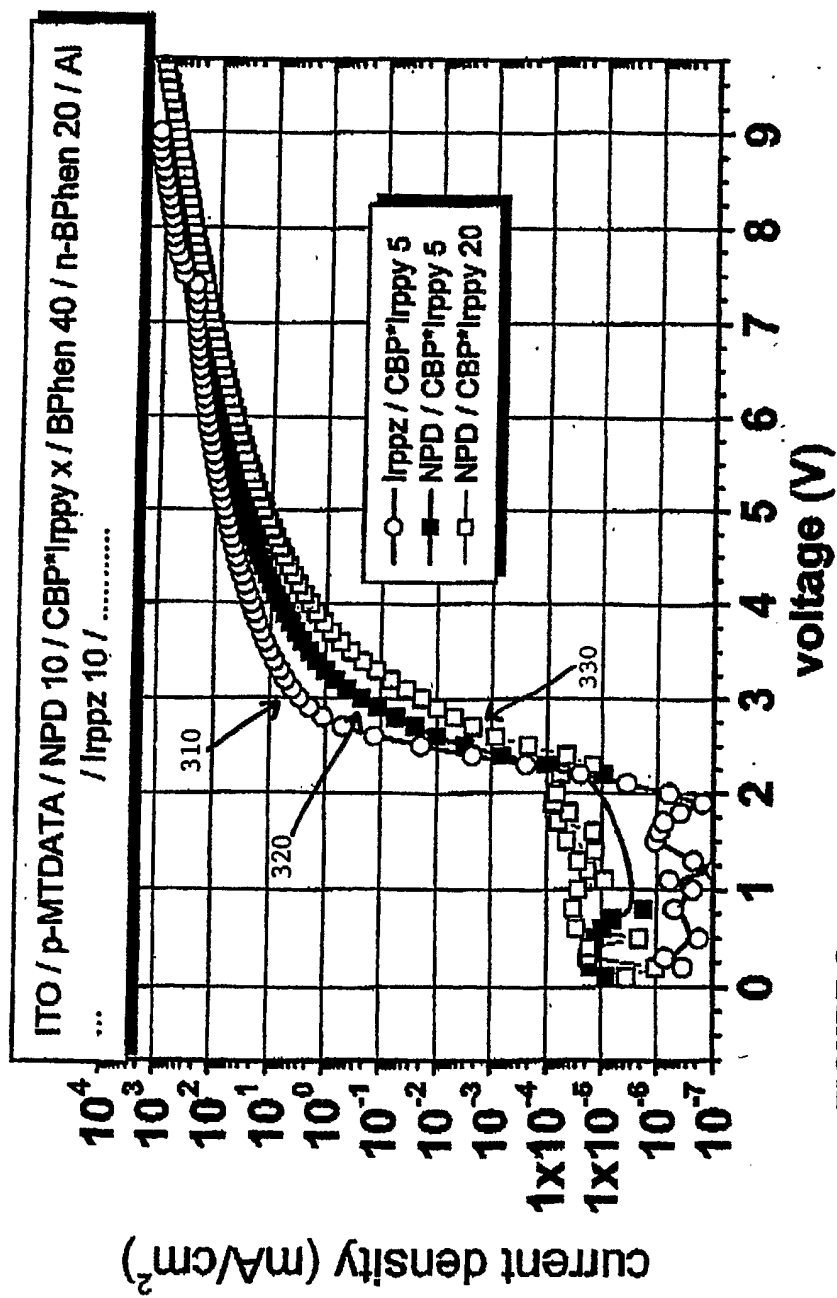


FIGURE 3

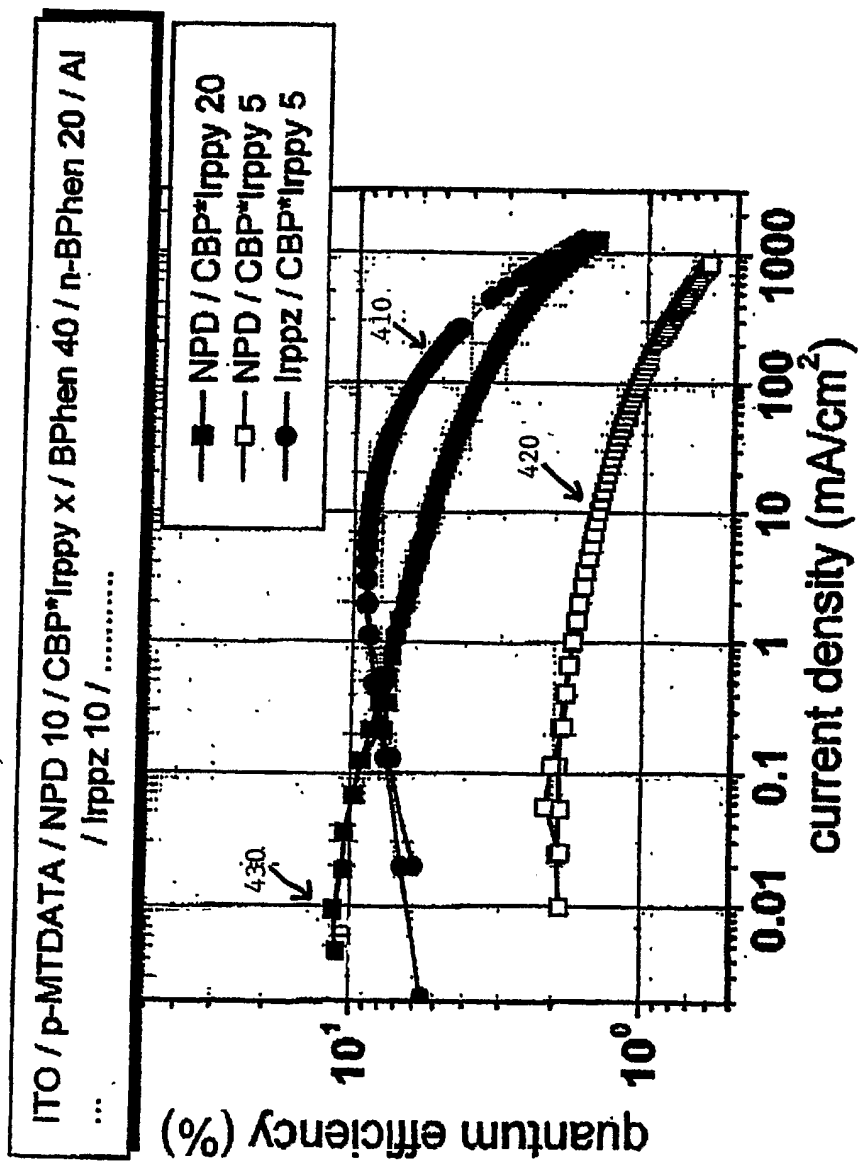


FIGURE 4

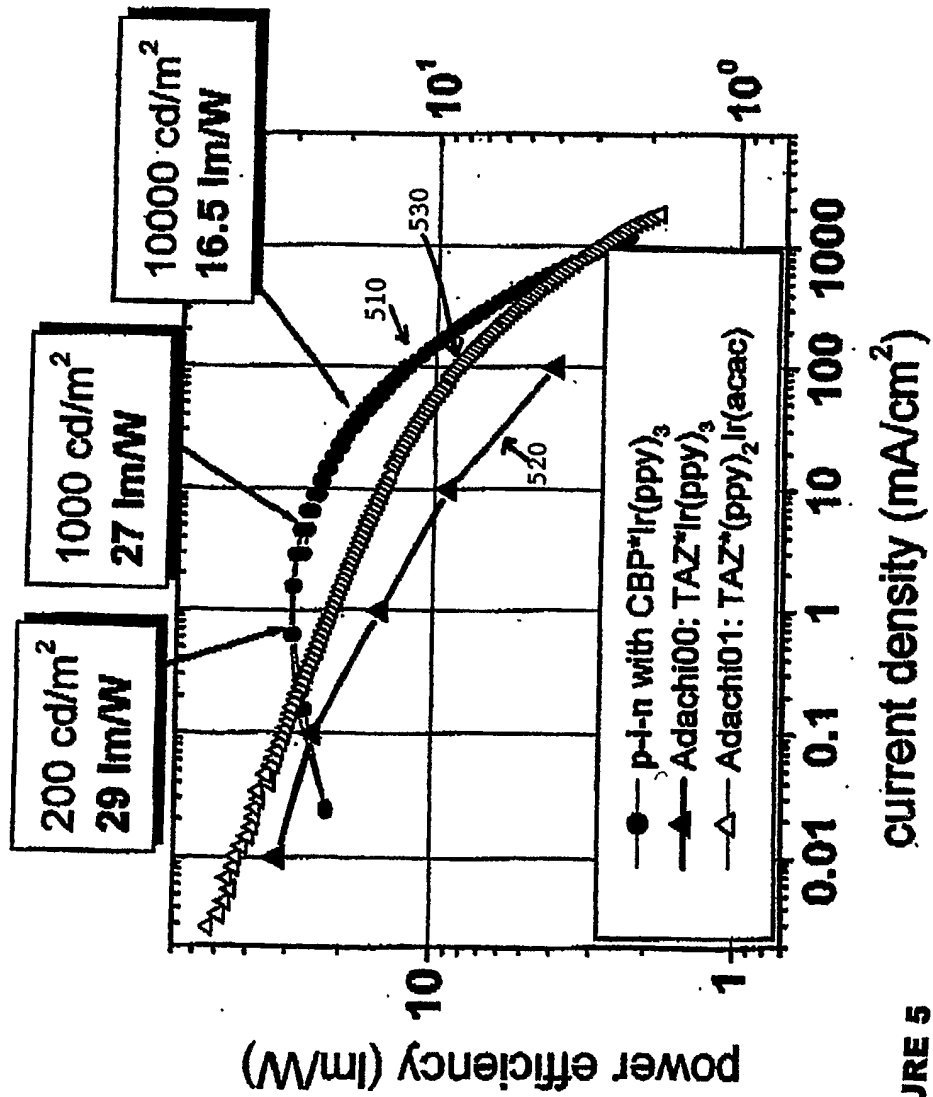


FIGURE 5

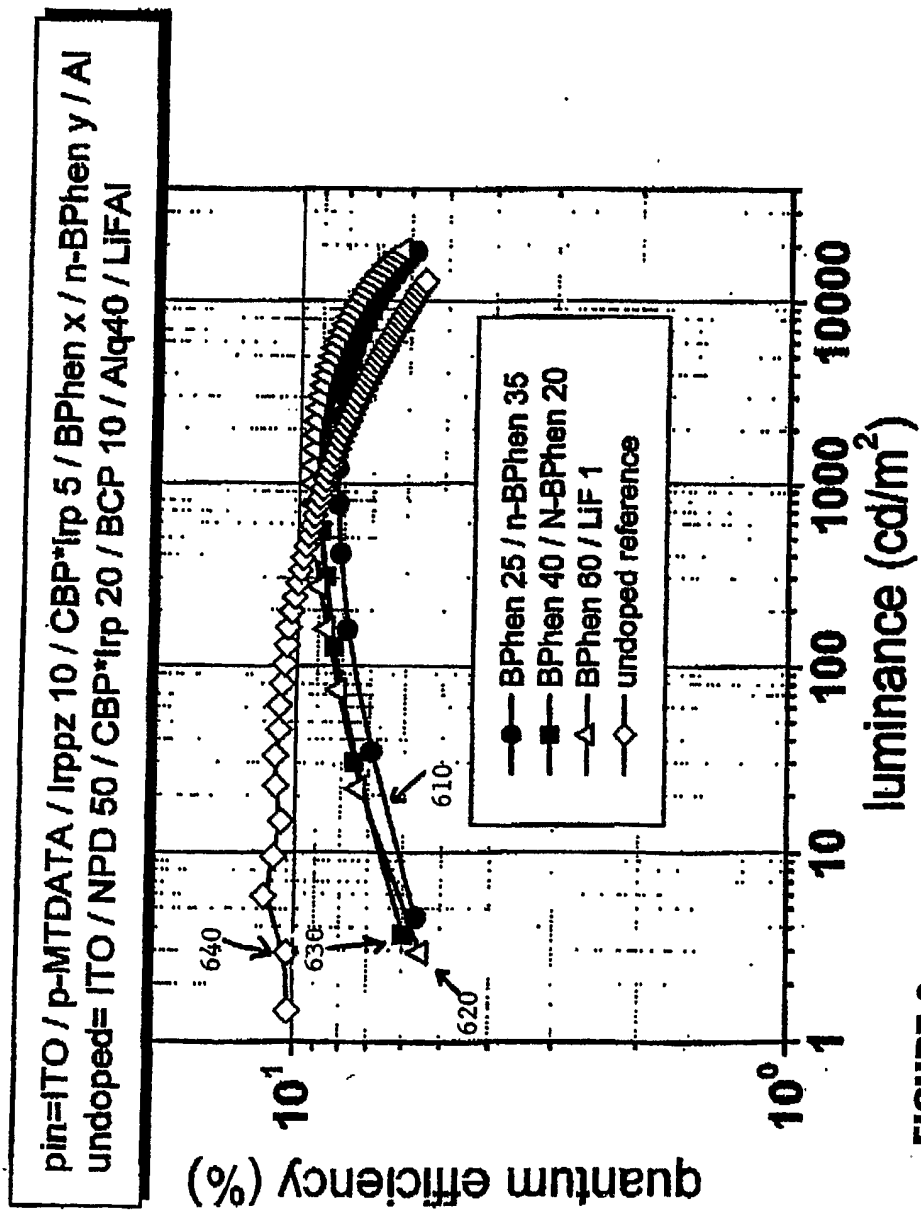


FIGURE 6

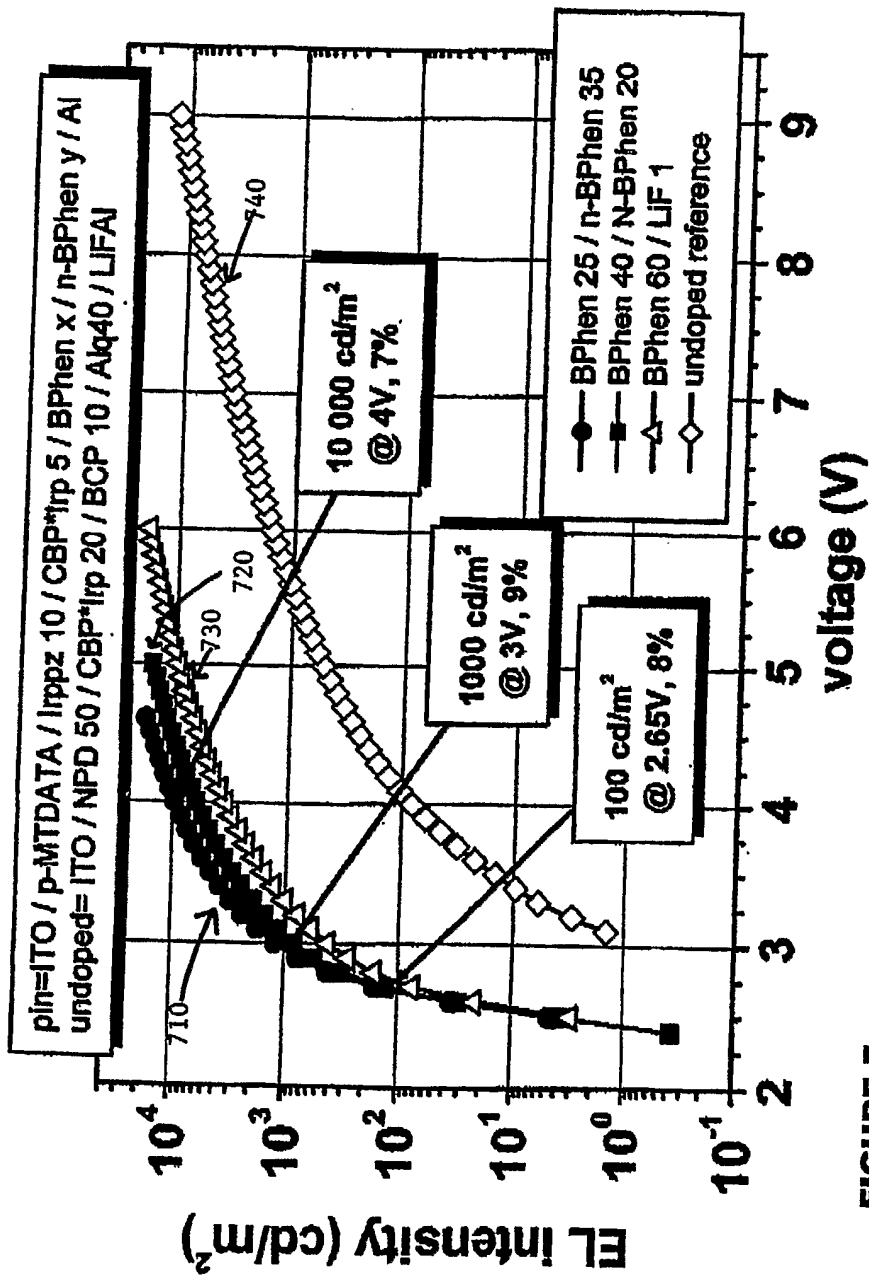


FIGURE 7

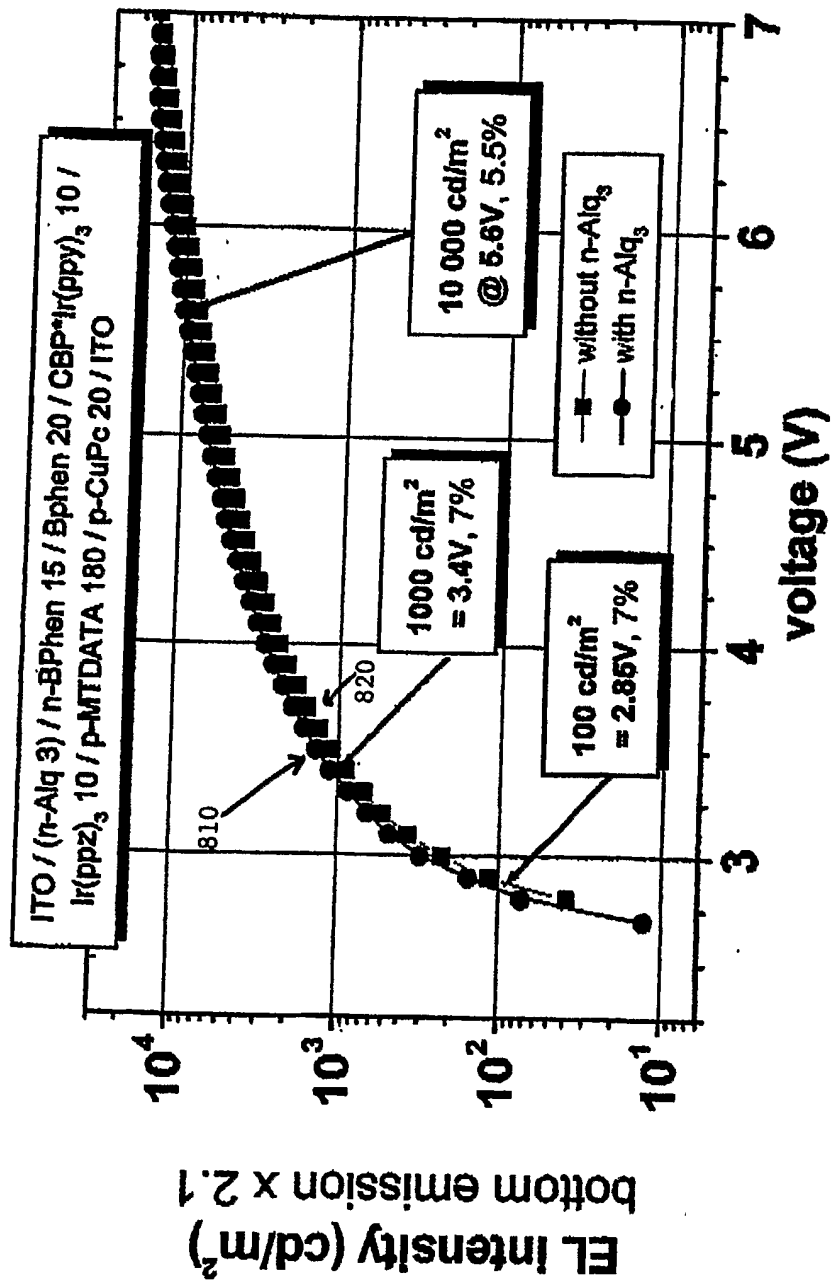


FIGURE 8

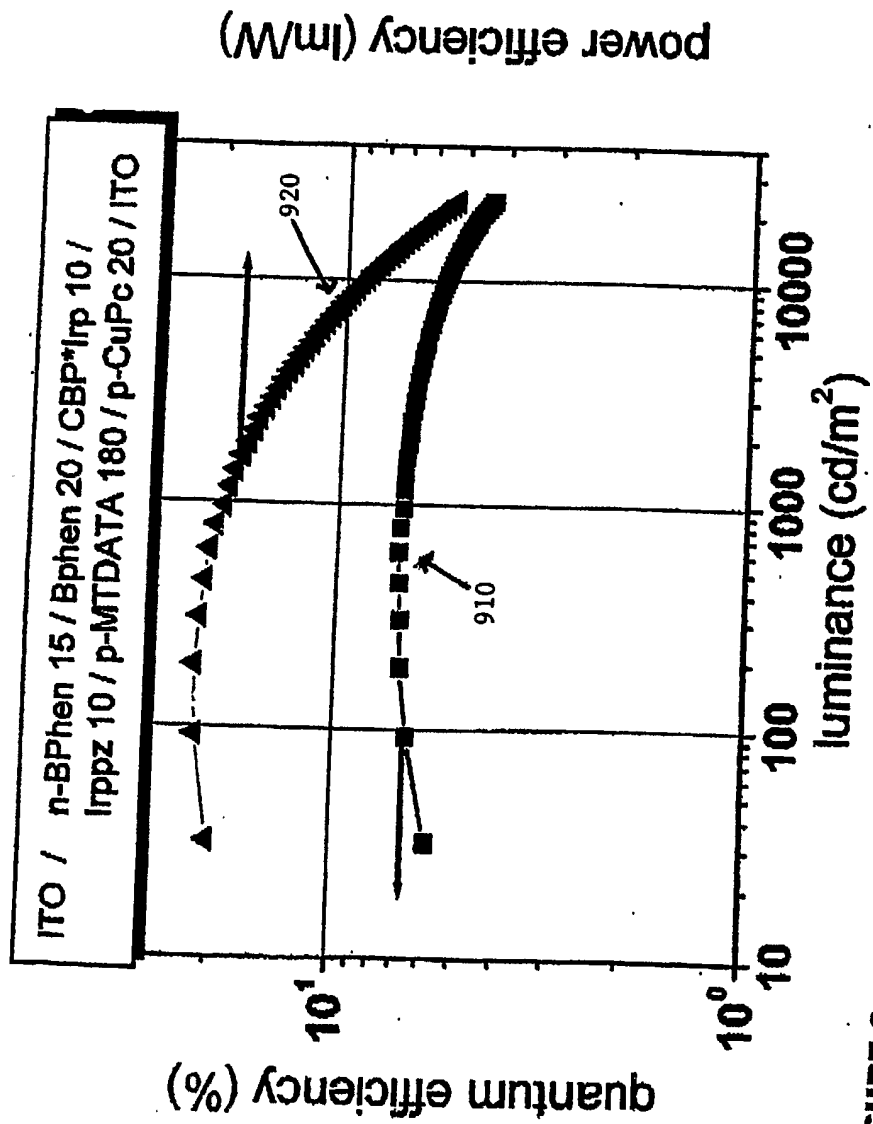


FIGURE 9

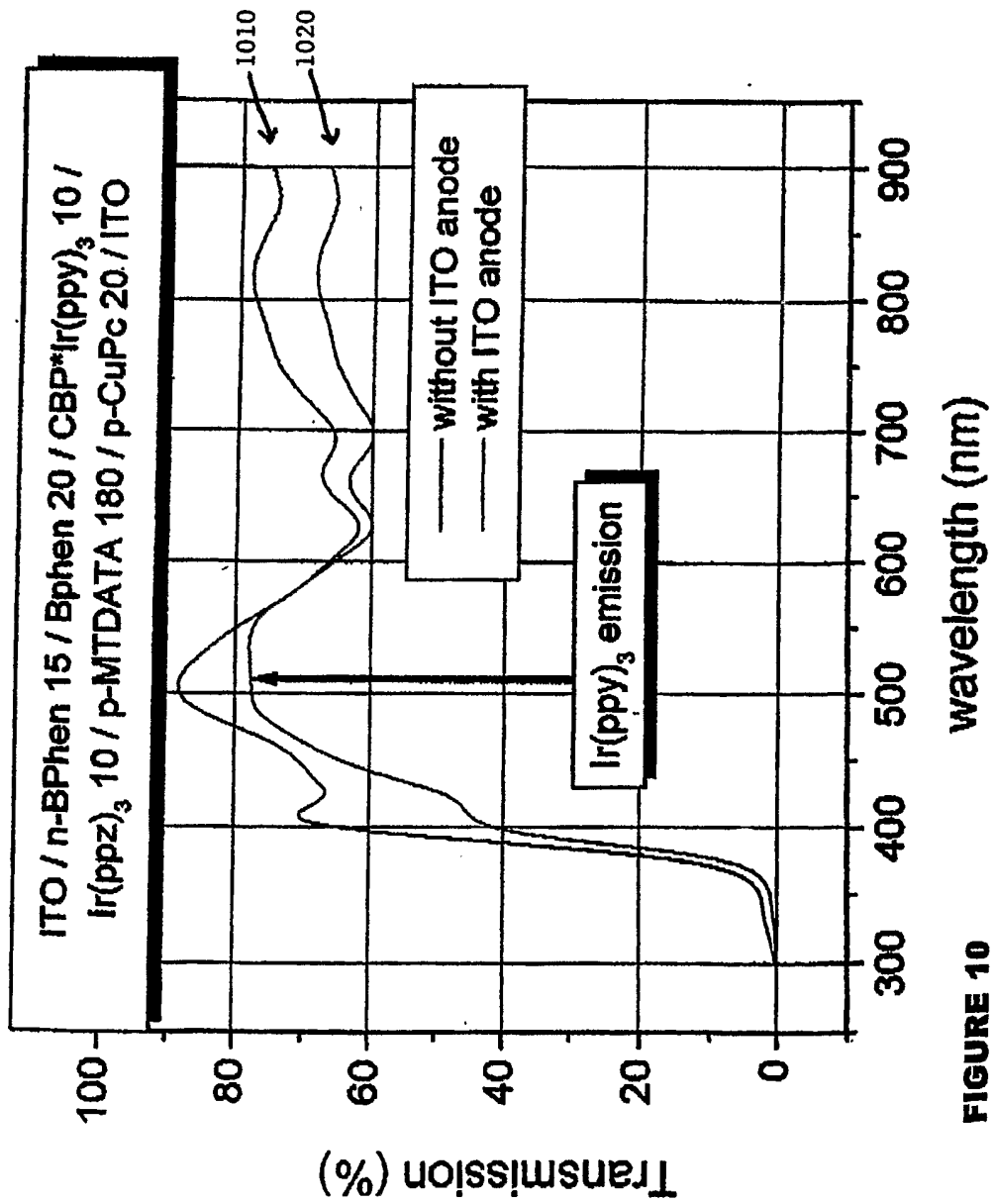


FIGURE 10

**INTERNATIONAL SEARCH REPORT**

International application No.

PCT/US03/19593

**A. CLASSIFICATION OF SUBJECT MATTER**

IPC(7) : H01L 51/20, 33/00

US CL : 257/40, 103

According to International Patent Classification (IPC) or to both national classification and IPC

**B. FIELDS SEARCHED**

Minimum documentation searched (classification system followed by classification symbols)

U.S. : 257/40, 103

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)  
East: organic, semiconductor, blocking, light

**C. DOCUMENTS CONSIDERED TO BE RELEVANT**

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	US 2002/0008233 A1 (Forrest et al) 24 January 2002 (24.01.2002), see entire disclosure.	1-41

Further documents are listed in the continuation of Box C.

See patent family annex.

* Special categories of cited documents:	
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"O" document referring to an oral disclosure, use, exhibition or other means	"&" document member of the same patent family
"P" document published prior to the international filing date but later than the priority date claimed	

Date of the actual completion of the international search

05 September 2003 (05.09.2003)

Date of mailing of the international search report

**30 SEP 2003**

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