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(71) Applicant (for all designated States except US): NXP B.V.
[NL/NL]; High Tech Campus 60, NL-5656 AG Eindhoven
(NL).

(72) Inventors; and

(75) **Inventors/Applicants (for US only): ZINKE, Manfred** [DE/DE] (DE). **FUHRMANN, Peter** [DE/DE] (DE). **BAUMEISTER, Markus** [DE/DE] (DE).

(74) **Agents:** VON LAUE, Hanns-Ulrich et al.; NXP Semiconductors Germany GmbH, Intellectual Property Department, Stresemannallee 101, 22529 Hamburg (DE).

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[Continued on next page]

(54) Title: CLOCK SUPERVISION UNIT

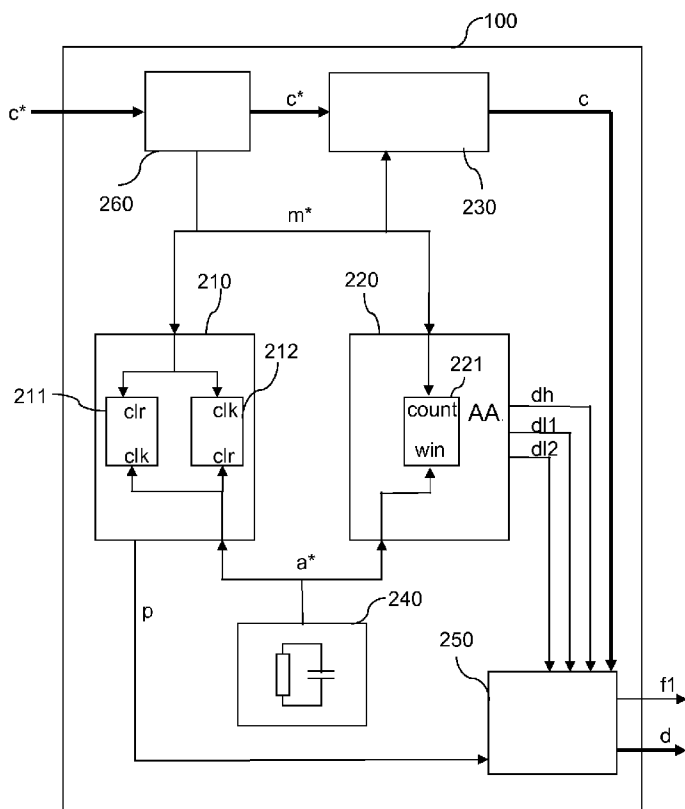


Fig.2

(57) Abstract: The present invention relates to a clock supervision unit (100) and an electronic system clocked by at least one clock (c*) and using the clock supervision unit (100). The clock supervision unit (100) analyzes the at least one clock (c*) based on a monitor clock (m*) provided together with the at least one clock (c*) or separately to the clock supervision unit (100). The clock supervision unit (100) at least comprises an activity unit (210), a deviation unit (220) and an auxiliary clock generator (240). The auxiliary clock generator (240) outputs an auxiliary clock (a*). The activity unit (210) detects the presence of the monitor clock (m*) based on the auxiliary clock (a*) and the presence of the auxiliary clock (a*) based on the monitor clock (m*). The deviation unit (220) detects clock faults in the monitor clock (m*) based on the auxiliary clock (a*). With the clock supervision unit (100) according to the present invention, the at least one clock (c*) can be supervised in more detail and it is possible to react on different clock faults in the at least one clock c* with different error handling procedures, e.g. by initiating a shutdown of the devices controlled by the electronic system.



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G06F

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal

C. DOCUMENTS CONSIDERED TO BE RELEVANT

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☒ Further documents are listed in the continuation of Box C.

☒ See patent family annex.

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P document published prior to the international filing date but later than the priority date claimed

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* & * document member of the same patent family

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European Patent Office, P.B. 5818 Patentlaan 2
NL - 2280 HV Rijswijk
Tel. (+31-70) 340-2040,
Fax: (+31-70) 340-3016

Authorized officer

Schneider, Michael

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C(Continuation). DOCUMENTS CONSIDERED TO BE RELEVANT

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