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[54] **TIME-BASE INSTABILITY COMPENSATION APPARATUS FOR USE IN COPY/DUPLICATION**

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[51] **Int. Cl.**[2] **H04B 1/66**

[52] **U.S. Cl.** ..... **179/15.55 T;** 358/8; 360/36

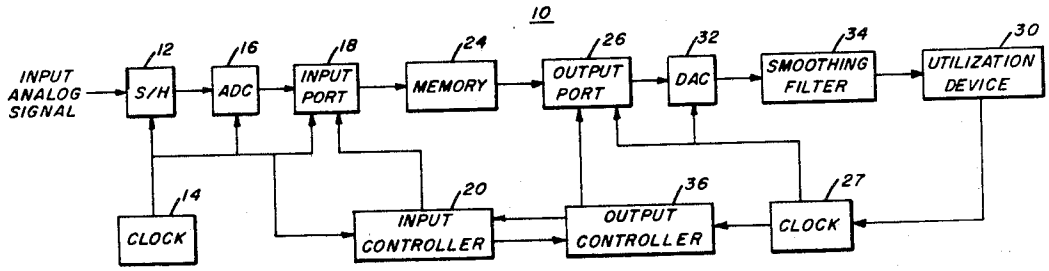
[57] **ABSTRACT**

Apparatus is disclosed which compensates for time-base instabilities in analog signals representative of a document image and includes an analog-to-digital converter for digitizing such analog signals, a digital storage device, such as a read/write memory, for storing such digitized signals, and a digital-to-analog converter which is adapted to be sequentially connected to selected locations of the memory to reconstruct the input analog signals for use by a printer. The read/write memory is of a wraparound variety. Further, input and output controllers are provided, having address pointers for the memory. The input pointer causes digitized analog signals to be stored in sequence at consecutive storage locations of the memory. The output pointer removes such stored digital signals from consecutive storage locations of memory and applies them to the digital-to-analog converter. These pointers are independently incremented under the control of input (document scanner) or output operations (printer), respectively, to remove surges in data flow thereby compensating for time-base instabilities.

3 Claims, 1 Sheet Drawing,

8 Pages Specification

The file of this unexamined application may be inspected and copies thereof may be purchased (849 O.G. 1221, Apr. 9, 1968).



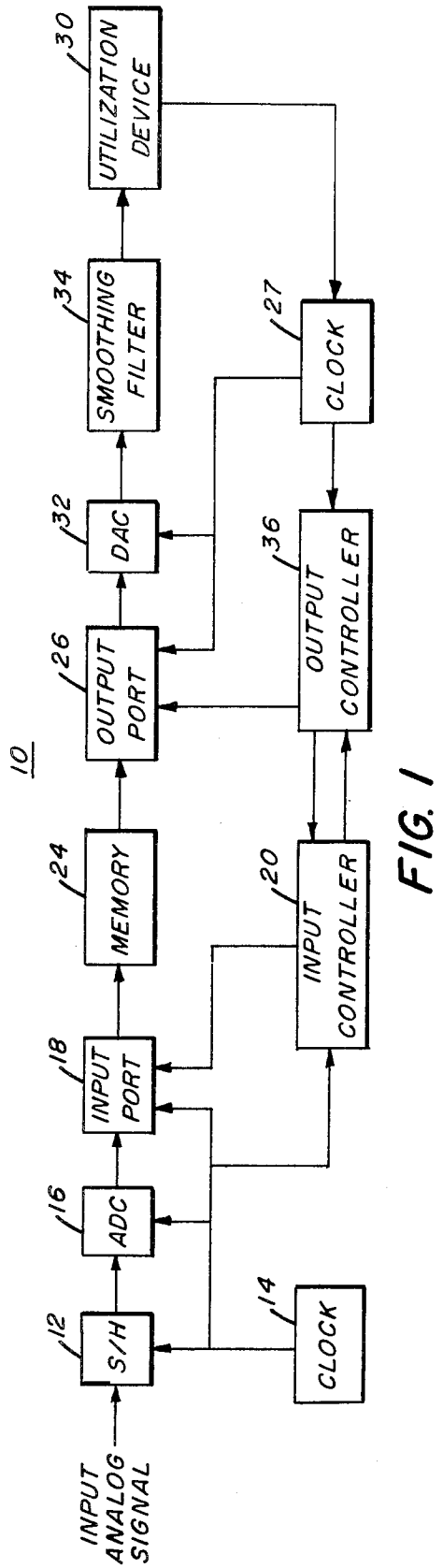

10
INPUT ANALOG SIGNAL
12
S/H
16
ADC
18
INPUT PORT
24
MEMORY
26
OUTPUT PORT
32
DAC
34
SMOOTHING FILTER
30
UTILIZATION DEVICE
14
CLOCK
20
INPUT CONTROLLER
36
OUTPUT CONTROLLER
27
CLOCK

FIG. 1

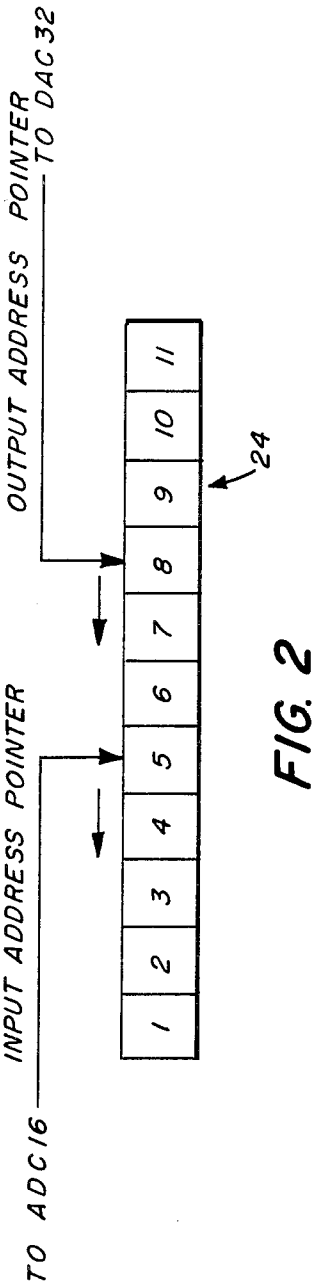

TO ADC 16
INPUT ADDRESS POINTER
OUTPUT ADDRESS POINTER
TO DAC 32
1
2
3
4
5
6
7
8
9
10
11
24

FIG. 2