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(71) Applicant (for all designated States except US): THE CURATORS OF THE UNIVERSITY OF MISSOURI [US/US]; 316 University Hall, Columbia, Missouri 65211 (US).

(72) Inventors; and

(75) Inventors/Applicants (for US only): ABOUKHOUSA, Mohamed Ahmed [—/US]; 1500 McCutchen Dr., Apt. 1, Rolla, Missouri 65401 (US). ZOUGHI, Reza [US/US]; 518 Eagles Nest Court, Wildwood, Missouri 63011 (US). KHARKIVSKIY, Sergiy [UA/US]; Nagogami Terrace, Apt. #123, Rolla, Missouri 65401 (US).

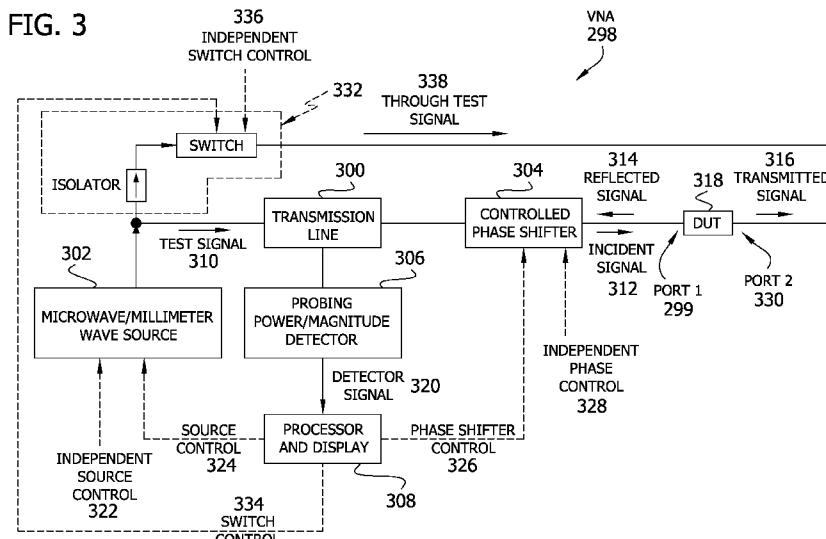
(74) Agents: BAIN, Robert M. et al.; Senniger Powers LLP, 100 North Broadway, 17th Floor, St. Louis, Missouri 63102 (US).

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(57) Abstract: Analyzing a device under test ("DUT") at higher frequencies. A phase shifter varies the phase of a standing wave on a transmission line coupled to the DUT. The standing wave magnitude is sampled at each of the phase shifts and one or more DUT characteristics are determined as a function of the sampled magnitudes and phase shifts. Further aspects include a related phase shifter comprising a waveguide having a plurality of sub-resonant slots formed therein and having active elements for loading the slots to control the phase shift applied to the signal.

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HIGH FREQUENCY ANALYSIS OF A DEVICE UNDER TEST

STATEMENT OF GOVERNMENT INTEREST

[0001] The U.S. Government has a paid-up license in this invention and the right in limited circumstances to require the patent owner to license others on reasonable terms as provided for by the terms of contract No. FA8103-07-C-0193 awarded by the U.S. Air Force.

BACKGROUND

[0002] Measurement of transmitted and reflected millimeter wave and microwave signals is useful for characterizing the properties of a device under test. Such properties, typically measured by a vector network analyzer, include the complex (i.e., magnitude and phase) reflection and transmission coefficients. Devices under test are, for example, high frequency circuit components, equipment that performs material characterization or nondestructive testing and evaluation, imaging systems, high-frequency transceivers, and ranging systems. Currently, taking measurements of transmitted and/or reflected signals at high frequencies involves expensive vector network analyzers, which employ phased-locked input sources and heterodyne detection processes to isolate or determine the frequencies to be measured. In addition to being prohibitively expensive, current vector network analyzers are considered bulky for applications where handheld, customized, and/or special purpose measurement modules are needed.

[0003] Accurate vector measurement of high frequency signals (i.e., their magnitude and phase) reflected back from and transmitted through a device under test ("DUT") helps characterize the electrical performance of the DUT. By way of analogy, FIG. 1

demonstrates these signals as a wave directed towards a slab 100, wherein the slab reflects and/or transmits an incident wave 102. FIG. 1 diagrammatically illustrates the incident wave 102 striking the slab, a reflected wave 104, and a transmitted wave 106. Based on measurements of the reflected and/or transmitted waves, the DUT can be characterized using known methods to determine its properties, such as its electrical impedance or the amount of signal distortion it causes to the supplied signal.

[0004] Among measurement devices known in the prior art for the measurement of the phase and magnitude of high frequency signals, FIGS. 2A-2D illustrate their operation. As shown in FIG. 2A, a vector network analyzer (VNA), generally indicated as 198, operably connects a stimulus source 214 (e.g., a built-in phased-locked oscillator) to a DUT 202. The source 214 supplies an incident signal 204 to the DUT 202. In a conventional test set-up, a plurality of directional coupling devices 210 separate the incident signal 204 from a reflected signal 206 and a transmitted signal 208. In turn, a receiver/detector 200 collects the signals 204, 206, 208 via the multiple directional coupling devices 210. In a typical vector network analyzer, such as VNA 198, the detector/receiver 200 comprises a tuned receiver, such as a super heterodyne receiver, allowing a high frequency input signal to be translated to a lower frequency through a process known in the art as down-conversion. The VNA 198 uses a reference signal (e.g., the incident signal 204 or a signal supplied from a second high-frequency source phase-locked to the primary stimulus source 214) in the down-converting process to mix with other signals. To down-mix the signals 204, 206 in this manner, VNA 198 must maintain signal separation and prevent the signals from interacting to form a combined or standing wave.

[0005] Other exemplary prior art techniques for measuring the phase and magnitude of high frequency signals are illustrated in FIGS. 2B-2D. As shown, these additional

methods do not utilize heterodyne/tuned receivers. FIG. 2B illustrates a slotted-line method of measuring a complex reflection coefficient of a standing wave. This method utilizes a single detector probe 220 to gather measurements on a transmission line 222. However, the probe 220 must be physically moved along the length of the transmission line 222 to obtain the multiple measurements needed to accurately measure the complex reflection coefficient. Since the distance 224 between the DUT (not shown) and the detector 220 is a factor in the complex coefficient calculation, each change in position must be accurately measured and recorded contemporaneously with the corresponding transmission line measurement. This repeated repositioning and distance measuring makes automation burdensome, adding to the cost and complexity of implementing the slotted line method shown in FIG. 2B.

[0006] Referring now to FIG. 2C, a sampled line method requires at least three fixed detector probes 240 along a length of transmission line 242 to obtain the desired measurements unambiguously. Although this prior art method avoids the repositioning of a single detector probe (see FIG. 2B), probe interaction and non-identical probes introduce measurement errors that significantly affect measurement accuracy.

[0007] Another method known in the art for measuring incident and reflected signals is the so-called perturbation two-port ("PTP") method illustrated in FIG. 2D. The PTP method is based on using a combination of two-port perturbation (PTP) networks 260 inserted before the DUT 262 and a scalar network analyzer 264 (i.e., high quality reflectometer realized with a directional coupler). Multiple PTP networks 266, each representing different electrical characteristics, are required. Each PTP network changes/transforms the sought DUT reflection coefficient appearing at the input of the scalar network analyzer 264. When these multiple PTP networks are used along with a multi-step calibration routine, the sought after reflection coefficient can be determined

(both magnitude and phase) after measuring the magnitude of the reflection coefficient seen at the input of the scalar network analyzer. Like the prior art VNA described above with respect to FIG. 2A, the PTP method demands signal separation. In the PTP method embodied in FIG. 2D, directional couplers (within the implementation of the scalar network analyzer) are again required to maintain separation between the incident and reflected signals. Furthermore, the PTP method requires that some of the used PTP networks have losses associated with them. Without signal separation and lossy PTP networks, the PTP method fails to work.

[0008] The conventional VNA 198 in FIG. 2A maintains signal separation of the incident, reflected, and transmitted signals, and employs one or more specialized tuned, heterodyne receivers to selectively isolate and analyze the signals collected, all of which increase the size, cost, and complexity of the device. Other prior art devices described above perform measurements without heterodyne procedures, but due to limitations inherent in their designs, measurement inaccuracies significantly undermine the useful operation of these devices. As noted above, it remained for the present inventors to discover a method and system of measuring the transmitted and reflected signals of a device under test without the cost and complexity of a tuned receiver, requiring directional coupling devices, or the introduction of significant inaccuracies in the measurement process.

SUMMARY

[0009] According to aspects of the invention, generating and manipulating the phase of a standing wave on a transmission line coupled to a device under test permits measuring the characteristics, e.g., phase and magnitude, of the transmitted and reflected wave signal associated with the device.

[0010] Briefly, a method for analyzing a device under test embodying aspects of the invention includes generating a standing wave on a transmission line coupled to the device under test, varying the phase of the standing wave with a phase shifter, and sampling the magnitude of the standing wave at each of a plurality of phase shifts. The method further includes determining at least one characteristic representative of the device under test as a function of the sampled magnitude of the standing wave and the plurality of phase shifts.

[0011] Another aspect of the invention is directed to a system for analyzing a device under test. The system includes a transmission line for coupling a signal source to the device under test. The signal source in turn provides a signal incident to the device under test. The signal incident to the device under test and a signal reflected from the device under test form a standing wave on the transmission line. The system also includes a phase shifter for varying the phase of the standing wave and a detector probe coupled to the transmission line for sampling a magnitude of the standing wave at each of a plurality of phase shift values. At least one characteristic representative of the device under test is determined as a function of the sampled magnitude of the standing wave on the transmission line and the plurality of phase shift values.

[0012] In another aspect, a vector network analyzer for determining phase and magnitude of signals at a frequency greater than very high frequency includes a phase shifter receiving and responsive to a phase control signal for applying a phase shift to a standing wave on a transmission line. The transmission line is coupled to a port of a device under test and a transmission line probe coupled to the transmission line samples a magnitude of the standing wave on the transmission line at each of a plurality of phase shift values. The probe is configured to generate a detector signal that corresponds to the sampled magnitude of the standing wave and a processor,

receiving and responsive to the detector signal from the transmission line probe, determines at least one characteristic representative of the devices as a function of the sampled magnitude of the standing wave on the transmission line. In addition, the processor is configured for generating the phase shifter control signal.

[0013] A phase shifter coupled with a transmission line for applying a phase shift to a signal on the transmission line embodies further aspects of the invention. The phase shifter includes a waveguide having a plurality of slots formed therein. The slots each have dimensions that are sub-resonant with respect to a wavelength of the signal, which is at a frequency greater than very high frequency, and have an active element corresponding thereto for loading the slots to control the phase shift applied to the signal.

[0014] In accordance with aspects of the present invention, proper use of a combination of a transmission line, a detector, and a phase shifter eliminates the need for expensive heterodyne/tuned receiver detection schemes.

[0015] Other features will be in part apparent and in part pointed out hereinafter.

BRIEF DESCRIPTION OF THE DRAWINGS

[0016] FIG. 1 illustrates the transmission and reflection of high-frequency signals.

[0017] FIGS. 2A-2D are diagrams of prior art network analyzers, with multiple port connections to measure the incident, reflected, and transmitted high frequency signals.

[0018] FIG. 3 illustrates a vector network analyzer according to an embodiment of the present invention.

[0019] FIG. 4 illustrates a controlled phase shifter according to an embodiment of the present invention.

[0020] FIG. 5 and FIG. 6 illustrate exemplary measurements collected in accordance with an embodiment of the present invention as compared to measurements collected by a prior art vector network analyzer.

[0021] Corresponding reference characters indicate corresponding parts throughout the drawings.

DETAILED DESCRIPTION

[0022] Referring now to FIG. 3, a vector network analyzer (VNA) 298 embodying aspects of the present invention is shown in block diagram form. Advantageously, the VNA 298 is capable of obtaining measurements of signals at frequencies greater than very high frequency, including microwave and millimeter frequency signals, for analyzing performance characteristics of a device under test (DUT) 318. The DUT 318 can be, for example, a radar antenna, etc.

[0023] In one embodiment, VNA 298 comprises a transmission line 300 coupled to an input port 299 of the DUT 318. In this embodiment, the transmission line 300 is configured to transmit signals along its length to and from DUT 318, as shown in FIG. 3. The transmission line 300 can be, but is not limited to, a waveguide, coaxial line, or a micro-strip line. When an appropriate source signal 310 is supplied to transmission line 300, a resulting standing wave is formed along the length of the transmission line 300 as a function of the superposition of an incident signal 312 and a reflected signal 314 from the input port 299 of the DUT 318 propagating in opposite directions.

[0024] Referring further to the embodiment of FIG. 3, the VNA 298 comprises a transmission line 300 coupled with a controlled phase shifter 304, an embodiment of which is described below. The exemplary embodiment demonstrated in FIG. 3 shows a wave source 302 capable of generating signals greater than very high frequency. The

source 302 is coupled to the transmission line 300 (see node 310 of FIG. 3). In the illustrated embodiment, this combination of elements allows for the generation of standing waves along a length of transmission line 300, due to the coherent addition of the signal applied to the input of the transmission line from the signal source 302 and the signal reflected from the port 299 of the DUT 318. According to aspects of the present invention, the controlled phase shifter 304 permits changes to the phase characteristics of the resulting standing wave.

[0025] An embodiment of the phase shifter 304 of the present invention is shown in FIG. 4. In this embodiment, phase shifter 304 utilizes a plurality of sub-resonant slots 402 formed in a printed circuit board ("PCB") 406 used as a wall of a rectangular waveguide 400. According to aspects of the invention, loading the slots 402 with active elements 404 allows phase shifts to be applied to the standing wave generated along the length of transmission line 300. The slots 402 are configured to have a length (L_s) 416 and width (W_s) 418, such that the slots 402 are sub-resonant with respect to the wavelength of the signals being measured. In other words, the slots do not radiate efficiently. Additionally, the slots are configured to have a same-row slots center-to-center separation (S) 410 and a slot-to-waveguide edge separation (s) 412 over the total waveguide length (L) 408 and width (a) (see element 414).

[0026] In FIG. 4, the slots 402 are cut out into a printed circuit board (PCB) 406 forming the broad wall of the rectangular waveguide 400. In an alternative embodiment, the slots 402 are on the narrow wall. The slots 402 are formed in a longitudinal or transverse direction in the wall of the waveguide 400. FIG. 4 demonstrates slots 402 configured in a transverse direction. It is contemplated that the slots 402 can be placed or configured in any such manner that provides sufficient interaction with the current following on the walls of the waveguide 400. For example, if

slots 402 are placed on or near the centerline 420 and rotated ninety degrees from the slot orientation demonstrated in FIG. 4, the slots 402 would be orthogonal to the current flow in the waveguide 400. The slots 402 can be formed in a variety of configurations to achieve sufficient phase shifts. However, slots 402 should be placed in a manner that minimally disturbs the standing wave shape and optimally affects the current flow on the wall of the waveguide 400. Additionally, slots 402 should have sub-resonant dimensions L_s 416 and W_s 418 such that the slots 402 do not cause signal reflection in and/or radiation from the waveguide 400. In the illustrated embodiment, the slots 402 are loaded with active elements such as PIN diodes 404, thereby allowing for electronic control of the applied phase shift.

[0027] In the example of FIG. 4, nine PIN diode loaded slots 402 are demonstrated, and turning each PIN diode 404 on and off using control direct current (dc) bias allows for 10 distinctive phase shifts to be applied to the signal on the transmission line 300. The phase shifter of FIG. 4 is provided by way of example and not limitation, and it should be understood that other slot configurations and active elements exist and are within the scope of the present invention.

[0028] The phase shifter can be calibrated using, for example, a high accuracy vector network analyzer to directly measure the scattering parameters of the phase shifter for use in the mathematical model described below. Otherwise, the phase shifter may be calibrated using a total of six known loads connected at the test port 299. Using the measurements of these six loads, the scattering parameters of the phase shifter can be determined by solving a set of six nonlinear equations.

[0029] Referring again to the embodiment illustrated in FIG. 3, at least one transmission line probe 306 is coupled to the transmission line 300 to sample the standing wave created on the transmission line 300 at a plurality of different phase

shifts. The probe 306 is further configured to generate a detector signal 320 that corresponds to the sampled magnitude of the standing wave signal. In one embodiment, the probe 306 is an active element, such as a diode. A diode will produce a dc signal that is proportional to the magnitude of the sampled standing wave. Advantageously, varying the phase shift applied to the standing wave permits a plurality of samples to be taken without altering the placement of the probe or requiring multiple probes, as required in the prior art devices. Advantageously, eliminating the need to move the probe relative to the standing wave and eliminating the need for multiple probes prevents the resulting errors from being introduced into the sampled measurements. As a result, the generated information representative of a device under test is more accurate while minimizing VNA cost and complexity. In an alternative embodiment, a plurality of probes 306 are coupled to the transmission line 300 to sample the standing wave created on the transmission line 300 at a plurality of different phase shifts, allowing for greater sampling accuracy over a wideband of frequencies.

[0030] FIG. 3 also illustrates a processor 308 configured to receive and respond to the detector signal 320. In response to the detector signal 320, processor 308 generates a source control signal 324, which it communicates to signal source 302 for controlling the incident signal injected into the transmission line 300 at 310. In addition, processor 308 also generates a phase shifter control signal 326 for controlling the amount of phase shift applied to the generated standing wave on the transmission line 300 by phase shifter 304. For instance, the phase shift control signal 326 turns on and off PIN diodes 404 for loading and unloading slots 402. The processor 308 optionally generates switch control signal 334 to turn the switch on and off. The processor 308 is also configured to generate information representative of the standing wave and the

DUT 318 as a function of the sampled magnitude of the standing wave on transmission line 300 and the applied phase shift at each sampling.

[0031] In another embodiment, the processor 308 outputs the representative information to a display device or other suitable output device such as, but not limited to, memory storage, hard drive storage, or a printing device. In other embodiments of the present invention, the signal source 302 and/or phase shifter 304 are configured to communicate information to the processor 308 as well.

[0032] Referring further to FIG. 3, the processor 308 provides control signal 326 to the phase shifter 304 for setting the phase shift, provides control signal 324 for setting the operating parameters of the signal source 302 (e.g., frequency and magnitude level), provides switch control signal 334 to turn the switch on and off, and processes the resultant detector signal 320 at each applied phase shift. In one embodiment, the processor 308, operating in response to stored computer instructions, automatically shifts the phase and/or sweeps through frequencies and determines the representative information in real-time. In other embodiments of the present invention, the processor operates in response to manual operator control such as, but not limited to, keypad or keyboard entry, dial settings, or switches.

[0033] In an alternative embodiment, VNA 298 also includes a switched directional isolating element 332 coupled to a second port 330 of the DUT 318. In this embodiment, the switched directional isolating element 332 comprises a switch and an isolator as shown in FIG. 3. When an appropriate source signal 310 is supplied to the transmission line 300, a resulting standing wave is formed along the length of the transmission line 300 as a function of the superposition of incident signal 312 and reflected signal 314 from the input port 299 as well as a through test signal 338 passing through the DUT 318 via port 330 to port 299. As before, incident signal 312 and

reflected signal 314 propagate in opposite directions. Likewise, incident signal 312 and through test signal 338 propagate in opposite directions. When the directional isolating element 332 is turned off in response to a switch control 334, a one-port (i.e., reflection) measurement can be conducted. On the other hand, when directional isolating element 332 is turned on, a two-port (transmission and reflection) measurement can be conducted. This combination of elements allows for the generation of standing waves along a length of transmission line 300, due to the coherent addition of the signal 310 applied to the input of the transmission line from the signal source 302 and the signal reflected 314 from the port 299 of the DUT 318, as well as the signal transmitted through the DUT 338 when the directional isolating element 332 is turned on. When the isolating element is turned on, the transmitted signal 316 is observed by the isolator 340. As with the signal source 302 and the phase shifter 304, directional isolating element 332 may also be configured to communicate information to the processor 308.

[0034] FIGS. 5 and 6 demonstrate exemplary results of an embodiment of the present invention as compared to the results of a prior art VNA (e.g., VNA 198).

[0035] The following mathematical models can be used to characterize the DUT 318 as a function of the applied phase shift and detector samples via probe 306 when the VNA 298 is configured to measure the reflected signal only (i.e. switch 332 is turned off):

$$V_d(\phi_m) = C | 1 + \Gamma e^{-2j\beta L} |^2, m = 1, 2, \dots, M$$

$$\Gamma = S_{11,p} + \frac{S_{21,p}S_{12,p}S_{11}}{1 - S_{22,p}S_{11}}$$

$S_{ij,p}$, $i=1, 2$, $j=1,2$: Phase shifter scattering parameters as a function of the phase shift Φ (as determined either through direct, independent VNA measurements, or solving six nonlinear equations using the measurement of six known loads).

$V_d(\Phi_m)$: voltage sampled at detector

C : detector response

β : propagation factor

L : distance between detector and phase shifter on transmission line

S_{11} : input port voltage reflection coefficient (of the DUT)

M : total number of phase shift settings.

The M system of nonlinear equations can be solved (e.g., using the Newton-Gauss method) to find S_{11} . S-parameters are used by the present invention, as it is typically difficult to measure total current or voltage at higher frequencies.

[0036] Several experiments were conducted on an automated prototype VNA system using embodiments of the current invention to confirm its operation. The developed prototype VNA performance was verified against the commercially available HP8510C vector network analyzer (VNA). Referring to the experimental results illustrated in FIGS. 5 and 6, the prototype VNA (using a voltage controlled phase shifter) and the HP8510C were both used to measure the reflection coefficient of various DUTs at 10 GHz. The utilized DUTs represented a wide range of devices of various reflection properties. The magnitude of the reflection coefficient of the utilized DUT ranged from -2dB to -30 dB and its phase ranged from 0 to 360 degrees. A DUT with these ranges advantageously spans the entire range of possible reflection measurements (as represented by the circle of unity radius in FIG. 5).

[0037] The prototype VNA was configured with an electronic phase shifter capable (of a type different from the embodiment illustrated in FIG. 4) of provided three phase

shifts settings ($M=3$) in response to dc control voltage. The phase shifter was independently calibrated. FIG. 5 compares the experimental vector reflection measurements (in polar form) obtained using the prototype VNA and the HP8510C VNA for the same DUT. As demonstrated in the exemplary results in FIG. 5, both devices produced comparable results. FIG. 6 demonstrates the experimentally-measured magnitude of the reflection coefficient for the variable DUT using both the prototype VNA and the commercially available HP8510C VNA. It is important to observe the linearity of the response of the prototype VNA, embodying certain aspects of the present invention, and how well these experimental results compare to the much more expensive HP8510C VNA.

[0038] As shown FIGS. 5 and 6, the prototype VNA exemplary experimental measurements closely match those of the HP8510C VNA. These exemplary experimental results confirm the operation of the developed VNA and approach for vector reflection coefficient measurement with accuracy comparable to the commercial, expensive, and complex HP8510C VNA. It also highlights the desired linearity of the detection scheme.

[0039] In another preferred embodiment, the controlled phase shifter 304 is a commercially available phase shifter capable of providing discrete phase shifts to signals on a transmission line 300. The commercially available phase shifter must be capable of providing accurate, discrete phase shifts for the wave region to be measured while weakly perturbing the signals.

[0040] In yet another preferred embodiment, the controlled phase shifter 304 is a phase shifter embodying aspects of FIG. 4, where such a phase shifter permits measurements at higher frequencies.

[0041] In another preferred embodiment, the signal 310 can be externally supplied, with the signal source configured to receive control signals from the processor for controlling the characteristics of the supplied signal. In another embodiment, the signal source is configured for independent source control.

[0042] In operation, a method embodying aspects of the invention includes generating a standing wave on a transmission line 300 coupled to the device under test 318, varying the phase of the standing wave with a phase shifter 304, and sampling the magnitude of the standing wave at each of a plurality of phase shifts. Further, the method determines at least one characteristic representative of the device under test 318 as a function of the sampled magnitude of the standing wave and the plurality of phase shifts. Such characteristics include, but are not limited to, phase and magnitude of the reflected signal 314 or the transmitted signal 316, dielectric properties of the DUT 318, calculated as a function of the phase and magnitude of the reflected signal 314, and structural properties of the DUT 318.

[0043] In operation, a system embodying aspects of the invention analyzes a device under test 318. The system includes a transmission line 300 for coupling a signal source 302 to the device under test 318, wherein the signal source 302 provides a signal 310 incident to the device under test and wherein the signal 312 incident to the device under test 318 and a signal 314 reflected from the device under test 318 form a standing wave on the transmission line 300. The system is further comprised of a phase shifter 304 for varying the phase of the standing wave and a detector probe 306 coupled to the transmission line 300 for sampling a magnitude of the standing wave at each of a plurality of phase shift values. Thus, at least one characteristic representative of device under test 318 is determined as a function of the sampled magnitude of the standing wave on the transmission line 300 and the plurality of phase shift values.

[0044] In operation, a vector network analyzer 298 embodying aspects of the invention determines the phase and magnitude of signals at a frequency greater than very high frequency for a device under test 318. The vector network analyzer 298 includes a phase shifter 304 receiving and responsive to a phase control signal 326 for applying a phase shift to a standing wave on a transmission line 300, said transmission line 300 being coupled to a port 299 of the DUT 318, and a transmission line probe 306 coupled to the transmission line 300 for sampling a magnitude of the standing wave on the transmission line 300 at each of a plurality of phase shift values, said probe 306 being configured to generate a detector signal 320 that corresponds to the sampled magnitude of the standing wave. In this instance, the vector network analyzer 298 further comprises a processor 308 receiving and responsive to the detector signal 320 from the transmission line probe 306 for determining at least one characteristic representative of the DUT 318 as a function of the sampled magnitude of the standing wave on the transmission line 300, said processor being further configured for generating the phase shifter control signal.

[0045] In another embodiment, the present invention is a phase shifter coupled with a transmission line for applying a phase shift to a signal on the transmission line, said signal being at a frequency greater than very high frequency, said phase shifter comprising a waveguide having a plurality of slots formed therein, said slots each having dimensions that are sub-resonant with respect to a wavelength of the signal on the transmission line, and said slots each having an active element corresponding thereto for loading the slots to control the phase shift applied to the signal.

[0046] While specific embodiments are discussed, it should be understood that this is done for illustrative purposes only. A person skilled in the relevant art will recognize

that other components and configurations can be used without parting from the spirit and scope of the invention.

[0047] The order of execution or performance of the operations in embodiments of the invention illustrated and described herein is not essential, unless otherwise specified. That is, the operations may be performed in any order, unless otherwise specified, and embodiments of the invention may include additional or fewer operations than those disclosed herein. For example, it is contemplated that executing or performing a particular operation before, contemporaneously with, or after another operation is within the scope of aspects of the invention. Embodiments of the invention may be implemented with computer-executable instructions. The computer-executable instructions may be organized into one or more computer-executable components or modules. Aspects of the invention may be implemented with any number and organization of such components or modules. For example, aspects of the invention are not limited to the specific computer-executable instructions or the specific components or modules illustrated in the figures and described herein. Other embodiments of the invention may include different computer-executable instructions or components having more or less functionality than illustrated and described herein.

[0048] When introducing elements of aspects of the invention or the embodiments thereof, the articles "a," "an," "the," and "said" are intended to mean that there are one or more of the elements. The terms "comprising," "including," and "having" are intended to be inclusive and mean that there may be additional elements other than the listed elements.

[0049] Having described aspects of the invention in detail, it will be apparent that modifications and variations are possible without departing from the scope of aspects of the invention as defined in the appended claims. As various changes could be made in

the above constructions, products, and methods without departing from the scope of aspects of the invention, it is intended that all matter contained in the above description and shown in the accompanying drawings shall be interpreted as illustrative and not in a limiting sense.

What is claimed is:

1. A method of analyzing a device under test comprising:
 - generating a standing wave on a transmission line coupled to the device under test;
 - varying the phase of the standing wave with a phase shifter;
 - 5 sampling the magnitude of the standing wave at each of a plurality of phase shifts; and
 - determining at least one characteristic representative of the device under test as a function of the sampled magnitude of the standing wave and the plurality of phase shifts.
2. The method of claim 1, wherein a greater than very high frequency signal source supplies the signal.
3. The method of claim 1, wherein the standing wave comprises a signal incident to the device under test and a signal reflected from the device under test.
4. The method of claim 1, wherein the standing wave further comprises a signal transmitted through the device under test.
5. The method of claim 1, wherein sampling the magnitude of the standing wave comprises measuring a voltage of the standing wave at each of the plurality of phase shifts and wherein determining at least one characteristic representative of the device under test comprises measuring a reflection coefficient of the device under test as a function of the measured voltage at each of the plurality of phase shifts.
6. The method of claim 1, wherein the phase shifter is coupled to the transmission line and comprises one or more sub-resonant slots, said slots each having an active element positioned therein, and wherein varying the phase comprises controlling the active element to load the slot in which the active element is positioned for altering the 5 phase of signals on the transmission line.
7. The method of claim 6, wherein the slots have sub-resonant dimensions formed in the wall of a waveguide.
8. A system for analyzing a device under test, the system comprising:

a transmission line for coupling a signal source to the device under test, wherein the signal source provides a signal incident to the device under test and wherein the signal incident to the device under test and a signal reflected from the device under test

5 form a standing wave on the transmission line;

a phase shifter for varying the phase of the standing wave; and

a detector probe coupled to the transmission line for sampling a magnitude of the standing wave at each of a plurality of phase shift values, wherein at least one characteristic representative of the device under test is determined as a function of the

10 sampled magnitude of the standing wave on the transmission line and the plurality of phase shift values.

9. A system of claim 8, wherein the signal source generates the signal incident to the device under test at a frequency greater than very high frequency.

10. A system of claim 8, wherein the standing wave comprises the signal incident to the device under test, the signal reflected from the device under test, and a signal transmitted through the device under test.

11. A system of claim 8, further comprising a processor configured for determining the at least one characteristic representative of the device under test.

12. A system of claim 11, wherein the detector probe measures a voltage of the standing wave at each of the plurality of phase shift values and wherein the at least one characteristic determined by the processor comprises a reflection coefficient of the device under test as a function of the measured voltage at each of a plurality of phase

5 shift values.

13. A system of claim 8, wherein the phase shifter is coupled to the transmission line and comprises one or more sub-resonant slots, said slots each having an active element positioned therein, and wherein the phase shifter controls the active element to load the slot in which the active element is positioned for varying the phase of the

5 standing wave.

14. A system of claim 13, wherein the phase shifter comprises a waveguide having the slots formed therein.

15. The system of claim 8, wherein the signal source provides the signal incident to the device under test at a frequency greater than very high frequency.

16. The system of claim 8, further comprising another detector probe coupled to the transmission line for sampling a magnitude of the standing wave at each of a plurality of phase shifts.

17. A vector network analyzer for determining phase and magnitude of signals at a frequency greater than very high frequency for a device under test (DUT), said vector network analyzer comprising:

5 a phase shifter receiving and responsive to a phase control signal for applying a phase shift to a standing wave on a transmission line, said transmission line being coupled to a port of the DUT;

10 a transmission line probe coupled to the transmission line for sampling a magnitude of the standing wave on the transmission line at each of a plurality of phase shift values, said probe being configured to generate a detector signal that corresponds to the sampled magnitude of the standing wave; and

15 a processor receiving and responsive to the detector signal from the transmission line probe for determining at least one characteristic representative of the DUT as a function of the sampled magnitude of the standing wave on the transmission line, said processor being further configured for generating the phase shifter control signal.

18. A vector network analyzer of claim 17, wherein the transmission line comprises one or more of the following: a waveguide, a coaxial line, and a micro-strip line.

19. A vector network analyzer of claim 17, wherein the transmission line probe comprises at least one active element for producing a signal proportional to the magnitude of the standing wave.

20. A vector network analyzer of claim 19, wherein the active element of the transmission line probe comprises a diode.

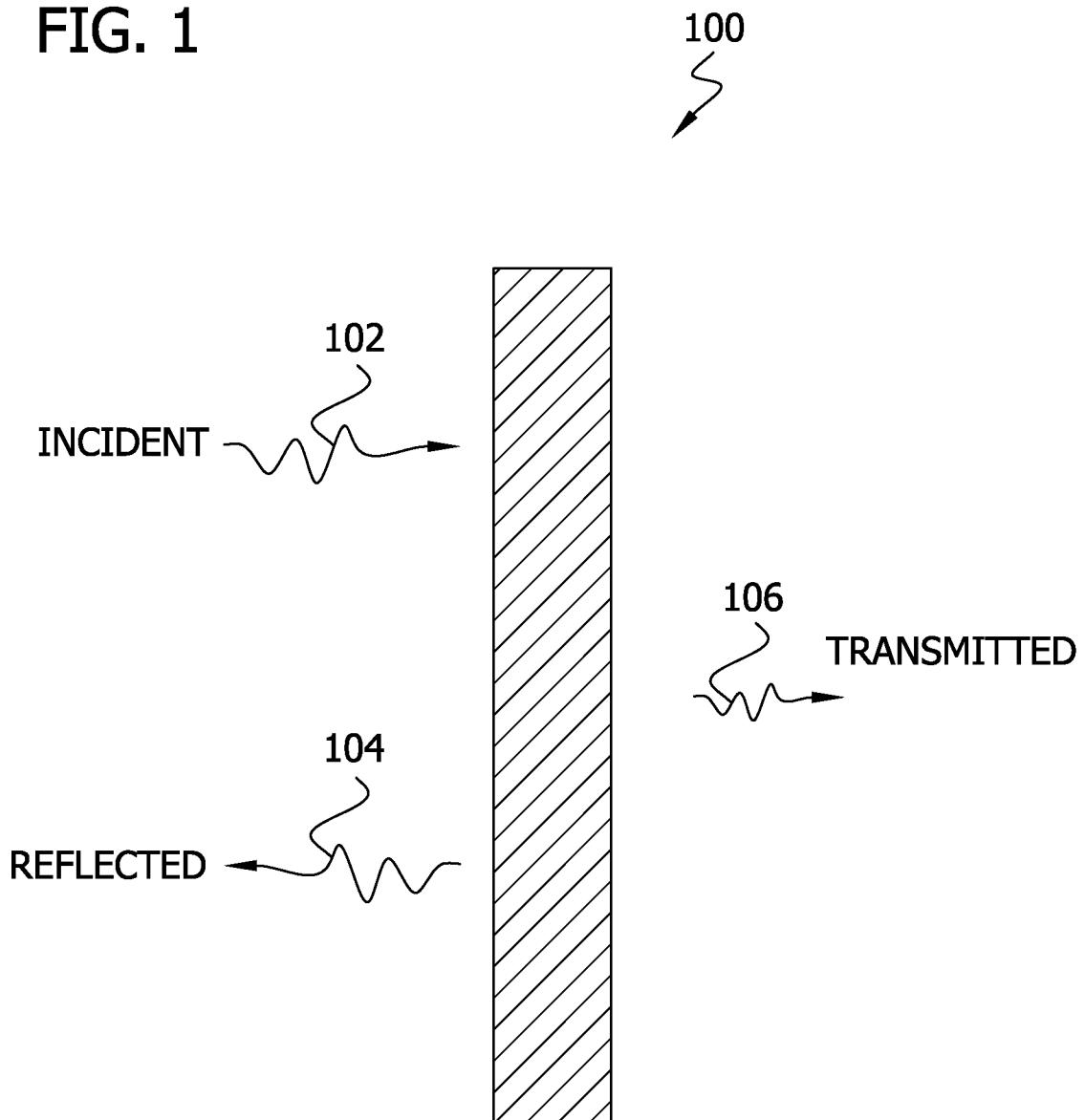
21. A vector network analyzer of claim 17, further comprising a signal source for providing a signal incident to the DUT, wherein the signal incident to the DUT and a signal reflected from the DUT form the standing wave on the transmission line, and

wherein said signal source is responsive to a source control signal for altering its
5 output.

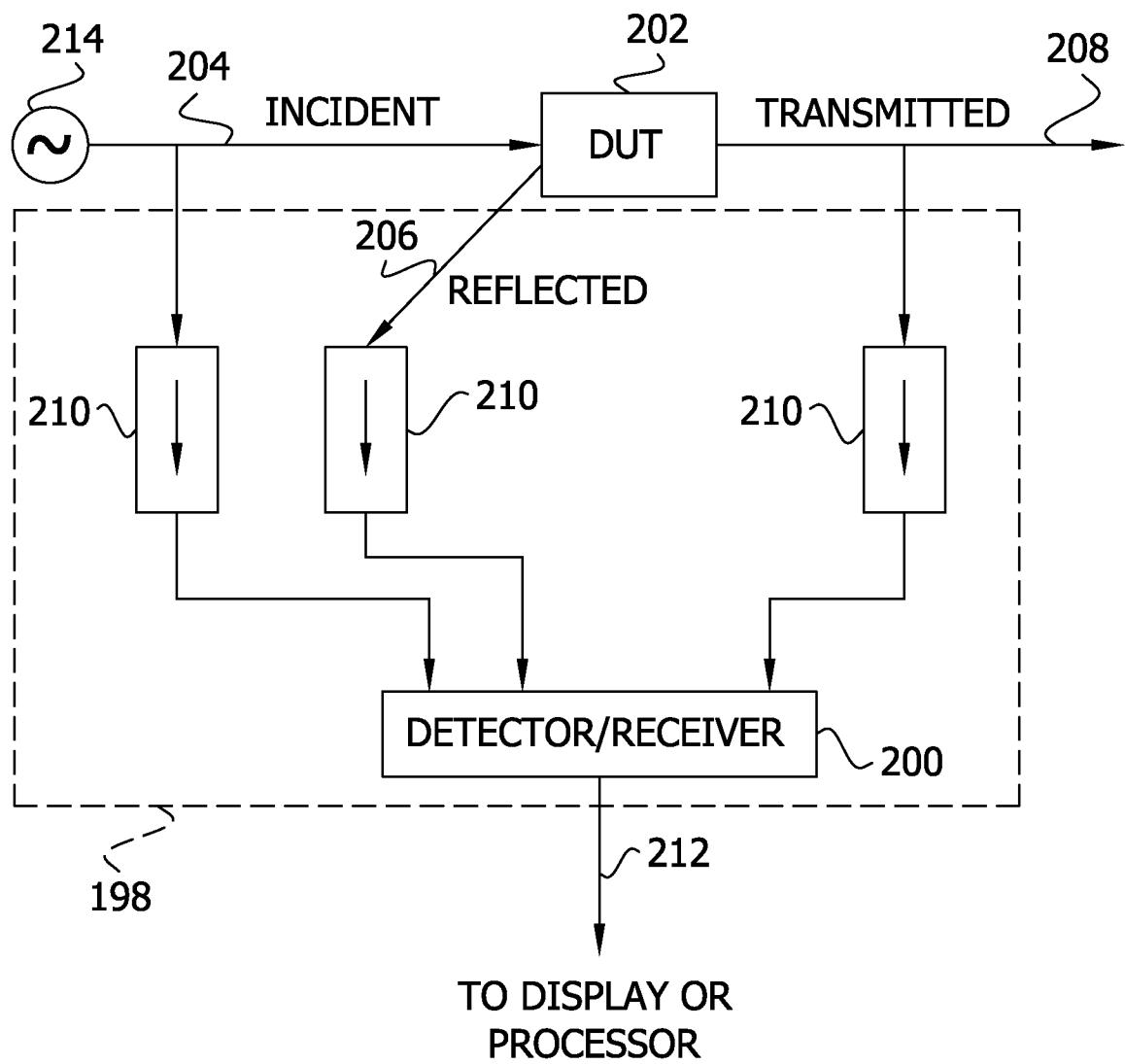
22. A vector network analyzer of claim 21, wherein the standing wave on the transmission line comprises the signal incident to the DUT, the signal reflected from the DUT, and a signal transmitted through the DUT opposite in direction to the signal incident to the DUT.
23. A vector network analyzer of claim 22, further comprising a directionally isolating element for enabling the signal transmitted through the DUT opposite in direction to the signal incident to the DUT to pass through the DUT.
24. A vector network analyzer of claim 23, wherein the directionally isolating element comprises a switch and a signal isolator.
25. A phase shifter coupled with a transmission line for applying a phase shift to a signal on the transmission line, said signal being at a frequency greater than very high frequency, said phase shifter comprising a waveguide having a plurality of slots formed therein, said slots each having dimensions that are sub-resonant with respect to a wavelength of the signal on the transmission line, and said slots each having an active element corresponding thereto for loading the slots to control the phase shift applied to the signal.
5

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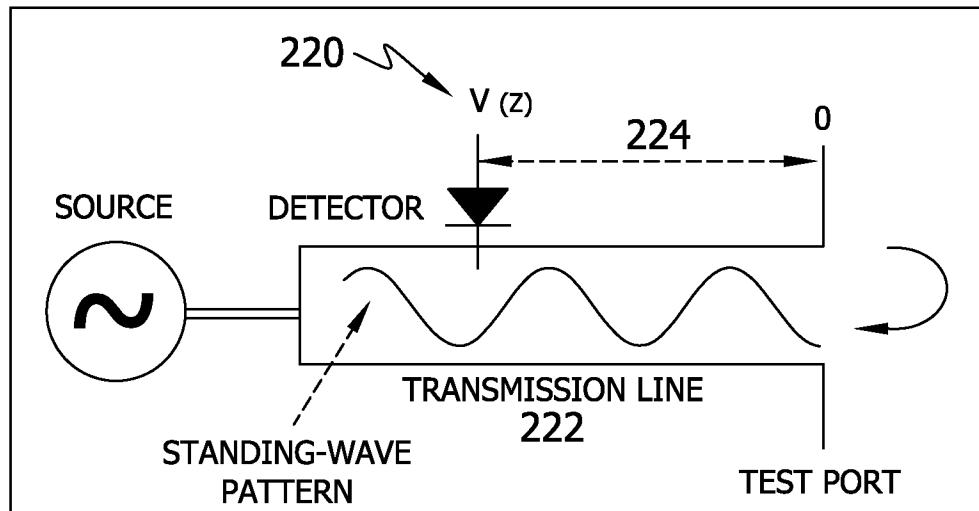
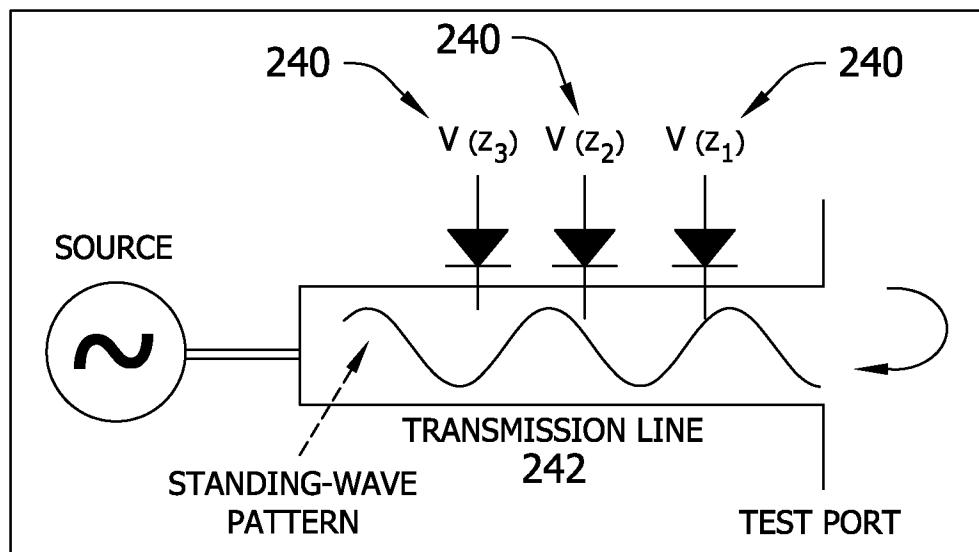
FIG. 1



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FIG. 2A
PRIOR ART

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FIG. 2B
PRIOR ART**SLOTTED LINE****FIG. 2C**
PRIOR ART**SAMPLED LINE (MULTIPORT TECHNOLOGY)**

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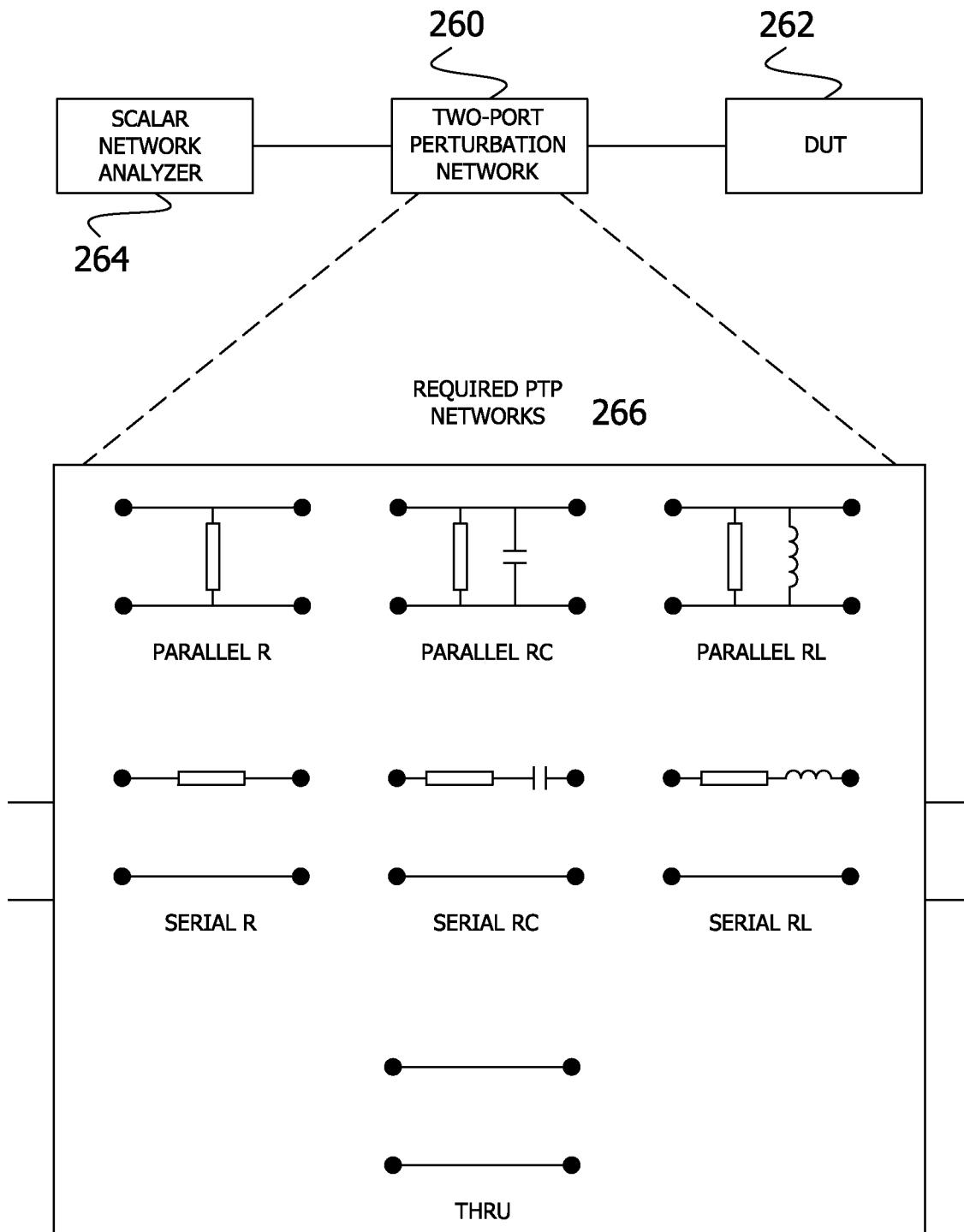
FIG. 2D
PRIOR ART

FIG. 3

336 INDEPENDENT SWITCH CONTROL

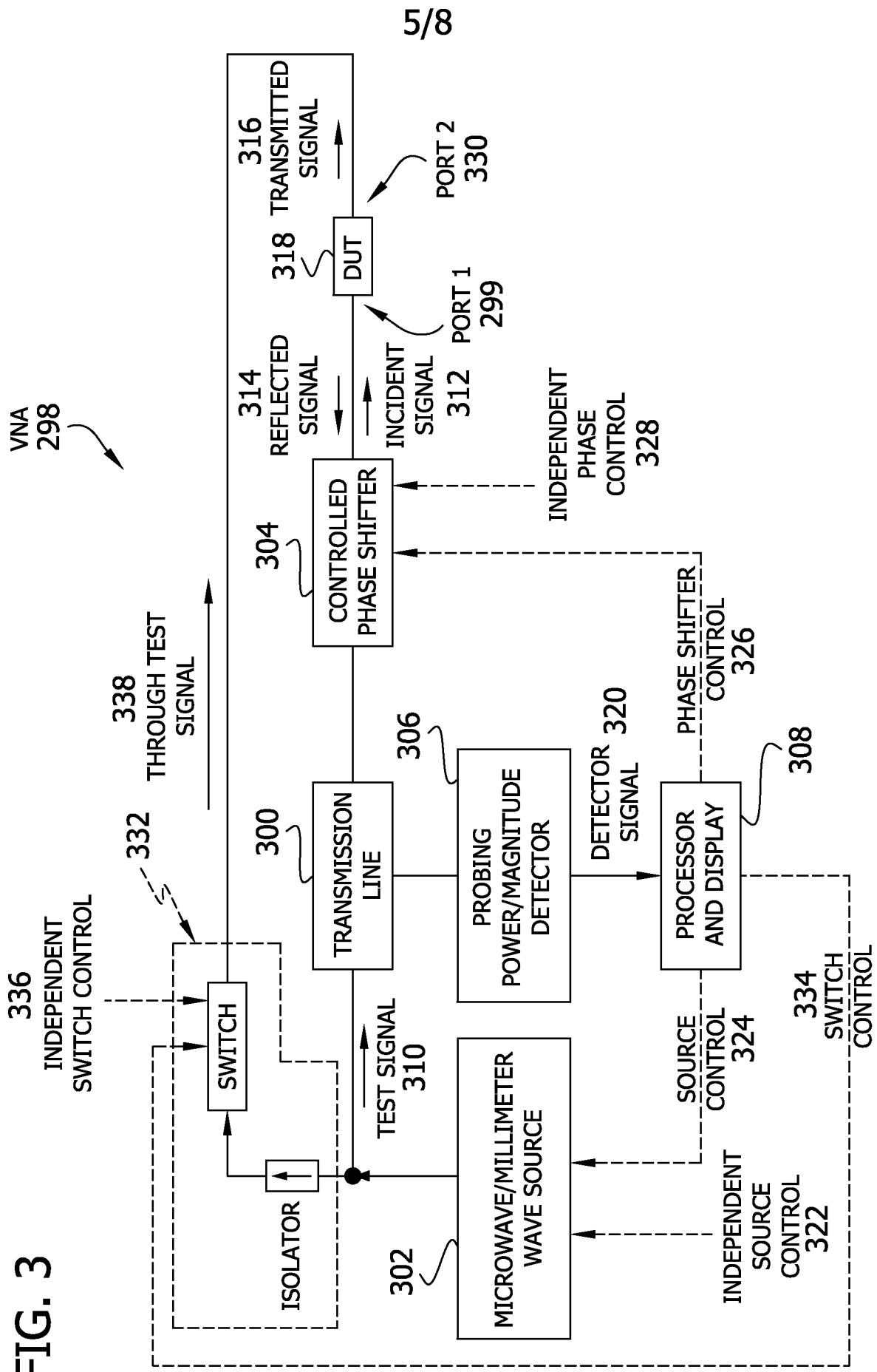


FIG. 4

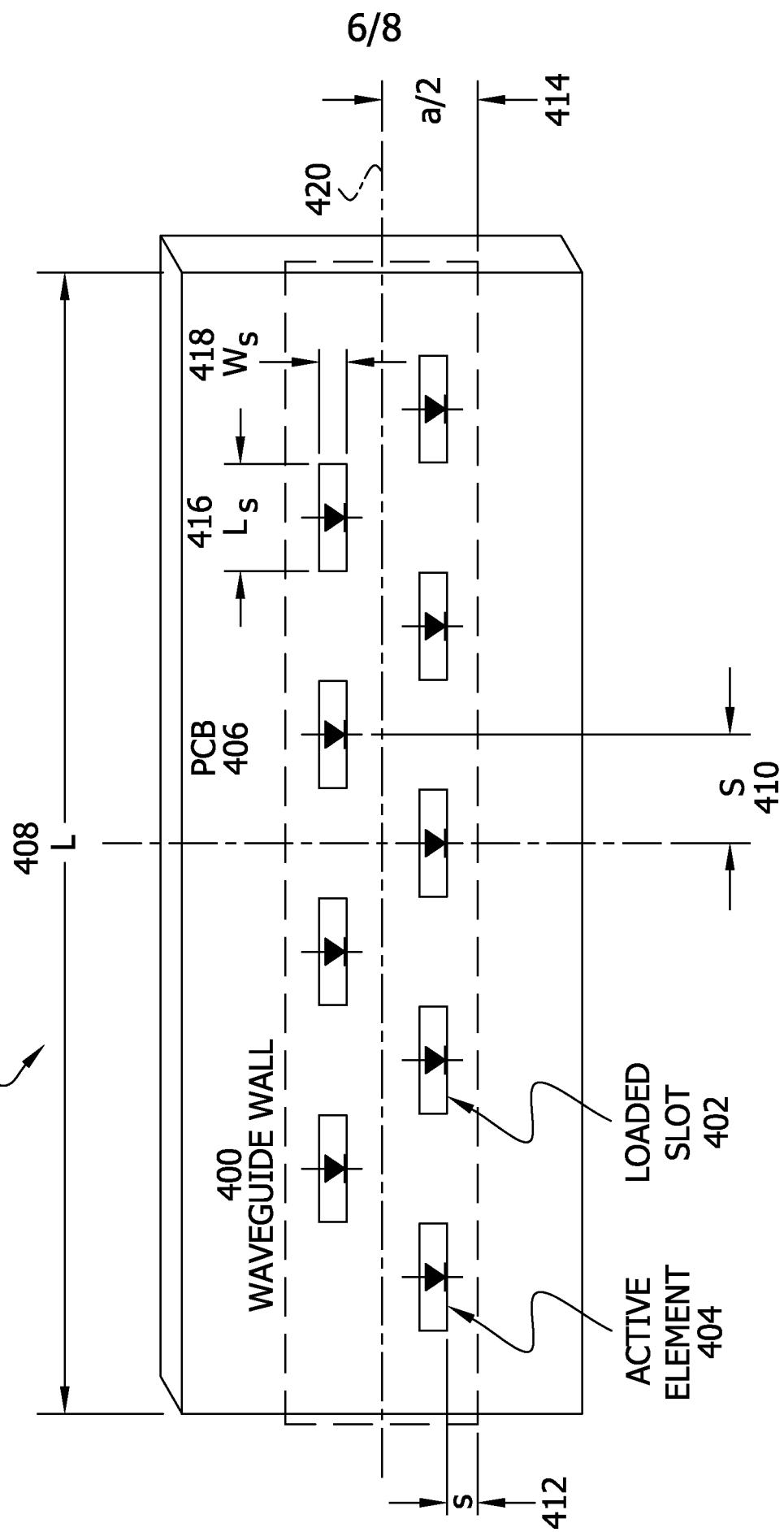
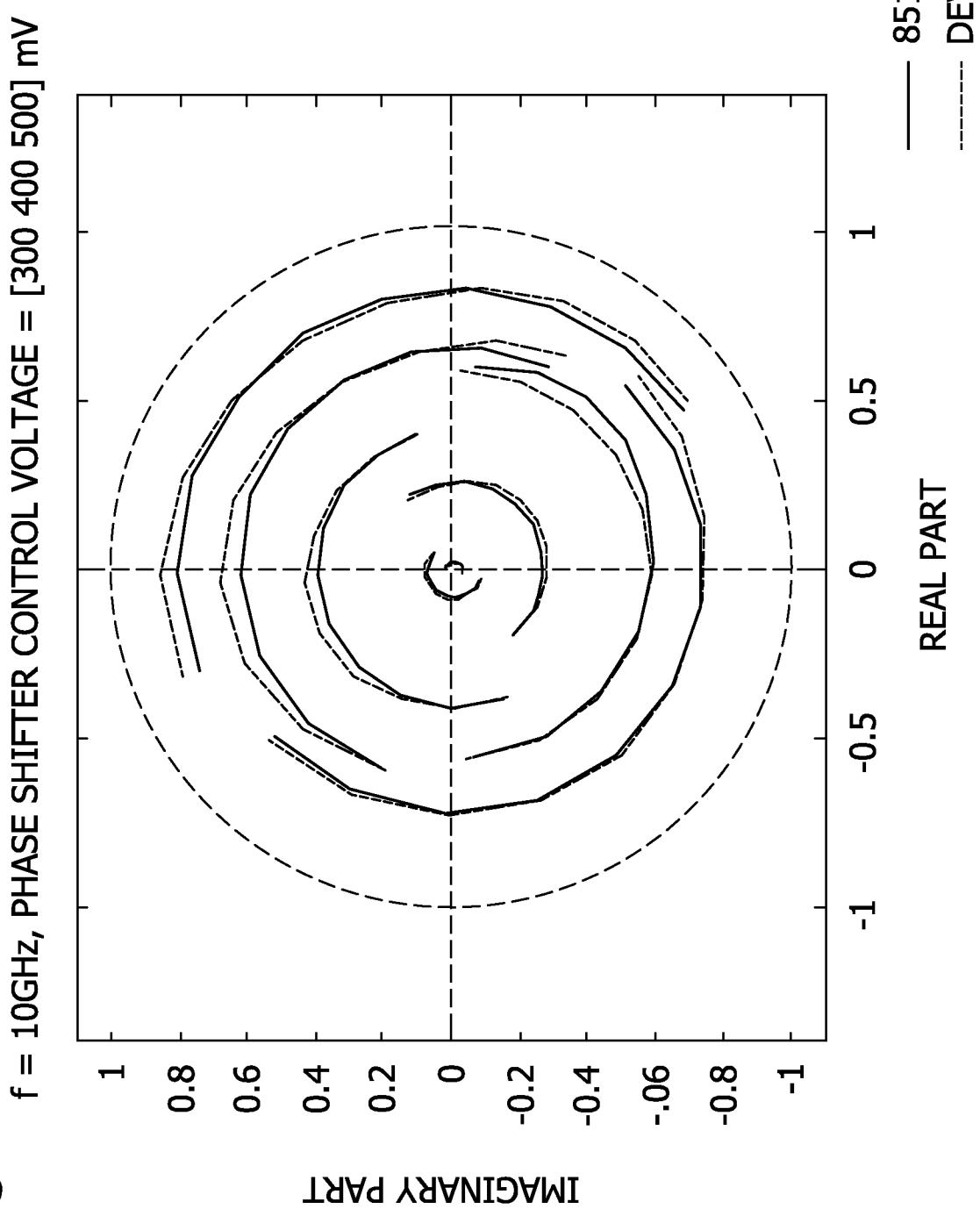
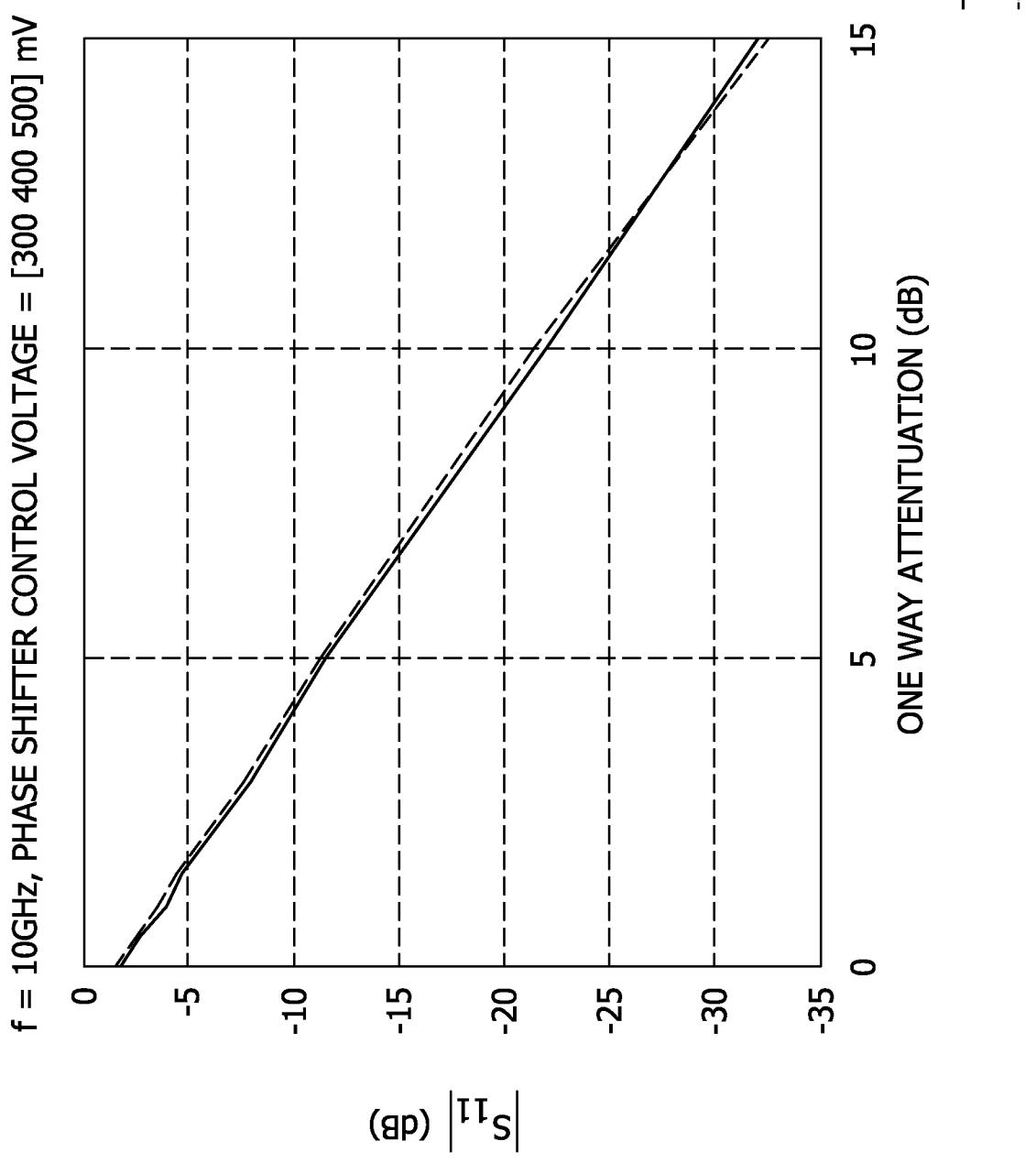


FIG. 5



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FIG. 6



INTERNATIONAL SEARCH REPORT

International application No.

PCT/US 10/20735

A. CLASSIFICATION OF SUBJECT MATTER
IPC(8) - G01R 23/20 (2010.01)
USPC - 324/622

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)
USPC - 324/622Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched
USPC - 324/612, 619, 622Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)
pubwest(DB=PGPB,USPT,EPAB,JPAB), Google Scholar.
Terms - standing wave, phase shifter, magnitude, reflection coefficient, sub-resonant, device under test, dut, active, isolation, element, transmitted, reverse, opposite, direction.

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
Y	US 4,721,901 A (Ashley) 26 January 1988 (26.01.1988); fig. 1; Abstract; col 1, ln 12-21, 36-44; col 2, ln 56-68; col 3, ln 1-17, 33-51.	1-25
Y	US 6,211,663 B1 (Moulthrop et al.) 03 April 2001 (03.04.2001); fig. 1; Abstract, col 2, ln 65-67, col 3, ln 32-41, col 4, ln 1-7, 43-45; col 6, ln 36-44.	1-25
Y	US 2005/0212705 A1 (Legay et al.) 29 September 2005 (29.09.2005); Abstract, para [0004], [0008], [0012].	6, 7, 13, 14, 19, 20, 25
A	US 5,715,183 A (Grace et al.) 03 February 1998 (03.02.1998).	1-25

 Further documents are listed in the continuation of Box C.

* Special categories of cited documents:	
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"E" earlier application or patent but published on or after the international filing date	"X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone
"L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)	"Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art
"O" document referring to an oral disclosure, use, exhibition or other means	"&" document member of the same patent family
"P" document published prior to the international filing date but later than the priority date claimed	

Date of the actual completion of the international search

12 March 2009 (12.03.2009)

Date of mailing of the international search report

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Name and mailing address of the ISA/US

Mail Stop PCT, Attn: ISA/US, Commissioner for Patents
P.O. Box 1450, Alexandria, Virginia 22313-1450
Facsimile No. 571-273-3201

Authorized officer:

Lee W. Young

PCT Helpdesk: 571-272-4300
PCT OSP: 571-272-7774