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[Continued on next page]

(54) Title: SCALABLE PARTITION MEMORY MAPPING SYSTEM

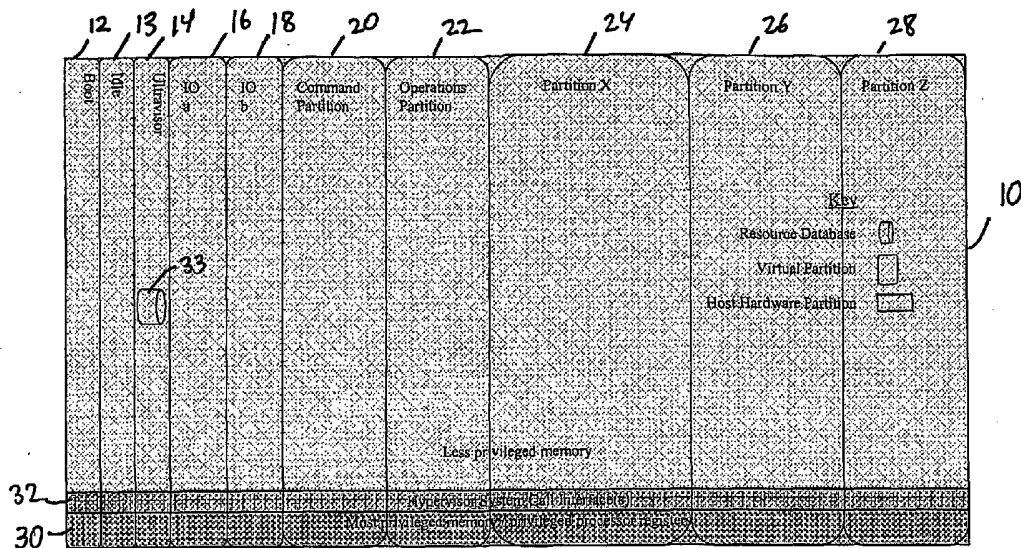


Figure 1: Partitioned Host System

(57) Abstract: A scalable partition memory mapping system is implemented in the ultravisor partition so that the virtualized system is scalable to a virtually unlimited number of pages. A virtualization infrastructure that allows multiple guest partitions to run within a host hardware partition. Partitioned host system (10) has lesser privileged memory that is divided into distinct logical or virtual partitions including special infrastructure partitions such as boot partition (12), idle partition (13), ultravisor partition (14), first and second I/O partitons (16 and 18), command partition (20), and operation (22), as well as virtual guest partitions (24, 26 and 28). The resource manager application of the ultravisor partition (14) manages a resource database (33) that keeps track of assignment of the resources to partitions.

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INTERNATIONAL SEARCH REPORT

International application No.

PCT/US04/33527

<p>A. CLASSIFICATION OF SUBJECT MATTER IPC(7) : G06F 17/30 US CL : 707/3,102,104.1 According to International Patent Classification (IPC) or to both national classification and IPC</p>														
<p>B. FIELDS SEARCHED Minimum documentation searched (classification system followed by classification symbols) U.S. : 707/3,102,104.1 Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched</p>														
<p>Electronic data base consulted during the international search (name of data base and, where practicable, search terms used) Please See Continuation Sheet</p>														
<p>C. DOCUMENTS CONSIDERED TO BE RELEVANT</p> <table border="1"> <thead> <tr> <th>Category *</th> <th>Citation of document, with indication, where appropriate, of the relevant passages</th> <th>Relevant to claim No.</th> </tr> </thead> <tbody> <tr> <td>A</td> <td>US 5,918,229 A (DAVIS et al) 29 June 1999 (19.06.1999), column 2, line 64 - column 28, line 20.</td> <td>1-6</td> </tr> <tr> <td>A. P</td> <td>US 6,791,910 B1 (JAMES et al) 14 September 2004 (14.09.2004), column 3, line 21 - column 9, line 9.</td> <td>1-6</td> </tr> <tr> <td>A. E</td> <td>US 2005/0044301 A1 (VASILEVSKY et al) 24 February 2005 (24.02.2005), paragraphs 5-182.</td> <td>1-6</td> </tr> </tbody> </table>			Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.	A	US 5,918,229 A (DAVIS et al) 29 June 1999 (19.06.1999), column 2, line 64 - column 28, line 20.	1-6	A. P	US 6,791,910 B1 (JAMES et al) 14 September 2004 (14.09.2004), column 3, line 21 - column 9, line 9.	1-6	A. E	US 2005/0044301 A1 (VASILEVSKY et al) 24 February 2005 (24.02.2005), paragraphs 5-182.	1-6
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<p>Name and mailing address of the ISA/US Mail Stop PCT, Attn: ISA/US Commissioner for Patents P.O. Box 1450 Alexandria, Virginia 22313-1450 Facsimile No. (703) 305-3230</p>		<p>Authorized officer <i>for Michelle R. Egan</i> John E Breen Telephone No. (571) 272-3609</p>												

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International application No.
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Continuation of B. FIELDS SEARCHED Item 3:

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scal\$3, partition\$3, map\$4, index\$3, indic\$3, log-based, memor\$3, allocat\$3, database\$1, data near base\$1, repositor\$3, log near base\$1, resource\$1, factor\$1, page41