

(19) World Intellectual Property Organization
International Bureau



(43) International Publication Date
30 March 2006 (30.03.2006)

PCT

(10) International Publication Number
WO 2006/033644 A1

(51) International Patent Classification⁷: **H03K 3/00**,
17/94

(21) International Application Number:
PCT/SG2005/000317

(22) International Filing Date:
21 September 2005 (21.09.2005)

(25) Filing Language: English

(26) Publication Language: English

(30) Priority Data:
10/947,783 21 September 2004 (21.09.2004) US

(71) Applicant (for all designated States except US): **CRE-
ATIVE TECHNOLOGY LTD.** [SG/SG]; 31 International
Business Park, Creative Resource, Singapore 609921 (SG).

(72) Inventors; and

(75) Inventors/Applicants (for US only): **MAKINO, Jun**
[JP/SG]; Blk 19B Hillview Ave, #12-05 Hillview Park,
Singapore 669555 (SG). **TING, Boon Ghee** [SG/SG]; Blk
305A Anchorvale Link #13-15, Singapore 541305 (SG).

(74) Agent: **BRADFORD, Victoria Sophie**; Lloyd Wise, Tan-
jong Pagar, P.O. Box 636, Singapore 910816 (SG).

(81) Designated States (unless otherwise indicated, for every
kind of national protection available): AE, AG, AL, AM,
AT, AU, AZ, BA, BB, BG, BR, BW, BY, BZ, CA, CH, CN,
CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, EG, ES, FI,
GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE,
KG, KM, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, LY,
MA, MD, MG, MK, MN, MW, MX, MZ, NA, NG, NI, NO,
NZ, OM, PG, PH, PL, PT, RO, RU, SC, SD, SE, SG, SK,
SL, SM, SY, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ,
VC, VN, YU, ZA, ZM, ZW.

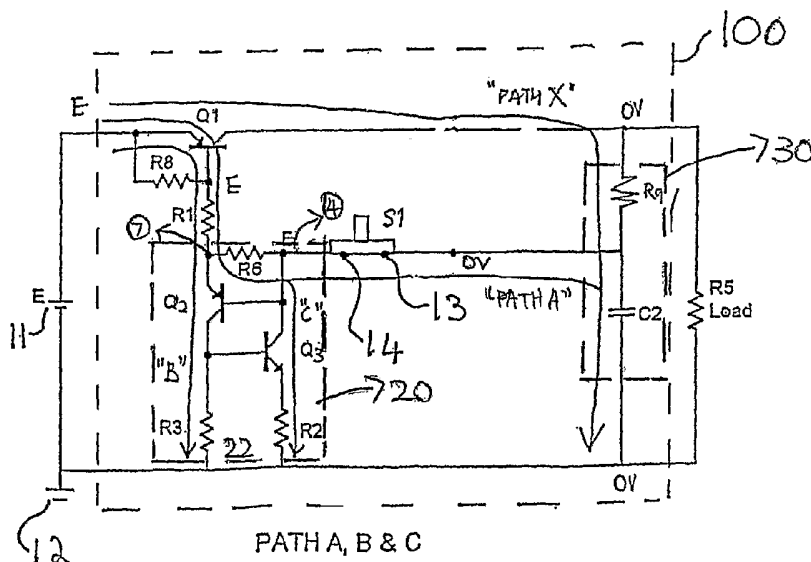
(84) Designated States (unless otherwise indicated, for every
kind of regional protection available): ARIPO (BW, GH,
GM, KE, LS, MW, MZ, NA, SD, SL, SZ, TZ, UG, ZM,
ZW), Eurasian (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM),
European (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI,
FR, GB, GR, HU, IE, IS, IT, LT, LU, LV, MC, NL, PL, PT,
RO, SE, SI, SK, TR), OAPI (BF, BJ, CF, CG, CI, CM, GA,
GN, GQ, GW, ML, MR, NE, SN, TD, TG).

Published:

— with international search report

For two-letter codes and other abbreviations, refer to the "Guid-
ance Notes on Codes and Abbreviations" appearing at the begin-
ning of each regular issue of the PCT Gazette.

(54) Title: A SWITCH CIRCUIT



(57) Abstract: A switch circuit for controlling the supply of electrical power from an electrical power source to a load includes a power supply switch comprising input, output and control terminals. The circuit further includes an electronic switching device connected to the control terminal of the power supply switch and having an activating input; a momentary switch coupled to the activating input of the electronic switching device; and a charge storage element coupled to the momentary switch. In a first mode, closing the momentary switch in excess of a triggering time charges the charge storage element and triggers the electronic switching device to turn on the circuit to supply power to the load. In a second mode, closing the momentary switch connects the charge storage element to the electronic switching device to turn off the electronic switching device and the power supply switch, thus interrupting the supply of power to the load.

A SWITCH CIRCUIT

Field of the Invention

The present invention relates to an arrangement for a switch circuit, in particular an improved arrangement for a switch circuit utilising a momentary switch.

Background of the Invention

Momentary switches are commonly used for turning on and off electrical and electronic equipment. They operate in combination with a switch circuit located on a microcontroller to produce a logic type operation so that when a switch pulse is supplied to a switch circuit, the circuit switches from "off" to "on" or from "on" to "off". The switch pulse is provided by actuation of the momentary switch which, when actuated, completes the circuit to provide a pulse to the switch circuit to switch the state of the switch circuit.

It is common practice for a number of different types of electrical and electronic equipment to use only a single momentary switch to switch the equipment both on and off.

However, one of the disadvantages of using a single momentary switch is that, during the off state, it is still necessary for power to be supplied to the switch circuit. This is because it is necessary to maintain a live switch circuit for sensing a switch pulse generated by the momentary switch at any time. Therefore, this power consumption constantly drains the battery or AC power even though the electronic equipment appears to be in an "off" state to a user. This consumption of electrical power is especially important to battery operated equipment such as laptop computers and mobile (or hand) phones where a long battery life is preferable in order to cut down on the need to constantly change or recharge batteries.

Summary of the Invention

The present invention relates to an improved arrangement for a switch circuit which is used for controlling the supply of electrical power from an electrical supply source e.g. a battery, to a load. In general terms, the present arrangement for a switch circuit comprises a charge storage element which performs the dual functions of both turning on and turning off the switch circuit.

According to a first aspect of the invention, there is provided a switch circuit comprising:

- a power supply switch comprising input, output and control terminals, wherein the input terminal of the power supply switch is coupled to the electrical power source and the output terminal of the power supply switch is coupled to the load;

- an electronic switching device connected to the control terminal of the power supply switch and having an activating input;

- a momentary switch coupled to the activating input of the electronic switching device;

- a charge storage element coupled to the momentary switch;

- the arrangement being such that

- (i) in a first mode, power is supplied to the load by closing the momentary switch to connect the charge storage element to the electronic switching device for a predetermined time in excess of a triggering time of the electronic switching device, the predetermined time being determined by the charge-up time of the charge storage element and triggering of the electronic switching device maintaining the power supply switch in an on state to supply power from the electrical power source to the load; and

- ii) in a second mode, the supply of power to the load is stopped by closing the momentary switch to connect the charge storage element to the electronic switching device, the charge storage element providing a signal to turn off the electronic switching device and the power supply switch to cut the supply of power from the electrical power source to the load.

In one embodiment, the charge storage element comprises a capacitor.

Preferably, the switch circuit further comprises a charge-up time controller for the charge storage element. The triggering time of the electronic switching device can be adjusted by changing the value of the charge-up time controller. In one embodiment, the charge-up time controller comprises a resistive element.

The charge storage element and the charge-up time controller are preferably disposed in series and across the load.

According to the invention in a second aspect, the arrangement for the switch circuit is implemented as a combination of an integrated circuit with a charge storage element and a momentary switch coupled to the integrated circuit, wherein the integrated circuit comprises a power supply switch and an electronic switching device. The activating input of the electronic switching device is adapted for connecting the integrated circuit to a momentary switch. Additionally, the integrated circuit is also provided with means for receiving a supply of electrical power from an electrical power source and a first and second connection means for connecting the integrated circuit to a respective load and a charge storage element .

According to the invention in a third aspect, a momentary switch is coupled to an assembled integrated circuit and packaged integrally with the integrated circuit to form a switching structure. The integrated circuit comprises some or all of the components of a switch circuit.(excluding the momentary switch).

Brief Description of the Drawings

Embodiments of the present invention will now be described, by way of example, with reference to the accompanying drawings of which:

- Figure 1 is a circuit diagram of a first example of a switch circuit, in which the switch circuit is in an "off" state;
- Figure 2 is a circuit diagram of the switch circuit of Figure 1 with the momentary switch in a closed position during switching on of the switch circuit;
- Figure 3 is a circuit diagram of the switch circuit of Figure 1 with the momentary switch in a closed position during switching off of the switch circuit;
- Figure 4 is a circuit diagram of a second example of a switch circuit;
- Figure 5 is a circuit diagram of a third example of a switch circuit;
- Figure 6 is a circuit diagram of a fourth example of a switch circuit;
- Figure 7 is a schematic diagram showing the switch circuit of Figures 1 to 3 being used to control electrical power from a rectified AC mains supply;
- Figure 8 is a schematic diagram showing the switch circuit of Figures 1 to 3 being used to control an AC power supply for a high power consumption load; and,
- Figure 9 is a circuit diagram of a fifth example of a switch circuit.
- Figure 10 is a perspective view of a switching structure in accordance with the third aspect of the invention.

Detailed Description of the Preferred Embodiment

Figure 1 illustrates a switch circuit 100 in accordance with one embodiment of the present invention. The switch circuit 100 acts as an interface between a power supply in the form of a battery 11 and a load represented by resistor R5. The switch circuit 100 comprises a first transistor Q1 which acts as a power supply switch, a resistor R1, an electronic switching device 20, a momentary switch S1 and a charge storage and discharge circuit 30.

As shown in Figure 1, the transistor Q1 is of pnp bipolar type, the emitter terminal of which being coupled to the positive terminal of the battery 11 while the collector terminal is connected to one side of the load R5. A resistor R8 is coupled between the emitter terminal and the base terminal of the transistor Q1.

The electronic switching device 20 comprises an input terminal 7 which is connected to the base of the transistor Q1 via resistor R1, an output 22 coupled to the negative terminal of the battery and an activating input 4 coupled to a first contact 14 of the momentary switch S1. In this embodiment, the electronic switching device comprises second and third bipolar transistors Q2 and Q3 connected as a thyristor device. Second transistor Q2 is a pnp bipolar transistor. The emitter of the second transistor Q2 is coupled to the base of transistor Q1 via a resistor R1 and to the contact 14 of the momentary switch S1 via a resistor R6. The contact 14 of the momentary switch S1 is also coupled to the base terminal of the second transistor Q2 and to the collector terminal of the third transistor Q3. Third transistor Q3 is an npn bipolar transistor. The base of the third transistor Q3 is coupled to the collector of the second transistor Q2 and the emitter of the third transistor Q3 is coupled to a ground potential 12 via a resistor R2. The collector of the second transistor Q2 is also coupled the ground potential 12 via resistor R3.

Another contact 13 of the momentary switch S1 is coupled to the collector terminal of transistor Q1 via resistor R9 and also to the ground potential via capacitor C2. In this embodiment, resistor R9 and capacitor C2 make up the charge storage and discharge circuit 30.

The positive terminal of the battery 11 is coupled to the emitter terminal of the first transistor Q1 and the resistor R8 while the negative terminal of the battery 11 is coupled to the ground potential 12. However, alternatively, the negative terminal of the battery 11 may be coupled to a floating potential.

In use, the switch circuit 100 operates as follows:

Initially, with the momentary switch S1 in the position shown in Figure 1, the switch circuit 100 is in the off state and the transistors Q1, Q2 and Q3 are turned off. Hence, there is no closed circuit path and the switch circuit 100 acts to prevent power being supplied from the battery 11 to the load R5.

When the switch circuit 100 is in the off state, the only power consumption is a reverse leakage power consumption through the transistors Q1, Q2, Q3 which is virtually negligible compared to the self discharging current of the battery 11.

Accordingly, the initial potential at the points 1, 2, 3, 4, 5, 6, 7, 8 in the circuit 100 and the status of the transistors are as follows:

The potential at point 3 = E (the potential of the battery 11);

The potential at point 1 = 0, as the transistor Q1 is switched off;

The potential across the base-emitter junction of the first transistor Q1 = 0;

The potential at point 2 = the potential at point 3 = E;

The current through R1 = 0;

The potential at point 7 = the potential at point 2 = E;

The voltage across the base-emitter junction of the second transistor Q2 = 0, as the second transistor Q2 is switched off;

The potential at point 4 = the potential at point 7 = E; Current through R3 = 0;

The potential at point 5 = 0;

The voltage across the base emitter junction of the third transistor Q3 = 0;

The current through the resistor R2 = 0;

The potential at point 6 = 0;

The potential across the capacitor C2 = 0; and

The potential at point 8 = the potential at point 1 = 0.

When the momentary switch S1 is pressed to complete the contacts 13, 14 as shown in Figure 2, point 4 is driven to zero potential and an initial closed circuit (path A) is formed. This causes the base-emitter junctions of the first and second transistors Q1, Q2 to be forward biased thereby turning on transistors Q1, Q2. The initial current generated contributes to the base current for the first and second transistors Q1, Q2 and surges across the resistor R1 through path A. This surge in base current drives the first and second transistors Q1, Q2 into saturation mode. Immediately after path A is formed, the initial surge current passes through to resistor R3 (path B). As the voltage across R3 is raised rapidly due to the surge current, the base-emitter junction of the third transistor Q3 is forward biased thereby turning on the third transistor Q3. Similarly, the third transistor Q3 is also driven into saturation mode.

The current through path A is in the form of a pulse which rapidly becomes zero due to the presence of the capacitor C2 which is charged up to a constant value V_s thereby rendering path A open circuit. In Fig. 2, the voltage V_s given by

$$(E - 2V_{be}) \times \frac{R_2 // R_3}{R_1 + R_2 // R_3}, \text{ whereby } R_2 // R_3 \text{ refers to the effective resistance of}$$

resistors R2 and R3 which are connected in parallel. When capacitor C2 is charged to a voltage of V_s , this prevents further current from passing through the capacitor C2 and path A becomes open circuit. Therefore, even if momentary switch S1 continues to remain pressed, path A will eventually become open circuit when C2 charges to V_s . As we shall see in the following paragraph, the switch circuit 100 is configured such that the battery 11 continues to deliver electrical power to the load even after capacitor C2 charges to V_s and path A becomes open circuit. Upon deactivation of S1, C2 continues to be charged up through path X to a voltage of about $E - 0.2V$. This is because the voltage drop across the collector-emitter junction of transistor Q1 is typically about 0.2 V when transistor Q1 is in saturation mode.

The second and third transistors Q2, Q3 form a thyristor device which is triggered by the surge current generated in the switch circuit 100 when the momentary switch S1 is actuated. The third transistor Q3 takes its base current from the second transistor Q2 and at the same time, the third transistor supplies the base current to the second transistor Q2. The advantage of using a thyristor device is that once the thyristor device enters a latched on state, it continues to conduct even without current supply at the activating input 4 (either due to the momentary switch being released or capacitor C2 being charged to a constant value V_s). This is because when the thyristor is in the latched on state, the voltage across the base-emitter junction of the second transistor Q2 is maintained forward biased with the third transistor Q3 in saturated mode as illustrated by path C while the voltage across the base-emitter junction of the third transistor Q3 is kept forward biased by the second transistor Q2 operating in saturated mode as shown by path B. In this way, even without current supply at the activating input 4, the combination of the second and third transistors, Q2, Q3 will keep each other conducting as long as the battery 11 is coupled to the emitter of the second transistor Q2, that is the battery 11 is coupled to point 7 in the switch circuit 100. Therefore, once the thyristor device enters a latched on state, the closed circuit path B and path C maintain the forward biasing of the base-emitter junctions of the second and third transistors Q2, Q3 even when path A becomes open circuit. Accordingly, with the thyristor device switched on, the base-emitter junction of the first transistor Q1 is also maintained in a forward biased mode with its base current driven into saturation mode. Therefore, in this state the switch circuit 100 is turned on and power is supplied through the transistor Q1 to the load R5.

However, a sufficient amount of triggering energy must be provided in order for the thyristor device to enter a latched on state and continue to conduct even after path A becomes open circuit. This triggering energy is expressed in terms of the minimum triggering current through transistor Q3 that has to be exceeded in order for the transistor Q3 to remain switched on permanently. The current through transistor Q3 is dependent on its base emitter voltage, that is the

voltage across points 5 and 6. Since the voltage at point 5 is dependent on the voltage at point 4, the voltage at point 5 and hence current through transistor Q3 rises as capacitor C2 is being charged up. When capacitor C2 is charged above a certain voltage, the current through transistor Q3 rises above the triggering current required and the transistor Q3 is switched on permanently. Accordingly, this means that switch S1 has to be pressed for a triggering time that is required for the triggering current to be exceeded. In this embodiment, the triggering time is dependent on how fast C2 charges up.

As seen from the above, the supply of electrical power from the battery 11 to a load R5 is activated by pressing switch S1 for a duration that exceeds the triggering time. When switch S1 is actuated to complete the contacts 13, 14, the capacitor C2 is charged up through both paths A and X while paths B and C draw current away from paths A and X. Since C2 is charged through resistor R9 along path X, R9 provides a charge-up time controlling function. The triggering time can thus be adjusted by adjusting the value of C2 and R9 and ideally this should not be too short, in order to prevent misfiring of the thyristor device

This advantageously allows the triggering time to be pre-fixed as it is independent of the value of any filtering capacitance which is commonly connected in parallel with the load. In our previous PCT application PCT/SG99/00084, a switch circuit was disclosed whereby the triggering time was determined by a capacitor connected in parallel with the load. In this case, the resulting triggering time was difficult to predict as it was not independent of the filtering capacitance.

When the momentary switch S1 is actuated again to complete the contacts 13, 14 (see Figure 3) a closed circuit path D is formed in the switch circuit 100 as the capacitor C2 discharges. Therefore, C2 functions as a charge storage device which is charged up when the switch circuit switches from "off" to "on" and discharges when the circuit switches from "on" to "off". Point 4 is

momentary short circuited to point 8 during actuation of the switch S1 and it becomes the same potential as point 8 which is E-0.2 volts. However, the voltage at point 2 during the on state of the circuit 100 is about E-0.7 volts. This is because the voltage drop across the base-emitter junction of transistor Q1 is typically about 0.7V. Therefore, when point 4 goes to E-0.2 volts, the base-emitter junctions of the first transistor Q1 and the second transistor Q2 are no longer forward biased and the first and second transistors Q1, Q2 are switched off. With the second transistor Q2 in its off mode, the collector of the second transistor Q2 is also switched off which switches off the third transistor Q3. Therefore, the discharge of capacitor C2 provides a signal to switch off switch circuit 100 and as the first transistor Q1 is switched off, no power is supplied from the battery 11 to the load R5. When the momentary switch S1 is released, the remaining charge in the capacitor C2 discharges through the load R5 and resistor R9.

As seen from the above, actuating momentary switch S1 momentarily while the switch circuit 100 is in the "on" state switches off the first transistor Q1 and the thyristor device as a reverse bias voltage is established across the first and second transistors Q1, Q2. If momentary switch S1 is not released but continues to remain pressed, the voltage at point 4 will decrease as capacitor C2 starts to discharge. When the voltage at point 4 reaches a sufficiently low level, transistor Q1 will start to conduct again as its base-emitter junction becomes forward biased. Accordingly, power will be supplied to the load R5, although at a lower level since the thyristor is not activated unlike during the "on" state of the switch circuit 100 mentioned in the previous paragraphs. When the momentary switch S1 is finally released, power supply to the load R5 will be cut off as Q1 switches off and any remaining charge in the capacitor C2 discharges through the load R5 and R9. After the capacitor C2 discharges completely, the switch circuit 100 reverts back to the initial off state in which no power is supplied to the load R5 from the battery 11 and the only power consumed by the circuit 100 is the reverse leakage current through the first, second and third transistors Q1, Q3, Q2 which is virtually negligible.

The values of resistor R9 and capacitor C2 are chosen to take into account the need for capacitor C2 to discharge as the switch circuit reverts back to the initial "off" stage where it is able to sense a switch pulse generated by the momentary switch at any time. Therefore the values of R9 and C2 cannot be too large or it will affect the ability of the switch circuit to react to switch pulses. On the other hand, C2 also has to sustain a voltage that is high enough to switch off Q1 and the electronic switching device when a request is made to switch from an "on" to "off" state. In a preferred embodiment of the invention, when the voltage supply is between 1.8 to 10V, the value of resistor R9 is between 22 to 82 k ohms and capacitance of C2 between 200nF to 10 μ F.

A second example of a switch circuit 150 is shown in Figure 4. The switch circuit 150 is identical to the switch circuit 100, except that the circuit includes a signal input contact 16 at point 5 of the circuit. The signal input contact 16 is connected to electronic equipment 17, which is the equivalent of the load R5 shown in Figures 1 to 3. The signal input contact 16 permits the electronic equipment 17 to turn itself off automatically, for example, after a predetermined period of time. The electronic equipment 17 turns the switch circuit 150 to the off state by applying a ground potential to the signal input contact 16 which switches off the third transistor Q3 which in turn switches off the second transistor Q2. This causes the electronic switching device 20 to become open circuit which switches off the first transistor Q1 to cut off power supply from the battery 11 to the electronic equipment 17. In another embodiment of Figure 4, the electronic equipment 17 may also be used to turn on the switch circuit 150 by supplying a sufficiently high voltage level to the signal input contact 16.

Figure 5 shows a third example of a switch circuit 200 which is similar to the switch circuit 150 except a signal input contact 24 is connected at point 4. The signal input contact 24 when coupled to the electronic equipment 17 permits the electronic equipment 17 to switch off the switch circuit 200 by applying a high voltage state signal to contact 24. This causes the base emitter junction of the

first and second transistors Q1, Q2 to be reversed biased which switches the first and second transistors Q1, Q2 off. With the first and second transistors Q1, Q2 switched off, the third transistor Q3 is also switched off as the potential at point 5 drops to zero and the switch circuit 200 is switched into the off state. In another embodiment of Figure 5, the electronic equipment 17 may also be used to turn on the switch circuit 200 by supplying a ground potential to the signal input contact 24.

Figure 6 shows a fourth example of a switch circuit 250. The switch circuit 250 is provided with a first switch signal input contact 16 connected to point 5 and a second switch signal input contact 24 connected to point 4. The switch circuit 250 controls the supply of power from the battery 11 to electronic equipment 26. The switch input contacts 16, 24 are coupled to a remote electronic system 27 which has its own power switch incorporated therein. The power switch incorporated within the remote electronic system 27 may be a switch circuit similar to any of the switch circuits 100, 150, 200, or may also be a switch circuit 250, controlled remotely by a further remote electronic system.

The switch circuit 250 permits remote power control from the other remote electronic system 27 and may be used for example, with a multi-unit system. The remote system 27 can switch the switch circuit 250 to the on state by applying a ground potential (or a sufficiently low level voltage) to the signal input contact 24 to forward bias transistor Q2. Alternatively, the remote system 27 can switch the switch circuit 250 to the off state by applying a ground potential signal to signal input contact 16.

In all the switch circuits 150, 200, 250 a momentary switch S1 is still provided which permits a user to manually switch circuits 150, 200, 250 between the off and on states. In the instances where a user wishes to use the momentary switch instead of the electronic equipment 17 or remote system 27 to toggle the switch circuit 150, 200, 250 between on and off states, the electrical connection

between the electronic equipment 17 and the signal input contact 16, 24 is maintained at a high impedance. Alternatively, it is also possible to disable the momentary switch S1 and rely on the remote system 27 to control the toggling of the switch circuit. In this case, if the control signal is applied at the signal input contact 16, maintaining a sufficiently high potential at signal input contact 16 keeps the switch circuit in an "on" state as the base-emitter junction of transistor Q3 is forward biased while maintaining a ground potential reverse bias the base-emitter junction of transistor Q3 and the switch circuit remains turned off. As for signal input contact 24, maintaining a ground potential at signal input contact 24 keeps the switch circuit in an "on" state as the base-emitter junction of transistor Q2 is forward biased while maintaining a high potential reverse bias the base-emitter junction of transistor Q2 and the switch circuit remains turned off.

As an alternative to the remote system being connected to separate input contacts 16, 24 it is possible that the remote system 27 could be connected by a single line to either the switch circuit 150 or the switch circuit 200. In the case of the switch circuit 150, the remote system 27 would switch on the circuit 150 by supplying a sufficiently high voltage level to the signal input contact 16 and switch off the circuit 150 by applying a ground potential to the signal input contact 16.

Where the remote system 27 is connected to signal input contact 24 in circuit 200, the remote system 27 would switch on the circuit 200 by applying a ground potential to the signal input contact 24 and switch off the circuit 200 by applying a sufficiently high voltage level to the signal input contact 24 to reverse bias the first and second transistors Q1, Q2.

Although the switches 100, 150, 200, 250 are shown controlling the supply of power from a battery 11 to load R5 or electronic equipment 17, 26, the circuits could also be used to control the supply of power from a rectified AC supply and

from an AC main supply to electronic equipment having a heavy power consumption.

Figure 7 shows an example of the switch circuit 100 being used to control a rectified AC power supply from a transformer K2 and a full wave rectifier 29 to a load 28. However, switch circuit 100 could be replaced by any of switch circuits 150, 200, 250.

Figure 8 shows the switch circuit 100 being used to control power supply from an AC mains supply to a heavy load 30 through a transformer K2, a relay R, a diode D4 and a battery 32.

Figure 9 is a circuit diagram showing a fifth example of a switch circuit 400. The switch circuit 400 is identical to the switch circuit 100 except that the bipolar transistors Q1, Q2, Q3 are replaced by enhancement type MOSFETs M1, M2, M3 respectively. The transistors M1, M2 are P channel enhancement type MOSFETs and the transistor M3 is a N channel enhancement type MOSFET. The principle of operation of the switch circuit 400 is identical to the switch circuit 100.

The embodiments described lend themselves to integrated circuit implementation, with most of the components assembled together on an integrated circuit. The remaining components of the switch circuit are connected to the integrated circuit via connection means provided on the integrated circuit. Furthermore, the integrated circuit may also be provided with means for connecting to an electrical power source and/or a load. In one embodiment, the integrated circuit is encapsulated in an electrically insulating material and with the connection means on the integrated circuit electrically coupled e.g. by wire bonds or flip chip connections to one end of the connection leads located on the surface or extending outwards of the encapsulating material. The connection leads provide electrical connection to the devices located outside of the integrated circuit. In a preferred embodiment, the switch

circuit is implemented with the first transistor Q1 , resistor R9 and electronic switching device 20 assembled on an integrated circuit. The capacitor C2, momentary switch S1, electrical power source 11 and load R5 are then connected to the integrated circuit via connection means provided on the integrated circuit. The momentary switch S1 and capacitor C2 are connected in series so that when the momentary switch is actuated, capacitor C2 is coupled to the activating input 4 of the electronic switching device. In this case, since resistor R9 is located within the IC, the triggering time of the resulting switch circuit is adjusted by varying the value of the externally located capacitor C2.

In addition to the above, the integrated circuit assembled may also be provided with connection means which allow the switch circuit to be switched on or off remotely via the electronic equipment being switched 17 or a remote system 27. In one embodiment, the connection means is coupled to the electronic switching device at the signal input contacts 16 or 24 so that the electronic switching device and hence the switch circuit may be selectively switched on or off remotely. Therefore, a user of the integrated circuit has the option of either using a momentary switch or remote system in combination with the integrated circuit.

Since the momentary switch returns to its normally open position when released, it is conventionally implemented with a relay or microcontroller which ensures continued power delivery to the load even after the switch is released. In accordance with a third aspect of the present invention, a momentary switch 520 is packaged together with an integrated circuit 510 to produce a switching structure 500 shown in Figure 10. The integrated circuit comprises some or all of the components of a switch circuit (excluding the momentary switch) used for controlling the supply of electrical power from an electrical power supply to a load. The momentary switch 520 is electrically coupled to the integrated circuit via connection means provided on the integrated circuit 510 thereby providing a stand alone momentary switch which is able to latch onto an "on" state without the assistance of a relay or microcontroller. Examples of switch circuits which

are applicable include both the embodiments encompassed by the present invention as well as those of our previous PCT application PCT/SG99/00084. The elimination of a microcontroller also advantageously reduces the "off" state power consumption since the embodiments of the switch circuit in both the present invention and that of PCT application PCT/SG99/00084 do not require a live switch circuit for sensing a switch pulse generated by actuating the momentary switch.

The switch circuit described in figures 1 to 9 comprises a charge storage element which performs the dual functions of turning on and off the switch circuit while in the switch circuit of PCT/SG99/00084 separate modules are required. Specifically, for the switch circuit of PCT/SG99/00084 a pulse generation device which comprises a capacitor and resistor connected in parallel is used for turning on the switch circuit while a charge storage device comprising a capacitor is used for turning off the switch circuit. Preferably the power supply switch Q1 and electronic switching device 20 which are common to both switch circuits are assembled on the integrated circuit 510 of the switching structure 500. The combination of the power supply switch Q1 and electronic switching device 20 form an electronic latching switch which switches between a conductive and non-conductive mode in response to an actuation of the momentary switch. Meanwhile, the charge storage element, charge storage device and at least a portion of the pulse generation device are preferably located outside of the switching structure 500 with connection means being provided to connect them to the integrated circuit. In one embodiment where the switching structure 500 is based on the switch circuit of PCT/SG99/00084, the pulse generation device is arranged with the resistor located on the integrated circuit 510 and the capacitor located outside the switching structure. In this way, the switching structure 500 in combination with the externally coupled components form a switch circuit.

In a preferred embodiment, the integrated circuit 510 packaged with the momentary switch 520 includes the first transistor Q1, resistor R9 and electronic

switching device 20 as well as means for connecting the momentary switch 520, a capacitor C2, an electrical power source 11 and a load R5 to the integrated circuit. The integrated circuit 510 is mounted on a lead frame 530 with the leads of the lead frame 530 providing electrical connection between the integrated circuit 510 and the external devices. An electrically insulating housing 540 encapsulates the integrated circuit 510 with the momentary switch 520 protruding out of the housing 540 so that it can be actuated externally by a user. Although the switch structure 500 shown in Figure 10 uses a lead frame 530, other types of packaging substrate such as surface mount substrates are also suitable.

An integrated circuit and switch structure as described above can be used in a wide range of applications such as in battery operated devices or even in conjunction with a microprocessor to reduce the off-state power consumption of the microprocessor.

Claims

1. A switch circuit for controlling the supply of electrical power from an electrical power source to a load, the switch circuit comprising:

a power supply switch having input, output and control terminals, wherein the input terminal of the power supply switch is coupled to the electrical power source and the output terminal of the power supply switch is coupled to the load;

an electronic switching device connected to the control terminal of the power supply switch and having an activating input;

a momentary switch coupled to the activating input of the electronic switching device; and

a charge storage element coupled to the momentary switch;
the arrangement being such that

(i) in a first mode, power is supplied to the load by closing the momentary switch to connect the charge storage element to the electronic switching device for a predetermined time in excess of a triggering time of the electronic switching device, the predetermined time being determined by the charge-up time of the charge storage element and triggering of the electronic switching device maintaining the power supply switch in an on state to supply power from the electrical power source to the load; and

(ii) in a second mode, the supply of power to the load is stopped by closing the momentary switch to connect the charge storage element to the electronic switching device, the charge storage element providing a signal to turn off the electronic switching device and the power supply switch to cut the supply of power from the electrical power source to the load.

2. A switch circuit according to claim 1, wherein the electronic switching device comprises a thyristor device.

3. A switch circuit according to claim 1 or claim 2 wherein the power supply switch comprises a transistor.
4. A switch circuit according to any one of the preceding claims wherein the electronic switching device is formed from at least one bipolar transistor.
5. A switch circuit according to any one of claims 1 to 3 wherein the electronic switching device is formed from at least one MOSFET.
6. A switch circuit according to any one of the preceding claims wherein the power supply switch is formed from at least one bipolar transistor.
7. A switch circuit according to any one of claims 1 to 5 wherein the power supply switch is formed from at least one MOSFET.
8. A switch circuit according to any one of the preceding claims, wherein the power supply switch and the electronic switching device are assembled together on an integrated circuit.
9. A switch circuit according to any one of the preceding claims, wherein the charge storage element comprises a capacitor.
10. A switch circuit according to any one of the preceding claims further comprising a charge-up time controller for the charge storage element.
11. A switch circuit according to claim 10 wherein the charge-up time controller comprises a resistive element.
12. A switch circuit according to claim 10 or claim 11 wherein the charge storage element and charge-up time controller are disposed in series and across the load.

13. A switch circuit according to claim 12 wherein the momentary switch is connected between the charge storage element and the charge-up time controller.

14. A switch circuit according to any one of claims 10 to 13, wherein the power supply switch, electronic switching device and charge-up time controller are assembled together on an integrated circuit.

15. An integrated circuit for controlling the supply of electrical power from an electrical power source to a load, the integrated circuit comprising:

means for receiving a supply of electrical power from an electrical power source;

a first connection means arranged to connect the integrated circuit to a load;

a second connection means arranged to connect the integrated circuit to a charge storage element, wherein the second connection means is coupled to the first connection means;

a power supply switch having input, output and control terminals, wherein the input terminal of the power supply switch is coupled to the means for receiving a supply of electrical power and the output terminal of the power supply switch is coupled to the first connection means; and

an electronic switching device connected to the control terminal of the power supply switch and having an activating input arranged to connect the integrated circuit to a momentary switch;

the arrangement being such that

(i) in a first mode, power is able to be supplied to a said load by closing a said momentary switch to connect to the electronic switching device for a predetermined time in excess of a triggering time of the electronic switching device a said charge storage element disposed in series with the said momentary switch, the predetermined time being determined by the charge-up time of the said charge storage element and triggering of the electronic

switching device maintaining the power supply switch in an on state to supply power from the said electrical power source to the said load; and

(ii) in a second mode, the supply of power to the said load is stopped by closing the said momentary switch to connect the said charge storage element to the electronic switching device, the said charge storage element providing a signal to turn off the electronic switching device and the power supply switch to cut the supply of power from the said electrical power source to the said load.

16. An integrated circuit according to claim 15 further comprising a charge-up time controller for controlling the charge-up time of the said charge storage element.

17. An integrated circuit according to claim 16, wherein the charge-up time controller comprises a resistive element.

18. An integrated circuit according to claim 16 or claim 17, wherein the charge-up time controller is connected between the first and second connection means such that the charge-up time controller and the said charge storage element are disposed in series and across the said load.

19. A combination of an integrated circuit according to any one of claims 15 to 18 and a momentary switch connected to the activating input in which the integrated circuit and momentary switch are packaged integrally.

20. A combination according to claim 19 in which the integrated circuit is mounted on a packaging substrate and the integrated circuit and momentary switch are encapsulated in an electrically insulating material, the momentary switch protruding out of the electrically insulating material so that it can be actuated externally.

21. A switching structure for controlling the supply of electrical power from an electrical power source to a load, the switching device comprising:

an integrated circuit; and

a momentary switch, the integrated circuit and momentary switch being packaged integrally and encapsulated in an electrically insulating material with the momentary switch protruding out of the electrically insulating material so that it can be actuated externally;

the integrated circuit comprising:

means for receiving a supply of electrical power from an electrical power source;

a first connection means arranged to connect the integrated circuit to a load;

an electronic latching switch having input, output and control terminals, wherein the input terminal of the electronic latching switch is coupled to the means for receiving a supply of electrical power and the output terminal of the electronic latching switch is coupled to the first connection means;

the electronic latching switch having a conductive mode and non-conductive mode of operation, the electronic latching switch switching between the conductive and non-conductive mode in response to an actuation of the momentary switch which is coupled to the control terminal, wherein

(i) in the conductive mode, the electronic latching switch couples the means for receiving a supply of electrical power to the first connection means, thereby causing power to be supplied from the said electrical power source from the said load; and

(ii) in the non-conductive mode, the electronic latching switch isolates the means for receiving a supply of electrical power from the first connection means, thereby cutting off the supply of power from the said electrical power source to the said load.

22. A switching structure according to claim 21, wherein the electronic latching switch is arranged to operate in combination with a pulse generation device and a first charge storage device, the arrangement being such that

(i) the electronic latching switch is switched from the non-conductive to the conductive mode by closing the momentary switch to connect the control terminal of the electronic latching switch to the pulse generation device; and

(ii) the electronic latching switch is switched from the conductive to the non-conductive mode by closing the momentary switch to connect the control terminal of the electronic latching switch to the first charge storage device.

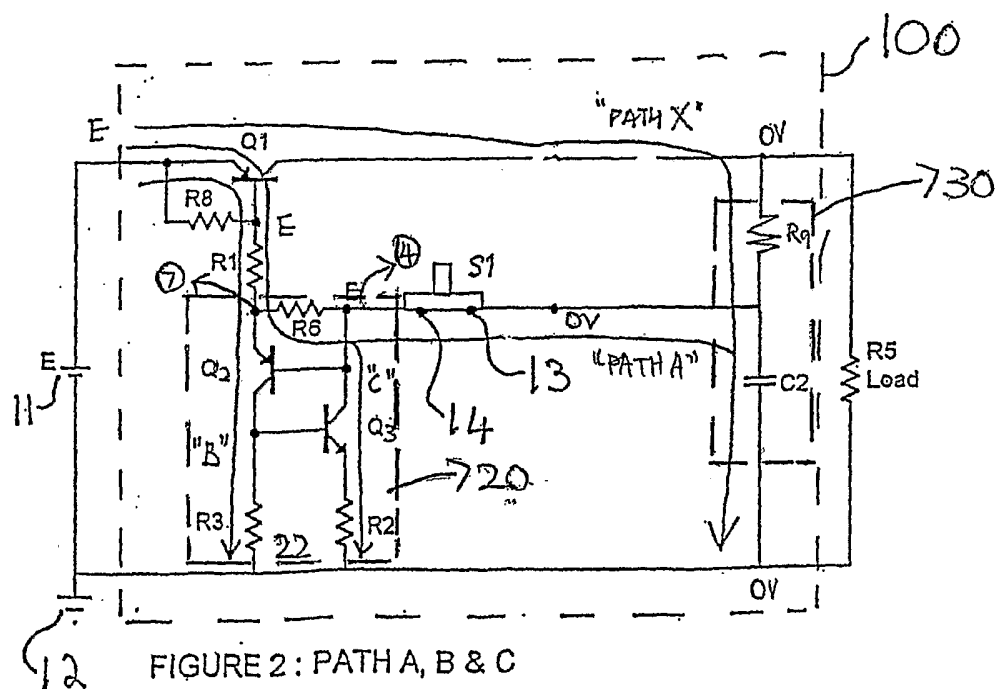
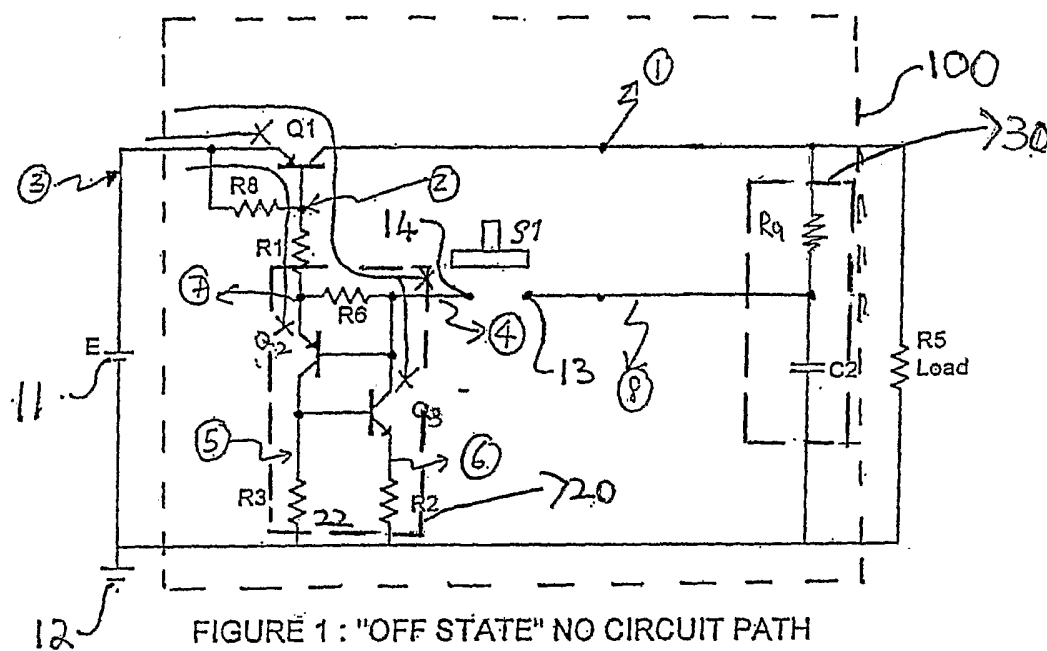
23. A switching structure according to claim 22, wherein the pulse generation device comprises a second charge storage device wherein the first and second charge storage device are located external of the switching structure.

24. A switching structure according to claim 23, wherein the electronic latching switch is adapted to operate in combination with a single charge storage device which functions as both the first and second charge storage device.

25. A switching structure according to claim 24, wherein the electronic latching is adapted to operate in combination with a capacitor which functions as the single charge storage device.

26. A switching structure according to claim 23, wherein the electronic latching switch is adapted to operate in combination with a capacitor which functions as the charge storage device and a combination of a capacitor connected in parallel with a resistor which function as the pulse generation device.

1/8



2/8

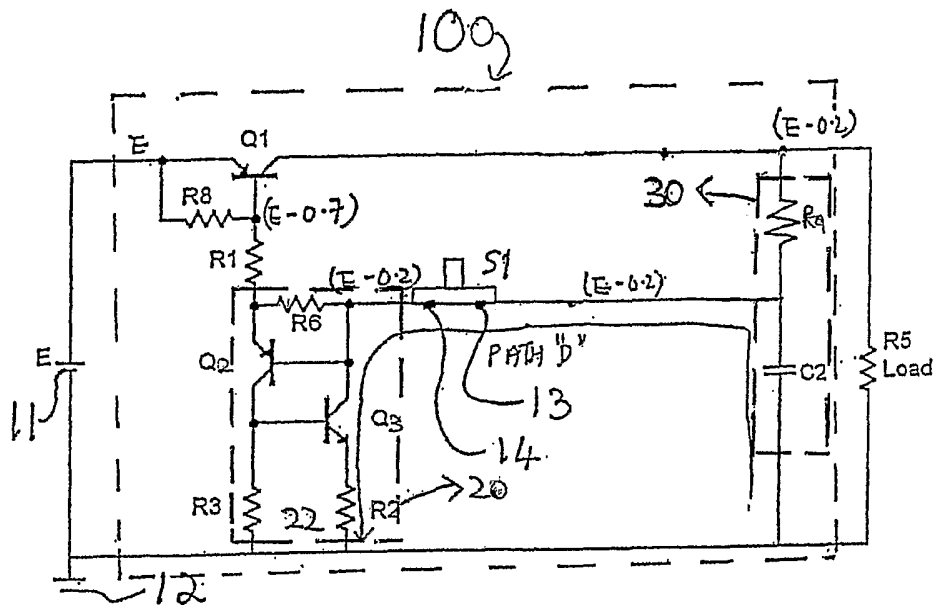


FIGURE 3: PATH D

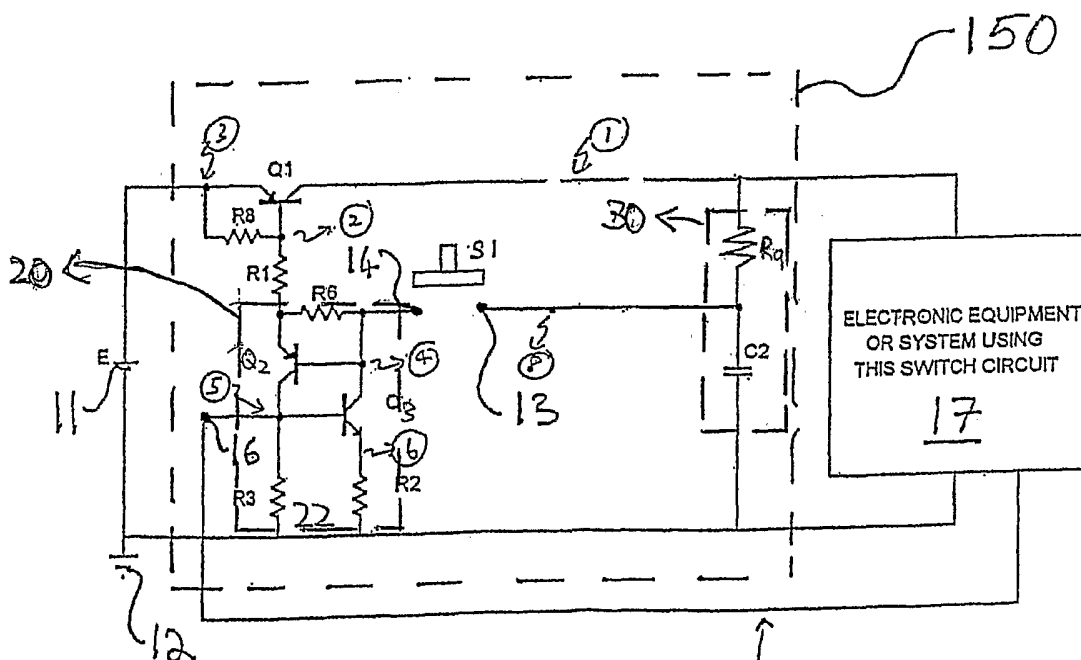


FIGURE 4 AUTOMATIC POWER DOWN WITH ELECTRICAL CONNECTION AT POINT.(5)

3/8

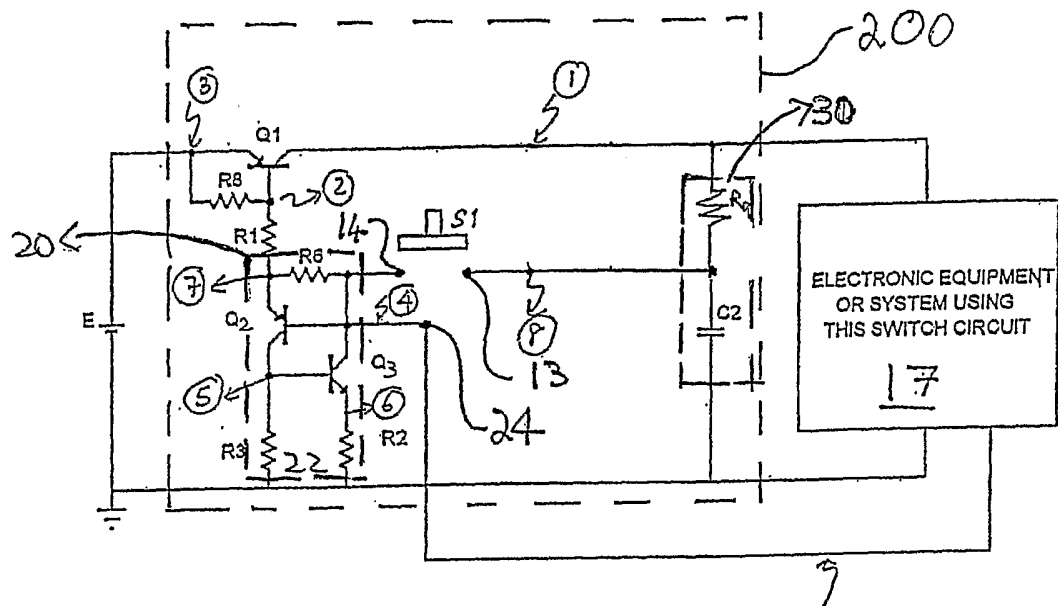


FIGURE 5 : AUTOMATIC POWER-DOWN WITH ELECTRICAL CONNECTION AT POINT (4)

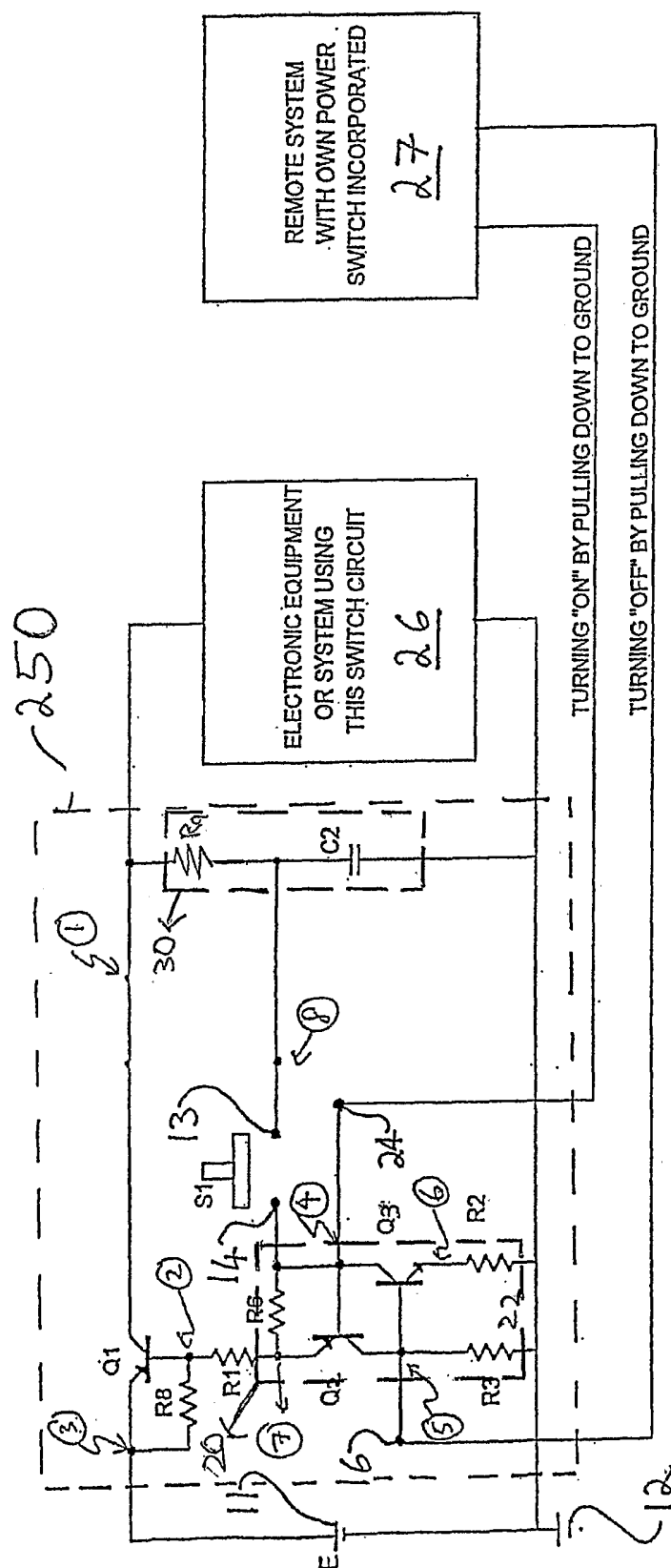


FIGURE 6: REMOTE CONTROL FROM ANOTHER SYSTEM, EXAMPLE ***

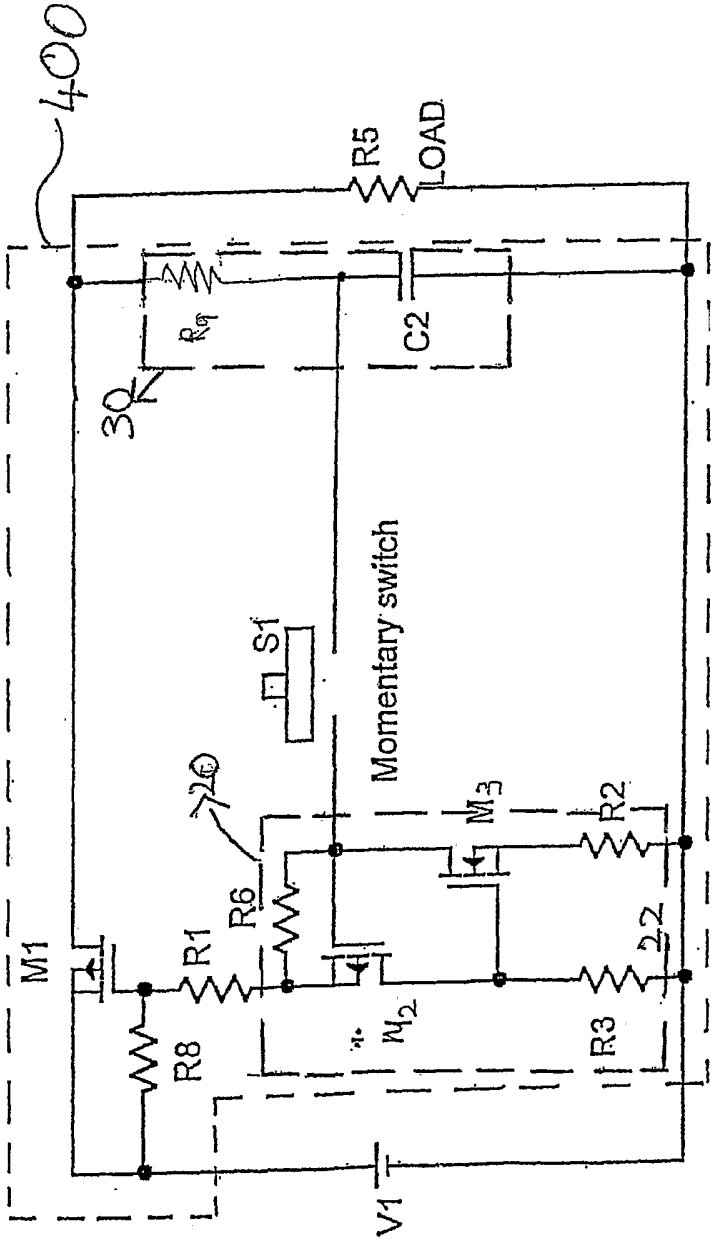


Figure. 9

8/8

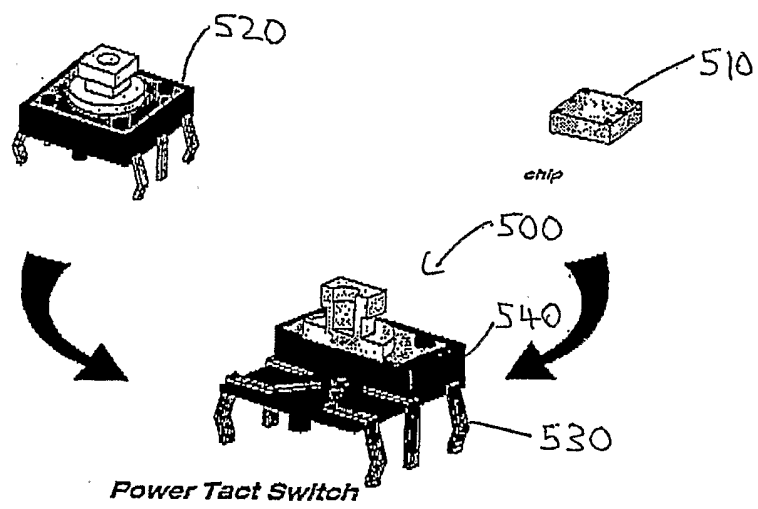


Figure 10

INTERNATIONAL SEARCH REPORT

International application No.

PCT/SG2005/000317

A. CLASSIFICATION OF SUBJECT MATTER		
Int. Cl. ⁷ : H03K 3/00, 17/94		
According to International Patent Classification (IPC) or to both national classification and IPC		
B. FIELDS SEARCHED		
Minimum documentation searched (classification system followed by classification symbols)		
Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched		
Electronic data base consulted during the international search (name of data base and, where practicable, search terms used) WPAT & INTERNET: electrical, supply, load, momentary, activate, charge and similar terms		
C. DOCUMENTS CONSIDERED TO BE RELEVANT		
Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	WO 2001/013492 A1 (MAJEEL LABORATORIES PTE LTD) 22 February 2001 Whole document	1-26
A	JP10-134688 A (MATSUSHITA ELECTRIC WORKS LIMITED) 22 May 1998 English translation found at http://www.ipdl.ncipi.go.jp/homepg_e.ipdl Whole document	
A	JP06-291628 A (TOKIMEC INC) 18 November 1994 English translation found at http://www.ipdl.ncipi.go.jp/homepg_e.ipdl Whole document	
☐ Further documents are listed in the continuation of Box C ☒ See patent family annex		
* "A"	Special categories of cited documents: document defining the general state of the art which is not considered to be of particular relevance	"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention
"E"	earlier application or patent but published on or after the international filing date	"X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone
"L"	document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)	"Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art
"O"	document referring to an oral disclosure, use, exhibition or other means	"&" document member of the same patent family
"P"	document published prior to the international filing date but later than the priority date claimed	
Date of the actual completion of the international search 28 October 2005		Date of mailing of the international search report 15 NOV 2005
Name and mailing address of the ISA/AU AUSTRALIAN PATENT OFFICE PO BOX 200, WODEN ACT 2606, AUSTRALIA E-mail address: pct@ipaustalia.gov.au Facsimile No. (02) 6285 3929		Authorized officer James Williams Telephone No : (02) 6283 2599

INTERNATIONAL SEARCH REPORT

Information on patent family members

International application No.

PCT/SG2005/000317

This Annex lists the known "A" publication level patent family members relating to the patent documents cited in the above-mentioned international search report. The Australian Patent Office is in no way liable for these particulars which are merely given for the purpose of information.

Patent Document Cited in Search Report				Patent Family Member	
WO	0113492	AU	58956/99	US	6873062
JP	10134688		NIL		
JP	06291628		NIL		
Due to data integration issues this family listing may not include 10 digit Australian applications filed since May 2001.					
END OF ANNEX					