



- (51) **International Patent Classification:**
H05K 1/02 (2006.01) *H05K 3/00* (2006.01)
- (21) **International Application Number:**
PCT/US2014/043083
- (22) **International Filing Date:**
19 June 2014 (19.06.2014)
- (25) **Filing Language:** English
- (26) **Publication Language:** English
- (30) **Priority Data:**
13/924,123 21 June 2013 (21.06.2013) US
- (71) **Applicant:** MOTOROLA SOLUTIONS, INC. [US/US];
1303 East Algonquin Road, Schaumburg, Illinois 60196 (US).
- (72) **Inventors:** WALDVOGEL, John M.; 1860 S. Falcon Drive, Libertyville, Illinois 60048 (US). MILLER, Herman J.; 2030 Tunbridge Trail, Algonquin, Illinois 60102 (US).
- (74) **Agents:** BESTOR, Daniel R. et al.; 1301 East Algonquin Road, IL02/SH5, Schaumburg, Illinois 60196 (US).
- (81) **Designated States** (*unless otherwise indicated, for every kind of national protection available*): AE, AG, AL, AM, AO, AT, AU, AZ, BA, BB, BG, BH, BN, BR, BW, BY, BZ, CA, CH, CL, CN, CO, CR, CU, CZ, DE, DK, DM, DO, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, GT, HN, HR, HU, ID, IL, IN, IR, IS, JP, KE, KG, KN, KP, KR, KZ, LA, LC, LK, LR, LS, LT, LU, LY, MA, MD, ME, MG, MK, MN, MW, MX, MY, MZ, NA, NG, NI, NO, NZ, OM, PA, PE, PG, PH, PL, PT, QA, RO, RS, RU, RW, SA, SC, SD, SE, SG, SK, SL, SM, ST, SV, SY, TH, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, ZA, ZM, ZW.
- (84) **Designated States** (*unless otherwise indicated, for every kind of regional protection available*): ARIPO (BW, GH, GM, KE, LR, LS, MW, MZ, NA, RW, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian (AM, AZ, BY, KG, KZ, RU, TJ, TM), European (AL, AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HR, HU, IE, IS, IT, LT, LU, LV,

[Continued on next page]

- (54) **Title:** RADIO FREQUENCY TRANSISTOR AND MATCHING CIRCUIT GROUNDING AND THERMAL MANAGEMENT APPARATUS

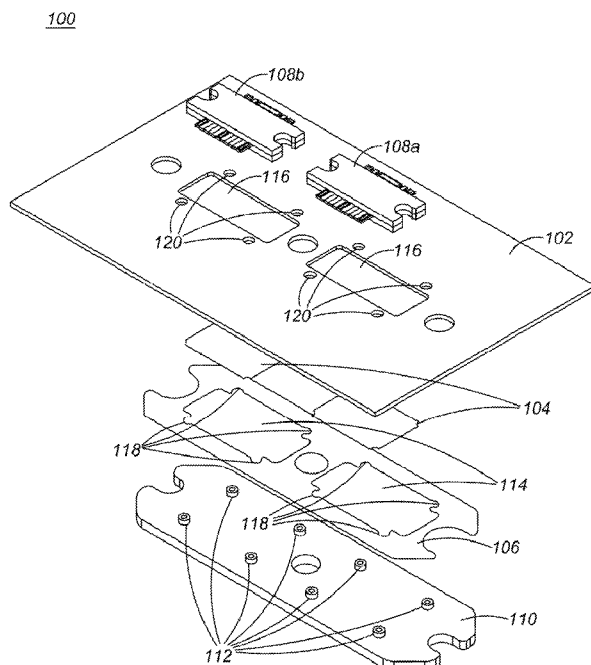


FIG. 1

(57) **Abstract:** In a communication device, a heat sink (110) includes a solderable top surface with multiple upward facing swaging protrusions (112). A spacer (106) is placed on top of the top surface of the heat sink with locating cut-outs (114) on the spacer aligned with the swaging protrusions. A solder pre form (104) is inserted into an opening (114) in the spacer. The solder pre form includes locating features for alignment with the spacer and the swaging protrusions. The spacer is configured to restrict melted flow from the solder pre form to a defined area of the heat sink top surface. A printed circuit board (102) including cut-outs and input and output connections for inserting a radio frequency device (108) and further including locating holes (120) for aligning the printed circuit board with the swaging protrusions is placed on top of the solder pre form and secured to the heat sink prior to a manufacturing process.



MC, MK, MT, NL, NO, PL, PT, RO, RS, SE, SI, SK, **Published:**
SM, TR), OAPI (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, — *with international search report (Art. 21(3))*
GW, KM, ML, MR, NE, SN, TD, TG).

RADIO FREQUENCY TRANSISTOR AND MATCHING CIRCUIT GROUNDING AND THERMAL MANAGEMENT APPARATUS

FIELD OF THE DISCLOSURE

[0001] The present disclosure relates generally to high power amplifier applications and more particularly to a method and apparatus for assembling a grounding and thermal management apparatus for a radio frequency device and matching circuit.

BACKGROUND

[0002] Communications devices such as radios used by public safety agencies, for example, emergency first responder organizations, include one or more power amplification components, such as, radio frequency (RF) devices that transmit high power energy. The RF devices may be, for example, RF transistors connected to a printed circuit board. One of the consequences of high power transmission is a significant amount of heat is dissipated from the RF devices. This heat must be adequately managed while maintaining electrical performance. Therefore, the RF devices and the connected circuit board structure are typically attached to a heat sink with a soldering material. However, current soldering techniques do not provide a specific and repeatable dimension between an input/output lead plane and a ground flange plane in the RF device, circuit board, and heat sink assembly. Therefore, there is no reliable platform for repeatable solder attachment of the RF device terminations in the input/output lead plane and the ground flange plane of the RF device, circuit board, and heat sink assembly.

[0003] In addition to the RF devices, other components, for example, ceramic components may also be connected to the circuit board. The reliability of the soldering material attaching these components may be affected by the heat sink assembly. Therefore, there is a need to address solder fatigue failure in power cycling by restricting the rigid attachment between the RF device/circuit board

structure and the heat sink to an area away from the ceramic components and thereby reduce applied stresses on the ceramic matching component solder joints. This is important for high-power, high-duty cycle infrastructure power amplifiers that are particularly prone to solder joint fatigue failure. Furthermore, there is a need for a soldering technique that is compatible with a high-temperature lead-free solder processing which is a strict requirement for the next generation infrastructure power amplifiers.

[0004] In addition, the grounding structure for the RF device and printed circuit board assembly is important because these communications devices have an RF circuit, rather than simply a direct current (DC) circuit. Hence, it is critical to maintain good ground paths for the bottom of the circuit board, as well as, for the RF devices generating power amplification. Current grounding techniques fail to provide optimal RF grounding of the matching circuit input/output transmission lines on the RF devices and circuit board assembly to the heat sink, i.e., where the RF grounding is as close as possible to the body of RF devices to improve electrical performance. As such, current grounding techniques fail to provide good thermal and grounding paths from the bottom of the RF transistor through the ground flange to heat sink directly below.

[0005] Furthermore, there is a need for an integrated circuit board and heat sink assembly that can be easily processed with standard surface mount technology (SMT) processing, such as solder paste stencil printing, part auto-placement and reflow soldering. There is also a need to lower processing cost with a simplified method for attaching the circuit board and heat sink assembly, thereby eliminating the need for the current specialty process capabilities being used at suppliers.

[0006] Accordingly, there is a need for an improved method and apparatus of assembling a grounding and thermal management apparatus for a radio frequency device and a matching circuit.

BRIEF DESCRIPTION OF THE FIGURES

[0007] The accompanying figures, where like reference numerals refer to identical or functionally similar elements throughout the separate views, together with the detailed description below, are incorporated in and form part of the specification, and serve to further illustrate embodiments of concepts that include the claimed invention, and explain various principles and advantages of those embodiments.

[0008] FIG. 1 is an isometric view of elements of a structure that are assembled in accordance with some embodiments.

[0009] FIG. 2 is a diagram of the alignment components of the structure in accordance with some embodiments.

[0010] FIG. 3 is a diagram of heat sink attachment assembly steps used in accordance with some embodiments.

[0011] FIG. 4 is a diagram of a cross section of a protrusion after it has been swaged in accordance with some embodiments.

[0012] FIG. 5 is an diagram of a structure assembled after a reflow solder process in accordance with some embodiments.

[0013] FIG. 6 is a flow diagram of the steps implemented in accordance with some embodiments.

[0014] FIG. 7 is a block diagram of an apparatus configured to use the assembled structure in accordance with some embodiments.

[0015] Skilled artisans will appreciate that elements in the figures are illustrated for simplicity and clarity and have not necessarily been drawn to scale. For example, the dimensions of some of the elements in the figures may be exaggerated relative to other elements to help to improve understanding of embodiments of the present invention.

[0016] The apparatus and method components have been represented where appropriate by conventional symbols in the drawings, showing only those specific

details that are pertinent to understanding the embodiments of the present invention so as not to obscure the disclosure with details that will be readily apparent to those of ordinary skill in the art having the benefit of the description herein.

DETAILED DESCRIPTION

[0017] Some embodiments are directed to methods and apparatuses including a heat sink, a spacer, a solder pre form, a printed circuit board, and at least one radio frequency device configured to provide power amplification. The heat sink includes a solderable top surface with multiple upward facing swaging protrusions. The spacer is placed on top of the top surface of the heat sink with locating cut-outs on the spacer aligned with the swaging protrusions. The solder pre form is inserted into an opening in the spacer. The solder pre form includes locating features for alignment with the spacer and the swaging protrusions. The spacer is configured to restrict melted flow from the solder pre form to a defined area of the heat sink top surface. The printed circuit board includes cut-outs and input and output connections for inserting the radio frequency device and further includes locating holes for aligning the printed circuit board with the swaging protrusions. The printed circuit board is placed on top of the solder pre form and secured to the heat sink prior to a manufacturing process in which the at least one radio frequency device is placed in the printed circuit board and attached via reflow soldering.

[0018] FIG. 1 is an isometric view of elements of a structure that are assembled in accordance with some embodiments. The elements include a printed circuit board (PCB) 102, solid solder pre forms 104, a spacer 106, one or more radio frequency (RF) devices 108 (i.e., RF devices 108a and 108b), and at least one heat sink 110. PCB 102 includes cut-outs 116 and input and output connections for the RF devices 108. The solder pre forms 104 and the spacer 106 are located and clamped between the heat sink 110 and the PCB 102 during a heat sink

attachment process, where the assembly is facilitated by an alignment fixture (not shown). The RF device(s) 108 may be, for example, RF transistors used for power amplification and thereby dissipate a high amount of heat that needs to be managed and put into the heat sink 110.

[0019] The heat sink 110 (also referred to herein as a heat spreader 110 or a coin 110) is a mostly flat component, constructed of a high electrically and thermally conductive material, for example, malleable copper. At least the top surface of heat spreader 110 is solderable and includes multiple integral swaging protrusions 112. The protrusion may be, for example, hollow cylindrical objects. The surface of the heat spreader 110 may be made solderable using a suitable plating process. Heat dissipated from the RF device(s) 108, which during subsequent surface mount technology assembly is inserted into the cut-outs 116 of PCB 102 and reflow soldered to heat spreader 110, is transferred from the RF device(s) 108 to the heat spreader 110, allowing for thermal management.

[0020] The spacer 106 (also referred to as a shim 106) is a structure with multiple apertures including, solid solder pre form openings 114 and locating cut outs 118. The spacer 106 is constructed of a solder non-wetting material, for example, PCB laminate or stainless steel of uniform thickness and good dimensional stability. During assembly, the spacer 106 is placed on top of the heat sink 110 with the locating cut outs 118 on the spacer 106 aligned with the swaging protrusions 112 of the heat sink 110. The locating cut outs 118 of the spacer 106 therefore lend the spacer 106 to low cost manufacturing. In addition, the spacer 106 allows a designer of the structure 100 to set a particular height dimension for the structure 100.

[0021] The relatively thin solid solder pre forms 104 include locating features so that the solder pre forms 104 can be properly aligned within the solder pre form openings 114 of the spacer 106 and located using the swaging protrusions 112 of the heat sink 110. The solder pre forms 104 thickness may be the same as or slightly less than that of the spacer 106. In some embodiments, the solder pre

forms 104 are slightly smaller than the solder pre form openings 114, but it are larger than the cut-outs 116 in the PCB 102 for the RF device(s) 108. This allows the spacer 106 to restrict the flow of molten solder from the pre forms 104 during a reflow soldering process to defined areas of the heat sink 110 surface and forces the solder attachment to occur in a clearly defined area, i.e., the area defined by the solder pre form openings 114.

[0022] The bottom of the PCB 102 is a ground plane. The ground of the PCB 102 and the RF devices 108 are linked together when the RF devices 108 are inserted into the cut-outs 116 of the PCB 102. Once the structure 100 goes through a reflow solder process, the solder pre forms 104 will melt and will attach the PCB 102 with the attached RF devices 108 to the heat spreader 110, thereby attaching the ground plane of the PCB 102 to the heat spreader 110. This will maintain the thermal management path from the RF devices 108 to the heat spreader 110 and provides a good quality ground from the RF device 108 to the heat spreader 110 and from the PCB 102 to the heat spreader 110.

[0023] During assembly of the structure 100, the heat sink 110 is placed with the protrusions 112 facing upwards. The spacer 106 is placed over the heat sink 110 and the locating cut outs 118 on the spacer 106 aligned with the swaging protrusions 112 of the heat sink 110. The solder pre forms 104 are then placed on the solder pre form openings 114 of the spacer 106, wherein locating features on the solder pre form 104 are also aligned with the swaging protrusions 112 of the heat sink 110. PCB 102 is placed on top of the solder pre forms 104, spacer 106 and heat sink 110 assembly, with the ground plane side of the PCB 102 oriented downward. Locating holes 120 in the PCB properly align the PCB 102 with the heat sink protrusions 112. The locating features 120 in the PCB 102, the locating features in the solder pre forms 104, and the locating cut outs 118 in the spacer 106 ensure the cut-outs 116 in the PCB 102 are properly located with respect to the solder pre forms. That is, each solder pre form 104 spans the cut-outs 116 for

the RF devices 108 and extends under the PCB ground plane in close proximity to the eventual placement of an RF device.

[0024] During assembly, the PCB 102 is clamped down on the solder pre forms 104, spacer 106 and heat sink 110 assembly using, for example, a simple fixture. The heat sink attachment is affected by forcibly inserting a swaging tool in the hollow swaging protrusions 112. This process forces the cylindrical protrusion material outward against the walls of the PCB locating holes 120, forming a secure attachment which easily withstands lead-free solder reflow processing. The heights of the swaging protrusions 112 are optimized such that the resulting attachment feature is entirely sub-flush to the top of the PCB surface, thus permitting subsequent surface mount technology (SMT) processing, for example, solder paste stencil printing and part placement. After swaging, the structure 100 is integrated such that it can be handled. For example, the structure 100 can be assembled at a PCB fabricator and shipped to an SMT production line for part population and reflow. The alignment and swaging processes do not require special capabilities and can be easily completed using simple tools.

[0025] The uniform thickness of the spacer 106 creates repeatable dimension from the top of the heat sink to the top of the PCB 102, a dimension which can be adjusted easily for multiple applications by adjusting the spacer thickness. Control of this dimension is critical to maintaining high PCB SMT production yields; i.e., insuring that the RF devices 108 leads and bottom ground flange are all sufficiently soldered. The solder pre form openings 114, the solder non-wetting nature of the spacer 106, and the fact that the spacer is clamped between the heat sink and the PCB ground plane deliver a repeatable means to produce an optimal electrical attachment of the RF transistor input/output transmission line ground plane to the heat sink. When the assembled structure 100 passes through a reflow oven, the solder pre forms 104 melt and simultaneously forms solder connections between the RF transistor 108 ground flange and the heat sink 110, as well as the matching circuit PCB ground plane and the heat sink. The spacer

opening geometry and non-wetting nature performs as a solder mask to restrict the solder attachment to the optimal area adjacent to the RF transistor.

[0026] FIG. 2 is a diagram of the alignment components of the structure in accordance with some embodiments. FIG. 2 shows the swaging protrusions 112 of the heat sink 110, locating cut outs 118 on the spacer 106 that aligned with the swaging protrusions 112 of the heat sink 110, locations 202 on a solder pre form 104 that align the solder pre form 104 with the locating cut outs 118 on the spacer 106, and locating holes 120 on the PCB 102 that align with the swaging protrusions 112 of the heat sink 110. The locating cut outs 118 on the spacer 106 help to define an area where the solder pre form 104 is to be placed. Solderable pads on the top of the PCB 102 align with the input/output of the RF devices.

[0027] FIG. 3 is a diagram of heat sink assembly steps used in accordance with some embodiments. In step 301, the heat sink 110 is placed on a fixture/assembly template 302, with the swaging protrusions 112 facing upwards. The fixture template 302 includes alignment pins 304 to hold the heat sink 112 stationary on the fixture template 302. In step 305, the spacer 106 is placed on top of the heat sink 110, using the swaging protrusions 112 as an alignment guide and the solder pre-forms 104 are placed in the solid solder pre form openings 114 in the spacer 106, using the protrusion 112 for alignment. In step 306, the PCB 102 is placed on top of the assembly, where the locating holes 120 on the PCB 102 are aligned with the swaging protrusion 112. The swaging protrusions 112 fit loosely through locating holes 120 on the PCB 102. In step 307, an assembly clamp 309 is used to hold the PCB 102 tightly with the heat sink 110 and a swaging tool 310 is used to force the swaging protrusion 112 out radially so that the PCB 102 is permanently attached to the heat sink 110, creating a circuit board assembly that can withstand a manufacturing/solder melting process with the components held in place tightly without a need for extra fixturing.

[0028] The swaging protrusions 112 do not extend above the PCB 102 because the swaging protrusions are designed to be relatively short to fit below the top of

the PCB 102 surface but strong enough to hold the assembly together during the manufacturing process. This allows the structure to be planar and suitable for SMT manufacturing. It should be noted that other materials such as screws or adhesives may be used to hold the assembly together although any material that extends above the surface of the PCB 102 may complicate the manufacturing process.

[0029] FIG. 4 is a diagram of a cross section of a protrusion 112 after it has been swaged in accordance with some embodiments. When the PCB 102 is clamped to the assembly, the protrusion 112 is forced outward to come in contact with the PCB 102, but does not extend above the surface 402 of the PCB 102.

[0030] FIG. 5 is a diagram of a structure assembled after a reflow solder process in accordance with some embodiments. Blocks 502 shows areas where the solder pre form 104 melted during the manufacturing process. The areas shown by blocks 502 have a superior ground attachment between the top of the heat sink 110 and bottom of the PCB 102 and RF device 108. During reflow soldering, the spacer 106 restricted the flow of the solder, as defined by the areas shown in blocks 502 so that the RF ground attachment is focused where it is needed, i.e., close to the RF transistor as shown by the area labeled “RF transistor ground attach.” The solder pre form is prevented from spreading outside the region where it is needed so that other components on the PCB that do not need the rigid solder attachment remain reliable. The assembled structure therefore allows for excellent RF grounding, good thermal management and preserves the reliability of the structure by not having the solder attachment where it is not needed. The disclosed assembly is compatible with processing of lead free solder.

[0031] FIG. 6 is a flow diagram of the steps implemented in accordance with some embodiments. At step 605, a heat sink, such as heat sink 110, is placed on an assembly template, with the swaging protrusions, such as protrusions 112, of the heat sink facing upwards. The assembly template includes alignment pins to hold the heat sink stationary on the assembly template. In step 610, a spacer, such

as spacer 106, is placed on top of the heat sink, using the swaging protrusions of the heat sink as an alignment guide. At step 615, solder pre-forms, such as solder pre forms 104, are placed within openings in the spacer, using the protrusion of the heat sink for alignment. At step 620, a PCB, such as PCB 102, is placed on top of the assembly such that locating holes on the PCB are aligned with the swaging protrusion of the heat sink. The swaging protrusions fit loosely through the locating holes, such as locating holes 120, on the PCB. At step 625, an assembly clamp is used to hold the PCB tightly with the heat sink. At step 630, a swaging tool is used to force the swaging protrusion out radially so that the PCB is permanently attached to the heat sink, creating a circuit board assembly that can withstand a manufacturing/solder melting process with the components held in place tightly without a need for extra fixturing.

[0032] FIG. 7 is a block diagram of a communication device 700 that is configured to use the assembled structure in accordance with some embodiments. The communication device may be, for example, a cell phone, a smart phone, a mobile radio, a base station, an access point, or any other communication device configured to incorporate an RF device inserted into a PCB. The communication device includes a communications unit 702 coupled to a common data and address bus 717 of a processing unit 703. The communication device may also include an input unit (e.g., keypad, pointing device, etc.) 706, an output transducer unit (e.g., speaker) 720, an input transducer unit (e.g., a microphone) (MIC) 721, and a display screen 705, each coupled to be in communication with the processing unit 703.

[0033] The processing unit 703 may include an encoder/decoder 711 with an associated code ROM 712 for storing data for encoding and decoding voice, data, control, or other signals that may be transmitted or received by the communication device. The processing unit 703 may further include a microprocessor 713 coupled, by the common data and address bus 717, to the encoder/decoder 711, a character ROM 714, a RAM 704, and a static memory 716.

The processing unit 703 may also include a digital signal processor (DSP) 719, coupled to the speaker 720, the microphone 721, and the common data and address bus 717, for operating on audio signals received from one or more of the communications unit 702, the static memory 716, and the microphone 721.

[0034] The communications unit 702 may include an RF interface 709 configurable to communicate with network components, and other user equipment within its communication range. The communications unit 702 may include one or more broadband and/or narrowband transceivers 708, such as an Long Term Evolution (LTE) transceiver, a Third Generation (3G) (3GPP or 3GPP2) transceiver, an Association of Public Safety Communication Officials (APCO) Project 25 (P25) transceiver, a Digital Mobile Radio (DMR) transceiver, a Terrestrial Trunked Radio (TETRA) transceiver, a WiMAX transceiver perhaps operating in accordance with an IEEE 802.16 standard, and/or other similar type of wireless transceiver configurable to communicate via a wireless network for infrastructure communications. The communications unit 702 may include one or more local area network or personal area network transceivers such as Wi-Fi transceiver perhaps operating in accordance with an IEEE 802.11 standard (e.g., 802.11a, 802.11b, 802.11g), or a Bluetooth transceiver, for subscriber device to subscriber device communications. The transceivers may be configured to include the assembly described in FIG. 1, 3 and 6 and may be coupled to a combined modulator/demodulator 710 that is coupled to the encoder/decoder 711. The character ROM 714 stores code for decoding or encoding data such as control, request, or instruction messages, channel change messages, and/or data or voice messages that may be transmitted or received by the communication device. Static memory 716 may store operating code associated with operating the communication device.

[0035] In the foregoing specification, specific embodiments have been described. However, one of ordinary skill in the art appreciates that various modifications and changes can be made without departing from the scope of the invention as set

forth in the claims below. Accordingly, the specification and figures are to be regarded in an illustrative rather than a restrictive sense, and all such modifications are intended to be included within the scope of present teachings.

[0036] The benefits, advantages, solutions to problems, and any element(s) that may cause any benefit, advantage, or solution to occur or become more pronounced are not to be construed as a critical, required, or essential features or elements of any or all the claims. The invention is defined solely by the appended claims including any amendments made during the pendency of this application and all equivalents of those claims as issued.

[0037] Moreover in this document, relational terms such as first and second, top and bottom, and the like may be used solely to distinguish one entity or action from another entity or action without necessarily requiring or implying any actual such relationship or order between such entities or actions. The terms "comprises," "comprising," "has," "having," "includes," "including," "contains," "containing" or any other variation thereof, are intended to cover a non-exclusive inclusion, such that a process, method, article, or apparatus that comprises, has, includes, contains a list of elements does not include only those elements but may include other elements not expressly listed or inherent to such process, method, article, or apparatus. An element preceded by "comprises ...a", "has ...a", "includes ...a", "contains ...a" does not, without more constraints, preclude the existence of additional identical elements in the process, method, article, or apparatus that comprises, has, includes, contains the element. The terms "a" and "an" are defined as one or more unless explicitly stated otherwise herein. The terms "substantially", "essentially", "approximately", "about" or any other version thereof, are defined as being close to as understood by one of ordinary skill in the art, and in one non-limiting embodiment the term is defined to be within 20%, in another embodiment within 5%, in another embodiment within 1% and in another embodiment within 0.5%. The term "coupled" as used herein is defined as connected, although not necessarily directly and not necessarily

mechanically. A device or structure that is “configured” in a certain way is configured in at least that way, but may also be configured in ways that are not listed.

[0038] The Abstract of the Disclosure is provided to allow the reader to quickly ascertain the nature of the technical disclosure. It is submitted with the understanding that it will not be used to interpret or limit the scope or meaning of the claims. In addition, in the foregoing Detailed Description, it can be seen that various features are grouped together in various embodiments for the purpose of streamlining the disclosure. This method of disclosure is not to be interpreted as reflecting an intention that the claimed embodiments require more features than are expressly recited in each claim. Rather, as the following claims reflect, inventive subject matter lies in less than all features of a single disclosed embodiment. Thus the following claims are hereby incorporated into the Detailed Description, with each claim standing on its own as a separately claimed subject matter.

Claims

We claim:

1. An apparatus comprising,
 - a heat sink including a solderable top surface with multiple upward facing swaging protrusions;
 - a spacer placed on top of the top surface of the heat sink with locating cut-outs on the spacer aligned with the swaging protrusions;
 - a solder pre form inserted into an opening in the spacer, the solder pre form including locating features for alignment with the spacer and the swaging protrusions, the spacer being configured to restrict melted flow from the solder pre form to a defined area of the heat sink top surface; and
 - a printed circuit board including cut-outs and input and output connections for inserting at least one radio frequency device configured to provide power amplification and further including locating holes for aligning the printed circuit board with the swaging protrusions, the printed circuit board being placed on top of the solder pre form and secured to the heat sink prior to a manufacturing process in which the radio frequency device is attached.
2. The apparatus of claim 1, wherein the heat sink is attached to the printed circuit board and the radio frequency device attachment by inserting a swaging tool in the swaging protrusions, thereby forcing a protrusion material outward against walls of the locating holes in the printed circuit board and beneath a top surface of the printed circuit board.
3. The apparatus of claim 1, wherein the locating holes in the printed circuit board, the locating cut-outs in the spacer and the locating features in the solder pre

form ensure that the cut-outs in the printed circuit board are properly located with respect to the solder pre form.

4. The apparatus of claim 1, wherein the bottom of the printed circuit board is a ground plane, wherein the ground of the printed circuit board and the ground of the at least one attached radio frequency device are linked together when the at least one radio frequency device is inserted into the cut-outs of the printed circuit board.

5. The apparatus of claim 1, wherein the solder pre form is configured to melt during the manufacturing process and attach the printed circuit board and the radio frequency device to the heat sink, wherein the solder pre form attaches a ground plane of the printed circuit board to the heat sink.

6. The apparatus of claim 1, wherein the solder pre form is smaller than the opening in the spacer and the solder pre form is of a thickness the same as or less than the thickness of the spacer.

7. The apparatus of claim 1, wherein heat dissipated from the at least one radio frequency device is transferred to the heat sink.

8. The apparatus of claim 1, wherein the spacer is constructed of a solder non-wetting material of a uniform thickness.

9. The apparatus of claim 1, wherein the swaging protrusions are cylindrical objects.

10. The apparatus of claim 1, wherein the heat sink is constructed with an electrically and thermally conductive material.

11. A method comprising,
- placing a heat sink including a solderable top surface with multiple upward facing swaging protrusions on a fixture;
 - locating a spacer on top of the top surface of the heat sink with locating cut-outs on the spacer aligned with the swaging protrusions;
 - inserting a solder pre form into an opening in the spacer, the solder pre form including locating features for alignment with the spacer and the swaging protrusions, the spacer being configured to restrict melted flow from the solder pre form to a defined area of the heat sink top surface;
 - placing a printed circuit board on top of the solder pre form, the printed circuit board including cut-outs and input and output connections for at least one radio frequency device configured to provide power amplification and further including locating holes for aligning the printed circuit board with the swaging protrusions; and
 - securing the printed circuit board to the heat sink prior to a manufacturing process.
12. The method of claim 11, wherein the securing comprises attaching the heat sink to the printed circuit board by inserting a swaging tool in the swaging protrusions, thereby forcing a protrusion material outward against walls of the locating holes in the printed circuit board and beneath a top surface of the printed circuit board.
13. The method of claim 11, further comprising linking a ground plane of the printed circuit board and the ground of the at least one radio frequency device together when the at least one radio frequency device is inserted into the cut-outs of the printed circuit board.

14. The method of claim 11, further comprising melting the solder pre form during the manufacturing process and attaching the printed circuit board including the at least one inserted radio frequency device to the heat sink with the melted solder pre form, wherein the melted solder pre form attaches a ground plane of the printed circuit board to the heat sink.

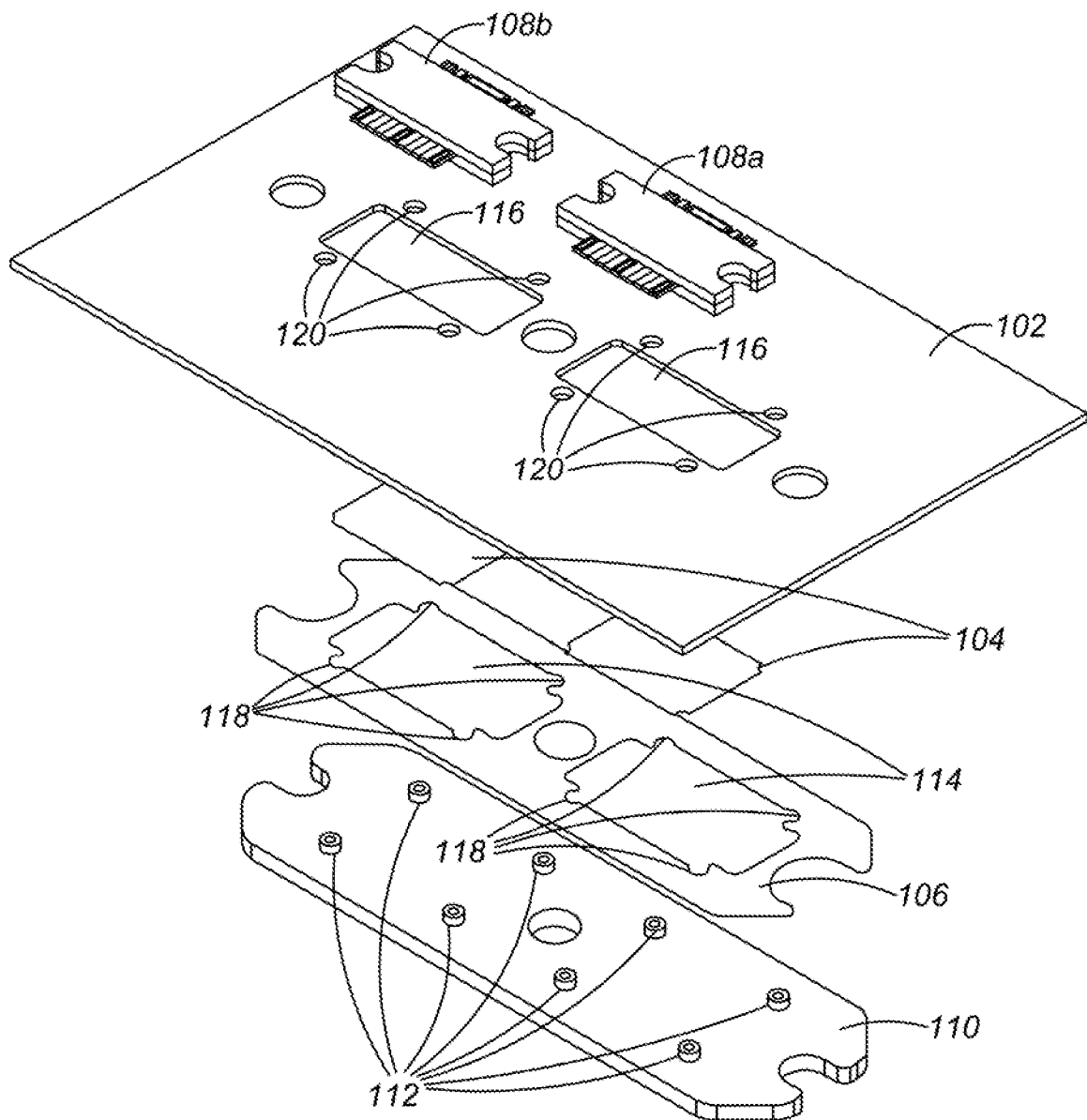
15. The method of claim 11, wherein the spacer is constructed of a solder non-wetting material of a uniform thickness.

16. The method of claim 11, wherein the swaging protrusions are cylindrical objects.

17. The method of claim 11, wherein the heat sink is constructed with an electrically and thermally conductive material.

18. A communication device comprising,
a communication unit including a transceiver configured to perform radio frequency transmission;
a processor configured to process voice, data, control, or other signals transmitted to or received by the communication device, and
a memory configured to store operating code associated with operating the communication device,
the transceiver including a heat sink including a solderable top surface with multiple upward facing swaging protrusions, a spacer placed on top of the top surface of the heat sink with locating cut-outs on the spacer aligned with the swaging protrusions, a solder pre form inserted into an opening in the spacer, the solder pre form including locating features for alignment with the spacer and the swaging protrusions, the spacer being configured to restrict melted flow from the solder pre form to a defined area of the heat sink top surface, and a printed circuit

board including cut-outs and input and output connections for inserting at least one radio frequency device configured to provide power amplification and further including locating holes for aligning the printed circuit board with the swaging protrusions, the printed circuit board being placed on top of the solder pre form and secured to the heat sink prior to a manufacturing process in which the radio frequency device is attached.

*1/7*100*FIG. 1*

2/7

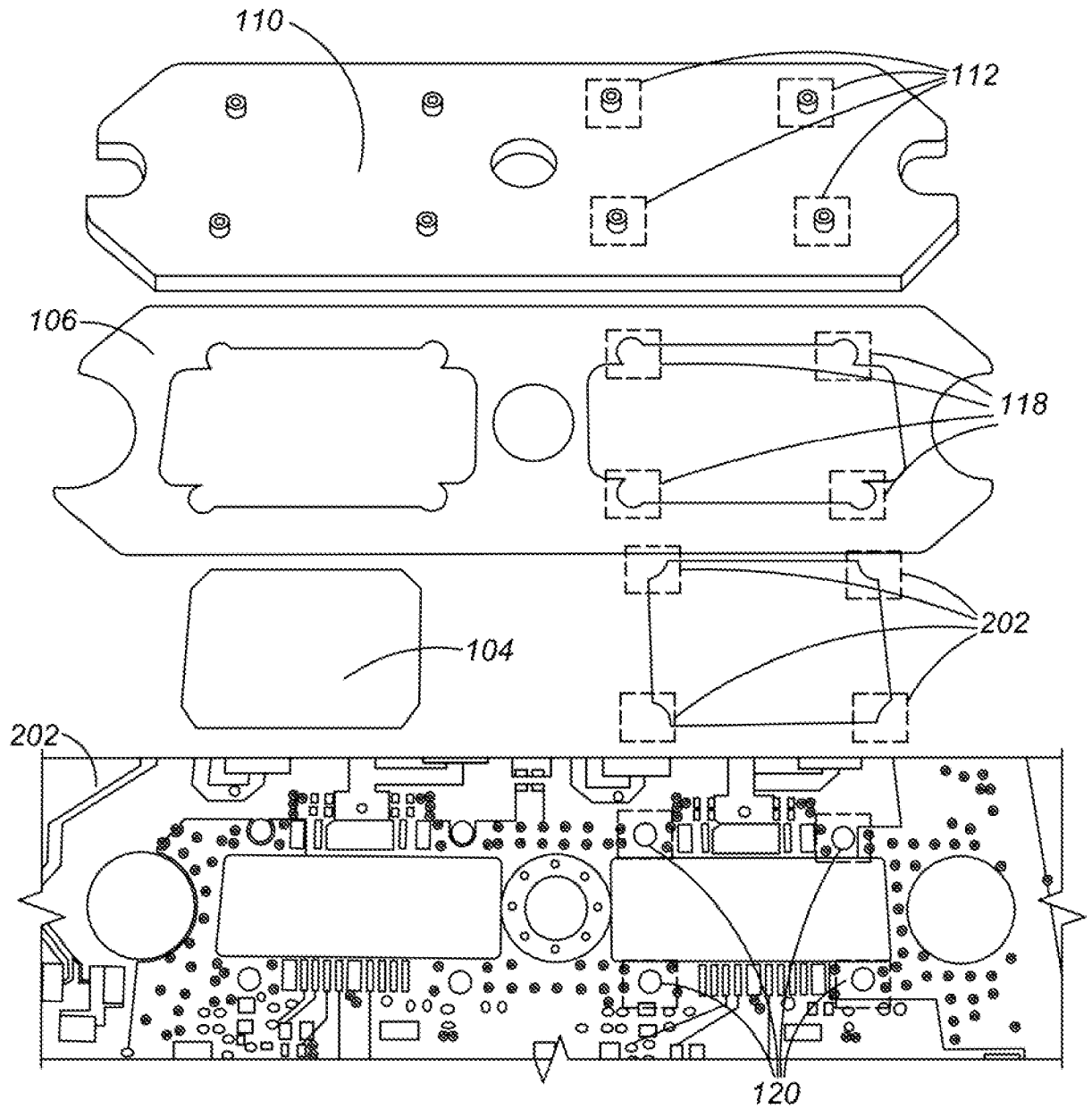


FIG. 2

3/7

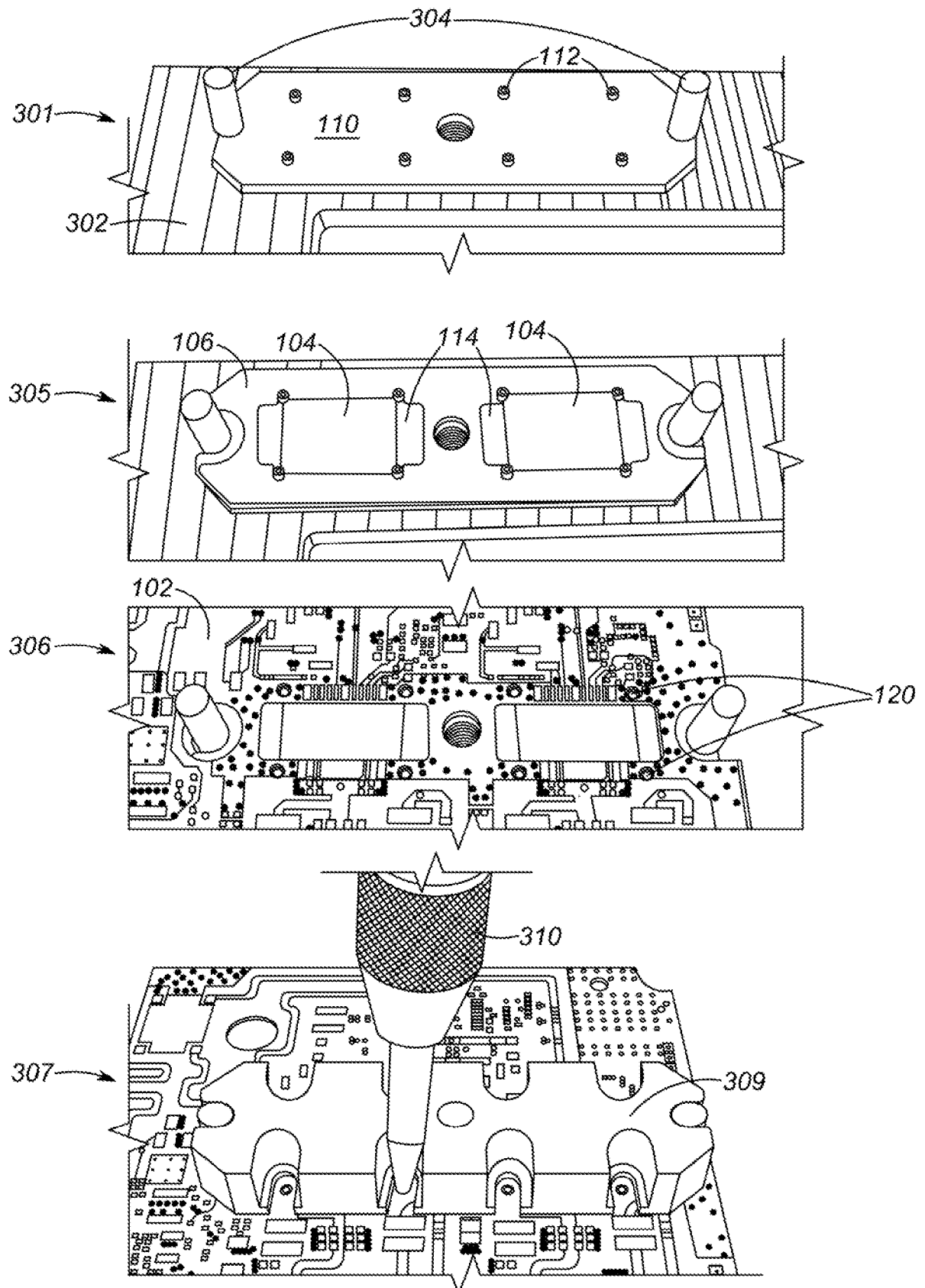


FIG. 3

4/7

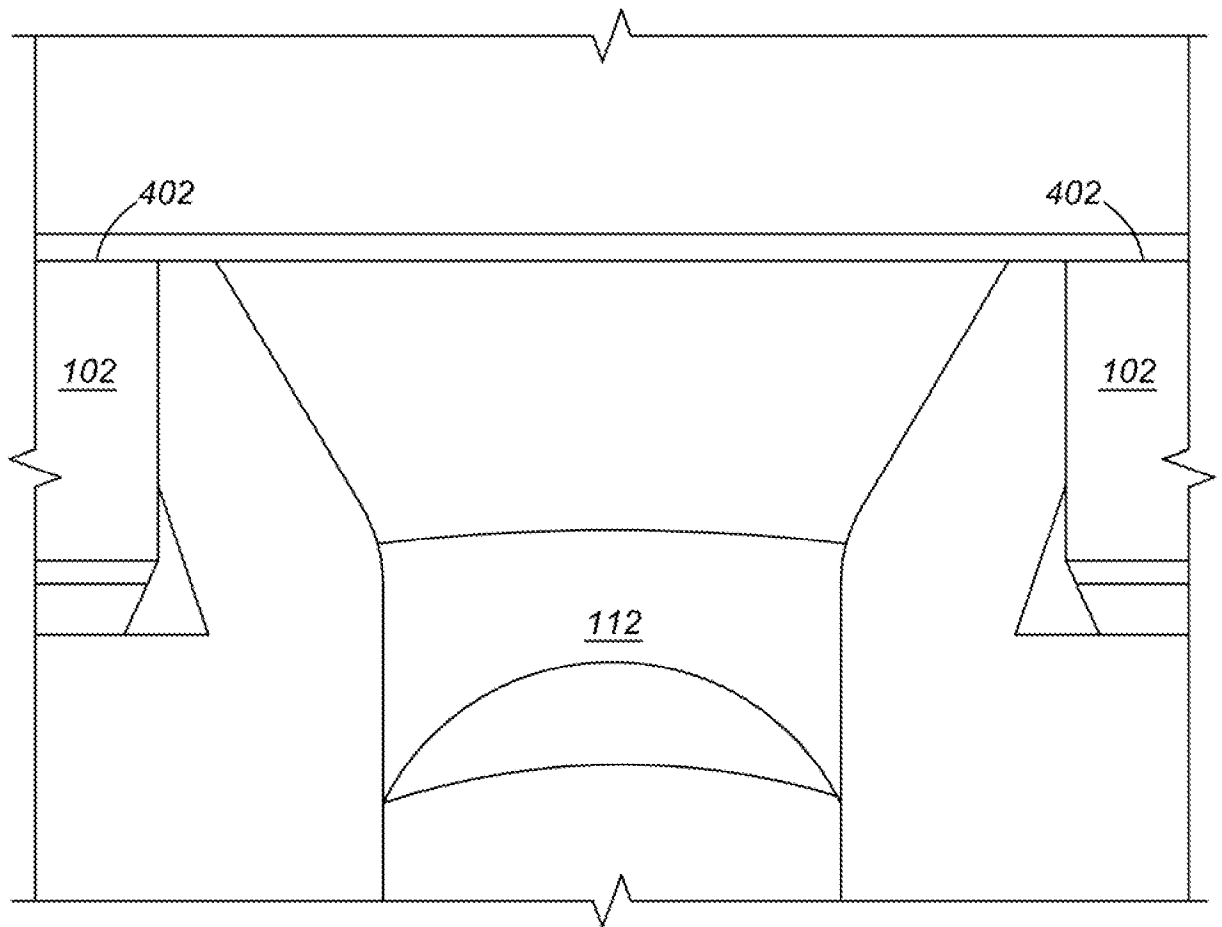


FIG. 4

5/7

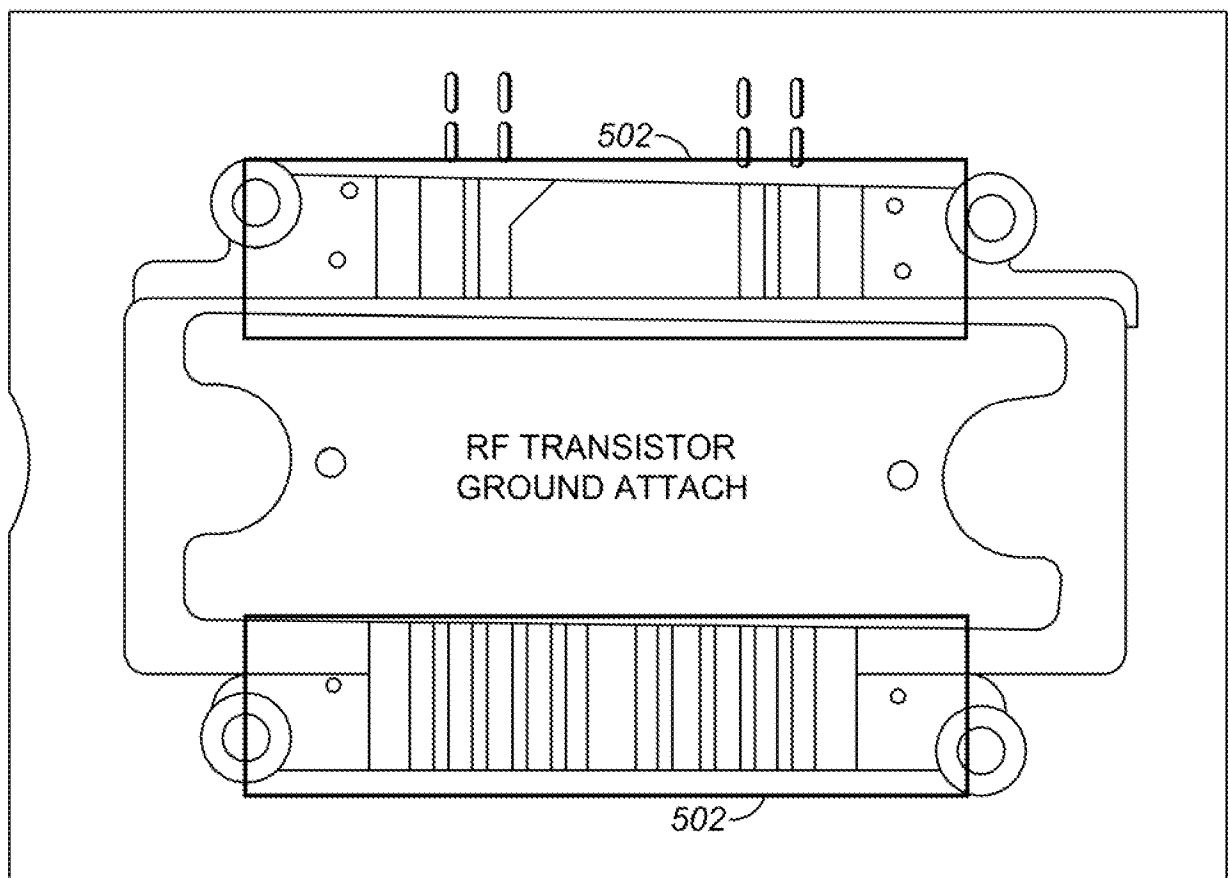


FIG. 5

6/7

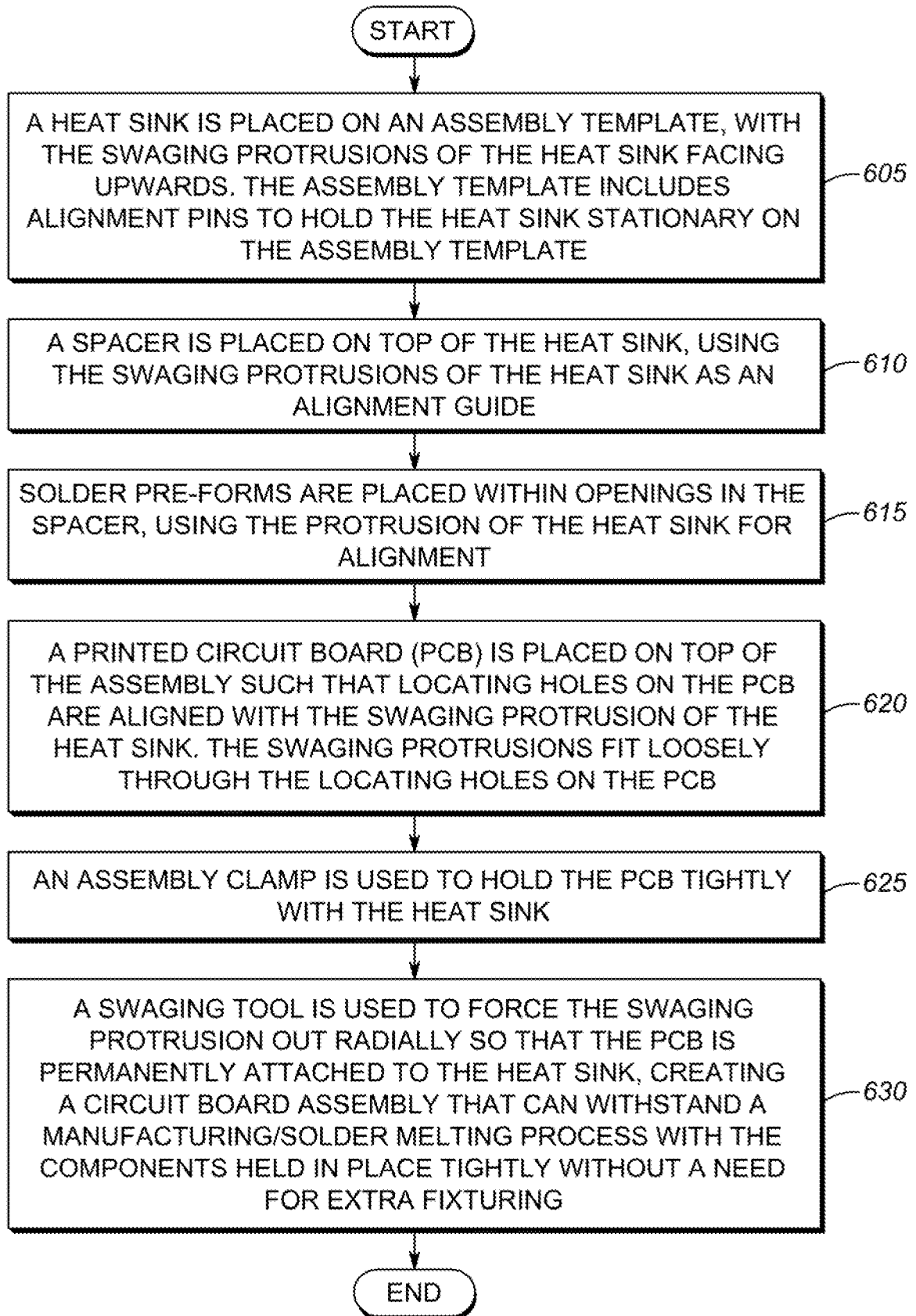


FIG. 6

7/7

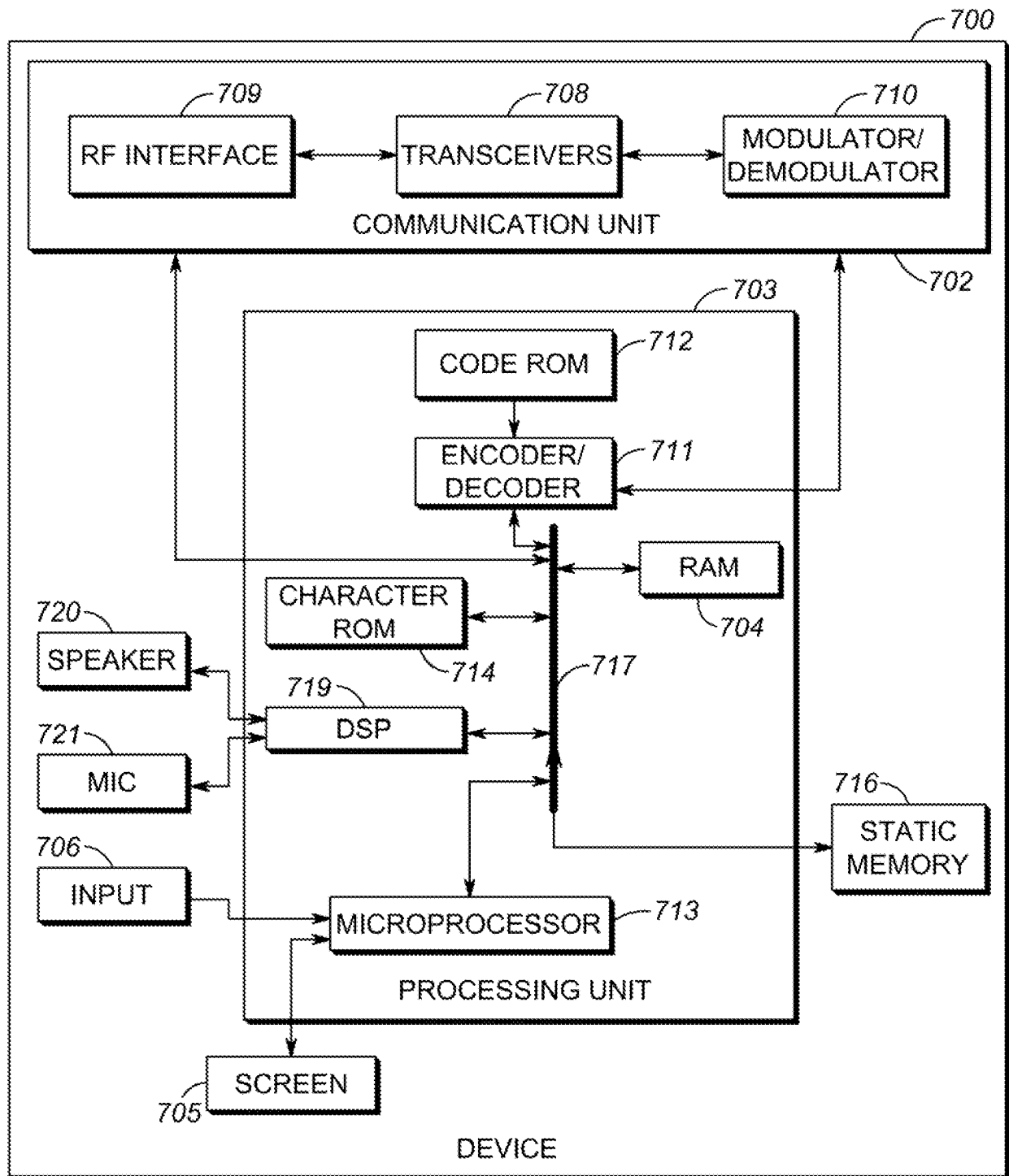


FIG. 7

INTERNATIONAL SEARCH REPORT

International application No
PCT/US2014/043083

A. CLASSIFICATION OF SUBJECT MATTER
INV. H05K1/02 H05K3/00
ADD.

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)
H05K

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

EPO-Internal, WPI Data

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	US 6 842 341 B1 (WALDVOGEL JOHN M [US] ET AL) 11 January 2005 (2005-01-11) the whole document -----	1-18
A	US 6 235 991 B1 (JOHNSON MICHAEL GUNNAR [US]) 22 May 2001 (2001-05-22) column 1, line 10 - column 2, line 54 column 6, line 38 - column 7, line 25; figure 6 -----	1-18
A	US 6 347 038 B1 (DUARTE MARC [FR] ET AL) 12 February 2002 (2002-02-12) column 4, lines 57-64; figures 5-7 column 5, lines 18-21; figure 9 -----	1-18



Further documents are listed in the continuation of Box C.



See patent family annex.

* Special categories of cited documents :

"A" document defining the general state of the art which is not considered to be of particular relevance

"E" earlier application or patent but published on or after the international filing date

"L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)

"O" document referring to an oral disclosure, use, exhibition or other means

"P" document published prior to the international filing date but later than the priority date claimed

"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention

"X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone

"Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art

"&" document member of the same patent family

Date of the actual completion of the international search

15 September 2014

Date of mailing of the international search report

06/10/2014

Name and mailing address of the ISA/

European Patent Office, P.B. 5818 Patentlaan 2
NL - 2280 HV Rijswijk
Tel. (+31-70) 340-2040,
Fax: (+31-70) 340-3016

Authorized officer

Degroote, Bart

INTERNATIONAL SEARCH REPORT

Information on patent family members

International application No

PCT/US2014/043083

Patent document cited in search report	Publication date	Patent family member(s)	Publication date
US 6842341	B1	11-01-2005	CN 1864450 A 15-11-2006
			EP 1671524 A2 21-06-2006
			MY 136905 A 28-11-2008
			TW I250832 B 01-03-2006
			US 6842341 B1 11-01-2005
			US 2005092814 A1 05-05-2005
			WO 2005039254 A2 28-04-2005

US 6235991	B1	22-05-2001	NONE

US 6347038	B1	12-02-2002	DE 60029527 T2 08-03-2007
			EP 1087652 A1 28-03-2001
			ES 2269083 T3 01-04-2007
			FR 2798814 A1 23-03-2001
			JP 4511705 B2 28-07-2010
			JP 2001135963 A 18-05-2001
			US 6347038 B1 12-02-2002
