



(51) International Patent Classification:
H01L 21/203 (2006.01)

(21) International Application Number:
PCT/US2015/061826

(22) International Filing Date:
20 November 2015 (20.11.2015)

(25) Filing Language: English

(26) Publication Language: English

(30) Priority Data:
62/085,009 26 November 2014 (26.11.2014) US
14/607,273 28 January 2015 (28.01.2015) US

(71) Applicant: **APPLIED MATERIALS, INC.** [US/US];
3050 Bowers Avenue, Santa Clara, California 95054 (US).

(72) Inventor: **RIKER, Martin Lee**; 2174 Lacey Drive, Mil-
pitas, California 95035 (US).

(74) Agents: **TABOADA, Alan** et al.; Moser Taboada, 1030
Broad Street, Suite 203, Shrewsbury, New Jersey 07702
(US).

(81) Designated States (*unless otherwise indicated, for every kind of national protection available*): AE, AG, AL, AM, AO, AT, AU, AZ, BA, BB, BG, BH, BN, BR, BW, BY, BZ, CA, CH, CL, CN, CO, CR, CU, CZ, DE, DK, DM, DO, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, GT, HN, HR, HU, ID, IL, IN, IR, IS, JP, KE, KG, KN, KP, KR, KZ, LA, LC, LK, LR, LS, LU, LY, MA, MD, ME, MG, MK, MN, MW, MX, MY, MZ, NA, NG, NI, NO, NZ, OM, PA, PE, PG, PH, PL, PT, QA, RO, RS, RU, RW, SA, SC, SD, SE, SG, SK, SL, SM, ST, SV, SY, TH, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, ZA, ZM, ZW.

(84) Designated States (*unless otherwise indicated, for every kind of regional protection available*): ARIPO (BW, GH, GM, KE, LR, LS, MW, MZ, NA, RW, SD, SL, ST, SZ, TZ, UG, ZM, ZW), Eurasian (AM, AZ, BY, KG, KZ, RU, TJ, TM), European (AL, AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HR, HU, IE, IS, IT, LT, LU, LV, MC, MK, MT, NL, NO, PL, PT, RO, RS, SE, SI, SK, SM, TR), OAPI (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, KM, ML, MR, NE, SN, TD, TG).

[Continued on next page]

(54) Title: COLLIMATOR FOR USE IN SUBSTRATE PROCESSING CHAMBERS

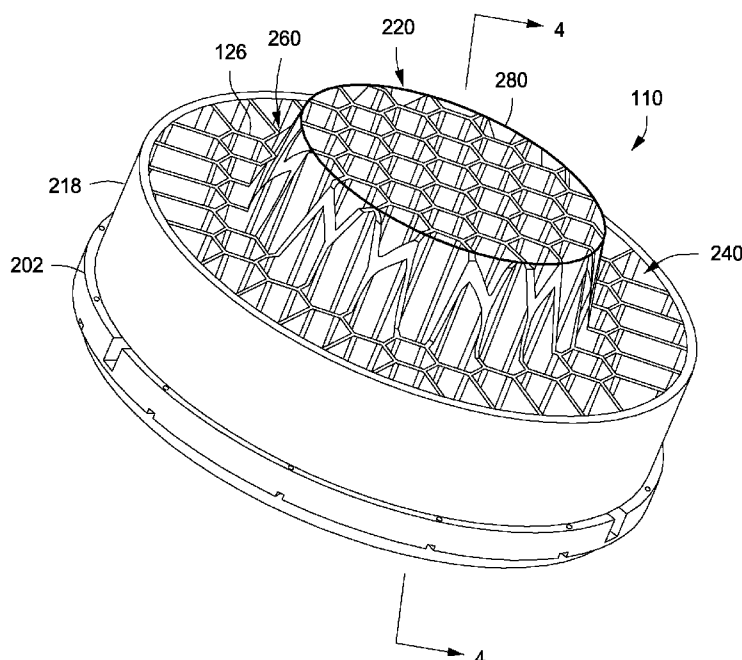


FIG. 2

(57) Abstract: Embodiments of collimators for use in substrate processing chambers are provided herein. In some embodiments, a collimator includes: a body having a central region, a peripheral region, and a transitional region disposed between the central and peripheral regions; a first plurality of apertures in the central region having a first aspect ratio; a second plurality of apertures in the peripheral region having a second aspect ratio less than the first aspect ratio; and a third plurality of apertures in the transitional region, wherein the third plurality of apertures are cut so the transitional region forms a conical shape surrounding the central region.



Published:

— *with international search report (Art. 21(3))*

COLLIMATOR FOR USE IN SUBSTRATE PROCESSING CHAMBERS

FIELD

[0001] Embodiments of the present disclosure generally relate to substrate processing chambers used in semiconductor manufacturing systems.

BACKGROUND

[0002] Reliably producing submicron and smaller features is a technological challenge for the next generation of very large scale integration (VLSI) and ultra large scale integration (ULSI) of semiconductor devices. However, as the miniaturization of circuit technology continues, the shrinking dimensions of interconnects in VLSI and ULSI technology have placed additional demands on the processing capabilities. For example, as circuit densities increase for next generation devices, the widths of interconnects, such as vias, trenches, contacts, gate structures and other features, as well as the dielectric materials therebetween, decrease while the thickness of the dielectric layers remains substantially constant, with the result of increasing the aspect ratios of the features.

[0003] Sputtering, also known as physical vapor deposition (PVD), is a commonly-used method of forming metallic features in integrated circuits. Sputtering deposits a material layer on a substrate. A source material, such as a target, is bombarded by ions strongly accelerated by an electric field. The bombardment ejects material from the target, and the material then deposits on the substrate. During deposition, ejected particles may travel in varying directions, rather than generally orthogonal to the substrate surface, thus resulting in overhanging structures formed on corners of high aspect ratio features in the substrate. Overhang may undesirably result in holes or voids formed within the deposited material, resulting in diminished electrical conductivity of the formed feature. Higher aspect ratio geometries have a higher degree of difficulty to fill without voids.

[0004] Controlling the ion fraction or ion density reaching the substrate surface to a particular range may improve the bottom and sidewall coverage during the metal layer deposition process (and reduce the overhang problem). In one example, the particles dislodged from the target may be controlled via a process tool such as a

collimator to facilitate providing a more vertical trajectory of particles into the feature. The collimator provides relatively long, straight, and narrow passageways between the target and the substrate to filter out non-vertically travelling particles which impact and stick to the passageways of the collimator.

[0005] The actual amount of filtering accomplished by a given collimator depends at least in part on the aspect ratio of the apertures through the collimator. As such, particles traveling on a path approaching normal to the substrate pass through the collimator and are deposited on the substrate, which improves coverage of the bottom of high aspect ratio features. However, certain problems exist with the use of prior art collimators, which typically have an overall hexagonal shape. Unfortunately, PVD chambers with a prior art collimator leave a six-point deposition near an edge of the substrate due to shadowing of the corners of the hexagonal collimator.

[0006] Thus, the inventors have provided improved embodiments of apparatus with improved deposition uniformity.

SUMMARY

[0007] Embodiments of collimators for use in substrate processing chambers are provided herein. In some embodiments, a collimator includes a body having a central region, a peripheral region, and a transitional region disposed between the central and peripheral regions; a first plurality of apertures in the central region having a first aspect ratio; a second plurality of apertures in the peripheral region having a second aspect ratio less than the first aspect ratio; and a third plurality of apertures in the transitional region, wherein the third plurality of apertures are cut so the transitional region forms a conical shape surrounding the central region.

[0008] In some embodiments, a collimator for use in a substrate processing chamber includes a body having a central region, a peripheral region, and a transitional region disposed between the central and peripheral regions; a first plurality of apertures in the central region having a first aspect ratio; a second plurality of apertures in the peripheral region having a second aspect ratio less than the first aspect ratio; and a third plurality of apertures in the transitional region, wherein the third plurality of apertures are cut so the transitional region forms a

conical shape surrounding the central region wherein the first plurality of apertures, the second plurality of apertures, and the third plurality of apertures are textured, and wherein upper portions of the first plurality of apertures, the second plurality of apertures, and the third plurality include a chamfer.

[0009] In some embodiments, a process chamber includes a chamber body defining an inner volume; a sputtering target disposed in an upper portion of the inner volume; a substrate support disposed below the sputtering target; and a collimator disposed in the inner volume between the sputtering target and the substrate support, wherein the collimator includes a body having a central region, a peripheral region, and a transitional region disposed between the central and peripheral regions; a first plurality of apertures in the central region having a first aspect ratio; a second plurality of apertures in the peripheral region having a second aspect ratio less than the first aspect ratio; and a third plurality of apertures in the transitional region, wherein the third plurality of apertures are cut so the transitional region forms a conical shape surrounding the central region.

[0010] Other and further embodiments of the present disclosure are described below.

BRIEF DESCRIPTION OF THE DRAWINGS

[0011] Embodiments of the present disclosure, briefly summarized above and discussed in greater detail below, can be understood by reference to the illustrative embodiments of the disclosure depicted in the appended drawings. However, the appended drawings illustrate only typical embodiments of the disclosure and are therefore not to be considered limiting of scope, for the disclosure may admit to other equally effective embodiments.

[0012] Figure 1 depicts a simplified schematic cross-sectional side view of a process chamber in accordance with the some embodiments of the present disclosure.

[0013] Figure 2 depicts an isometric bottom/side view of a collimator in accordance with some embodiments of the present disclosure.

[0014] Figure 3 depicts a top view of a collimator in accordance with some embodiments of the present disclosure.

[0015] Figure 4 depicts a cross-sectional side view of the collimator of Figure 2.

[0016] To facilitate understanding, identical reference numerals have been used, where possible, to designate identical elements which are common to the figures. The figures are not drawn to scale and may be simplified for clarity. Elements and features of some embodiments may be beneficially incorporated in other embodiments without further recitation.

DETAILED DESCRIPTION

[0017] Embodiments of collimators, such as those used for microelectronic device fabrication of semiconductor substrates, are provided herein. Collimators as disclosed herein advantageously improve deposition uniformity across a substrate to be processed.

[0018] Embodiments of the present disclosure are illustratively described herein with respect to a physical vapor deposition (PVD) chamber. However, the inventive collimator may generally be used in any substrate processing chamber to filter non-vertically traveling particles. Figure 1 illustrates a processing chamber 100 (e.g., a PVD chamber) suitable for sputter depositing materials and having a collimator 110 disposed therein in accordance with embodiments of the present disclosure. Illustrative examples of suitable PVD chambers which may be adapted to benefit from the disclosure include the ALPS® Plus and SIP ENCORE® PVD processing chambers, both commercially available from Applied Materials, Inc., Santa Clara, of California. Other processing chambers available from Applied Materials, Inc., as well as from other manufactures, may also be adapted in accordance with the embodiments described herein.

[0019] The chamber 100 has a chamber body 105 defining an inner volume 106. The chamber body 105 includes a grounded chamber wall 150 and a grounded conductive adapter 144 disposed above the grounded chamber wall 150. In some embodiments, the processing chamber 100 includes a process kit 140 having a lower shield 180, an upper shield 186, and a collimator 110. The processing

chamber 100 also includes a sputtering source, such as a target 142 having a sputtering surface 145, and a substrate support 152, for receiving a substrate 154 thereon, having a peripheral edge 153. The substrate support 152 may be disposed within the grounded chamber wall 150.

[0020] The target 142 is supported by the grounded conductive adapter 144 through a dielectric isolator 146. The target 142 comprises the material to be deposited on a surface of the substrate 154 during sputtering, and may include copper for depositing as a seed layer in high aspect ratio features formed in the substrate 154. As used herein, the term aspect ratio refers to the ratio of the height, length, or depth of an element to the width of the element. In some embodiments, the target 142 may also include a bonded composite of a metallic surface layer of sputterable material, such as copper, and a backing layer of a structural material, such as aluminum.

[0021] In some embodiments, the substrate support 152 supports a substrate 154 having high aspect ratio features to be sputter coated, the bottoms of which are in planar opposition to a principal surface of the target 142. The substrate support 152 has a planar substrate-receiving surface disposed generally parallel to the sputtering surface of the target 142. The substrate support 152 may be vertically movable through a bellows 158 connected to a bottom chamber wall 160 to allow the substrate 154 to be transferred onto the substrate support 152 through a load lock valve (not shown) in a lower portion of the processing chamber 100. The substrate support may then be raised to a deposition position as shown.

[0022] In some embodiments, processing gas may be supplied from a gas source 162 through a mass flow controller 164 into the lower portion of the processing chamber 100. A controllable direct current (DC) power source 148, coupled to the processing chamber 100, may be used to apply a negative voltage or bias to the target 142. A radio frequency (RF) power source 156 may be coupled to the substrate support 152 to induce a DC self-bias on the substrate 154. In some embodiments, the substrate support 152 may be grounded. In some embodiments, the substrate support 152 may alternatively be electrically floated.

[0023] In some embodiments, a magnetron 170 is positioned above the target 142. The magnetron 170 may include a plurality of magnets 172 supported by a base plate 174 connected to a shaft 176, which may be axially aligned with the central axis of the processing chamber 100 and the substrate 154. The magnets 172 produce a magnetic field within the processing chamber 100 near the front face of the target 142 to generate plasma so a significant flux of ions strike the target 142, causing sputter emission of target material. The magnets 172 may be rotated about the shaft 176 to increase uniformity of the magnetic field across the surface of the target 142.

[0024] In some embodiments, the processing chamber 100 may include a grounded lower shield 180 having a support flange 182 supported by and electrically coupled to the chamber wall 150. An upper shield 186 is supported by and electrically coupled to a flange 184 of the conductive adapter 144. The upper shield 186 and the lower shield 180 are electrically coupled as are the conductive adapter 144 and the chamber wall 150. In some embodiments, both the upper shield 186 and the lower shield 180 are comprised of stainless steel. In some embodiments, the processing chamber 100 may include a middle shield (not shown) coupled to the upper shield 186. In some embodiments, the upper shield 186 and lower shield 180 may be electrically floating within the processing chamber 100. In some embodiments, the upper shield 186 and lower shield 180 may alternatively be coupled to an electrical power source.

[0025] In some embodiments, the upper shield 186 may have an upper portion which closely fits an annular side recess of the target 142 with a narrow gap 188 between the upper shield 186 and the target 142, which is sufficiently narrow to prevent plasma from penetrating and sputter coating the dielectric isolator 146. The upper shield 186 may also include a downwardly projecting tip 190, which covers the interface between the lower shield 180 and the upper shield 186 to prevent bonding of the lower shield 180 and the upper shield 186 by sputter deposited material.

[0026] In some embodiments, the lower shield 180 may extend downwardly into a cylindrical outer band 196, which generally extends along the chamber wall 150 to below the top surface of the substrate support 152. The lower shield 180 may have a

base plate 198 extending radially inward from the cylindrical outer band 196. The base plate 198 may include an upwardly extending cylindrical inner band 103 surrounding the perimeter of the substrate support 152. In some embodiments, a cover ring 102 rests on the top of the cylindrical inner band 103 when the substrate support 152 is in a lower, loading position and rests on the outer periphery of the substrate support 152 when the substrate support is in an upper, deposition position to protect the substrate support 152 from sputter deposition.

[0027] The lower shield 180 encircles the sputtering surface 145 of the target 142 facing the substrate support 152 and also encircles a peripheral wall of the substrate support 152. The lower shield 180 also covers and shadows the chamber wall 150 of the processing chamber 100 to reduce deposition of sputtered deposits originating from the sputtering surface 145 of the target 142 onto the components and surfaces behind the lower shield 180. For example, the lower shield 180 can protect the surfaces of the substrate support 152, portions of the substrate 154, the chamber wall 150, and the bottom wall 160 of the processing chamber 100.

[0028] In some embodiments, directional sputtering may be achieved by positioning the collimator 110 between the target 142 and the substrate support 152. The collimator 110 may be mechanically and electrically coupled to the upper shield 186. In some embodiments, the collimator 110 may be coupled to a middle shield (not shown), positioned lower in the processing chamber 100. In some embodiments, the collimator 110 may be integral to the upper shield 186. In some embodiments, the collimator 110 is welded to the upper shield 186. In some embodiments, the collimator 110 may be electrically floating within the processing chamber 100. In some embodiments, the collimator 110 may be coupled to an electrical power source. The collimator 110 includes a plurality of apertures 128 to direct gas and/or material flux within the chamber. In some embodiments, the apertures 128 may have a hexagonal shape, as illustrated in Figures 2- 4.

[0029] FIG. 2 is an isometric view of the collimator 110 in accordance with some embodiments of the present disclosure. Figure 3 is a top plan view of a collimator 110 which may be disposed in the processing chamber 100 of Figure 1. In some embodiments, the collimator 110 has a generally honeycomb structure having walls

126 separating apertures 128 in a close-packed arrangement. An aspect ratio of the apertures 128 may be defined as the depth of the aperture 128 (equal to the length of the collimator at a specific location) divided by the width 129 of the aperture 128. In some embodiments, the thickness of the walls 126 is between about 0.06 inches and about 0.18 inches. In some embodiments, the thickness of the walls 126 is between about 0.12 inches and about 0.15 inches. In some embodiments, a circle 305 circumscribing each of the apertures 128 may have a diameter of 1.5 inches. In some embodiments, the collimator 110 is comprised of a material selected from aluminum, copper, and stainless steel.

[0030] The honeycomb structure of the collimator 110 may serve as an integrated flux optimizer 310 to optimize the flow path, ion fraction, and ion trajectory behavior of ions passing through the collimator 110. In some embodiments, the walls 126 adjacent to a shield portion 202 have a chamfer 350 and a radius. The shield portion 202 of the collimator 110 may assist in the installation of the collimator 110 in the processing chamber 100.

[0031] In some embodiments, the collimator 110 may be machined from a single mass of aluminum. The collimator 110 may optionally be coated or anodized. Alternatively, the collimator 110 may be made from other materials compatible with the processing environment, and may also be comprised of one or more sections. Alternatively, the shield portion 202 and the integrated flux optimizer 310 are formed as separate pieces and coupled together using suitable attachment means, such as welding. In some embodiments, the walls 126 of the collimator 110 may be textured (e.g., bead blasted) to improve adhesion of high stress films (e.g., copper alloys) to the walls 126.

[0032] In some embodiments, the collimator 110 may be electrically biased in bipolar mode so as to control direction of the ions passing through the collimator 110. For example, a controllable direct current (DC) or AC collimator power source (power source 390) may be coupled to the collimator 110 to provide an alternating pulsed positive or negative voltage to the collimator 110 so as to bias the collimator 110. In some embodiments, the power source 390 is a DC power source.

[0033] The collimator 110 functions as a filter to trap ions and neutrals emitted from the material from the target 142 at angles exceeding a predetermined angle, near normal relative to the substrate 154. The apertures 128 of the collimator 118 may have an aspect ratio change across the width of the collimator 110 to allow a different percentage of ions emitted from a center or a peripheral region of the material from the target 142 to pass through the collimator 110. As a result, both the number of ions and the angle of arrival of ions deposited onto peripheral regions and center regions of the substrate 154 are adjusted and controlled. Therefore, material may be more uniformly sputter deposited across the surface of the substrate 154. Additionally, material may be more uniformly deposited on the bottom and sidewalls of high aspect ratio features, particularly high aspect ratio vias and trenches located near the periphery of the substrate 154.

[0034] FIG. 4 is cross-sectional view of the collimator 110 shown in Figure 2. The collimator 110 includes a body 218 having central region 220 having a first plurality of apertures with a high aspect ratio, such as from about 2.5:1 to about 3.3:1. In some embodiments, the first plurality of apertures in the central region have an aspect ratio of about 3.3:1. The aspect ratio of a second plurality of apertures of the collimator 110 decreases in a peripheral region 240. In some embodiments the second plurality of apertures in the peripheral region 240 have an aspect ratio of about 1:1. A higher aspect ratio allows for more apertures in the central region 220 of the collimator 110. In some embodiments, for example, the central region 220 includes 61 apertures.

[0035] In some embodiments, the radial aperture decrease of the collimator 110 is accomplished by providing a transitional region 260 disposed between the central region 220 and the peripheral region 240. The third plurality of apertures are cut along a predetermined angle so the transitional region 260 forms a conical shape surrounding the first plurality of apertures. In some embodiments, the predetermined angle may be between 15° and 45°. The transitional region advantageously provides a circular profile 280 of apertures in central region 220, which overcomes the six-point deposition near an edge of the substrate 154 due to shadowing caused by corners of a conventional hexagonal collimator.

[0036] Upper portions of the walls 126 of the apertures 128 have a chamfer 406 to decrease a rate at which the apertures 128 are clogged by sputtered material. The chamfer 406 extends a predetermined distance 402 into the aperture 128 and is formed at a predetermined angle 404. In some embodiments, the predetermined distance 402 is between about 0.15 inches to about 1 inch and the predetermined angle is between about 2.5° and about 15°. In some embodiments, the predetermined distance 402 and the predetermined angle 404 are about 0.15 inches and 15°, respectively. In some embodiments, the predetermined distance 402 and the predetermined angle 404 are about 1 inch and 2.5°, respectively.

[0037] Thus, embodiments of a cooled process tool adapter and process chambers using same have been disclosed herein. The cooled process tool adapter advantageously facilitates supporting a process tool in a process chamber while removing heat from the process tool generated during use.

[0038] While the foregoing is directed to embodiments of the present disclosure, other and further embodiments of the disclosure may be devised without departing from the basic scope thereof.

Claims:

1. A collimator for use in a substrate processing chamber, comprising:
 - a body having a central region, a peripheral region, and a transitional region disposed between the central and peripheral regions;
 - a first plurality of apertures in the central region having a first aspect ratio;
 - a second plurality of apertures in the peripheral region having a second aspect ratio less than the first aspect ratio; and
 - a third plurality of apertures in the transitional region, wherein the third plurality of apertures are cut so the transitional region forms a conical shape surrounding the central region.
2. The collimator of claim 1, wherein the first plurality of apertures, the second plurality of apertures, and the third plurality of apertures are textured.
3. The collimator of claim 1, wherein the third plurality of apertures are cut at a predetermined angle.
4. The collimator of claim 3, wherein the predetermined angle is between 15° and 45°.
5. The collimator of claim 1, wherein the first plurality of apertures includes about 61 apertures.
6. The collimator of any of claims 1 to 5, wherein upper portions of the first plurality of apertures, the second plurality of apertures, and the third plurality of apertures include a chamfer.
7. The collimator of claim 6, wherein the chamfer is about 2.5° to about 15°.
8. The collimator of claim 6, wherein the chamfer has a length of about 0.15 inches to about 1 inch.

9. The collimator of any of claims 1 to 5, wherein an aspect ratio of the first plurality of apertures about 2.5:1 to about 3.3:1.
10. The collimator of any of claims 1 to 5, wherein each of the first plurality of apertures, the second plurality of apertures, and the third plurality of apertures is hexagonal.
11. The collimator of any of claims 1 to 5, wherein a circle circumscribing each of the first plurality of apertures has a diameter of about 1.5 inches.
12. The collimator of claim 1, wherein the first plurality of apertures, the second plurality of apertures, and the third plurality of apertures are textured, and wherein the third plurality of apertures are cut at a predetermined angle.
13. The collimator of claim 12, wherein the chamfer is between about 2.5° and about 15° and has a length of about 0.15 inches to about 1 inch.
14. The collimator of any of claims 12 to 13, wherein an aspect ratio of the first plurality of apertures is about 2.5:1 to about 3.3:1.
15. A substrate processing chamber, comprising:
a chamber body defining an inner volume;
a sputtering target disposed in an upper portion of the inner volume;
a substrate support disposed below the sputtering target; and
a collimator disposed in the inner volume between the sputtering target and the substrate support, wherein the collimator is as described in any of the preceding claims.

1/3

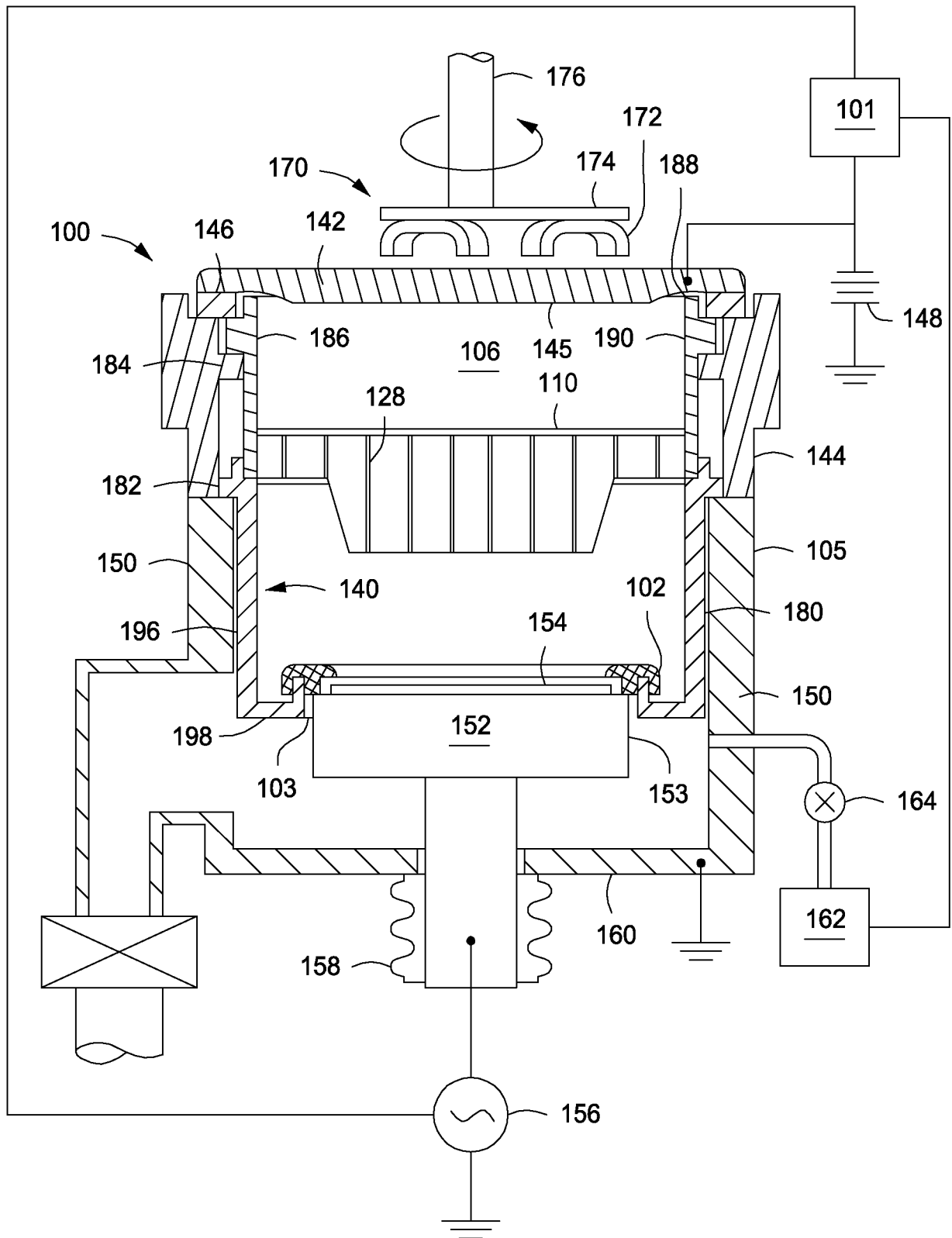


FIG. 1

2/3

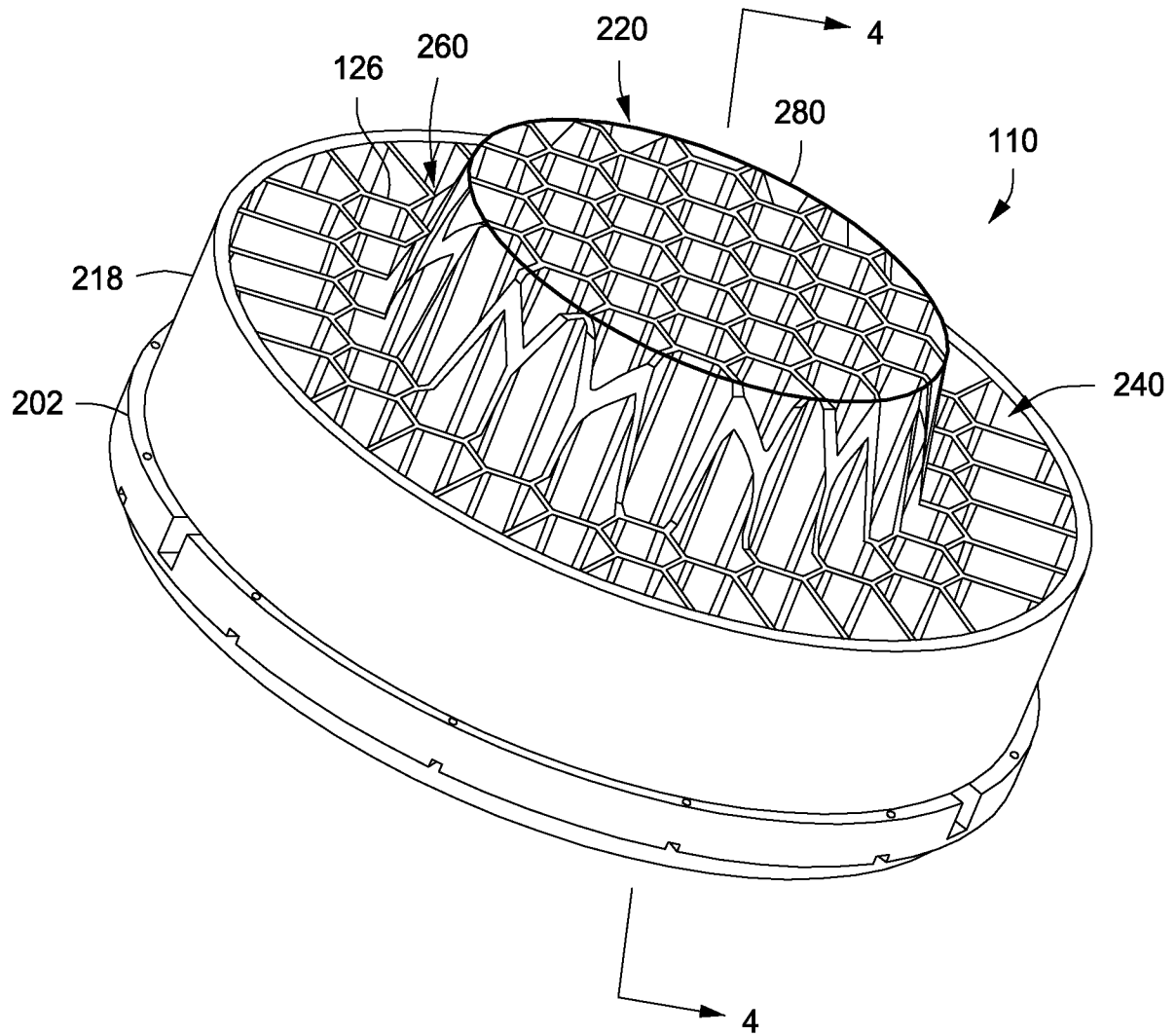


FIG. 2

3/3

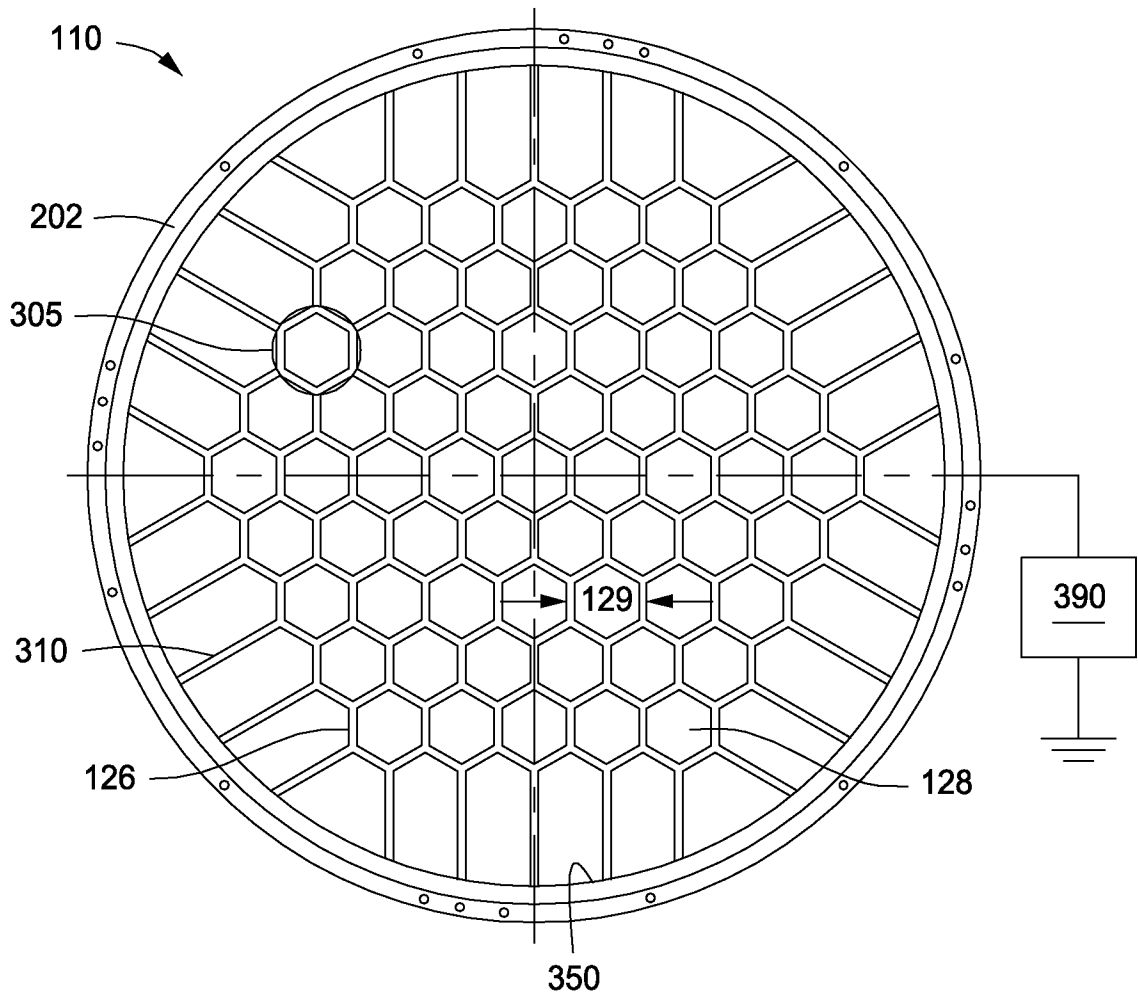


FIG. 3

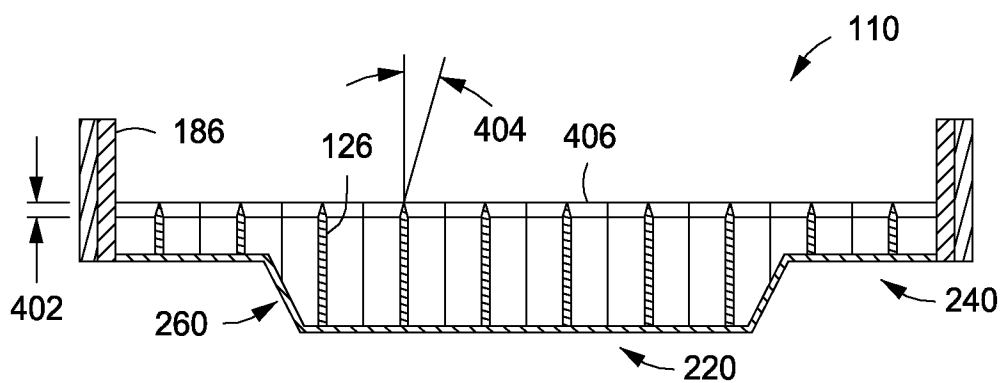


FIG. 4

A. CLASSIFICATION OF SUBJECT MATTER**H01L 21/203(2006.01)i**

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

H01L 21/203; C23C 14/00; C25B 9/00; C23C 14/34; C23C 14/32; H01L 21/44

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Korean utility models and applications for utility models

Japanese utility models and applications for utility models

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

eKOMPASS(KIPO internal) & keywords: substrate, collimator, aperture, chamber, region

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	US 2009-0308739 A1 (MARTIN LEE RIKER et al.) 17 December 2009 See abstract, paragraphs [0031]-[0055], [0059]-[0076] and figures 1-5, 9A-10C.	1-5, 9-12, 14-15
A		6-8, 13
A	US 2006-0249369 A1 (MARIA SANTINA MARANGON et al.) 09 November 2006 See abstract, paragraphs [0045]-[0053] and figures 8-9.	1-15
A	US 2004-0211665 A1 (KI HWAN YOON et al.) 28 October 2004 See abstract, paragraphs [0045]-[0056], [0128]-[0133] and figures 3-6, 8A-8C.	1-15
A	US 6362097 B1 (RICHARD ERNEST DEMARAY et al.) 26 March 2002 See abstract, column 5, line 35 - column 7, line 47 and figures 2-3B.	1-15
A	US 2003-0029715 A1 (SANG-HO YU et al.) 13 February 2003 See abstract, paragraphs [0031]-[0043] and figures 3-6. ** NOTE: This International Search Report has been established on the assumption that claim 13 refers to claim 6.	1-15



Further documents are listed in the continuation of Box C.



See patent family annex.

* Special categories of cited documents:

"A" document defining the general state of the art which is not considered to be of particular relevance

"E" earlier application or patent but published on or after the international filing date

"L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)

"O" document referring to an oral disclosure, use, exhibition or other means

"P" document published prior to the international filing date but later than the priority date claimed

"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention

"X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone

"Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art

"&" document member of the same patent family

Date of the actual completion of the international search

26 February 2016 (26.02.2016)

Date of mailing of the international search report

26 February 2016 (26.02.2016)

Name and mailing address of the ISA/KR

International Application Division

Korean Intellectual Property Office

189 Cheongsa-ro, Seo-gu, Daejeon, 35208, Republic of Korea

Facsimile No. +82-42-472-7140

Authorized officer

CHOI, Sang Won

Telephone No. +82-42-481-8291



INTERNATIONAL SEARCH REPORT

Information on patent family members

International application No.

PCT/US2015/061826

Patent document cited in search report	Publication date	Patent family member(s)	Publication date
US 2009-0308739 A1	17/12/2009	CN 102066603 A	18/05/2011
		CN 102066603 B	10/04/2013
		CN 102301451 A	28/12/2011
		JP 2011-524471 A	01/09/2011
		KR 10-2011-0020918 A	03/03/2011
		KR 10-2014-0014378 A	06/02/2014
		TW 201100571 A	01/01/2011
		US 2009-0308732 A1	17/12/2009
		WO 2009-155208 A2	23/12/2009
		WO 2010-123680 A2	28/10/2010
		WO 2010-123680 A3	13/01/2011
US 2006-0249369 A1	09/11/2006	EP 1710324 A1	11/10/2006
		EP 1710324 B1	03/12/2008
US 2004-0211665 A1	28/10/2004	CN 101466863 A	24/06/2009
		CN 101466863 B	10/08/2011
		JP 2005-504885 A	17/02/2005
		JP 2006-500472 A	05/01/2006
		JP 2009-533877 A	17/09/2009
		KR 10-1174946 B1	17/08/2012
		US 2003-0022487 A1	30/01/2003
		US 2003-0029715 A1	13/02/2003
		US 2006-0276020 A1	07/12/2006
		US 2007-0202254 A1	30/08/2007
		US 2008-0268635 A1	30/10/2008
		US 2008-0268636 A1	30/10/2008
		US 2009-0004850 A1	01/01/2009
		US 2011-0086509 A1	14/04/2011
		US 2011-0124192 A1	26/05/2011
		US 6740585 B2	25/05/2004
		US 7416979 B2	26/08/2008
		US 7611990 B2	03/11/2009
		US 8110489 B2	07/02/2012
		US 8187970 B2	29/05/2012
		WO 03-030224 A2	10/04/2003
		WO 03-080887 A2	02/10/2003
		WO 2003-030224 A3	19/02/2004
		WO 2003-080887 A3	26/08/2004
		WO 2007-121249 A2	25/10/2007
		WO 2007-121249 A3	27/12/2007
		WO 2009-134916 A2	05/11/2009
		WO 2009-134916 A3	04/02/2010
		WO 2009-134925 A2	05/11/2009
		WO 2009-134925 A3	04/03/2010
US 6362097 B1	26/03/2002	EP 1090416 A2	11/04/2001
		JP 2002-520877 A	09/07/2002
		JP 4523158 B2	11/08/2010

INTERNATIONAL SEARCH REPORT

Information on patent family members

International application No.

PCT/US2015/061826

Patent document cited in search report	Publication date	Patent family member(s)	Publication date
US 2003-0029715 A1	13/02/2003	KR 10-2001-0071874 A	31/07/2001
		TW 585929 A	01/05/2004
		WO 00-04575 A2	27/01/2000
		WO 00-04575 A3	30/03/2000
US 2003-0029715 A1	13/02/2003	CN 101466863 A	24/06/2009
		CN 101466863 B	10/08/2011
		JP 2005-504885 A	17/02/2005
		JP 2006-500472 A	05/01/2006
		JP 2009-533877 A	17/09/2009
		KR 10-1174946 B1	17/08/2012
		US 2003-0022487 A1	30/01/2003
		US 2004-0211665 A1	28/10/2004
		US 2006-0276020 A1	07/12/2006
		US 2007-0202254 A1	30/08/2007
		US 2008-0268635 A1	30/10/2008
		US 2008-0268636 A1	30/10/2008
		US 2009-0004850 A1	01/01/2009
		US 2011-0086509 A1	14/04/2011
		US 2011-0124192 A1	26/05/2011
		US 6740585 B2	25/05/2004
		US 7416979 B2	26/08/2008
		US 7611990 B2	03/11/2009
		US 8110489 B2	07/02/2012
		US 8187970 B2	29/05/2012
		WO 03-030224 A2	10/04/2003
		WO 03-080887 A2	02/10/2003
		WO 2003-030224 A3	19/02/2004
		WO 2003-080887 A3	26/08/2004
		WO 2007-121249 A2	25/10/2007
		WO 2007-121249 A3	27/12/2007
		WO 2009-134916 A2	05/11/2009
		WO 2009-134916 A3	04/02/2010
		WO 2009-134925 A2	05/11/2009
		WO 2009-134925 A3	04/03/2010