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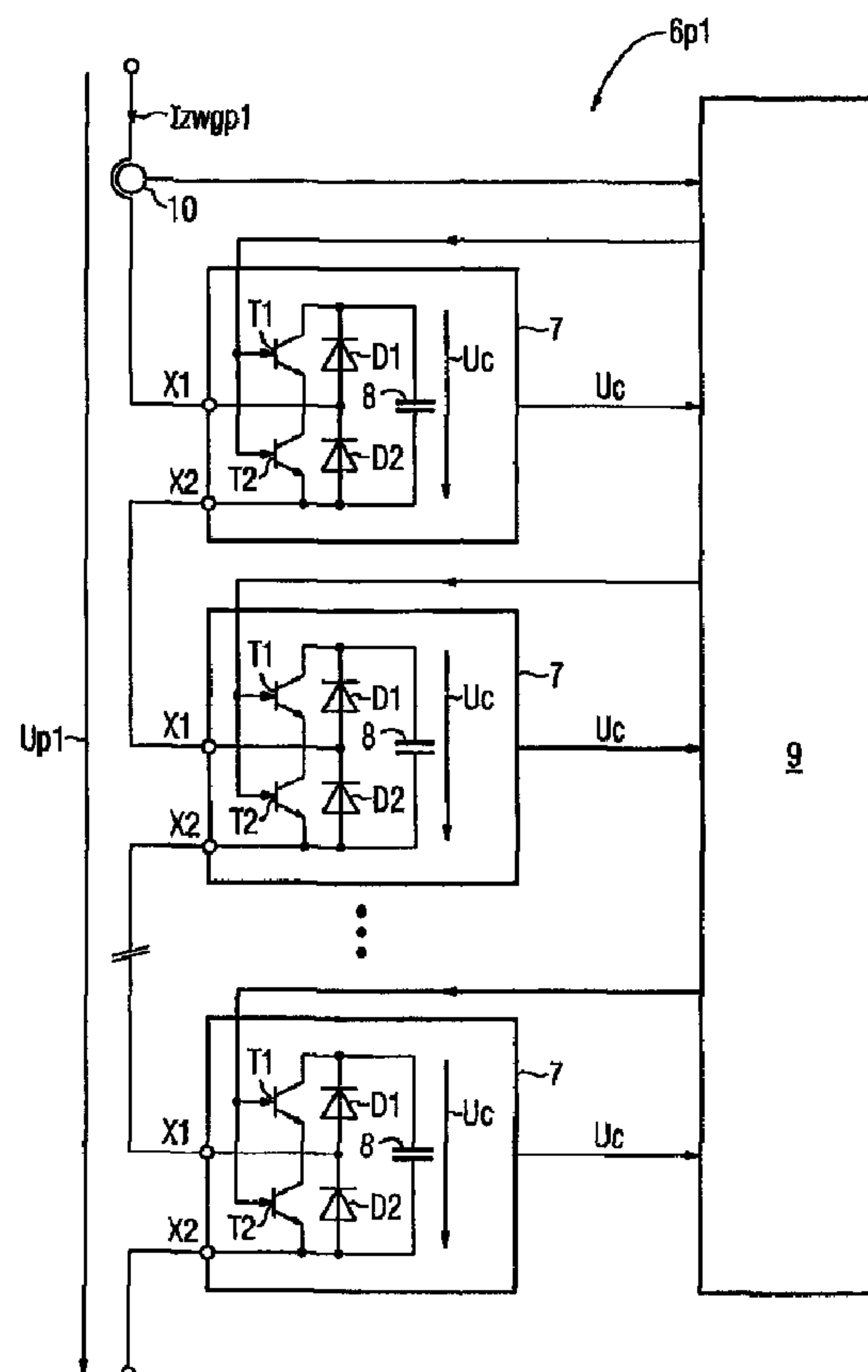
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(54) **Titre : SURVEILLANCE DU VIEILLISSEMENT DES CONDENSATEURS D'UN CONVERTISSEUR PAR MESURE DE LA CAPACITE**

(54) **Title: MONITORING OF THE AGING OF THE CAPACITORS IN A CONVERTER BY MEANS OF CAPACITANCE MEASUREMENT**



(57) **Abrégé/Abstract:**

In order to provide an apparatus (1) for converting an electrical current with at least one phase module (2a, 2b, 2c), which has an AC voltage terminal (3₁, 3₁, 3₁) and at least one DC voltage terminal (p, n), wherein a phase module branch (6p1, 6p2, 6p3, 6n1,



(57) Abrégé(suite)/Abstract(continued):

6n2, 6n3) is formed between each DC voltage terminal (p, n) and the AC voltage terminal ($3_1, 3_1, 3_1$), and wherein each phase module branch (6p1, 6p2, 6p3, 6n1, 6n2, 6n3) has a series circuit comprising submodules (7), which each have a capacitor (8) and at least one power semiconductor (T_1, T_2), by means of which apparatus (1) aging of the energy store can be established in a simple manner, the invention proposes capacitor diagnosis means for the time-dependent determination of the capacitance of each capacitor (8).

Abstract

In order to provide an apparatus (1) for converting an electrical current with at least one phase module (2a, 2b, 2c), which has an AC voltage terminal (3₁, 3₁, 3₁) and at least one DC voltage terminal (p, n), wherein a phase
5 module branch (6p1, 6p2, 6p3, 6n1, 6n2, 6n3) is formed between each DC voltage terminal (p, n) and the AC voltage terminal (3₁, 3₁, 3₁), and wherein each phase module branch (6p1, 6p2, 6p3, 6n1, 6n2, 6n3) has a series circuit comprising submodules (7), which each have a capacitor (8) and at least one power semiconductor (T₁, T₂), by means of which apparatus (1) aging of the energy store
10 can be established in a simple manner, the invention proposes capacitor diagnosis means for the time-dependent determination of the capacitance of each capacitor (8).

54106-305

- 1 -

MONITORING OF THE AGING OF THE CAPACITORS IN A CONVERTER BY
MEANS OF CAPACITANCE MEASUREMENT

FIELD OF INVENTION

The present invention relates to a device for converting an electrical current with at least one phase module, which has an AC voltage connection and at least one DC voltage connection, a phase module branch being formed between each DC voltage connection and the AC voltage connection, and each phase module branch having a series circuit comprising submodules, which each have a capacitor and at least one power semiconductor.

The invention furthermore relates to a method for converting a current.

BACKGROUND

Such a device and such a method are known, for example, from the paper by A. Lesnicar and R. Marquardt "An Innovative Modular Multilevel Converter Topology Suitable for a Wide Power Range", which was presented at Powertech 2003. This paper discloses a power converter, which is intended to be connected to an AC voltage system. The power converter has a phase module for each phase of the AC voltage system to be connected to it, each phase module having an AC voltage connection and two DC voltage connections. Phase module branches extend between each DC voltage connection and the AC voltage connection such that a so-called 6-pulse bridge circuit is provided. The module branches comprise a series circuit of submodules, which each comprise two disconnectable power semiconductors, with which in each case inverse freewheeling diodes are connected in

PCT/DE2006/002250

- 2 -

parallel. The disconnectable power semiconductors and the freewheeling diodes are connected in series, with a capacitor being provided in parallel with said series circuit. Said components of the submodules are wired to one another such that either the capacitor voltage or the voltage zero is dropped across the two-pole output of each submodule.

The disconnectable power semiconductors are controlled by means of so-called pulse width modulation. The regulating means for controlling the power semiconductors have measuring sensors for detecting currents whilst obtaining current values. The current values are supplied to a central control unit, which has an input interface and an output interface. A modulator, i.e. a software routine, is provided between the input interface and the output interface. The modulator has, inter alia, a selector unit and a pulse width generator. The pulse width generator generates the control signals for the individual submodules. The disconnectable power semiconductors are changed over from an on-state position, in which a current flow via the disconnectable power semiconductors is made possible, to an off-state position, in which a current flow via the disconnectable power semiconductors is interrupted, by means of the control signals generated by the pulse width generator. In this case, each submodule has a submodule sensor for detecting a voltage dropped across the capacitor.

Further papers relating to the control method for a so-called multi-level power converter topology are known from R. Marquardt, A. Lesnicar, J. Hildinger "Modulares Stromrichterkonzept für Netzkupplungsanwendung bei hohen Spannungen" [Modular power converter concept for power supply system coupling application in the case of high voltages], presented at the ETG technical conference in Bad Nauheim, Germany 2002, from A. Lesnicar, R. Marquardt, "A new modular voltage source inverter topology", EPE' 03 Toulouse, France 2003 and from

R. Marquardt, A. Lesnicar "New Concept for High Voltage - Modular Multilevel Converter", PESC 2004 Conference in Aachen, Germany.

The German patent application 10 2005 045 090.3, which is as yet unpublished, discloses a method for controlling a polyphase power converter with distributed energy stores. The disclosed device likewise has a multi-level power converter topology with phase modules, which have an AC voltage connection arranged symmetrically in the centre of each phase module and two DC voltage connections. Each phase module comprises two phase module branches, which extend between the AC voltage connection and one of the DC voltage connections. In turn, each phase module branch comprises a series circuit of submodules, each submodule comprising disconnectable power semiconductors and freewheeling diodes reverse-connected in parallel therewith. In addition, each submodule has a unipolar capacitor. Regulating means are used for regulating the power semiconductors, which regulating means are also designed to set branch currents which flow between the phase modules. By controlling the branch currents, current oscillations, for example, can be actively damped and operating points at lower output frequencies can be avoided. Furthermore, uniform loading of all of the disconnectable semiconductor switches and symmetrization of very asymmetrical voltages can be brought about.

Energy stores such as, for example, capacitors in the intermediate circuit of power converter modules have the property that they age during operation. In this case, capacitors lose capacitance, as a result of which, at a specific voltage, the charge stored by them becomes smaller and smaller. In the maintenance of installations of intermediate circuit power converters it would be advantageous

54106-305

- 4 -

to know which capacitors have lost so much capacitance that replacement is necessary. It is thereby possible to reduce the failure rate of the capacitors during operation of the power converter and to increase availability. In power converter
5 topologies having large common intermediate circuit capacitors such as in two-point or three-point technology, for example, it makes little sense to monitor the capacitance reduction of the capacitors since the change would only amount to a fraction of the total capacitance and would therefore be virtually
10 undetectable metrologically. However, such a capacitance reduction can lead to the failure of the power converter. A further disadvantage of the large common intermediate circuit capacitors is that the latter can be exchanged or replaced only with difficulty or not at all.

15 SUMMARY

It is an object of some embodiments of the invention to provide a device of the type mentioned in the introduction with which aging of the capacitors can be ascertained in a simple manner.

Some embodiments of the invention provide capacitor diagnosis
20 means, which continually determine the capacitance of the capacitors of the device according to some embodiments of the invention. The device according to some embodiments of the invention has distributed capacitors with a correspondingly lower capacitance than devices having a central capacitor in
25 the DC voltage circuit. It has been recognized that it is now possible to detect the capacitance change metrologically. In the context of the invention, it is less crucial to know what capacitance the individual capacitors have exactly. Rather, it is important to find out how the capacitances of the individual

54106-305

- 5 -

capacitors change over time. It is thus possible for capacitors having decreasing capacitance to be differentiated from fault-free capacitors and be detected. According to some embodiments of the invention, the respective capacitance of the individual capacitors is therefore determined by means of the capacitor diagnosis means in a manner dependent on time.

Expediently, the capacitor diagnosis means have for each phase module a branch current sensor for detecting a phase module branch current flowing in the assigned phase module, and for each submodule a submodule sensor for detecting a capacitor voltage dropped across the capacitor of the assigned submodule and also means for detecting the switching states of the respectively assigned submodule, an evaluation unit determining the capacitance of the capacitors of each phase module in a manner dependent on the respectively measured phase module branch current I_{zwg} , the switching state or states and the respective capacitor voltage U_c . This integration - divided into temporal intervals - of the detected branch current yields the charge change ΔQ of the capacitors of the monitored phase module. The submodule sensors serve for detecting the voltage dropped across the respective capacitor. The capacitance of each capacitor results from the division of the charge change ΔQ by the voltage change ΔU_c , the voltage and the charge being detected in each case between the turn-on and turn-off instants of the submodule. If the submodule is turned on, the capacitor voltage is dropped across the output terminals of the submodule. In the turned-off state, the voltage zero is dropped across the terminals.

Proceeding from the method mentioned in the introduction, some embodiments of the invention may achieve said object by means of a converter having at least one phase module having at least one DC voltage connection.

54106-305

- 6 -

and an AC voltage connection, a phase module branch being formed between each DC voltage connection and the AC voltage connection, said phase module branch having a series circuit comprising submodules, which each have a capacitor and at least one power semiconductor, in which the capacitance of the capacitor is determined in a manner dependent on time.

In accordance with a further development that is expedient in this regard, in a manner dependent on time, the phase module branch current of each phase module is detected whilst obtaining phase module branch current values and the capacitor voltage dropped across each capacitor is measured whilst obtaining capacitor voltage values and, finally, a voltage change is determined, the phase module branch current, for obtaining the charge change between the turn-on and turn-off of the power semiconductors of the submodules, is integrated and the change in the capacitance ΔC of the capacitors of the respective phase module is determined from the charge change and from the voltage change. Turning on and turning off the power semiconductors should be understood to mean the switching states of the power semiconductors in which a current flow to the capacitor in the respective submodule is made possible.

According to one aspect of the present invention, there is provided a device for converting an electrical current, the device comprising: at least one phase module each having an AC voltage connection, at least one DC voltage connection, and at least one phase module branch respectively disposed between each said at least one DC voltage connection and said AC voltage connection; each said at least one phase module branch having a series circuit including submodules each having a

54106-305

- 6a -

capacitor and at least one power semiconductor; and a capacitor diagnosis device for a time-dependent determination of a capacitance of said capacitor of each of said submodules; said capacitor diagnosis device including, for each said at least
5 one phase module, a branch current sensor for detecting a phase module branch current flowing in a respective phase module, a submodule sensor for each of said submodules for detecting a capacitor voltage dropped across said capacitor of a respective submodule, a device for detecting switching states of said at
10 least one power semiconductor of said respective submodule, and an evaluation unit determining said capacitance of said capacitors of each said at least one phase module in a manner dependent on a respectively measured branch current, said switching states of said at least one power semiconductor and a
15 respective capacitor voltage.

According to another aspect of the present invention, there is provided a method for converting a current, the method which comprises the following steps: providing a converter including at least one phase module each having at least one DC voltage
20 connection, an AC voltage connection, and at least one phase module branch respectively disposed between each said at least one DC voltage connection and said AC voltage connection; providing each said at least one phase module branch with a series circuit including submodules each having a capacitor and
25 at least one power semiconductor; determining a capacitance of said capacitor of each of said submodules in a manner dependent on time; and detecting a branch current of each phase module while obtaining branch current values and measuring a capacitor voltage dropped across said capacitor of each submodule while

54106-305

- 6b -

obtaining capacitor voltage values and finally determining a voltage change, integrating said phase module branch current, for obtaining a charge change between a turn-on and a turn-off of said at least one power semiconductor of the submodules, in
5 sections and determining a change in the capacitance of said capacitors of said respective phase module from said charge change and from said voltage change.

BRIEF DESCRIPTION OF THE DRAWINGS

Further configurations and advantages of the invention are the
10 subject matter of the following description of exemplary embodiments of the invention with reference to the figures in the drawing, wherein identical reference symbols refer to identically acting component parts, and wherein

figure 1 shows an exemplary embodiment of the device according
15 to the invention,

figure 2 shows a phase module of a device in accordance with figure 1.

54106-305

- 7 -

DETAILED DESCRIPTION

Figure 1 shows an exemplary embodiment of the device 1 according to the invention which comprises three phase modules 2a, 2b and 2c. Each phase module 2a, 2b and 2c is connected to a positive DC voltage line p and to a negative DC voltage line n, with the result that each phase module 2a, 2b, 2c has two DC voltage connections. In addition, in each case one AC voltage connection 3₁, 3₂, 3₃ is provided for each phase module 2a, 2b and 2c. The AC voltage connections 3₁, 3₂, 3₃ are connected to a three-phase AC voltage system 5 via a transformer 4. The phase voltages U₁, U₂ and U₃ drop across the phases of the AC voltage system 5, with system currents I_{n1}, I_{n2} and I_{n3} flowing. The AC-voltage-side phase current of each phase module is denoted by I₁, I₂ and I₃. The DC voltage current is I_d. Phase module branches 6p₁, 6p₂ and 6p₃ extend between each of the AC voltage connections 3₁, 3₂, or 3₃ and the positive DC voltage line p. The phase module branches 6n₁, 6n₂ and 6n₃ are formed between each AC voltage connection 3₁, 3₂, 3₃ and the negative DC voltage line n. Each phase module branch 6p₁, 6p₂, 6p₃, 6n₁, 6n₂ and 6n₃ comprises a series circuit of submodules (not illustrated in detail in figure 1) and an inductance, which is denoted by L_{Kr} in figure 1.

Figure 2 illustrates the series circuit of the submodules 7 and in particular the design of the submodules by means of an electrical equivalent circuit diagram in more detail, with only the phase module branch 6p₁ being singled out in figure 2. The rest of the phase module branches have an identical design, however. It can be seen that each submodule 7 has two disconnectable power semiconductor switches T1 and T2 connected in series. Disconnectable power semiconductors are, for example, so-called IGBTs, GTOs, IGCTs or the like. They are

known to a person skilled in the art as such, with the result that a detailed illustration is not required at this juncture. A freewheeling diode D1, D2 is reverse-connected in parallel with each disconnectable power semiconductor T1, T2. A capacitor 8 is connected as the energy store in parallel with the series circuit of the disconnectable power semiconductors T1, T2 or the freewheeling diodes D1 and D2. Each capacitor 8 is charged in unipolar fashion. Two voltage states can now be generated at the two-pole connection terminals X1 and X2 of each submodule 7. If, for example, a drive signal is generated by a drive unit 9, with which drive signal the disconnectable power semiconductor T2 is changed over into its on-state position, in which a current flow via the power semiconductor T2 is made possible, the voltage zero is dropped across the terminals X1, X2 of the submodule 7. In this case, the disconnectable power semiconductor T1 is in its off-state position, in which a current flow via the disconnectable power semiconductor T1 is interrupted. This prevents the discharge of the capacitor 8. If, on the other hand, the disconnectable power semiconductor T1 is changed over to its on-state position, but the disconnectable power semiconductor T2 is changed over to its off-state position, the full capacitor voltage U_c is present at the terminals X1, X2 of the submodule 7.

The exemplary embodiment of the device according to the invention shown in figures 1 and 2 is also referred to as a so-called multi-level power converter. Such a multi-level power converter is suitable, for example, for driving electrical machines, such as motors or the like, for example. Furthermore, such a multi-level power converter is also suitable for use in the sector of energy distribution and transmission. Thus, the device according to the invention is used, for example, as a back-to-back link, which comprises two power converters which are connected to one another on the DC-voltage side, the power converters each being connected to an AC voltage system.

Such back-to-back links are used for the exchange of energy between two energy distribution systems, the energy distribution systems having, for example, a different frequency, phase angle, neutral-point connection or the like. Furthermore, applications in the field of wattless power compensation as so-called FACTS (Flexible AC Transmission Systems) come into consideration. High-voltage DC transmission over long distances is also conceivable with such multi-level power converters.

It can furthermore be discerned in figure 2 that a branch current sensor 10 is provided for measuring the branch currents, which sensor detects the current flowing via the phase module whilst obtaining phase module branch current values, the branch current values being transmitted via an optical fiber communication line toward the evaluation unit 9. Furthermore, submodule sensors (not illustrated in the drawing) are provided for each submodule 7, said sensors being used to detect a voltage drop U_c across the capacitors 8 whilst obtaining capacitor voltage values. The capacitor voltage values are likewise transmitted to the evaluation unit 9, in which case information regarding the capacitor to which the respective measured value relates is simultaneously included. The evaluation unit assigns the incoming measured value to a measurement time. Integration in sections of the phase module branch current I_{zwg} yields the charge change ΔQ of the capacitors 8 of the respective phase module. The expression integration in sections should be understood to mean an integration in specific time intervals. In other words, integration is effected only when the switching states of the power semiconductors of the respective submodule permit the current flow to the capacitor of the submodule. As a result of the determination of a voltage change, that is to say the change in the voltage dropped across the capacitor 8 in a specific

PCT/DE2006/002250

- 10 -

time duration, the capacitance of the capacitors results in accordance with

$$C \approx \frac{\Delta Q}{\Delta UC}$$

54106-305

- 11 -

CLAIMS:

1. A device for converting an electrical current, the device comprising: at least one phase module each having an AC voltage connection, at least one DC voltage connection, and at
5 least one phase module branch respectively disposed between each said at least one DC voltage connection and said AC voltage connection;

each said at least one phase module branch having a series circuit including submodules each having a capacitor and
10 at least one power semiconductor; and

a capacitor diagnosis device for a time-dependent determination of a capacitance of said capacitor of each of said submodules;

said capacitor diagnosis device including, for each
15 said at least one phase module, a branch current sensor for detecting a phase module branch current flowing in a respective phase module, a submodule sensor for each of said submodules for detecting a capacitor voltage dropped across said capacitor of a respective submodule, a device for detecting switching
20 states of said at least one power semiconductor of said respective submodule, and an evaluation unit determining said capacitance of said capacitors of each said at least one phase module in a manner dependent on a respectively measured branch current, said switching states of said at least one power
25 semiconductor and a respective capacitor voltage.

2. A method for converting a current, the method which comprises the following steps:

54106-305

- 12 -

providing a converter including at least one phase module each having at least one DC voltage connection, an AC voltage connection, and at least one phase module branch respectively disposed between each said at least one DC voltage connection and said AC voltage connection;

providing each said at least one phase module branch with a series circuit including submodules each having a capacitor and at least one power semiconductor;

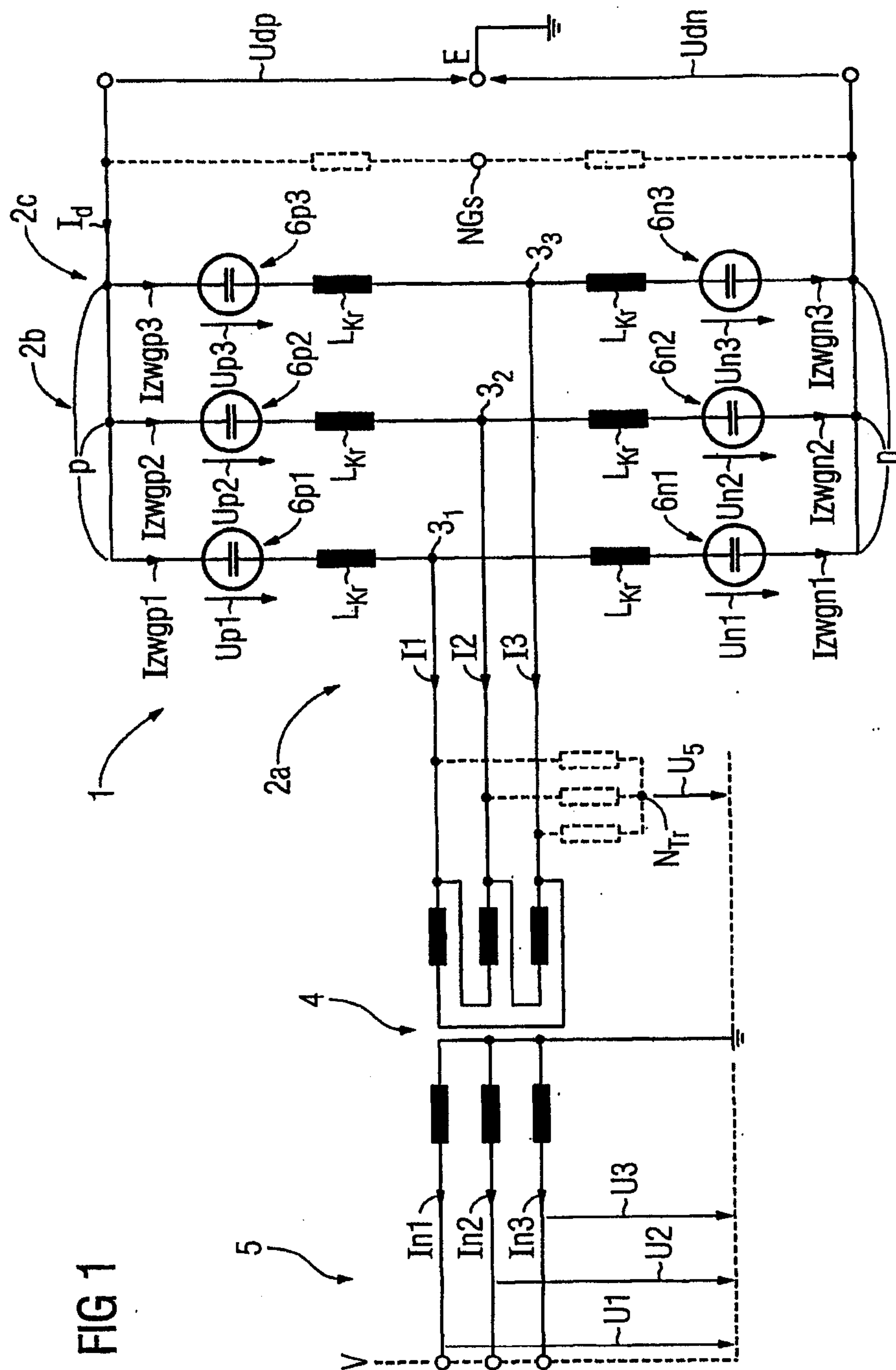
determining a capacitance of said capacitor of each of said submodules in a manner dependent on time; and

detecting a branch current of each phase module while obtaining branch current values and measuring a capacitor voltage dropped across said capacitor of each submodule while obtaining capacitor voltage values and finally determining a voltage change, integrating said phase module branch current, for obtaining a charge change between a turn-on and a turn-off of said at least one power semiconductor of the submodules, in sections and determining a change in the capacitance of said capacitors of said respective phase module from said charge change and from said voltage change.

PCT/DE2006/002250

1/2

FIG 1



PCT/DE2006/002250

2/2

FIG 2

