#### (12) INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

# (19) World Intellectual Property Organization

International Bureau







# (10) International Publication Number WO 2009/001368 A3

(51) International Patent Classification:

606F 15/76 (2006.01) 606F 17/50 (2006.01)

606F 9/30 (2006.01)

(21) International Application Number:

PCT/IN2007/000262

(22) International Filing Date:

28 June 2007 (28.06.2007)

(25) Filing Language:

English

(26) Publication Language:

English

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- (81) Designated States (unless otherwise indicated, for every kind of national protection available): AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BH, BR, BW, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DO, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, GT, HN, HR, HU, ID, IL, IN, IS, JP, KE, KG, KM, KN, KP, KR, KZ, LA, LC, LK, LR, LS, LT, LU, LY, MA, MD, ME, MG, MK, MN, MW, MX, MY, MZ, NA, NG, NI, NO, NZ, OM, PG, PH, PL, PT, RO, RS, RU, SC, SD, SE, SG, SK, SL, SM, SV, SY, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, ZA, ZM, ZW.
- (84) Designated States (unless otherwise indicated, for every kind of regional protection available): ARIPO (BW, GH, GM, KE, LS, MW, MZ, NA, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HU, IE, IS, IT, LT, LU, LV, MC, MT, NL, PL, PT, RO, SE, SI, SK, TR), OAPI (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

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#### (54) Title: SYSTEM-ON-CHIP FABRIC, AS WELL AS METHOD AND SYSTEM FOR PRODUCING IT

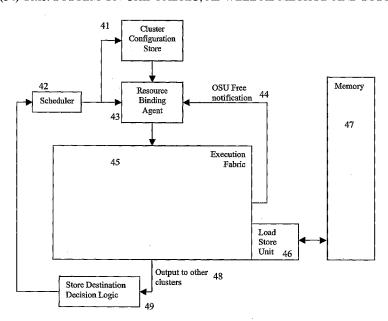


Fig-4

(57) Abstract: This invention provides a fabric within a SoC framework, along with a system and method, in which resources can be composed as computational structures that best match the application's needs. The fabric, disclosed herein, contains compute, storage and communication resources that can be aggregated at runtime to perform specific application tasks. The system comprises of a scheduler (42), a cluster configuration store (41), an execution fabric (45) containing a plurality of computational resources, a resource binding agent (43), a Load Store Unit (46), and Store Destination Decision Logic (49). The method of the present invention comprises the steps of developing High Level Language (HLL) descriptions of applications modules; converting the HLL description of the modules of the application to an intermediate representation; compiling into clusters using the dataflow graph of the application; performing binding operations; and Performing controlled dataflow execution wherein a set of clusters are scheduled and executed on the fabric.



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#### **Declarations under Rule 4.17**:

- as to the identity of the inventor (Rule 4.17(i))
- as to applicant's entitlement to apply for and be granted a patent (Rule 4.17(ii))
- of inventorship (Rule 4.17(iv))

#### Published:

— with international search report (Art. 21(3))

- before the expiration of the time limit for amending the claims and to be republished in the event of receipt of amendments (Rule 48.2(h))
- (88) Date of publication of the international search report:

24 September 2009

#### INTERNATIONAL SEARCH REPORT

International application No. PCT/IN 2007/000262

A. CLASSIFICATION OF SUBJECT MATTER

C. DOCUMENTS CONSIDERED TO BE RELEVANT

IPC8: G06F 15/76 (2006.01); G06F 9/30 (2006.01); G06F 17/50 (2006.01)

According to International Patent Classification (IPC) or to both national classification and IPC

#### B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)  $IPC^8$ : G06F

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used) EPODOC, WPI, IEEE Xplore, esp@cenet

Category*	Citation of document, with indication, where ap	ppropriate, of the relevant passages	Relevant to claim No.			
Y A	US 2005/0283768 A1 (OZONE) 22 E abstract, paragraphs 11-15; figs. 1, 3-	1-7, 9-13, 16 8, 14, 15, 17				
Y A	US 6175948 B1 (MILLER et al.) 16 J abstract; claims 1, 3	1-7, 9-13, 16 8, 14, 15, 17				
Y A	US 2005/0021871 A1 (GEORGIOU et (27.01.2005) abstract, figs. 5, 8	1-7, 9-13, 16 8, 14, 15, 17				
Further documents are listed in the continuation of Box C.  * Special categories of cited documents:  "A" document defining the general state of the art which is not considered to be of particular relevance  "E" earlier application or patent but published on or after the international filing date  "L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)  "O" document referring to an oral disclosure, use, exhibition or other means  "P" document published prior to the international filing date but later than the priority date claimed  "A" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention "X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art  "&" document member of the same patent family						
Name and m	actual completion of the international search 19 June 2009 (19.06.2009)  ailing address of the ISA/ AT  Austrian Patent Office  sdner Straße 87, A-1200 Vienna	Date of mailing of the international se 20 July 2009 (20.0 Authorized officer HÁMORINÉ Ga	07.2009)			
Facsimile No	o. +43 / 1 / 534 24 / 535	Telephone No. +36 /1/ 474-5866				

### INTERNATIONAL SEARCH REPORT

International application No. PCT/IN 2007/000262

C (Continuation). DOCUMENTS CONSIDERED TO BE RELEVANT							
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Α	WO 2004/004008 A1 (KONINKL PHILIPS ELECTRONICS NV) 8 January 2004 (08.01.2004) abstract, figs. 1-4	1-17					
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Information on patent family members

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