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— *of inventorship (Rule 4.17(iv))*

(54) Title: SIGNAL STRENGTH DETECTOR

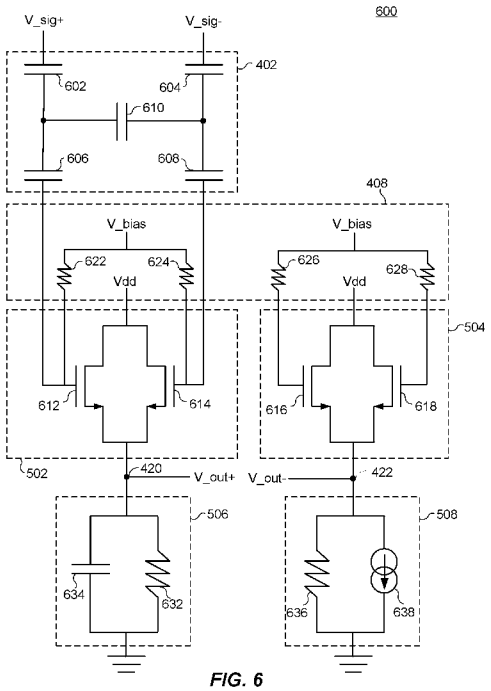

FIG. 6

(57) Abstract: The disclosure relates to technology for signal strength detection. An apparatus for detecting signal strength is disclosed. The apparatus comprises a differential sensor stage having an input and an output, a differential reference stage having an input and an output, a capacitor stage configured to couple an input signal to the input of the differential sensor stage, a biasing circuit configured to bias the differential sensor stage and the differential reference stage. The apparatus has a differential output between the output of the differential sensor stage and the output of the differential reference stage. The differential output is configured to provide a signal indicative of strength of the input signal.

WO 2020/172688 A3

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B. FIELDS SEARCHED

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Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)
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C. DOCUMENTS CONSIDERED TO BE RELEVANT

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X	A. VOUILLOZ ET AL: "A low-power CMOS super-regenerative receiver at 1 GHz", IEEE JOURNAL OF SOLID-STATE CIRCUITS, vol. 36, no. 3, 1 March 2001 (2001-03-01), pages 440-451, XP055743230, USA ISSN: 0018-9200, DOI: 10.1109/4.910483 figures 1, 8 page 446, left-hand column, line 41 - page 447, right-hand column, line 3 -----	1-69
X	EP 1 556 968 A2 (ADVANCED MICRO DEVICES INC [US]) 27 July 2005 (2005-07-27) figures 1,2 paragraphs [0005] - [0006] paragraph [0009] paragraphs [0029] - [0044] ----- -/--	1-69

[X] Further documents are listed in the continuation of Box C.

[X] See patent family annex.

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1

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C(Continuation). DOCUMENTS CONSIDERED TO BE RELEVANT

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X A	US 2012/083205 A1 (MARCU CRISTIAN [US] ET AL) 5 April 2012 (2012-04-05) figures 2, 7, 11 paragraphs [0005] - [0007] paragraphs [0030] - [0036] paragraphs [0040] - [0041] paragraph [0045] -----	1-42, 44-69 43

1

INTERNATIONAL SEARCH REPORT

Information on patent family members

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