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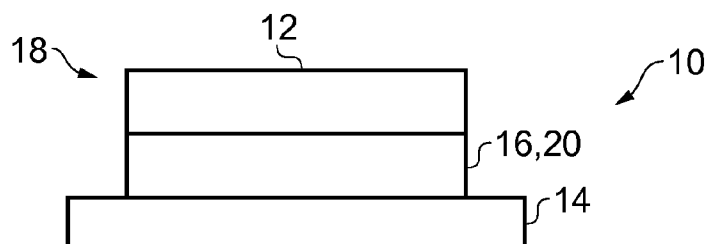


FIG. 1

(57) Abstract: An apparatus comprising: a module; a substrate; and electrolyte between the module and the substrate, wherein an electronic component is formed between the module and the substrate and wherein the electrolyte is configured to function as the electrolyte in the electronic component and also as the adhesive to attach the module to the substrate.



TITLE

MODULAR ELECTRONICS APPARATUSES AND METHODS

TECHNOLOGICAL FIELD

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Examples of the present disclosure relate to modular electronics apparatuses and methods. Some examples relate to modular electronics apparatuses and methods in attaching at least one module to a substrate.

10 BACKGROUND

In electronics, such as flexible and printed electronics, a modular approach may be adopted in which various flexible or rigid components are populated and joined up on a carrier substrate to form an electronic device.

15

In such an approach the modules require control and power.

BRIEF SUMMARY

20 According to various, but not necessarily all, examples of the disclosure there is provided an apparatus comprising: a module; a substrate; and electrolyte between the module and the substrate, wherein an electronic component is formed between the module and the substrate and wherein the electrolyte is configured to function as the electrolyte in the electronic component and also as the adhesive to attach the
25 module to the substrate.

According to various, but not necessarily all, examples of the disclosure there is provided a method comprising: providing a substrate; providing electrolyte on the substrate; and positioning a module on the electrolyte wherein an electronic
30 component is formed between the module and the substrate and wherein the electrolyte functions as the electrolyte in the electronic component and also as the adhesive to attach the module to the substrate.

According to various, but not necessarily all, examples of the disclosure there is provided an electronic device comprising an apparatus, the apparatus comprising a module; a substrate; and electrolyte between the module and the substrate, wherein an electronic component is formed between the module and the substrate and
5 wherein the electrolyte is configured to function as the electrolyte in the electronic component and also as the adhesive to attach the module to the substrate.

The electronic device may comprise a plurality of apparatuses as described in the paragraphs above. The electronic components of the plurality of apparatuses may
10 comprise an electrochemical transistor and the apparatuses may be electrically routed such that the modules are electrically in parallel with the respective electrochemical transistors and are short circuited by the respective transistors when the modules are not being addressed.

15 According to various, but not necessarily all, there is provided an apparatus comprising a module; a substrate; and electrolyte between the module and the substrate, wherein an electronic component is created between the module and the substrate and wherein the electrolyte is configured to function as the electrolyte in the electronic component and also as the adhesive to attach the module to the
20 substrate

BRIEF DESCRIPTION

For a better understanding of various examples that are useful for understanding
25 the brief description, reference will now be made by way of example only to the accompanying drawings in which:

Fig 1 illustrates an example of an apparatus;

Fig 2 illustrates an example of an apparatus;

Fig 3 illustrates an example of an apparatus;

30 Fig 4 illustrates an example of an apparatus;

Fig 5 illustrates an example of a layout for routing to address an array of modules;

Fig 6 illustrates an example of an apparatus;

Fig 7 illustrates an example layout of a module;

Fig 8 illustrates an example of a layout for routing to address an array of modules;

Fig 9 illustrates an example of an apparatus;

Fig 10 illustrates an example of an apparatus;

Fig 11A illustrates an example of a plurality of modules attached using electrolyte to a substrate;

Fig 11B illustrates an example of a device architecture formed as an array of modules;

Fig 12 illustrates an example of a method; and

Fig 13 illustrates an example of a method.

DETAILED DESCRIPTION

Examples of the disclosure relate to an electronic component formed between a module and a substrate. In examples, the electronic component comprises electrolyte and the electrolyte functions as the electrolyte in the electronic component and also as the adhesive to attach the module to the substrate.

In examples the electronic component may be any electronic component that comprises electrolyte.

In some examples, a transistor such as an electrochemical transistor may be formed between the module and the substrate. In such examples, the electrolyte may function as the electrolyte in the electrochemical transistor and also as the adhesive to attach the module to the substrate.

In examples, the electrochemical transistor may be used for read out and/or control of circuitry on the module. In some examples electrochemical transistors may be formed as individual modules are attached to the substrate and an active matrix backplane for the modules may therefore be formed as the modules are assembled onto the substrate.

In some examples, an energy storage component such as a supercapacitor or a battery may be formed between the module and the substrate. In such examples,

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the electrolyte may function as the electrolyte in the energy storage component and also as the adhesive to attach the module to the substrate.

5 In some examples the energy storage component may supply the corresponding module with power without use of long electrical connections. Additionally or alternatively the energy storage component may be optimized for the corresponding module.

10 Examples of the disclosure provide the technical effect of forming the electronic component beneath the module when the module is assembled on the substrate. This simplifies the manufacturing procedure.

15 In addition, the use of the electrolyte as the electrolyte in the electronic component and also as an adhesive also simplifies the manufacturing procedure and decreases the materials involved.

The Figures illustrate an apparatus 10 comprising a module 12, a substrate 14 and electrolyte 16 between the module 12 and the substrate 14, wherein an electronic component 18 is formed between the module 12 and the substrate 14 and wherein
20 the electrolyte 16 is configured to function as the electrolyte 16 in the electronic component 18 and also as the adhesive 20 to attach the module 12 to the substrate 14.

Fig. 12 illustrates a method 100 comprising providing a substrate 14, providing
25 electrolyte 16 on the substrate 14 and positioning a module 12 on the electrolyte 16 wherein an electronic component 18 is formed between the module 12 and the substrate 14 and wherein the electrolyte 16 functions as the electrolyte 16 in the electronic component 18 and also as the adhesive 20 to attach the module 12 to the substrate 14.

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Fig. 1 illustrates an example of an apparatus 10. In the illustrated example, the apparatus 10 comprises a module 12, electrolyte 16 and a substrate 14. An

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electronic component 18 is formed between the module 12, the substrate 14 and comprises the electrolyte 16.

5 The electrolyte 16 is configured to function as the electrolyte 16 in the electronic component 18 and also as the adhesive 20 to attach the module 12 to the substrate 14.

In examples, the module 12 may be a single component or may be a circuit made up of several components. The module 12 may be rigid or flexible.

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The module 12 may comprise any suitable module 12 for use in a flexible or rigid electronic device such as a wearable device, a mobile telephone, a tablet computer, a laptop computer and so on.

15 In some examples, when the module 12 is assembled onto a substrate 14 and joined up to another module or modules, a functioning electronic device such as those mentioned above may be formed.

20 The module 12 may comprise/be a module configured to obtain data and/or a module configured to transmit data and/or a module configured to receive data and/or a control module and/or a module configured to process data.

In some examples the module 12 may be considered a module for obtaining data and/or transmitting data and so on.

25

For example, the module 12 may comprise/be a sensor module and/or a transistor module and/or a signal processing module and/or a memory module and/or a display module and/or a data processing module.

30 In examples the module 12 may comprise display pixels such as indicator light emitting diodes (LEDs).

The module 12 may be fabricated, in some examples, using standard flexible printed circuit board processes with copper on polyimide.

5 In the example of Fig 1 the substrate 14 is configured to function as a carrier substrate for the module 12.

The substrate 14 may be made from any suitable material or materials. In some examples, the substrate may be made from one or more plastics such as polyethylene terephthalate (PET) and so on.

10

The substrate 14 may be rigid or flexible. In the example of Fig. 1 the substrate is illustrated as being slightly larger than the module 12. However, in examples, the substrate 14 may be larger or smaller than the substrate illustrated in the example of Fig. 1. For example, the substrate 14 may be large enough to allow attachment
15 of a plurality of modules 12 to the substrate 14.

In examples, any number of modules 12 may be attached to the substrate 14.

20 In the example of Fig. 1, the electrolyte 16 is located between the module 12 and the substrate 14. In some examples, there may be one or more intervening elements between the electrolyte 16 and the module 12 and/or substrate 14. That is, the module 12 may be directly attached to the substrate 14 using the electrolyte 16, and in such examples there are no intervening elements. In other examples the module 12 may be indirectly attached to the substrate 14 and in such examples there are
25 one or more intervening elements at least partially between the module 12 and the electrolyte 16 and/or the electrolyte 16 and the substrate 14.

In examples, intervening elements may comprise one or more electrodes and/or routing and/or electrical contacts and so on. In some examples there may be one or
30 more electrodes 22 on the substrate 14 and/or module 12 (see, for example, Fig. 2).

The electrolyte 16 is configured to function as the electrolyte 16 in the electronic component 18 and also as the adhesive to attach the module 12, directly or indirectly, to the substrate 14.

- 5 In examples, the electrolyte 16 may be a gel electrolyte or a solid electrolyte.

As used herein, the terms gel electrolyte and solid electrolyte are intended to mean the state of the electrolyte 16 at room temperature and/or the normal operating temperature of the electronic component 18. For example, the electrolyte 16 may be printed onto the substrate 14 and solidified at room temperature. In some examples the electrolyte 16 may be printed by heating the electrolyte 16 to temperatures up to approximately 150°C during printing. The electrolyte 16 may then solidify gradually when cooled back down to room temperature.

- 10 In some examples, the electrolyte 16 may comprise poly vinyl acetate/phosphoric acid (PVA/H₃PO₄) and/or poly vinyl acetate/sulfate acid (PVA/H₂SO₄) gel electrolyte.

Additionally or alternatively the electrolyte 16 may comprise polyethylene oxide (PEO) doped with a lithium salt (for example LiClO₄).

In general, any suitable electrolyte 16 may be used that is configured to function as the electrolyte 16 in the electronic component 18 and also as the adhesive 20 to attach the module 12 to the substrate 14.

25 In examples the electrolyte may bond the module 12 to the substrate 14 using chemical bonding and so on.

In some examples the module 12 may be placed onto the electrolyte 16 before the electrolyte has solidified to allow the module 12 to be attached to the substrate 14 once the electrolyte 16 has solidified.

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The electronic component 18 may comprise any electronic component comprising electrolyte, for example a control component, and/or a storage component. In examples the electronic component may comprise/be an electrochemical transistor 30 (see, for example, Figs. 3 and 4).

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In some examples, the electrochemical transistor 30 may be configured to control circuitry 40 (see Fig, 2) on the module 12 and/or may be configured to be used to read out the circuitry 40 on the module 12.

10 In examples, an array of modules may be attached to the substrate 14 in this way and, in some examples an active matrix backplane for the module array may be formed directly when the modules 12 are assembled onto the substrate 14 (See Figs 5 and 8).

15 In some examples, the electronic component 18 may comprise/be an energy storage component 44 (see, for example, Figs. 9 and 10). The energy storage component 44 may be configured to provide power to the module 12, for example to circuitry 40 on module 12.

20 In examples, the energy storage component 44 may be a supercapacitor 52 or a battery 50 (see Figs. 9 and 10).

The energy storage component 44 may be optimised for the module 12. For example at least one characteristic of the energy storage component 44 may be
25 optimized for the corresponding module 12. In examples the output voltage and/or power density and/or energy density and/or shape and/or size of the energy storage component 44 may be individualized for the corresponding module 12.

In examples, the apparatus 10 may comprise one or more electrical connections
30 between substrate 14 and module 12.

Fig. 2 illustrates a further example of an apparatus 10. In the example of Fig. 2, the apparatus comprises some elements of the apparatus 10 described in relation to Fig. 1 and comprises additional elements.

5 In the example of Fig. 2 the apparatus 10 comprises a further substrate 24 on the substrate 14. The further substrate 24 comprises an opening 26. In some examples, the further substrate 24 may comprise a plurality of openings of the same or different sizes. The opening or openings may be cut out openings and so on formed using laser cutting, for example. In some examples the further substrate 24 may be
10 formed on the substrate 14 using wet-coating/patterning methods, for example by printing UV-curable polymer material to form a continuous substrate layer except where the openings are located followed by a UV-curing process action to solidify the polymer layer.

15 The further substrate 24 may be made of any suitable material. For example, the further substrate may be made from plastic or plastics such as PET and so on. The further apparatus 24 may be provided on the substrate 14 using any suitable method. For example the further substrate may be laminated on to the substrate
20 14.

In examples the further substrate 24 may be directly or indirectly on the substrate 14 and there may be any number of intervening elements at least partially between the substrate 24 and further substrate 24.

25 In Fig. 2, the electrolyte 16 and the module 12 are located in the opening 26 of the further substrate 24. For example, the electrolyte 16 may be deposited in the opening 26 and the module 12 placed on top of the electrolyte 16 in the opening 26.

In the example of Fig. 2, the module 12 and electrolyte 16 are illustrated as entirely
30 within the opening 26. In some examples, the electrolyte 16 and module 12 may be located at least partially in the opening 26 of the further substrate 24. For example, some of the module and/or electrolyte 16 may protrude above the surface of the further substrate 24.

In Fig. 2, the substrate 14 comprises an electrode 22 and the module 12 comprises an electrode 22.

5 The electrodes 22 may be made from any suitable material/materials. For example, the electrodes 22 may be made from any suitable conducting material such as silver or copper coated with conductive carbon and so on. In examples the electrodes 22 may be provided using any suitable method/methods. For example, the electrodes 22 may be formed by printing or coating methods.

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In the example of Fig. 2 the electrodes 22 are connected to routing 28 on the substrate 14 and further substrate 28. The electrode 22 on the substrate 14 is connected to the routing 28 on the first substrate 14 and the electrode 22 on the module 12 is connected to the routing 28 on the further substrate 24. The routing 15 28 illustrated in Fig. 2 is for the electronic component 18.

In examples, the routing may comprise any suitable material/materials and may be provided using any suitable method. For example, the routing may comprise any suitable conductive material such as printed silver.

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In some examples, the substrate 14 and/or the module 12 may comprise a plurality of electrodes 22.

For example, in examples where the electronic component 18 is/comprises an 25 electrochemical transistor 30, the source and drain electrodes of the electrochemical transistor 18 may be located on the module 12 and the gate electrode of the electrochemical transistor 30 may be located on the substrate 14. In other examples, the gate electrode of the electrochemical transistor 30 may be located on the module 12 and the source and drain electrodes of the electrochemical transistor 30 may be 30 located on the substrate 14.

The module 12 comprises circuitry 40 configured to provide the functionality of the module 12. For example, the module 12 may comprise sensor circuitry and/or data processing circuitry and so on.

- 5 In some examples, the electrochemical transistor 18 may be configured to control the circuitry 40 and/or read out the circuitry 40 of the module 12. For example the electrochemical transistor 30 may be coupled in parallel with the circuitry 40 on the module 12.
- 10 In some examples, the circuitry 40 of the module 12 may be configured to control the gate voltage of the electrochemical transistor 30.

In examples where the electronic component 18 is/comprises an energy storage component 44, such as a battery 50 or supercapacitor 52, the module 12 may
15 comprise at least one of the anode 46 or cathode 48 of the energy storage component 44 and the substrate 14 may comprise the counter electrode.

Fig. 3 illustrates an example of an apparatus 10. The apparatus 10 may be the apparatus 10 of Fig 1 or 2.

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Fig. 3 also illustrates an example of assembly of a module 12 onto a substrate 14 to form an electrochemical transistor 30 between the module 12 and the substrate 14.

- 25 The example of Fig. 3 comprises four parts. These have been labelled a - d in Fig. 3. The left column of Fig 3 illustrates an overhead view and the right column a cross sectional view.

Part a of Fig. 3, illustrates an example of a module 12. In the illustrated example,
30 the underside of the module 12 comprises a source electrode 32 and a drain electrode 34. In examples, the drain and source electrodes 34, 32 may be formed using any suitable method and may comprise any suitable material/materials. For

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example, the drain and source electrodes 34, 32 may be formed by patterning conductive carbon and so on.

5 The module 12 illustrated in Fig. 3 also comprises a channel 38 in contact with the source electrode 32 and drain electrode 34.

The channel 38 may be formed using any suitable method and may comprise any suitable material/materials. For example, the channel 38 may comprise PEDOT:PSS.

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The module 12 in the example of Fig 3 also comprises contact electrodes 22 on the upper surface of the module 12. One of the contact electrodes 22 is connected to the source electrode 32 and the other to the drain electrode 34.

15 The contact electrodes are connected to the respective source and drain electrodes 32, 34 using electrical connections 54 which in the illustrated example are vias 56.

In the example of Fig. 3, the module 12 comprises circuitry 40. The circuitry 40 is configured to provide the functionality of the module 12. For example, the circuitry
20 40 may comprise sensor circuitry and/or transistor circuitry and/or data processing circuitry and/or display circuitry and so on.

In Fig. 3, the source electrode 32 and drain electrode 34 are covered by an insulator 58. Any suitable material may be used for the insulator 58. For example SU-8
25 polymer dielectric may be used.

The insulator is configured to prevent the source and drain electrodes 32, 34 from being in contact with the electrolyte 16 when the module 12 is positioned upon the electrolyte 16 to form the electrochemical transistor 30.

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Part b of Fig. 3 illustrates an example of the substrate 14 which in this example is the target substrate for the module 12 illustrated in part a of Fig. 3. Substrate 14 may be as described above in relation to Figs. 1 and 2.

The substrate 14 comprises a gate electrode 36. The gate electrode 36 is on the upper surface of the substrate 14 and may be formed by any suitable method using any suitable material/materials. For example, gate electrode may be formed from PEDOT:PSS.

In part b of the example of Fig. 3, the substrate 14 comprises routing 28 connected to the gate electrode 36. In the illustrated example the routing 28 is printed silver but any suitable routing 28 may be used.

In the example of Fig. 3, the routing 28 is connected to the gate electrode 36 using a contact 60. The contact 60 may be formed from any suitable material/materials using any suitable method. In the example of Fig. 3, the contact 60 is formed from conductive carbon.

The substrate 14 also comprises an insulator 58 covering the contact 60 and routing 28. The insulator 58 may be any suitable material/materials as described above with regard to the insulator of the source/drain electrodes 32, 34 in part a of Fig. 3.

The insulator 58 is configured to prevent the contact 60 and routing 28 from being in contact with the electrolyte 16 when the electrolyte 16 is provided on the substrate 14.

In part c of the example of Fig. 3 electrolyte 16 has been provided on the substrate 14. The electrolyte 16 may be any suitable electrolyte as described above in relation to Figs. 1 and 2. The electrolyte 16 may be applied to the substrate 14 using any suitable method. For example, the electrolyte may be deposited over the substrate 14, including the gate electrode 36, by printing.

In part c of the examples of Fig. 3 the gate electrode 36 is illustrated in the cross section, however, the routing 28, insulator 58 and contact 60 are not illustrated for the sake of clarity.

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In the example of part c of Fig. 3 the module 12 is shown being positioned upon the electrolyte 16 on the substrate 14. The module 12 may be positioned on the electrolyte 16 using any suitable method. For example, the module 12 may be picked and placed upon the electrolyte 16.

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The electrochemical transistor 30 is formed when the module 12 is placed onto the electrolyte 16 and aligned over the gate electrode 36 as illustrated in the example of part d of Fig. 3.

10 Part d of Fig. 3 therefore illustrates an example of the apparatus 10 illustrated in Figs. 1 and 2 when the electronic component 18 is an electrochemical transistor 30.

As in part c of the example of Fig. 3, in part d the gate electrode 36 is illustrated on the substrate 14 in the cross section but the other components on the substrate 14
15 are not shown in the cross section for the sake of clarity.

In the example of Fig. 3 the electrolyte 16 is configured to function as the electrolyte 16 for the electronic component 18, which is an electrochemical transistor 30, and also as the adhesive 20 to attach the module 12 to the substrate 14.

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Fig. 4 illustrates a further example of an apparatus 10. Fig. 4 also illustrates a further example of assembly of a module 12 on to a substrate 14, where an electrochemical transistor 30 is formed between the module 12 and the substrate 14.

25 In the example of Fig. 4 the overhead view illustrated in the left column shows two gate electrodes 36 and associated routing 28, contact 60 and insulator 58.

However, the right column of Fig. 4 illustrates only one of the gate electrodes 36 as shown by the dashed line in the overhead view of part a of Fig. 4.

30

Part a in the example of Fig. 4 is similar to part b in the example of Fig. 3. Illustrated in part a of Fig. 4 is a substrate 14 comprising a gate electrode 36 routing 28, a contact 60 and insulator 58.

However, in the example illustrated in Fig. 4 a further substrate 24 is provided on the substrate 14. As illustrated in part b of Fig. 4 the further substrate 24 is provided on top of the elements on the substrate 14.

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The further substrate 24 may be made of any suitable material/materials and may be provided on the substrate 14 using any suitable method. For example, the further substrate may be laminated onto the substrate 14. In examples the further substrate 24 may be as described above with reference to Fig 2.

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The further substrate 24 in the example of Fig. 4 comprises a plurality of openings 26. The openings 26 are located above the gate electrodes 36. The openings 26 may be formed in the further substrate 24 using any suitable method. For example, the openings may be cut out openings and so on formed using laser cutting, for example. In some examples the further substrate 24 may be formed on the substrate 14 using wet-coating/patterning methods, for example by printing UV-curable polymer material to form a continuous substrate layer except where the openings are located followed by a UV-curing process action to solidify the polymer layer.

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In part c of the example of Fig. 4 the electrolyte 16 has been provided in the openings of the further substrate 24. This is illustrated in the overhead view by the squares in part c of Fig. 4.

25 As with the example of Fig. 3, in the cross section of part c of Fig. 4 the routing 28 and so on have been omitted for the sake of clarity.

In part c of the example of Fig. 4 a module 12 is illustrated being positioned on the electrolyte 16 in the opening 26 of the further substrate 24.

30

The module 12 may be as described above in relation to Figs. 1 to 3. In some examples, the module 12 is the same as the module 12 illustrated Fig. 3.

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As illustrated in part d of the example of Fig. 4 the module 12 is positioned on the electrolyte 16 to form the electrochemical transistor 30 between the module 12 and the substrate 14.

- 5 In part d of the example of Fig. 4 the source and drain electrodes 32, 34 are indicated at the electrical contacts on the top surface of the module 12. In some examples, routing 28 may be connected to the source and drain electrodes 32, 34 using the electrical contacts 22 on the upper surface of the module 12.
- 10 In some examples fewer or more modules may be used than those illustrated in the example of Fig. 4. For example, in some examples a single module 12 may be located in an opening 26 of a further substrate 24. In other examples, a greater number of modules may be located in openings 26 of further substrates 24, for example a number of modules 12 may be connected together in such a manner to
- 15 provide a functioning electronic device.

In some examples, routing 28 on the substrate 14 may connect the electronic component 18, which is in this example an electrochemical transistor 30, row-wise and routing 28 on the further substrate 24 may connect the electronic component

20 18 column-wise.

Fig. 5 illustrates an example of a layout for routing to address an array of modules.

- In part a of the example of Fig. 5, four gate electrodes 36 and the associated routing
- 25 28 and connectors and so on are illustrated. The gate electrodes 36 and so on may be as those described above in relation to Figs. 3 and 4.

- In part a of the example of Fig. 5 the gate electrodes are interconnected row-wise as illustrated by the V_{G1} and the V_G which represent the gate voltages that may be
- 30 applied to the gate electrodes 36. The gate electrodes 36 are connected using routing 28 on the substrate 14.

At part b of the example of Fig. 5 a further substrate 24 is provided on the substrate 14. This may be as described above in relation to Fig. 4. Four openings 26 are left above the gate electrodes 36 as an example of Fig. 4.

5 At part c of the example of Fig. 5 four modules 12 are positioned in the openings 26.

In the example of Fig. 5 the modules 12 are sensor modules, however in other examples any suitable type of module 12 may be used.

10 In Fig. 5 the modules 12 have electrodes 22 on the upper surface connected to the source and drain electrodes 32, 34 (see Fig 4).

In the example of Fig. 5 the sensor modules 12 have variable impedance, denoted as Z1, Z2, Z3 and Z4. In the example, the sensor circuitry is located on the upper
15 surface of the modules 12 and is electrically connected between the electrodes 22 on the upper surface of the modules 12 for the drain electrode 34 and source electrode 32.

An electrochemical transistor is formed between the substrate 14 and the modules
20 12.

As illustrated in part d of the example of Fig. 5 the sensor modules 12 are electronically connected in series column-wise using routing on the further substrate
24.

25 The sensor modules of part d of the example of Fig. 5 are therefore electrically connected in parallel with the electrochemical transistor 30 formed between the substrate 14 and the modules 12.

30 In the example of Fig. 5, when no gate voltage is applied the electrochemical transistors are in the conducting on state and therefore the sensor modules 12 are short circuited.

In order to take a measurement from a sensor module 12 a gate voltage, for example V_{G1} or V_{G2} , is applied to the required gate row causing the electrochemical transistors 30 in that row to be in the non-conducting off state. The corresponding sensor modules of the row having the gate voltage applied will no longer be short circuited.

The impedance of the selected column may then be measured. As the other sensors in that column will remain short circuited due to their corresponding electrochemical transistors 30 being in the conducting on state a measurement of the required module 12 may be taken.

For example, with reference to the example of Fig. 5, if a measurement of the module 12 having impedance marked as Z1 was to be taken, a gate voltage V_{G1} may be applied to the upper row of gate electrodes. This would place the electrochemical transistors 30 corresponding to sensors having impedance Z1 and Z2 in Fig 5 into the non-conducting off state.

The impedance of the column marked V_{D1} could then be measured to take a reading of the impedance Z1. The module having impedance Z3 would remain short circuited in this example as no gate voltage V_{G2} is being applied.

A layout such as the one illustrated in the example of Fig. 5 provides a technical benefit as the modules 12 are connected without the use of crossovers and therefore a simplified fabrication process is provided.

A modular array formed with an active matrix backplane according to examples of the disclosure may be assembled fully by scalable printing processes.

Electrochemical transistors, as used in examples of the disclosure, can be switched at low voltages (approximately 1.5 V) and therefore a backplane as in examples of the disclosure is compatible with flexible power sources such as a printed battery.

Fig. 6 illustrates a further example of an apparatus 10. In some examples, the apparatus 10 of Fig. 6 may be as described above in relation to Figs. 1 and 2.

5 In the example of Fig. 6, the electronic component 18 formed between the module 12 and the substrate 14 is an electrochemical transistor 30.

10 However, in the example of Fig. 6, the gate electrode 36 is located on the underside of the module 12 and the source and drain electrodes 32, 34 are located on the substrate 14. The channel 38 is also located on the substrate 14 in the example of Fig. 6.

15 The substrate 14 in example of Fig. 6 may be similar to the substrate 14 illustrated in the example of Figs. 3 to 5. In Fig 6 the substrate 14 comprises insulator 58, module 12, substrate 14, circuitry 40, source electrode 32 and drain electrode 34.

In the example of Fig. 6 the source and drain electrodes 32, 34 are connected to electrodes 22 on the underside of the substrate 14 using electrical connections 54 that may be vias 56.

20 The apparatus 10 illustrated in the example of Fig. 6 is therefore similar to the apparatus illustrated in the examples of Figs. 3 to 5 however in the example of Fig. 6 the electrodes and other elements on the module 12 and substrate 14 have been swapped.

25 In the example of Fig. 6 the source electrode 32 and drain electrode 34 are formed on the substrate 14. This may be advantageous as these electrodes may be patterned via lithography processes, for example, to achieve high resolution. Additionally or alternatively the module 12 may be fabricated by printing processes for example.

30 The apparatus 10 in Fig 6 comprises electrolyte 16 between the module 12 and substrate 14. The electrolyte 16 may be any suitable electrolyte and may be provided using any suitable method. The electrolyte is configured to function as the

electrolyte for the electronic component 18, which in the example of Fig 6 is an electrochemical transistor 30, and as the adhesive attaching the module 12 to the substrate 14.

- 5 The module 12 of Fig 6 also comprises circuitry 40 configured to provide the functionality of the module 12. The circuitry 40 may be as described above with reference to Fig 2.

Fig. 7 illustrates an example layout of a module 12. For example, a module 12 as
10 illustrated in the example of Fig. 6. The module 12 may, in some examples, be a sensor module. In other examples the module 12 may be any suitable module 12.

In the example of Fig. 7, a gate voltage V_G is applied across the module 12 and divided over the sensor impedance, illustrated as Z_S , and a reference impedance of
15 a resistor, illustrated as Z_{REF} .

The voltage potential set by the sensor is coupled to the gate electrode 36 using an electrical connection 54 which may be a via 56. When a module 12 comprising sensor layout illustrated in Fig. 7 is positioned on the electrolyte 16 as, for example,
20 in Fig. 6, the gate electrode 36 will modulate the channel resistance of the electrochemical transistor according to the impedance of the sensor Z_S of the module 12.

In examples, therefore, the module 12, such as a sensor module, may control the
25 voltage applied to the gate electrode 36 of the electrochemical transistor 30.

In other examples, other layouts for modules 12 may be used.

Fig. 8 illustrates an example of a layout for routing to address an array of modules.
30 For example, the layout of the example of Fig 8 may be used to address modules as illustrated in the examples of Fig. 6 and 7.

In part a of the example of Fig. 8 source and drain electrodes 32, 34 are provided on a substrate 14. The source and drain electrodes 32, 34 may be applied using any suitable method and may comprise any suitable material/materials. For example, the source and drain electrodes 32, 34 may comprise any suitable conducting material such as conductive carbon.

As illustrated in part a of example of Fig. 8 the source and drain electrodes 32, 34 are connected column-wise to ground. A voltage V_{D1}/V_{D2} may be applied to the column-wise routing.

In part b of the example of Fig. 8 the channel 38 has been provided for each source and drain electrode 32, 34.

Any suitable material/materials may be used for the channel 38 and the channel 38 may be applied using any suitable method. For example, the channel 38 may be patterned onto the substrate 14.

In addition, in part b of the example of Fig. 8 insulator 58 has been applied to the source and drain electrodes as, for example, illustrated in the example of Fig. 6.

At part c of the example of Fig. 8 a further substrate 24 is provided on the substrate 14. The further substrate 24 may comprise any suitable material and may be provided on the substrate 14 using any suitable method. For example, the further substrate may be laminated over the first substrate 14.

Similarly to the example of Fig. 5, at part c of the example of Fig. 8 openings 26 in the further substrate 24 are located above the electrodes and other elements on the substrate 14.

At part d of the example of Fig. 8 electrolyte 16 is provided in the openings 26 and modules 12 positioned on the electrolyte 16 in the openings 26.

In the example of Fig. 8 the modules 12 may be modules as illustrated in the examples of Figs. 6 and 7 above. In the example of Fig. 8, the modules 12 are sensor modules comprising the layout illustrated in the example of Fig. 7.

- 5 At part d of the example of Fig. 8 the modules are connected row-wise. In the illustrated example, the modules 12 in a row are connected to the same ground. A gate voltage V_{G1} or V_{G2} may be applied to the connected row-wise modules.

10 In order to address a module 12 in the example of Fig. 8 a gate voltage V_{G1} or V_{G2} is applied to the row comprising the module 12 to be addressed. The impedance of the column, between V_{D1} and ground or V_{D2} and ground, may then be measured.

15 In the example of Fig. 8 the row without the gate voltage applied will have zero gate voltage and the corresponding electrochemical transistors 30 will be in the conducting on state. The modules 12 of that row will therefore be short circuited.

Accordingly, when measuring the impedance of the selected column it will correspond to the channel resistance of the selected module 12 which is modulated by the impedance of the sensor in question of the measured module 12.

20

For example, to take a reading of the upper left module 12 in the example of Fig. 8 a gate voltage V_{G1} may be applied to the upper row which would apply a voltage to the gate electrodes of the transistors 30 in the upper row.

- 25 The impedance between V_{D1} and ground could then be measured and would correspond to the impedance of the sensor of the module 12 to be measured.

This is because the lower left module 12 in the same row would have no gate voltage applied and therefore would remain short circuited.

30

Accordingly, the impedance of the column V_{D1} will correspond to the channel resistance of the module to be measured 12 which corresponds to the impedance of the sensor on the module 12.

In the examples of Figs. 1 to 8 the electrolyte 16 has two functions. The matrix of the electrolyte acts as a host for the ions, producing a solid ion conducting polymer/salt complex and also acts as an adhesive attaching the module 12 to the substrate 14.

Fig. 9 illustrates an example of an apparatus 10. Fig. 9 also illustrates an example of assembly of a module 12 onto a substrate 14 to form a energy storage component 44 between the module 12 and the substrate 14. The apparatus 10 may be as described above in relation to Figs 1 and/or 2.

In the example of Fig. 9, three parts are illustrated. These have been labelled a to c in Fig. 9. In the example of Fig. 9, the left column illustrates an overhead view and the right column a cross sectional view.

Part a in the example of Fig. 9 illustrates a substrate 14. An electrode 22 is provided on the substrate 14. The electrode 22 may comprise any suitable material/materials and may be formed by any suitable method. The electrode 22, in some examples may comprise activated carbon and/or carbon black ink mixed with binder.

The electrode 22 may be patterned on the substrate 14 at the location that the module 12 will be located. In some examples, the electrode 22 may be formed by printing a metallic conductor configured to serve as the current collector and printing electrode inks.

In examples, the current collector may comprise a film of conductive metal such as silver, copper, aluminium or any other suitable material.

The electrode 22 in part a of the example of Fig. 9 may form an anode 46 and/or a cathode 48 of an energy storage component 44.

Part b of the example of Fig. 9 illustrates a module 12. The module 12 may be as illustrated in the examples of Figs. 1 and/or 2.

In Fig. 9 the module 12 comprises an electrode 22 on the underside of the module 12, the electrode 22 on the module 12 may form the counter electrode to the electrode 22 on the substrate 14.

5

The electrode 22 of the module 12 may comprise any suitable material/materials and may be formed using any suitable method. The electrode 22 on the module 12 may comprise the same material as the electrode 22 on the substrate 14. In other examples, the electrode 22 on the module 12 may comprise a different material than the electrode 22 on the substrate 14.

10

In the illustrated example, the electrode 22 on the underside of the module 12 comprises the same material as the electrode 22 on the substrate 14.

15

In the example of Fig 9, the module 12 also comprises two electrodes 22 on the upper surface of the module 12.

As illustrated in part b of the example of Fig. 9 an electrical connection 54, such as a via 56, may electrically connect the electrode 22 on the underside of the module 12 to one of the electrodes 22 on the topside of the module 12. In the example of Fig 9 the left electrode 22 on the topside of the module 12 is electrically connected to the electrode 22 on the underside of the module 12.

20

In the example of Fig. 9 the electrodes 22 on the substrate 14 and the underside of the module 12 form supercapacitor electrodes. In Fig 9, the electrode 22 on the underside of the module 12 is coated with electronically functional layers to form a supercapacitor electrode.

25

The module 12 comprises circuitry 40. The circuitry 40 may be any suitable circuitry configured to provide functionality of the module 12 such as sensor circuitry, data processing circuitry, transistor circuitry and so on.

30

25

In part b of the example of Fig. 9 electrolyte 16 is provided on the electrode 22 on the substrate 14.

5 The electrolyte may be any suitable electrolyte and may be provided using any suitable method. For example, the electrolyte may be as described in relation to Figs 1 and/or 2. In examples the electrolyte 16 may comprise polyvinyl acetate/phosphoric acid (PVA/H₃PO₄) or polyvinyl acetate/sulphate acid (PVA/H₂SO₄) gel electrolyte.

10 Part b of the example of Fig. 9 illustrates the module 12 being positioned on the electrolyte 16. The supercapacitor is formed with the module 12 being positioned on the electrolyte 16.

15 The electrolyte 16 is configured to function as the electrolyte for the electronic component 18, which in this example is an energy storage component 44, and is also configured to function as the adhesive 20 to attach the module 12 to the substrate 14.

20 In part c of the example of Fig. 9 an interconnect 62 connects the electrode 22 on the right of the top side of the module to the electrode 22 of the substrate 14.

In examples the energy storage component 44 may provide power to the module 12, for example the energy storage component 44 may provide power to the circuitry 40 of the module 12.

25

In Fig 9 the illustrated arrows indicate that the voltage generated by the supercapacitor is available across the electrodes 22 on the topside of the module 12. For example, a component placed between the top electrodes 22 of the module 12 in Fig 9 is coupled in parallel with the supercapacitor and may be powered in this way.

30

In the example of Fig 9 the electrode 22 is electrically connected using routing 28 which may be provided using any suitable method and may comprise any suitable material/materials.

- 5 In other examples, the energy storage component 40 may comprise a battery 50. In such examples, the battery electrodes 22 on the substrate 14 and the underside of the module 12 may be formed using any suitable method. For example the electrodes 22 may be formed by printing a layer of charge collector and then printing the electrodes 22 over the charge collector. The charge collector may be formed
- 10 from any suitable material/materials. For example, the charge collector may comprise a film of a conductive metal such as silver, copper, aluminium or any other suitable material.

In some examples, the energy storage component may comprise a lithium ion

15 battery. In such examples, the first electrode 22 may comprise LiFeO_4 or LiCoO_2 and the second electrode may comprise printed graphite.

Fig. 10 illustrates a further example of an apparatus 10. Fig. 10 also illustrates an example of assembly of modules 12 onto a substrate 14.

20

The example illustrated in Fig. 10 is similar to the example illustrated in Fig. 9. However, the example illustrated in Fig. 10 shows two modules in the overhead view. As in the example of Fig. 4 a single module section is shown in the cross sectional view of Fig. 10 as illustrated by the dashed line in part a of the example of

25 Fig. 10.

25

Fig. 10 illustrates an example of the assembly of modules 12 onto a carrier substrate to form energy storage components 44 between the substrate 14 and the modules 12. The example of Fig. 10 is similar to the example described above with reference

30 to Fig. 9.

30

In the example of Fig. 10 at part b a further substrate 24 is provided on the substrate 14.

The further substrate 24 may comprise any suitable material/materials, for example, plastic such as PET. The further substrate 24 may be provided on the substrate 14 using any suitable method. For example, the further substrate may be laminated
5 onto the substrate 14.

The further substrate comprises a plurality of openings 26. The openings may be provided using any suitable method. For example, the openings may be cut out openings and so on formed using laser cutting, for example. In some examples the
10 further substrate 24 may be formed on the substrate 14 using wet-coating/patterning methods, for example by printing UV-curable polymer material to form a continuous substrate layer except where the openings are located followed by a UV-curing process action to solidify the polymer layer..

15 At part c of the example of Fig. 10 electrolyte 16 is provided in the openings 26 where the module 12 will be located. The electrolyte 16 may be provided using any suitable method, for example, the electrolyte 16 may be printed at room temperature or by heating the electrolyte 16 up to approximately 150°C during printing.

20 As illustrated at part c of the example of Fig. 10 the module 12 is positioned on the electrolyte 16 in the larger opening 26. The electrolyte 16 functions as electrolyte 16 of the energy storage component 44 and as the adhesive 20 to attach the module 12 to the substrate 14. For example, placing the module 12 onto the electrolyte 16 before the electrolyte has solidified allows the module 12 to be attached to the
25 substrate 14 once the electrolyte 16 has solidified.

Part d of the example of Fig, 10 illustrates the module 12 in position in the opening 26 on top of the electrolyte 16.

30 In part e of the example of Fig. 10 an interconnect 62 is provided through a second opening 26 and the further substrate 24 to connect the electrode 22 on the substrate 14 to the electrode 22 on the right on the top side of the module 12. The interconnect

62 may be made of any suitable material/materials and may be provided using any suitable method.

Fig. 11A illustrates an example of a plurality of modules 12 attached using
5 electrolyte 16 to a substrate 14.

In the example of Fig. 11A three modules 12 are illustrated. In other examples fewer or more modules 12 may be included.

10 In the example of Fig. 11A an electronic component 18 is formed between the modules 12 and the substrate 14. In the illustrated example an energy storage component 44 is formed between each of the modules 12 and the substrate 14.

In the example of Fig. 11A therefore each of the modules 12 may be powered by
15 their own energy source formed between the module 12 and the substrate 14.

In the example of Fig 11A an optimised energy storage component 44 may be provided for each of the modules 12. For example, the output voltage, and/or power density, and/or energy density, and/or shape and/or size of each energy storage
20 component 44 may be individualised depending upon the energy requirement of each module 12.

In addition, the energy storage components 44 do not require long conductor wiring to connect to modules 12 and therefore there is less resistive losses in transferring
25 power from energy source to module 12 compared to, for example, a shared energy source.

In the example of Fig. 11A three different types of module 12 are illustrated. The different types of module 12 are indicated in the example of Fig. 11A using lines
30 going from top left to bottom right, bottom left to top right and vertical lines. These lines are only for illustrative purposes and are not intended to imply any functional or structural limitation of the modules 12.

In example of Fig. 11A the module indicated with lines from top left to bottom right comprises a sensor module 64. The module 12 indicated with lines bottom left to top right comprises a transistor module 66 and the module 12 indicated with vertical lines comprises a signal processing module 68.

5

In other examples any combination of different modules and numbers of modules may be used. For example, the modules 12 may be in some examples memory elements, display pixels such as indicator light emitting diodes and/or data processing units and so on.

10

The design illustrated in the example of Fig 11A reduces package, size and cost and increase safety and flexibility of a device.

Fig. 11B illustrates an example of a device architecture formed as an array of modules 12.

15

The example of Fig 11B comprises the three modules 12 illustrated in the example of Fig 11A. The modules 12 in the example of Fig. 11B are indicated using the same markings as those illustrated in the example of Fig. 11A.

20

In the example of Fig. 11B the transistor modules 66 are connected to the signal processing modules 68. A column decoder 72 connects column-wise the columns of sensor modules 64 and signal processing modules 68. In the example of Fig. 11B the column decoder 72 may also comprise a signal multiplexer and/or signal de-multiplexer.

25

A row decoder 70 connects row-wise the connections from the column decoder 72.

In other examples any suitable device architecture of modules 12 may be used.

30

Fig. 12 illustrates an example of a method 100.

30

At block 102, a substrate 14 is provided. The substrate 14 may be made of any suitable material/materials and may be provided using any suitable method.

5 For example, the substrate 14 may be as described above in relation to Figs. 1 and/or 2.

At block 104 electrolyte 16 is provided on the substrate 14.

10 The electrolyte 16 may be any suitable electrolyte 16 and may be provided using any suitable method. For example, the electrolyte 16 may be printed on the substrate 14 and solidified at room temperature. The electrolyte 16 may be printed at elevated temperature up to approximately 150°C.

15 In examples, the electrolyte 16 may be as described above in relation to Figs. 1 and/or 2.

In some examples, the electrolyte 16 may not be provided directly on the substrate 14 but may be provided at least partially on one or more elements on the substrate 14.

20

At block 106 a module 12 is positioned on the electrolyte 16. In examples, an electronic component 18 is formed between the module 12 and the substrate 14 when the module 12 is positioned on the electrolyte 16.

25 In examples, the module 12 may be as described in relation to Figures 1 and/or 2.

The electrolyte 16 functions as the electrolyte 16 in the electronic component 18 and also as the adhesive 20 to attach the module 12 to the substrate 14.

30 The module 12 may be any suitable module 12. For example, the module 12 may comprise a sensor module and/or a signal processing module and/or a transistor module and/or a display module and so on.

In the examples, positioning a module 12 on the electrolyte 16 may comprise providing one or more connections between the substrate 14 and/or an electrical element, such as routing 28. Additionally or alternatively positioning a module 12 on the electrolyte may comprise providing one or more electrodes 22 on the substrate 14 and/or the module 12.

In examples, the actions of blocks 104 and 106 may be repeated to attach a plurality of modules 12 to the substrate 14 and to form an electronic component 18 between each of the modules 12 and the substrate 14. In examples, the electronic component 18 formed between each of the modules 12 may be different or may be the same.

In examples, the electronic component formed may be a transistor such as an electrochemical transistor 30 or an energy storage component 44 such as a battery 50 or a supercapacitor 52.

Fig. 13 illustrates an example of a further method 200.

At block 202 a substrate 14 is provided. Block 202 may be as block 102 described in relation to Fig. 12.

At block 204 at least one electrode is provided on the substrate 14.

The at least one electrode 22 may be made from any suitable material/materials and may be provided using any suitable method. In examples, the at least one electrode 22 may be as described above in relation to Fig. 2.

In some examples, the at least one electrode 22 may comprise a source electrode 32 and drain electrode 34 for a transistor such as an electrochemical transistor 30.

In some examples, a channel 38 for an electrochemical transistor 30 may also be provided.

32

In examples, the at least one electrode 22 may comprise a gate electrode 36 for a transistor, such as an electrochemical transistor 30.

5 In other examples, the at least one electrode may comprise an anode and/or cathode for an energy storage component 44 such as a battery 50 or supercapacitor 52.

At block 206 a further substrate 24 is provided on the substrate 14.

10 The further substrate 24 may be made of any suitable material/materials and may be provided on the substrate 14 using any suitable method. For example, the further substrate 24 may be as described above in relation to Fig. 2.

15 In some examples, the further substrate 24 may be laminated on the substrate 14.

The further substrate 24 may comprise one or more openings. In examples, the openings may be cut out openings and may, for example, be located over the at least one electrode provided on the substrate 14.

20 At block 208 electrolyte 16 is provided on the substrate 14. In examples, block 208 may be as block 104 described in relation to Fig. 12.

25 In examples, the electrolyte 16 may be provided in at least some of the openings 26 in the further substrate 24 on the substrate 14.

At block 210 at least one electrode 22 is provided on a module 12.

The at least one electrode 22 may be as described above in relation to Fig. 2. The module 12 may be as described above in relation to Figs 1 and/or 2.

30

In some examples, the at least one electrode 22 of the module 12 may comprise a source electrode 32 and drain electrode 34 for a transistor such as an

33

electrochemical transistor 30. In some examples a channel 38 may also be provided on the module 12.

5 In other examples, the at least one electrode 22 may comprise a gate electrode 36 for an electrochemical transistor 30.

10 In some examples, the at least one electrode 22 on the module 12 may comprise an anode 46 and/or cathode 48 for an energy storage component 44 such as a battery 50 or a supercapacitor 52.

In examples, the at least one electrode 22 on the module 12 may be the counter electrode/electrodes 22 of the electrode 22 provided on the substrate 14.

15 For example, if the at least one electrode 22 on the substrate 14 comprises a gate electrode 36 the at least one electrode on the module 12 may comprise a source electrode 32 and drain electrode 34 or vice versa.

20 If the at least one electrode on the substrate 22 comprises an anode 46 for an energy storage component 44 the at least one electrode 22 on the module 12 may comprise a cathode 48 for the energy storage component 44.

At block 212 the module 12 is positioned on the electrolyte 16.

25 The module 12 may be positioned on the electrolyte 16 using any suitable method. For example, the module 12 may be pick and placed onto the electrolyte 16.

In examples, the positioning of the module 12 on the electrolyte 16 in the opening 26 may be as described above in relation to Fig. 2.

30 In examples, the electrolyte 16 functions as the electrolyte 16 for an electronic component 18 formed between the module 12 and the substrate 14 and also as an adhesive 20 to attach the module 12 to the substrate 14.

34

For example, an electrochemical transistor 30 or energy storage component 44 may be formed by positioning the module 12 on the electrolyte 16.

At block 214 routing on the substrate 14 and further substrate 24 is provided. The
5 routing may be for the electronic component 18 formed between the module 12 and the substrate 14.

In examples, the routing 28 may be as described above in relation to Fig. 2.

10 In some examples an energy harvesting unit may be included in the apparatus 10. For example the energy storage component 44 of examples of the disclosure may be charged using a energy harvesting unit such as a photovoltaic. Additionally or alternatively the energy storage component of examples of the disclosure may be charged through wireless charging.

15

As used in this application, the term 'circuitry' refers to all of the following:

(a) hardware-only circuit implementations (such as implementations in only analog and/or digital circuitry) and

(b) to combinations of circuits and software (and/or firmware), such as (as
20 applicable): (i) to a combination of processor(s) or (ii) to portions of processor(s)/software (including digital signal processor(s)), software, and memory(ies) that work together to cause an apparatus, such as a mobile phone or server, to perform various functions) and

(c) to circuits, such as a microprocessor(s) or a portion of a microprocessor(s), that
25 require software or firmware for operation, even if the software or firmware is not physically present.

This definition of 'circuitry' applies to all uses of this term in this application, including in any claims. As a further example, as used in this application, the term "circuitry" would also cover an implementation of merely a processor (or multiple processors)
30 or portion of a processor and its (or their) accompanying software and/or firmware.

The term "circuitry" would also cover, for example and if applicable to the particular claim element, a baseband integrated circuit or applications processor integrated

circuit for a mobile phone or a similar integrated circuit in a server, a cellular network device, or other network device.

The blocks illustrated in the Figs 12 and 13] may represent steps in a method and.

5 The illustration of a particular order to the blocks does not necessarily imply that there is a required or preferred order for the blocks and the order and arrangement of the block may be varied. Furthermore, it may be possible for some blocks to be omitted.

10 The term 'comprise' is used in this document with an inclusive not an exclusive meaning. That is any reference to X comprising Y indicates that X may comprise only one Y or may comprise more than one Y. If it is intended to use 'comprise' with an exclusive meaning then it will be made clear in the context by referring to "comprising only one.." or by using "consisting".

15

In this brief description, reference has been made to various examples. The description of features or functions in relation to an example indicates that those features or functions are present in that example. The use of the term 'example' or 'for example' or 'may' in the text denotes, whether explicitly stated or not, that such
20 features or functions are present in at least the described example, whether described as an example or not, and that they can be, but are not necessarily, present in some of or all other examples. Thus 'example', 'for example' or 'may' refers to a particular instance in a class of examples. A property of the instance can be a property of only that instance or a property of the class or a property of a sub-
25 class of the class that includes some but not all of the instances in the class. It is therefore implicitly disclosed that a features described with reference to one example but not with reference to another example, can where possible be used in that other example but does not necessarily have to be used in that other example.

30 Although examples of the disclosure have been described in the preceding paragraphs with reference to various examples, it should be appreciated that modifications to the examples given can be made without departing from the scope of the invention as claimed. For example the size of the electrodes 22 may be varied.

36

In some examples the electrode 22 on the underside of the module 12 can be any arbitrary size smaller than the size of the module 12 for example.

5 Features described in the preceding description may be used in combinations other than the combinations explicitly described.

Although functions have been described with reference to certain features, those functions may be performable by other features whether described or not.

10 Although features have been described with reference to certain embodiments, those features may also be present in other embodiments whether described or not.

15 Whilst endeavoring in the foregoing specification to draw attention to those features of the invention believed to be of particular importance it should be understood that the Applicant claims protection in respect of any patentable feature or combination of features hereinbefore referred to and/or shown in the drawings whether or not particular emphasis has been placed thereon.

I/we claim:

20

CLAIMS

1. An apparatus comprising:
a module;
5 a substrate; and
electrolyte between the module and the substrate, wherein an electronic component is formed between the module and the substrate and wherein the electrolyte is configured to function as the electrolyte in the electronic component and also as the adhesive to attach the module to the substrate.
10
2. An apparatus as claimed in claim 1, wherein at least one electrode of the electronic component is located on the module and at least one electrode of the electronic component is located on the substrate.
- 15 3. An apparatus as claimed in claim 1 or 2, further comprising a further substrate on the substrate, the further substrate comprising at least one opening, wherein the electrolyte and module are located at least partially in the opening of the further substrate.
- 20 4. An apparatus as claimed in claim 3 wherein the apparatus comprises routing for the electronic component on the substrate and on the further substrate.
5. An apparatus as claimed in any preceding claim, wherein the electronic component comprises an electrochemical transistor.
25
6. An apparatus as claimed in claim 5 wherein the source and drain electrodes of the electrochemical transistor are located on the module and the gate electrode of the electrochemical transistor is located on the substrate or the gate electrode of the electrochemical transistor is located on the module and the source and drain
30 electrodes of the electrochemical transistor are located on the substrate.
7. An apparatus as claimed in any of claims 5 to 6 wherein the module comprises circuitry and the electrochemical transistor is configured to control the

circuitry on the module and/or is configured to be used to read out the circuitry on the module.

8. An apparatus as claimed in claim 7, wherein the electrochemical transistor is electrically coupled in parallel with the circuitry on the module.
9. An apparatus as claimed in claim 7, wherein the circuitry of the module is configured to control the gate voltage of the electrochemical transistor.
10. An apparatus as claimed in any of claims 1 to 4, wherein the electronic component comprises an energy storage component.
11. An apparatus as claimed in claim 10, wherein the module comprises at least one of the anode or the cathode of the energy storage component and the substrate comprises the counter electrode.
12. An apparatus as claimed in claim 10 or 11, wherein the energy storage component is a battery or a supercapacitor.
13. An apparatus as claimed in any of claims 10 to 12, wherein the energy storage component is optimised for the module.
14. A method comprising:
providing a substrate;
providing electrolyte on the substrate; and
positioning a module on the electrolyte wherein an electronic component is formed between the module and the substrate and wherein the electrolyte functions as the electrolyte in the electronic component and also as the adhesive to attach the module to the substrate.
15. A method as claimed in claim 14, further comprising providing at least one electrode of the electronic component on the module and providing at least one electrode of the electronic component on the substrate.

1/11

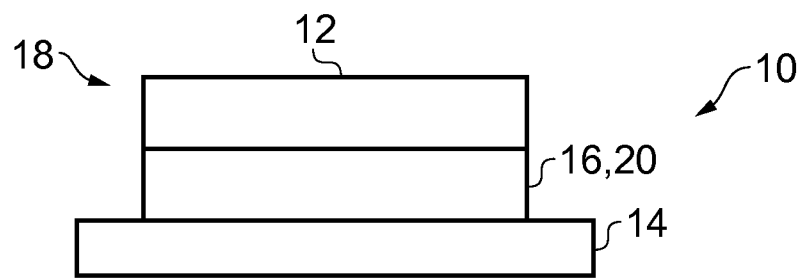


FIG. 1

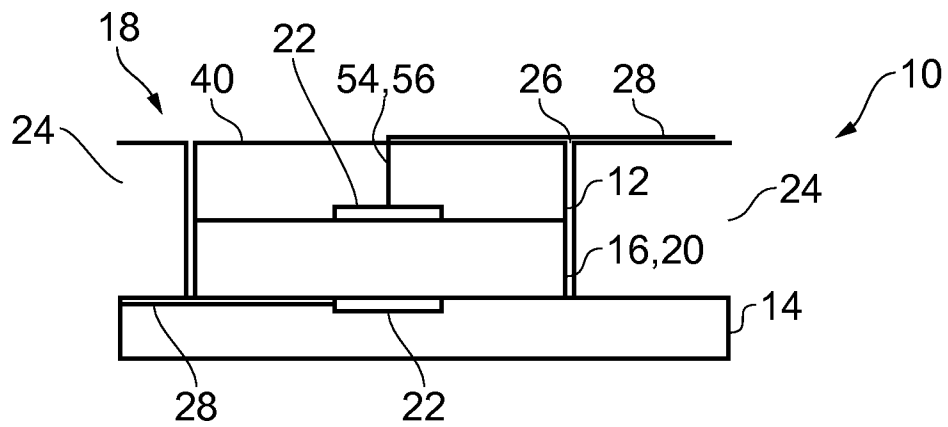


FIG. 2

2/11

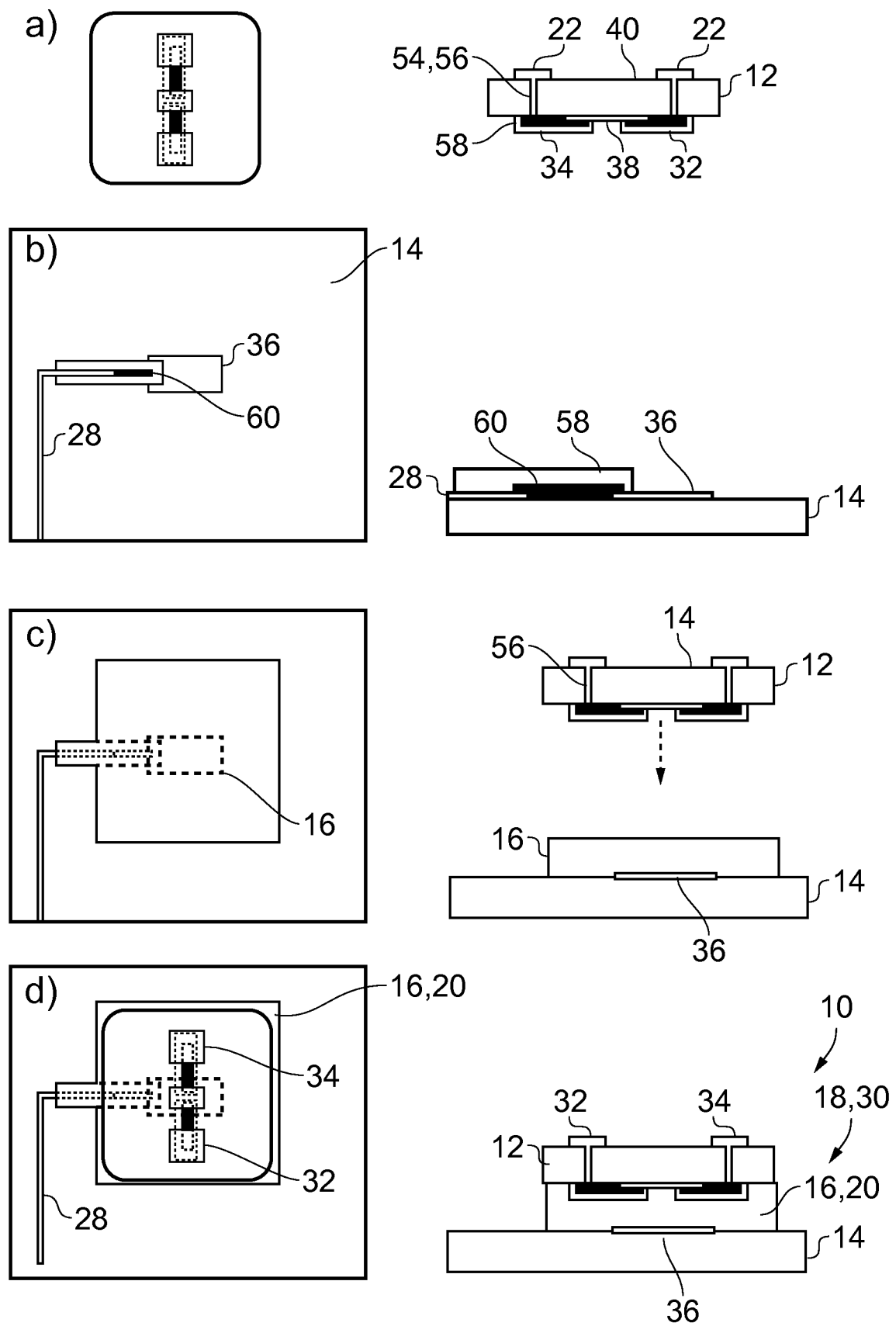


FIG. 3

3/11

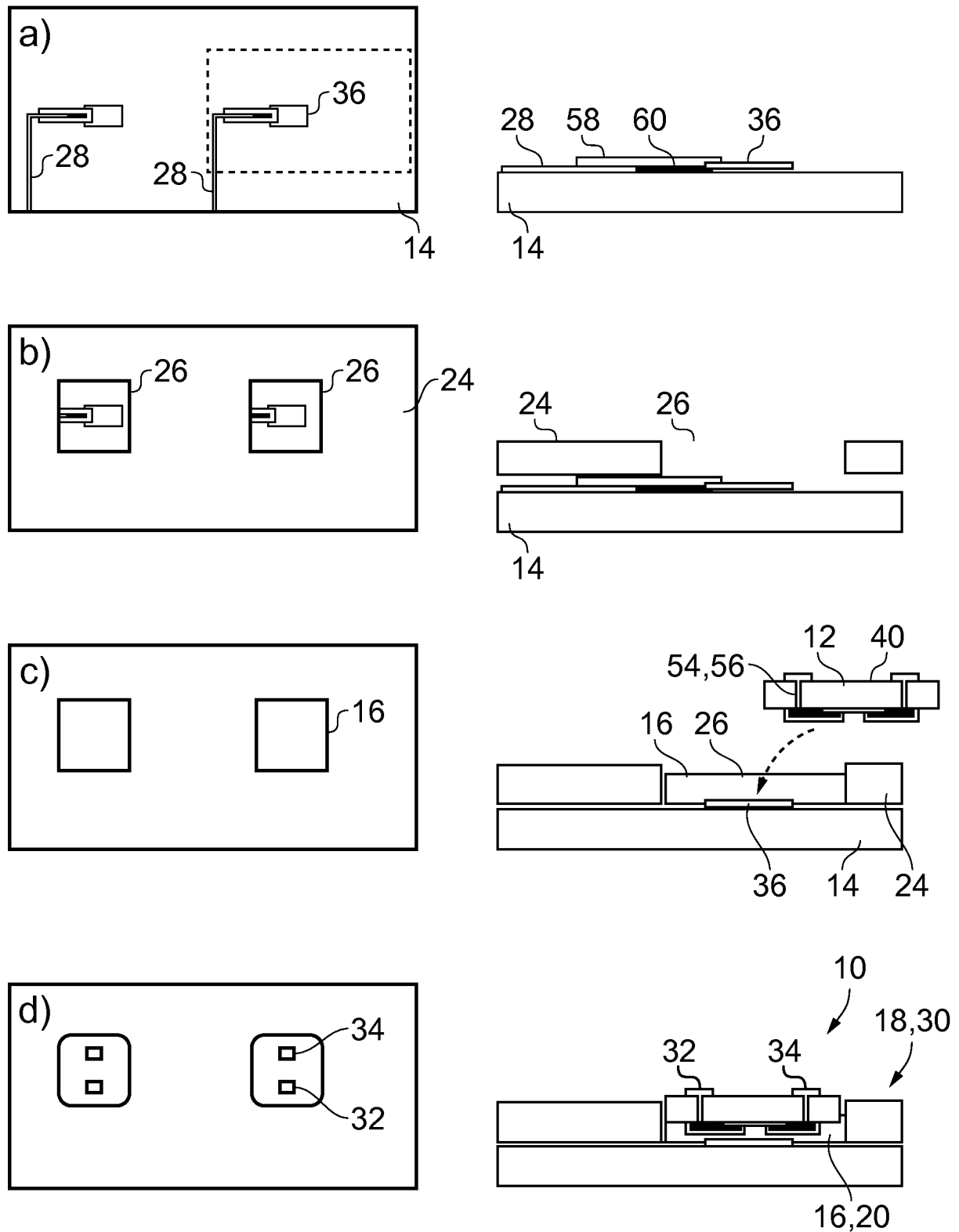


FIG. 4

4/11

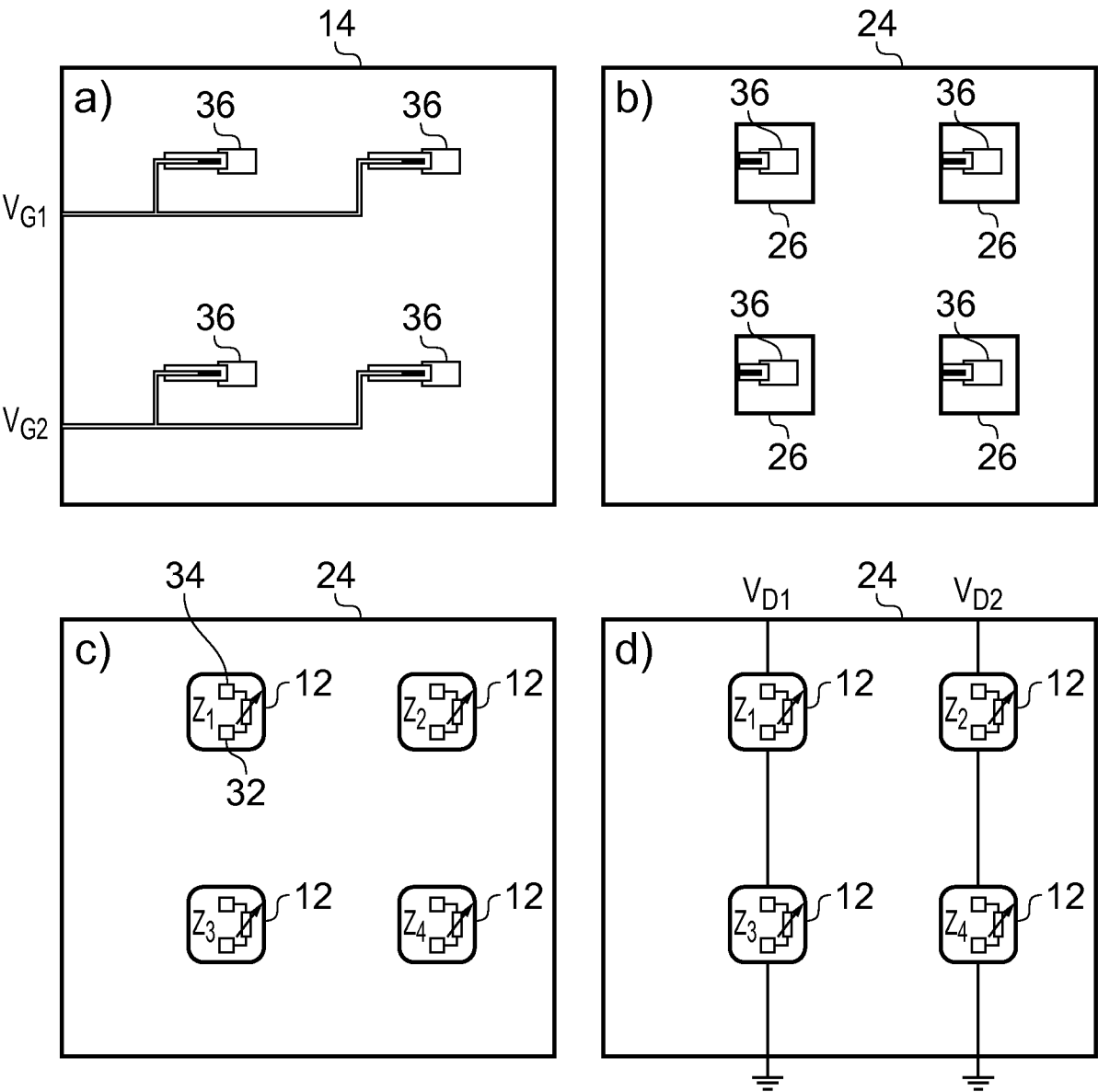


FIG. 5

5/11

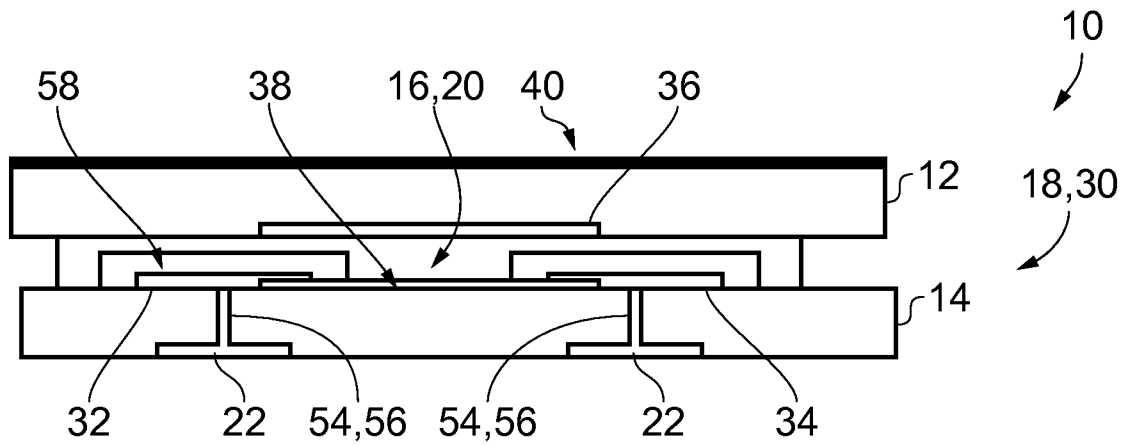


FIG. 6

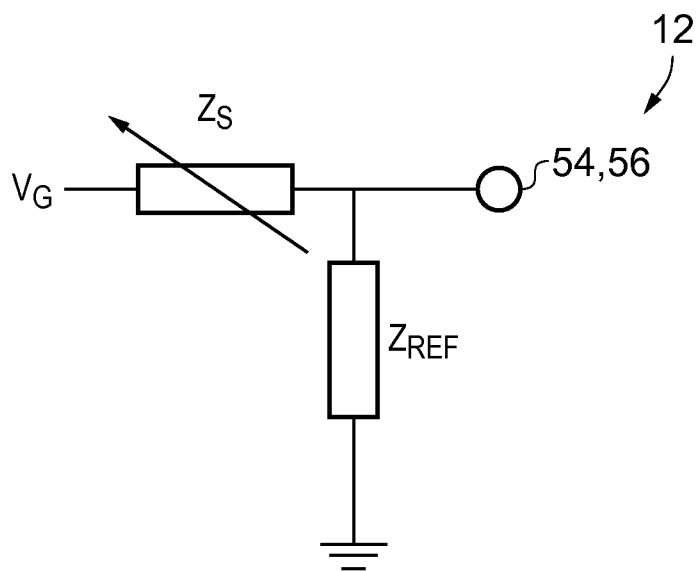


FIG. 7

6/11

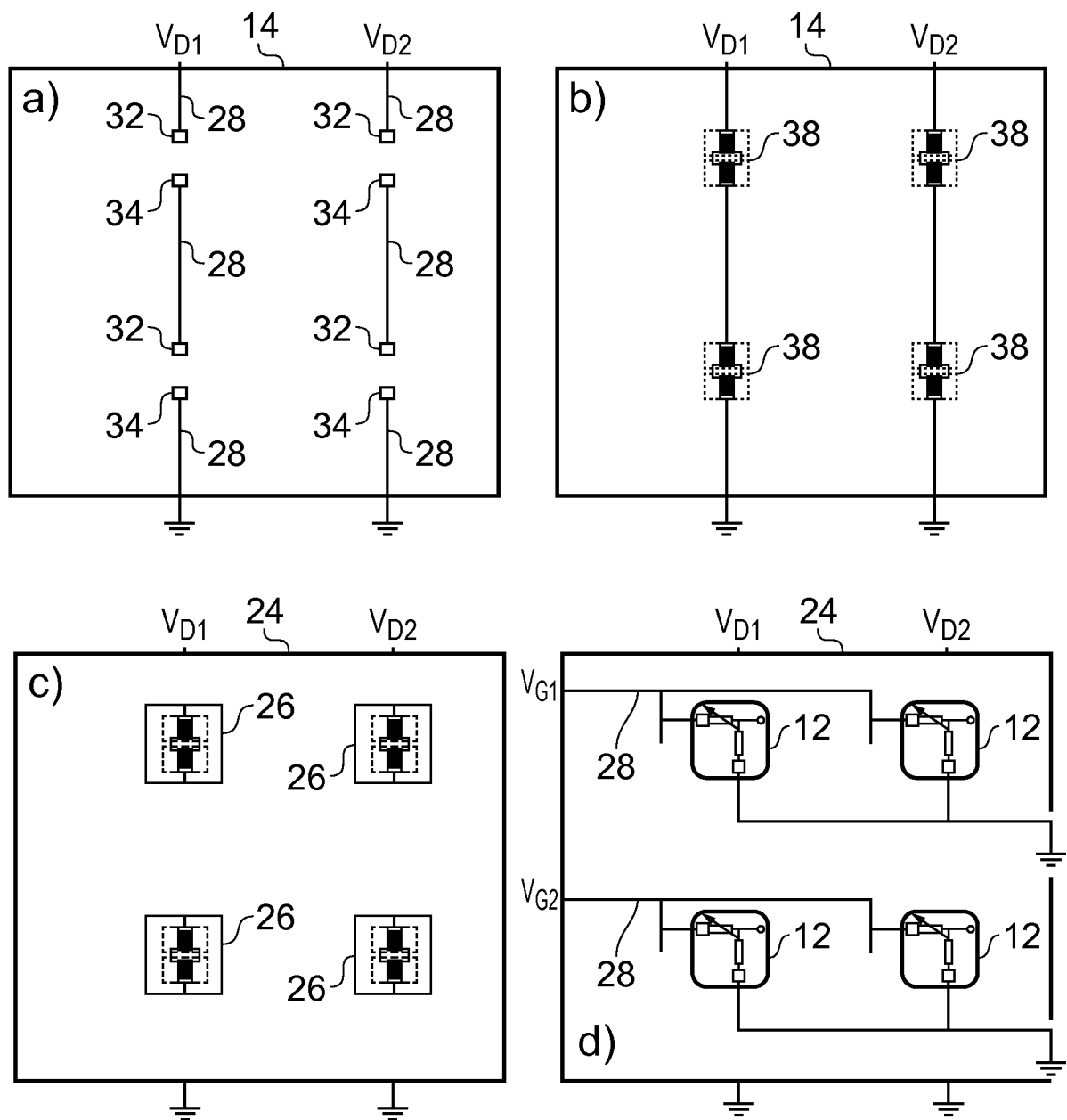
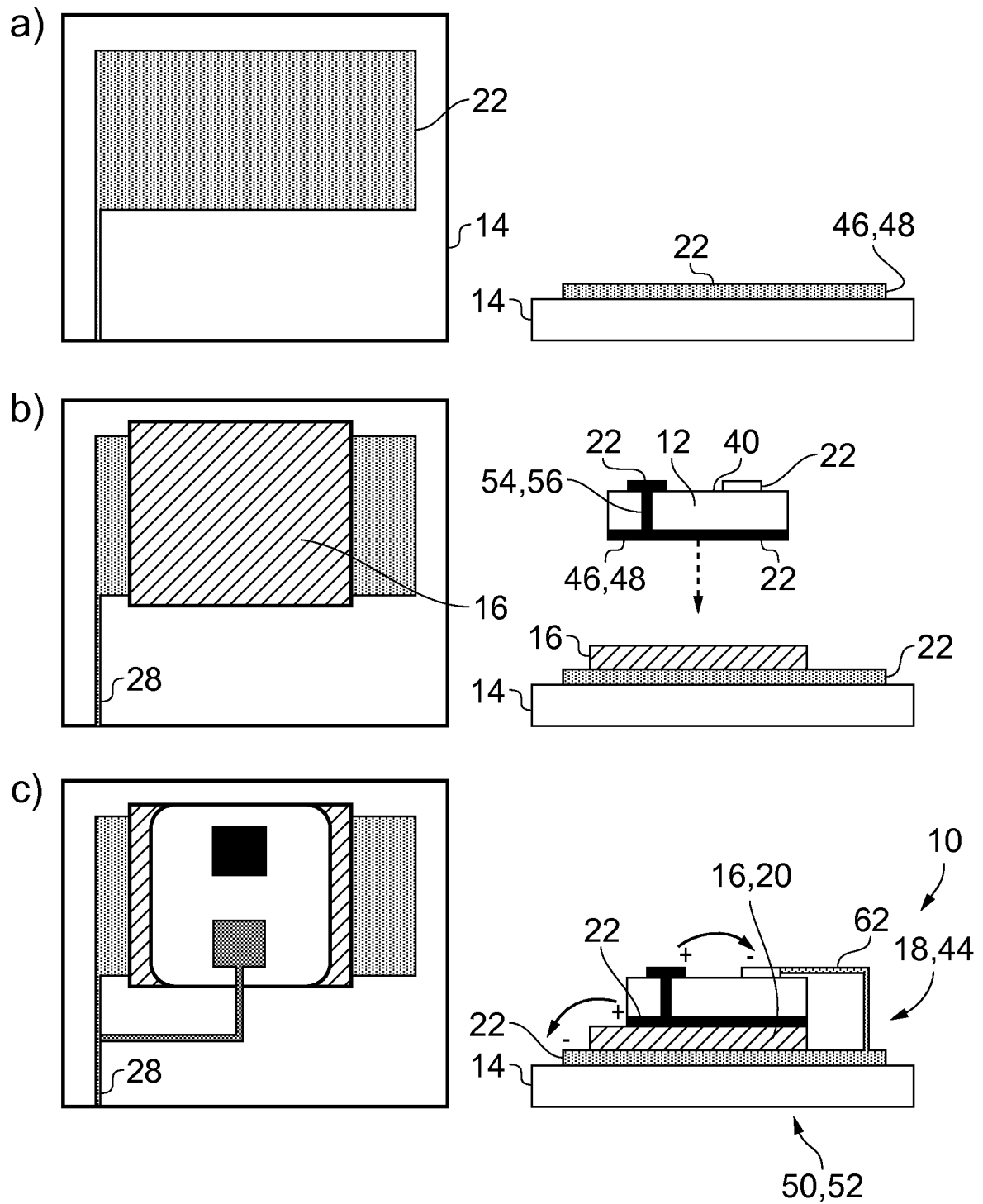


FIG. 8

7/11



8/11

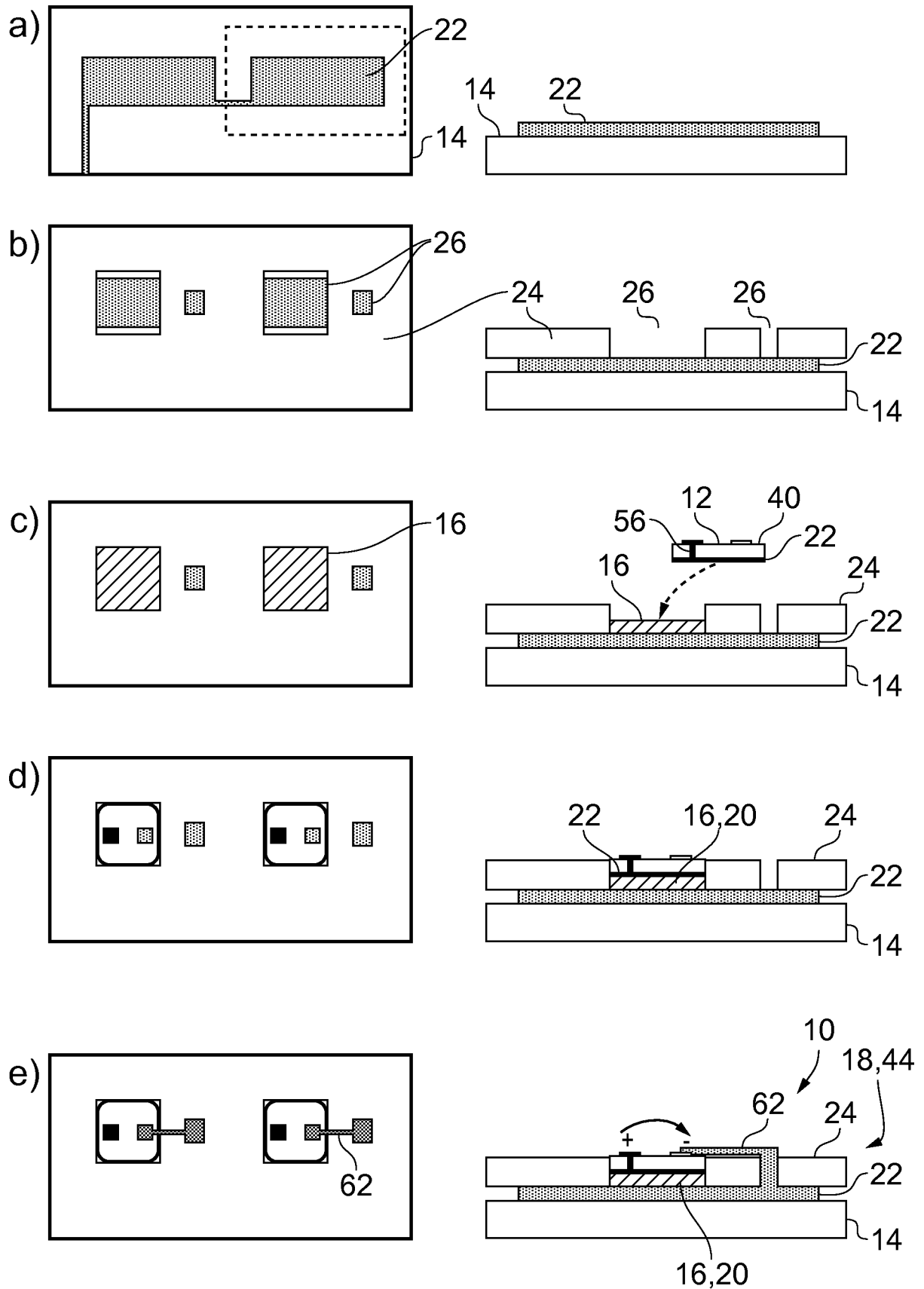


FIG. 10

9/11

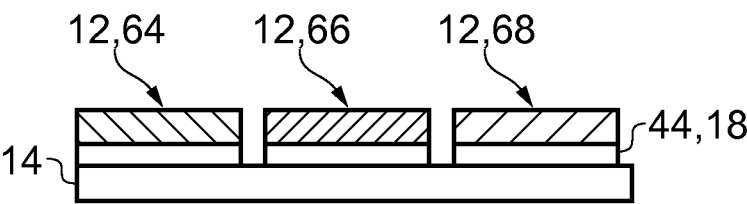


FIG. 11A

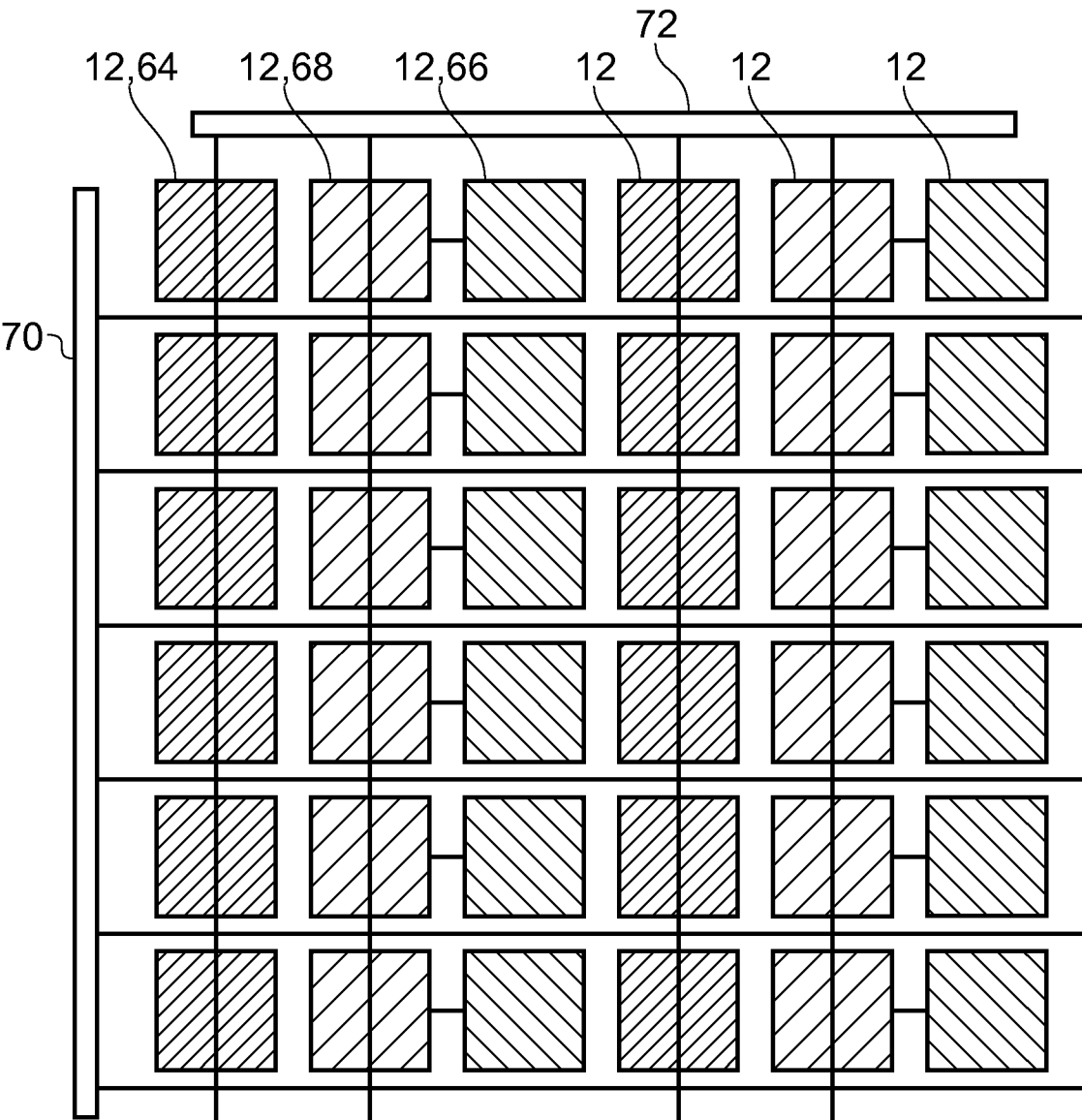


FIG. 11B

10/11

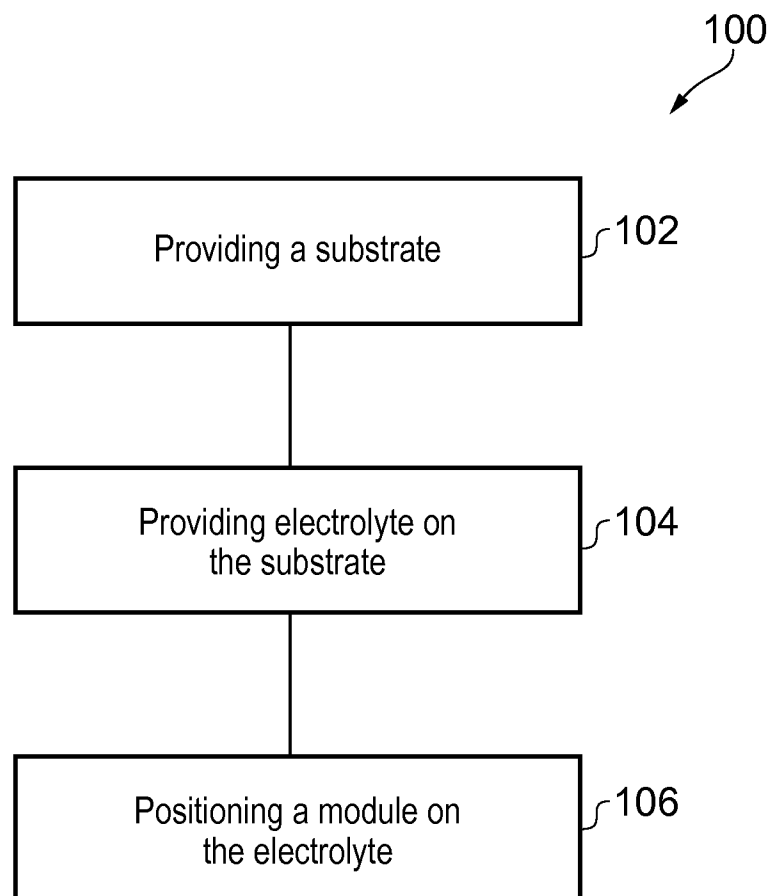


FIG. 12

11/11

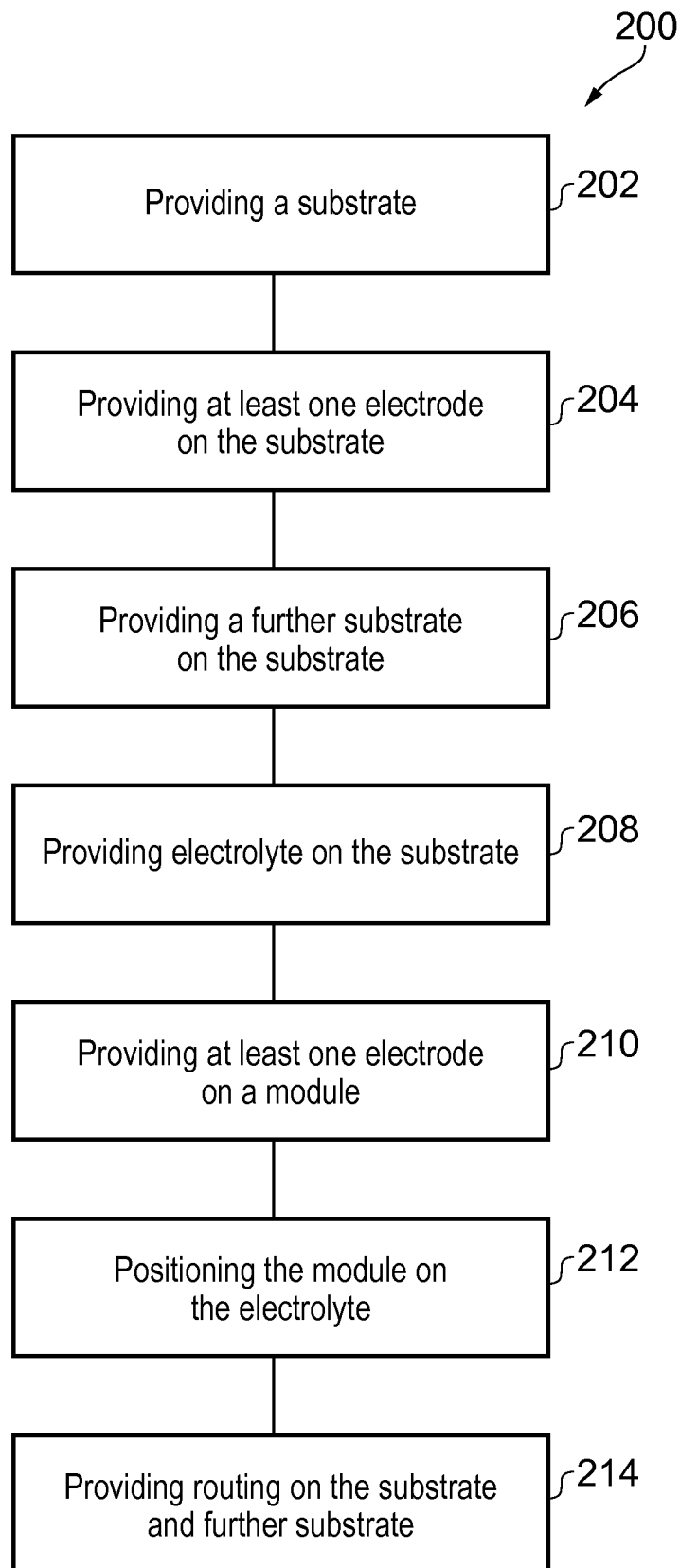


FIG. 13

INTERNATIONAL SEARCH REPORT

International application No
PCT/FI2015/050813

A. CLASSIFICATION OF SUBJECT MATTER

INV. H01L51/00 H01L27/12 H01L27/28 H01L51/50
ADD.

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

H01L

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

EP0-Internal, WPI Data

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	US 2007/125419 A1 (GUI JOHN Y [US] ET AL) 7 June 2007 (2007-06-07) paragraphs [0033] - [0035]; figure 8 -----	1,2,14, 15
X	US 2008/012012 A1 (HARA TOSHIKI [JP]) 17 January 2008 (2008-01-17)	1,2,14, 15
A	paragraphs [0048] - [0070]; figure 2 -----	3-13



Further documents are listed in the continuation of Box C.



See patent family annex.

* Special categories of cited documents :

"A" document defining the general state of the art which is not considered to be of particular relevance

"E" earlier application or patent but published on or after the international filing date

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"O" document referring to an oral disclosure, use, exhibition or other means

"P" document published prior to the international filing date but later than the priority date claimed

"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention

"X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone

"Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art

"&" document member of the same patent family

Date of the actual completion of the international search

24 February 2016

Date of mailing of the international search report

04/03/2016

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INTERNATIONAL SEARCH REPORT

Information on patent family members

International application No

PCT/FI2015/050813

Patent document cited in search report	Publication date	Patent family member(s)	Publication date
US 2007125419 A1	07-06-2007	NONE	

US 2008012012 A1	17-01-2008	JP 4096985 B2	04-06-2008
		JP 2008020849 A	31-01-2008
		US 2008012012 A1	17-01-2008
