



- (51) International Patent Classification:
H04B 7/26 (2006.01)
- (21) International Application Number:
PCT/US2013/027332
- (22) International Filing Date:
22 February 2013 (22.02.2013)
- (25) Filing Language: English
- (26) Publication Language: English
- (30) Priority Data:
61/612,188 16 March 2012 (16.03.2012) US
13/629,546 27 September 2012 (27.09.2012) US
- (71) Applicant: **INTEL CORPORATION** [US/US]; 2200 Mission College Blvd., Santa Clara, CA 95054 (US).
- (72) Inventors: **HE, Hong**; 8F, Raycom Infotech Park A, No.2, Kexueyuan South Road, Haidian District, Beijing, 100190 (CN). **FWU, Jong-Kae**; 1519 Dominion Ave., Sunnyvale, California 94087 (US). **ZHU, Yuan**; Room 605, Building 16, Mo Li Yuan Bei, Yuan Jia Yuan, Chaoyang, Beijing 100012 (CN).
- (74) Agents: **MADDEN, Robert, B.** et al.; Schwegman Lundberg & Woessner, P.A., P.O. Box 2938, Minneapolis, MN 55402 (US).

- (81) Designated States (unless otherwise indicated, for every kind of national protection available): AE, AG, AL, AM, AO, AT, AU, AZ, BA, BB, BG, BH, BN, BR, BW, BY, BZ, CA, CH, CL, CN, CO, CR, CU, CZ, DE, DK, DM, DO, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, GT, HN, HR, HU, ID, IL, IN, IS, JP, KE, KG, KM, KN, KP, KR, KZ, LA, LC, LK, LR, LS, LT, LU, LY, MA, MD, ME, MG, MK, MN, MW, MX, MY, MZ, NA, NG, NI, NO, NZ, OM, PA, PE, PG, PH, PL, PT, QA, RO, RS, RU, RW, SC, SD, SE, SG, SK, SL, SM, ST, SV, SY, TH, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, ZA, ZM, ZW.
- (84) Designated States (unless otherwise indicated, for every kind of regional protection available): ARIPO (BW, GH, GM, KE, LR, LS, MW, MZ, NA, RW, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian (AM, AZ, BY, KG, KZ, RU, TJ, TM), European (AL, AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HR, HU, IE, IS, IT, LT, LU, LV, MC, MK, MT, NL, NO, PL, PT, RO, RS, SE, SI, SK, SM, TR), OAPI (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

Published: — with international search report (Art. 21(3))

(54) Title: UPLINK CONTROL CHANNEL RESOURCE MAPPING FOR AN ENHANCED PDCCH IN LTE SYSTEMS

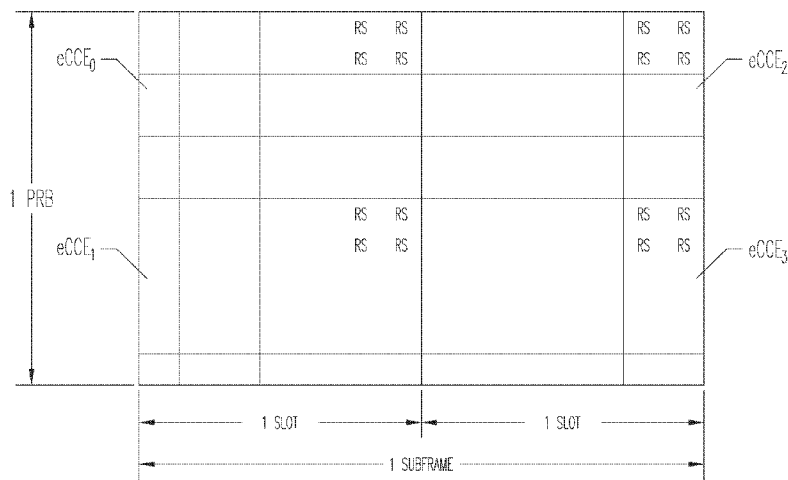


Fig. 2

(57) Abstract: An enhanced physical downlink control channel (ePDCCH) for Long Term Evolution (LTE) systems is described that is constructed using enhanced control channel elements (eCCEs). Techniques are also described by which user equipment (UE) may be implicitly allocated uplink resources for transmitting acknowledgements to data received via downlink resources allocated by an ePDCCH.

WO 2013/138047 A1

5 UPLINK CONTROL CHANNEL RESOURCE MAPPING FOR AN
ENHANCED PDCCH IN LTE SYSTEMS

PRIORITY CLAIM

[0001] This application claims the benefit of priority to U.S. Patent
10 Application Serial No. 13/629,546, filed on September 27, 2012, which claims
the benefit of priority to U.S. Provisional Patent Application Serial No.
61/612,188, filed on March 16, 2012, all of which are incorporated herein by
reference in their entireties.

15 BACKGROUND

[0002] A major feature of LTE-Advanced (Long Term Evolution-
Advanced or LTE-A), as part of Release 10 of the LTE specification by the 3rd
Generation Partnership Project (3GPP), is increased support for multi-user
20 MIMO (multi-input multi-output) in which spatial multiplexing is used to
provide separate downlink and uplink communications paths between a base
station (referred in LTE systems as an evolved Node B or eNB) and multiple
terminals (where a terminal is referred to in LTE systems as user equipment or
UE). As more UEs are scheduled per subframe for multi-user MIMO
25 operations, the demand for physical downlink control channel (PDCCH)
resources to provide scheduling for physical uplink control channel (PUCCH)
resources is increased. The design of the PDCCH in Releases 8/9/10 of the LTE
specification provides for a maximum PDCCH size of three OFDM (orthogonal
frequency division multiplexing) symbols which is inadequate for meeting this
30 increased demand. Consequently, a new PDCCH design, referred to an
enhanced PDCCH (ePDCCH), was introduced in the PDSCH (physical
downlink shared channel) region for Release 11 of the LTE specification. The
structure of the PDCCH is based upon so-called control channel elements
(CCEs), while the ePDCCH uses a design based upon physical resource blocks

(PRBs) in order to increase capacity and enhance support for inter-cell interference coordination (ICIC) in heterogeneous network scenarios. The limitation of the Release 8/9/10 PDCCH design for performing inter-cell interference coordination (ICIC) stems from the fact that, due to PDCCH interleaving, the CCEs used for the transmission of downlink control information (DCI) formats in the PDCCH are distributed over the entire bandwidth in an irregular fashion. Placing the ePDCCH in the PDSCH region with a PRB-based scheme, on the other hand, allows the ePDCCH to be distributed over the bandwidth so as to better support frequency-domain ICIC.

5

10 [0003] The use of the PRB-based ePDCCH, however, cannot be used in the same manner as the CCE-based PDCCH to dynamically allocate uplink resources for acknowledging downlink data transmissions. That is a concern of the present disclosure.

15 BRIEF DESCRIPTION OF THE DRAWINGS

[0004] Fig. 1 illustrates a UE and an eNB in accordance with some embodiments.

[0005] Fig. 2 illustrates the resource element mapping for enhanced control channel elements in an ePDCCH in accordance with some embodiments

20

[0006] Figs. 3 through 7 illustrate example schemes for mapping PUCCH resources as derived from a detected ePDCCH in accordance with some embodiments.

25 DETAILED DESCRIPTION

[0007] The following description and the drawings sufficiently illustrate specific embodiments to enable those skilled in the art to practice them. Other embodiments may incorporate structural, logical, electrical, process, and other changes. Portions and features of some embodiments may be included in, or substituted for, those of other embodiments. Embodiments set forth in the claims encompass all available equivalents of those claims.

30

[0008] LTE uses a combination of forward error-correction coding and ARQ (automatic repeat request), referred to as hybrid ARQ. Hybrid ARQ uses forward error correction codes to correct some errors. When uncorrected errors are detected, the corrupted transmissions are discarded and the receiver requests retransmission. As the term is used herein, a hybrid-ARQ acknowledgement may either be a negative acknowledgement (NACK), signifying that a transmission error has occurred and that a retransmission is requested, or a positive acknowledgement (ACK) indicating that the transmission was received correctly.

10 [0009] When the eNB transmits data to a UE, the UE requires allocation of uplink resources by the eNB in order to respond with a hybrid-ARQ acknowledgement. Described herein is an ePDCCH configuration and techniques by which such uplink resources may be dynamically allocated to the UE in cases where the allocation of the downlink resources is over an ePDCCH.

15

LTE air interface

[0010] Fig. 1 shows an example of a UE 100 and an eNB 150. The UE and eNB incorporate processing circuitries 110 and 160, respectively. The processing circuitry 110 in the UE is interfaced to a plurality of RF transceivers 120 that are each connected to one of a plurality of antennas 130. The processing circuitry 160 in the eNB is interfaced to a plurality of RF transceivers 170 that are each connected to one of a plurality of antennas 180. The illustrated components are intended to represent any type of hardware/software configuration for providing an LTE air interface and performing the processing functions as described herein.

[0011] The LTE air interface, also referred to as the radio access network (RAN), has a protocol architecture that may be basically described as follows. The topmost layer is the packet data compression protocol (PDCP) layer which transmits and receives IP (internet protocol) packets. The PDCP layer communicates with the RLC layer via radio bearers to which IP packets are mapped. At the medium access control (MAC) layer, the connection to the RLC

layer above is through logical channels, and the connection to the physical layer below is through transport channels. The MAC layer handles multiplexing/demultiplexing between the logical channels, hybrid-ARQ operations, and scheduling, the latter being performed solely at the eNodeB for both the uplink and the downlink. Data in a transport channel is organized into transport blocks, with respect to which the hybrid-ARQ function is performed at both the UE and eNB. The primary transport channels used for the transmission of data, the uplink shared channel (UL-SCH) and downlink shared channel (DL-SCH), are mapped to the physical uplink shared channel (PUSCH) and physical downlink shared channel (PDSCH), respectively, at the physical layer.

[0012] The physical layer of LTE is based upon orthogonal frequency division multiplexing (OFDM) for the downlink and a related technique, single carrier frequency division multiplexing (SC-FDM), for the uplink. In OFDM/SC-FDM, complex modulation symbols according to a modulation scheme such as QAM (quadrature amplitude modulation) are each individually mapped to a particular OFDM/SC-FDM subcarrier transmitted during an OFDM/SC-FDM symbol, referred to as a resource element (RE). An RE is the smallest physical resource in LTE. LTE also provides for MIMO (multi-input multi-output) operation where multiple layers of data are transmitted and received by multiple antennas and where each of the complex modulation symbols is mapped into one of the multiple transmission layers and then mapped to a particular antenna port. Each RE is then uniquely identified by the antenna port, sub-carrier position, and OFDM symbol index within a radio frame as explained below.

[0013] LTE transmissions in the time domain are organized into radio frames, each having a duration of 10 ms. Each radio frame consists of 10 sub-frames, and each sub-frame consists of two consecutive 0.5 ms slots. Each slot comprises six indexed OFDM symbols for an extended cyclic prefix and seven indexed OFDM symbols for a normal cyclic prefix. A group of resource elements corresponding to twelve consecutive subcarriers within a single slot is referred to as a resource block (RB) or, with reference to the physical layer, a physical resource block (PRB).

[0014] In the case of FDD (frequency division duplex) operation, where separate carrier frequencies are provided for uplink and downlink transmission, the above-described frame structure is applicable to both the uplink and downlink without modification. In TDD (time division duplex) operation, subframes are allocated for either uplink or downlink transmission with a special subframe occurring at the transition from downlink to uplink transmission (but not at the transition from uplink to downlink transmission). The eNB manages the allocation of uplink and downlink subframes within each radio frame during TDD operation.

10

LTE control signaling

[0015] A physical channel corresponds to the set of time-frequency resources used for transmission of a particular transport channel, and each transport channel is mapped to a corresponding physical channel. There are also physical control channels without a corresponding transport channel that are needed for supporting the transmission of the downlink and uplink transport channels. These include the physical downlink control channel (PDCCH), by which the eNB transmits downlink control information (DCI) to the UE, and the physical uplink control channel (PUCCH) that carries uplink control information (UCI) from the UE to the eNB. Insofar as is relevant to the present disclosure, the DCI carried by the PDCCH may include scheduling information that allocates uplink and downlink resources to the UE, while the UCI carried by the PUCCH may include hybrid-ARQ acknowledgements for responding to transport blocks received by the UE.

25 [0016] PDCCHs are transmitted in a designated control region of each subframe. The mapping of PDCCHs to resource elements is done with a particular structure based on control channel elements (CCEs), where a CCE is a set of thirty-six contiguous resource elements. The number of CCEs required for a certain PDCCH depends on the size of the DCI being carried.

30 [0017] Each PDCCH may be addressed to a specific UE by appending a UE-specific CRC (cyclic redundancy check) to the PDCCH, which also serves for error detection. Thus, a UE detects a PDCCH intended for it by performing the CRC calculation and seeing whether the calculation checks. The CRC is

made UE-specific by including the UE's (or UEs') radio network temporary identifier (RNTI) in the CRC calculation. LTE also defines search spaces to limit the set of CCEs that the UE needs to monitor in order to detect a PDCCH intended for it.

5 [0018] If a UE has already been allocated PUSCH resources in an uplink subframe in which control signaling such as a hybrid-ARQ acknowledgement is to be sent, the control signaling can be time multiplexed with data in the PUSCH. Otherwise, the PUCCH is used. Each PUCCH resource is made up of one resource block within each of two slots of an uplink subframe. Control
10 signaling from multiple UEs can be multiplexed into a single PUCCH region with a combination of time-domain and frequency-domain code division multiplexing. A symbol constituting the control signaling is multiplied by an orthogonal cover sequence to effect spreading in time, and the resulting symbols are then used to modulate a phase rotated (corresponding to a cyclic shift in the
15 time domain) length-12 reference signal sequence in the frequency domain to effect spreading in frequency. The resource used by a PUCCH is thus not only specified in the time-frequency domain by its assigned resource blocks, but also by the cyclic shift and orthogonal cover sequence applied. By assigning
20 different cyclic shifts and orthogonal cover sequences to different UEs, PUCCHs may be transmitted by different UEs using the same time-frequency resource.

[0019] A hybrid-ARQ acknowledgement is sent via a single BPSK or QPSK (binary or quadrature phase shift keying) symbol that is code division multiplexed in a PUCCH in the manner just described to spread the symbol over the pair of resource blocks in what is referred to as a Format 1 PUCCH. A
25 PUCCH Format 1 resource is represented by a PUCCH index, $n_{PUCCH}^{(l)}$, from which the resource block pair, the phase rotation, and the orthogonal cover sequence are derived in the manner described by the LTE specifications (See 3GPP TS 36.211 Release 10).

[0020] Downlink scheduling assignments to a UE apply to the same
30 subframe in which they are transmitted. In the situation where a UE receives a PDSCH allocation in a particular subframe, the UE needs to send a hybrid-ARQ acknowledgement in a designated subsequent subframe. The UE may use a

previously allocated uplink resource in that subsequent subframe (i.e., either a PUSCH or PUCCH resource). Otherwise, for a hybrid-ARQ acknowledgement in a Format 1 PUCCH, the eNB allocates the uplink resource in the same PDCCH that allocates the PDSCH containing the data which is to be
5 acknowledged by indicating the PUCCH index to use as a function of the lowest CCE index found in the detected PDCCH. The eNB thus implicitly signals the uplink resource allocation to the UE.

[0021] In the case of FDD, there is a one-to-one correspondence between downlink subframes transmitting data and uplink subframes transmitting hybrid-
10 ARQ acknowledgements for that data. In TDD, on the other hand, an asymmetric allocation of uplink and downlink subframes may necessitate that a single uplink subframe be used to acknowledge multiple downlink subframes, the latter group of downlink subframes being referred to as a bundling window. Multiple PUCCH resource block pairs and the code division multiplexing
15 described above may be used to transmit multiple hybrid-ARQ acknowledgements from the same or multiple UEs in the same subframe.

PUCCH mapping schemes for ePDCCH

[0022] Described herein are PUCCH resources mapping methods for
20 TDD hybrid-ARQ multiplexing mode that may include multiplexing for UEs without carrier aggregation defined in 3GPP Release 10 and multiplexing for PUCCH Format 1b with channel selection for UEs with carrier aggregation as defined in 3GPP Release 10. In the described mapping schemes, the PUCCH
25 resources mapping is based on the corresponding lowest PRB index of the ePDCCH implicitly so that no PUCCH collision issue occurs. Nor are any scheduling constraints imposed upon the eNB scheduler. The mapping schemes also allow for the interleaving of PUCCH resources for different subframes in the same bundling window to compress and reduce the PUCCH overhead, and,
30 consequently to increase the PUSCH throughput.

[0023] As defined herein, an ePDCCH is composed of enhanced control channel elements (eCCEs) where one eCCE consists of a set of predefined REs within the PRB pairs that are used for defining the mapping of the ePDCCH to

resource elements. Fig. 2 shows an embodiment where there are four indexed eCCEs in each PRB pair used for the ePDCCH. Note that an eCCE may contain REs that are reserved for other signals, such as CRS (cell-specific reference signal) and CSI-RS (channel state information reference signal) designated as RS in the figure.

[0024] For TDD hybrid-ARQ multiplexing and a subframe n with bundling window size $M > 1$, where M is the number of elements in the set K defined in Table 10.1.1 of 3GPP TS 36.213, denote $n_{PUCCH,i}^{(l)}$ as the PUCCH resources derived from subframe $n-k_i$ and HARQ-ACK(i) as the hybrid-ARQ response for subframe $n-k_i$, where $k_i \in K$ and $0 \leq i \leq M-1$. For a PDSCH transmission or a PDCCH indicating downlink SPS (semi-persistent scheduling) release in subframe $n-k_i$, the PUCCH resources correspondingly used for hybrid-ARQ feedback in subframe n may be calculated using different mapping schemes as described below.

[0025] For each ePDCCH detected in subframe $n-k_i$, the following symbols are used in the example mapping schemes described below to determine the corresponding PUCCH resource index $n_{PUCCH,i}^{(l)}$.

n_{eCCE} : the index of the lowest eCCE in the ePDCCH detected in subframe $n-k_i$, $0 \leq n_{eCCE} < E$.

E : the number of eCCEs in one PRB ($E = 4$ is used in the example embodiments described herein).

$I_{PRB,ePDCCH}^{lowest\ index}$: the lowest PRB index including at least one eCCE of the ePDCCH detected in the subframe $n-k_i$. Note that the eCCE within the lowest PRB index may be part or full of ePDCCH resources.

$N_{RB,ePDCCH}^i$: either a cell-specific or a UE-specific parameter that indicates the ePDCCH PRBs reserved for use in transmitting the ePDCCH in a downlink subframe from a system perspective or a UE-specific perspective. It is signaled semi-statically by higher layer signaling or dynamically signaled in each downlink subframe through a special physical channel such as the physical control format information channel (PCFICH) channel in as specified in Release 8 or as determined by a UE implicitly according to system bandwidth as $N_{RB,ePDCCH}^i = N_{RB}^{DL}$,

where N_{RB}^{DL} is the number of PRBs in the system downlink bandwidth configuration as defined in 3GPP TS 36.211. It should be noted that, in the mapping schemes discussed below, the PUCCH resources reserved for PUCCH transmission may be minimized with an appropriate setting for $N_{RB,ePDCCH}^i$.

5

Consequently, more uplink PRBs could be used for PUSCH transmission to result in a higher uplink throughput.

Δ : provided by higher layer or implicit mapping depending on bundling window size, e.g., $\Delta = M \cdot Q$, where $Q \geq 1$.

10

$N_{PRB,ePDCCH}^{offset}$: indicates the lowest PRB index for ePDCCH transmission, configured by higher layers in UE-specific or cell-specific way.

15

$N_{ePUCCH}^{(l)}$: indicates the PUCCH resources offset for the ePDCCH, configured by higher layers in a UE-specific or cell-specific way.

20

[0026] In a first mapping scheme, the PUCCH resources are mapped implicitly subframe by subframe using the following equation:

$$n_{PUCCH,i}^{(l)} = E \cdot \left(\sum_{l=0}^{i-1} N_{RB,ePDCCH}^l + (I_{PRB_ePDCCH}^{lowest_index} - N_{PRB,ePDCCH}^{offset}) \right) + n_{eCCE} + N_{ePUCCH}^{(l)}$$

25

One embodiment of this scheme is as shown in Fig. 3 where it is assumed that

$$N_{RB,ePDCCH}^i = N = 2 \text{ and that } N_{ePUCCH}^{(l)} = 0.$$

[0027] In a second mapping scheme, a time-domain first mapping of the PUCCH resources along with PRB-level interleaving is performed using the following equation:

30

$$n_{PUCCH,i}^{(l)} = E \cdot \Delta \cdot (I_{PRB_ePDCCH}^{lowest_index} - N_{PRB,ePDCCH}^{offset}) + n_{eCCE} + E \cdot i + N_{ePUCCH}^{(l)}$$

An example mapping pattern is as shown in Fig. 4 where it is assumed that $Q = 1$ and $M = 2$.

35

[0028] In a third mapping scheme, a frequency-domain first mapping of the PUCCH resources along with slot-level interleaving is performed as follows.

First, a value p is selected from $\{0,1\}$ that satisfies:

$$N_p \leq n_{eCCE} < N_{p+1} \text{ where } N_0 = 0, N_1 = 2, \text{ and } N_2 = 4.$$

The PUCCH resource is then calculated as:

$$n_{PUCCH,i}^{(1)} = E/2 \cdot (I_{PRB_ePDCCH}^{lowest_index} - N_{PRB_ePDCCH}^{offset}) + n_{eCCE} + F_1 + F_2 + \delta_i + N_{ePUCCH}^{(1)} \quad (3)$$

where:

$$F_1 = \sum_{l=0}^{i-M-i-2} N_{RB_ePDCCH}^{M-i-l} \cdot N_p \quad (4)$$

$$F_2 = \sum_{j=0}^i N_{RB_ePDCCH}^j \cdot N_{p+1}$$

$$\delta_i = (N_{RB_ePDCCH}^i - I_{PRB_ePDCCH}^{lowest_index} - 1) \cdot E/2 \cdot p$$

If $N_{RB_ePDCCH}^i$ = a constant value N for each downlink subframe in the bundling window, then equations (4), (5), and (6) can be simplified as:

$$F_1 = \sum_{l=0}^{i-M-i-2} N_{RB_ePDCCH}^{M-i-l} \cdot N_p = (M-i-1) \cdot N \cdot N_p$$

$$F_2 = \sum_{j=0}^i N_{RB_ePDCCH}^j \cdot N_{p+1} = i \cdot N \cdot N_{p+1}$$

$$\delta_i = (N - I_{PRB_ePDCCH}^{lowest_index} - 1) \cdot E/2 \cdot p$$

An example mapping pattern produced by this mapping scheme is as shown in Fig. 5.

[0029] In a fourth mapping scheme, the PUCCH resources are mapped with slot-level interleaving. First, a value p is selected from $\{0, 1\}$ that satisfies:

$$N_p \leq n_{eCCE} < N_{p+1} \text{ where } N_0 = 0, N_1 = 2, \text{ and } N_2 = 4.$$

The PUCCH resource is then calculated as:

$$n_{PUCCH,i}^{(1)} = E \cdot \Delta \cdot (I_{PRB_ePDCCH}^{lowest_index} - N_{PRB_ePDCCH}^{offset}) + n_{eCCE} + (M-i-1) \cdot N_p + i \times N_{p+1} + N_{ePUCCH}^{(1)}$$

In the example of this mapping scheme illustrated by Fig. 6, it is assumed that $Q = 1$ and $M = 2$.

[0030] In a fifth mapping scheme, the PUCCH resources are mapped with eCCE-level interleaving using the following equation:

$$n_{PUCCH,i}^{(1)} = \Delta \cdot (E \cdot (I_{PRB_ePDCCH}^{lowest_index} - N_{PRB_ePDCCH}^{offset}) + n_{eCCE}) + i + N_{ePUCCH}^{(1)}$$

In the example of this mapping scheme illustrated by Fig. 7, it is assumed that $Q = 1$ and $M = 2$.

Example embodiments

[0031] In a first embodiment, a device operating as a UE in an LTE
 5 network comprises an RF transceiver for providing an LTE air interface for
 communicating with a base station operating as an eNB and processing circuitry
 to: receive transmitted downlink resource allocations from an eNB in an
 ePDCCH made up of one or more indexed eCCEs) contained within indexed
 PRBs; and, transmit a hybrid-ARQ acknowledgement corresponding to a
 10 detected ePDCCH allocating PDSCH resources via a PUCCH resource that is
 implicitly indicated by the detected ePDCCH. The ePDCCH may be detected in
 a downlink subframe belonging to a specified bundling window of M indexed
 downlink subframes. The PUCCH resource may be defined by a PUCCH
 resource index $n_{PUCCH,i}^{(l)}$ that is a function of subframe index, PRB index, and
 15 eCCE index. The PUCCH index may be defined as a function of subframe
 index, lowest index of a PRB containing at least one eCCE in the detected
 ePDCCH, and lowest eCCE index.

[0032] In a second embodiment, a device operating as an eNB in an LTE
 network, comprises: an RF transceiver for providing an LTE air interface for
 20 communicating with a UE and processing circuitry to: transmit data to a UE
 (user equipment) via a downlink physical downlink shared channel (PDSCH) in
 a subframe; allocate PDSCH resources to the UE for the subframe in an
 enhanced physical downlink control channel (ePDCCH) made up of one or more
 enhanced control channel elements (eCCEs) contained within physical resource
 25 blocks (PRBs) that indexed in a cell-specific manner; and allocate a physical
 uplink control channel (PUCCH) resource to the UE for acknowledging the
 transmitted data by implicitly indicating the PUCCH resource in the ePDCCH.
 The processing circuitry may be further to, for an ePDCCH allocating downlink
 resources to a UE in a subframe belonging to a specified bundling window of M
 30 indexed subframes, receive a hybrid-ARQ acknowledgement corresponding to
 each eCCE of the ePDCCH via a physical uplink control channel (PUCCH)
 resource defined by a PUCCH resource index $n_{PUCCH,i}^{(l)}$ that is a function of:

subframe index, PRB index, and eCCE index. The PUCCH index may be defined as a function of subframe index, lowest index of a PRB containing at least one eCCE in the detected ePDCCH, and lowest eCCE index.

[0033] In either of the first or second embodiments, the processing circuitry may be further configured such that: for each subframe in the bundling window having an ePDCCH allocating downlink resources, the PUCCH resource index $n_{PUCCH,i}^{(l)}$ is calculated such that consecutive PUCCH resource indices are mapped to by those ePDCCHs ordered in accordance with an ordered list of the eCCEs making up the ePDCCHs where the eCCEs are ordered first according to subframe index, then according to PRB index, and then according to eCCE index. An example of the mapping pattern produced by this embodiment is as shown in Fig. 3.

[0034] In either of the first or second embodiments, the processing circuitry may be further configured such that: for each subframe in the bundling window having an ePDCCH allocating downlink resources, the PUCCH resource index $n_{PUCCH,i}^{(l)}$ is calculated such that consecutive PUCCH resource indices are mapped to by those ePDCCHs ordered in accordance with an ordered list of the eCCEs making up the ePDCCHs where the eCCEs are ordered first according to PRB index, then according to subframe index, and then according to eCCE index. An example of the mapping pattern produced by this embodiment is as shown in Fig. 4.

[0035] In either of the first or second embodiments, the processing circuitry may be further configured such that: for each subframe in the bundling window having an ePDCCH allocating downlink resources, the PUCCH resource index $n_{PUCCH,i}^{(l)}$ is calculated such that consecutive PUCCH resource indices are mapped to by those ePDCCHs ordered in accordance with an ordered list of the eCCEs making up the ePDCCHs where the eCCEs are ordered first according to slot number within the subframe, then according to subframe index, then according to PRB index, and then according to eCCE index. An example of the mapping pattern produced by this embodiment is as shown in Fig. 5.

[0036] In either of the first or second embodiments, the processing circuitry may be further configured such that: for each subframe in the bundling

window having an ePDCCH allocating downlink resources, the PUCCH resource index $n_{PUCCH,i}^{(l)}$ is calculated such that consecutive PUCCH resource indices are mapped to by those ePDCCHs ordered in accordance with an ordered list of the eCCEs making up the ePDCCHs where the eCCEs are ordered first according to PRB index, then according to slot number within the subframe, then according to subframe index, and then according to eCCE index. An example of the mapping pattern produced by this embodiment is as shown in Fig. 6.

[0037] In either of the first or second embodiments, the processing circuitry may be further configured such that: for each subframe in the bundling window having an ePDCCH allocating downlink resources, the PUCCH resource index $n_{PUCCH,i}^{(l)}$ is calculated such that consecutive PUCCH resource indices are mapped to by those ePDCCHs ordered in accordance with an ordered list of the eCCEs making up the ePDCCHs where the eCCEs are ordered first according to PRB index, then according to eCCE index, then according to subframe index. An example of the mapping pattern produced by this embodiment is as shown in Fig. 7.

[0038] In either of the first or second embodiments, the processing circuitry may be further configured such that: for each ePDCCH detected in a subframe $n-k_i$, the PUCCH resource index $n_{PUCCH,i}^{(l)}$ for transmission in a subframe n is calculated as:

$$n_{PUCCH,i}^{(l)} = E \cdot \left(\sum_{l=0}^{i-1} N_{RB,ePDCCH}^l + (I_{PRB_ePDCCH}^{lowest_index} - N_{PRB,ePDCCH}^{offset}) \right) + n_{eCCE} + N_{ePUCCH}^{(l)}$$

where n is an integer, K is the set of downlink subframe indexes in the bundling window of subframe n , M is the number of downlink subframes in the bundling window, $k_i \in K$, $0 \leq i \leq M-1$, $N_{RB,ePDCCH}^i$ indicates the ePDCCH physical resource blocks (PRBs) reserved for use in transmitting the ePDCCH in a downlink subframe, $I_{PRB_ePDCCH}^{lowest_index}$ is the lowest PRB index including at least one eCCE of the ePDCCH detected in the subframe $n-k_i$, $N_{PRB,ePDCCH}^{offset}$ indicates the lowest PRB index for ePDCCH transmission, E is the number of eCCEs in one

PRB, n_{eCCE} is the lowest index of the eCCE in the ePDCCH detected in subframe $n-k_i$, $0 \leq n_{eCCE} < E$, which is mapped to a PUCCH resource index $n_{PUCCH,i}^{(l)}$, and $N_{ePUCCH}^{(l)}$ indicates the PUCCH resources offset for the ePDCCH.

[0039] In either of the first or second embodiments, the processing circuitry may be further configured such that: for each ePDCCH detected in a subframe $n-k_i$, the PUCCH resource index $n_{PUCCH,i}^{(l)}$ for transmission in a subframe n is calculated as:

$$n_{PUCCH,i}^{(l)} = E \cdot \Delta \cdot (I_{PRB_ePDCCH}^{lowest\ index} - N_{PRB,ePDCCH}^{offset}) + n_{eCCE} + E \cdot i + N_{ePUCCH}^{(l)}$$

where n is an integer, K is the set of downlink subframe indexes in the bundling window of subframe n , M is the number of downlink subframes in the bundling window, $k_i \in K$, $0 \leq i \leq M-1$, $N_{RB,ePDCCH}^i$ indicates the ePDCCH physical resource blocks (PRBs) reserved for use in transmitting the ePDCCH in a downlink subframe, $I_{PRB_ePDCCH}^{lowest\ index}$ is the lowest PRB index including at least one eCCE of the ePDCCH detected in the subframe $n-k_i$, $N_{PRB,ePDCCH}^{offset}$ indicates the lowest PRB index for ePDCCH transmission, E is the number of eCCEs in one PRB, n_{eCCE} is the lowest index of the eCCE in the ePDCCH detected in subframe $n-k_i$, $0 \leq n_{eCCE} < E$, which is mapped to a PUCCH resource index $n_{PUCCH,i}^{(l)}$, $N_{ePUCCH}^{(l)}$ indicates the PUCCH resources offset for the ePDCCH, and Δ is a specified integer that depends upon the size of the bundling window.

[0040] In either of the first or second embodiments, the processing circuitry may be further configured such that the PUCCH resource index $n_{PUCCH,i}^{(l)}$ for transmission in a subframe n is calculated by selecting a value p from $\{0,1\}$ that satisfies:

$$N_p \leq n_{eCCE} < N_{p+1} \text{ where } N_0 = 0, N_1 = 2, \text{ and } N_2 = 4$$

and calculating $n_{PUCCH,i}^{(l)}$ as:

$$n_{PUCCH,i}^{(l)} = E/2 \cdot (I_{PRB_ePDCCH}^{lowest\ index} - N_{PRB,ePDCCH}^{offset}) + n_{eCCE} + F_1 + F_1 + \delta_i + N_{ePUCCH}^{(l)}$$

where n is an integer, K is the set of downlink subframe indexes in the bundling window of subframe n , M is the number of downlink subframes in the bundling window, $k_i \in K$, $0 \leq i \leq M-1$, $N_{RB,ePDCCH}^i$ indicates the ePDCCH physical resource blocks (PRBs) reserved for use in transmitting the ePDCCH in a

- 5 downlink subframe, $I_{PRB_ePDCCH}^{lowest_index}$ is the lowest PRB index including at least one eCCE of the ePDCCH detected in the subframe $n-k_i$, $N_{PRB,ePDCCH}^{offset}$ indicates the lowest PRB index for ePDCCH transmission, E is the number of eCCEs in one PRB, n_{eCCE} is the lowest index of the eCCE in the ePDCCH detected in subframe $n-k_i$, $0 \leq n_{eCCE} < E$, which is mapped to a PUCCH resource index $n_{PUCCH,i}^{(l)}$,
- 10 $N_{ePUCCH}^{(l)}$ indicates the PUCCH resources offset for the ePDCCH, Δ is a specified integer that depends upon the size of the bundling window, and where:

$$F_1 = \sum_{l=0}^{l=M-i-2} N_{RB,ePDCCH}^{M-1-l} \cdot N_p,$$

$$F_2 = \sum_{j=0}^i N_{RB,ePDCCH}^j \cdot N_{p+1}, \text{ and}$$

15
$$\delta_i = (N_{RB,ePDCCH}^i - I_{PRB_ePDCCH}^{lowest_index} - 1) \cdot E / 2 \cdot p.$$

[0041] In either of the first or second embodiments, the processing circuitry may be further configured such that the PUCCH resource index $n_{PUCCH,i}^{(l)}$ for transmission in a subframe n is calculated by selecting a value p from $\{0,1\}$ that satisfies:

20
$$N_p \leq n_{eCCE} < N_{p+1} \text{ where } N_0 = 0, N_1 = 2, \text{ and } N_2 = 4$$

and calculating $n_{PUCCH,i}^{(l)}$ as:

$$n_{PUCCH,i}^{(l)} = E \cdot \Delta \cdot (I_{PRB_ePDCCH}^{lowest_index} - N_{PRB,ePDCCH}^{offset}) + n_{eCCE} + (M - i - 1) \cdot N_p + i \times N_{p+1} + N_{ePUCCH}^{(l)}$$

- 25 where n is an integer, K is the set of downlink subframe indexes in the bundling window of subframe n , M is the number of downlink subframes in the bundling window, $k_i \in K$, $0 \leq i \leq M-1$, $N_{RB,ePDCCH}^i$ indicates the ePDCCH physical resource blocks (PRBs) reserved for use in transmitting the ePDCCH in a

downlink subframe, $I_{PRB_ePDCCH}^{lowest_index}$ is the lowest PRB index including at least one eCCE of the ePDCCH detected in the subframe $n-k_i$, $N_{PRB_ePDCCH}^{offset}$ indicates the lowest PRB index for ePDCCH transmission, E is the number of eCCEs in one PRB, n_{eCCE} is the lowest index of the eCCE in the ePDCCH detected in subframe
 5 $n-k_i$, $0 \leq n_{eCCE} < E$, which is mapped to a PUCCH resource index $n_{PUCCH,i}^{(l)}$, $N_{ePUCCH}^{(l)}$ indicates the PUCCH resources offset for the ePDCCH, Δ is a specified integer that depends upon the size of the bundling window.

[0042] In either of the first or second embodiments, the processing circuitry may be further configured such that: for each ePDCCH detected in a
 10 subframe $n-k_i$, the PUCCH resource index $n_{PUCCH,i}^{(l)}$ for transmission in a subframe n is calculated as:

$$n_{PUCCH,i}^{(l)} = \Delta \cdot (E \cdot (I_{PRB_ePDCCH}^{lowest_index} - N_{PRB_ePDCCH}^{offset}) + n_{eCCE}) + i + N_{ePUCCH}^{(l)}$$

where n is an integer, K is the set of downlink subframe indexes in the bundling window of subframe n , M is the number of downlink subframes in the bundling
 15 window, $k_i \in K$, $0 \leq i \leq M-1$, $N_{RB_ePDCCH}^i$ indicates the ePDCCH physical resource blocks (PRBs) reserved for use in transmitting the ePDCCH in a downlink subframe, $I_{PRB_ePDCCH}^{lowest_index}$ is the lowest PRB index including at least one eCCE of the ePDCCH detected in the subframe $n-k_i$, $N_{PRB_ePDCCH}^{offset}$ indicates the lowest PRB index for ePDCCH transmission, E is the number of eCCEs in one
 20 PRB, n_{eCCE} is the lowest index of the eCCE in the ePDCCH detected in subframe $n-k_i$, $0 \leq n_{eCCE} < E$, which is mapped to a PUCCH resource index $n_{PUCCH,i}^{(l)}$, $N_{ePUCCH}^{(l)}$ indicates the PUCCH resources offset for the ePDCCH, and Δ is a specified integer that depends upon the size of the bundling window.

[0043] In any of the above embodiments, the processing circuitry may be
 25 further configured such that the PUCCH resource index $n_{PUCCH,i}^{(l)}$ is calculated as a function of a PUCCH resource offset $N_{ePUCCH}^{(l)}$ for the ePDCCH that is configured by higher layers. The PUCCH resource offset $N_{ePUCCH}^{(l)}$ for the ePDCCH may, for example, be a UE-specific parameter determined by the eNB.

[0044] The embodiments as described above may be implemented as methods for operation and/or in various hardware configurations that may include a processor for executing instructions that perform the methods. Such instructions may be contained in a suitable storage medium from which they are transferred to a memory or other processor-executable medium.

[0045] The subject matter has been described in conjunction with the foregoing specific embodiments. It should be appreciated that those embodiments may also be combined in any manner considered to be advantageous. Also, many alternatives, variations, and modifications will be apparent to those of ordinary skill in the art. Other such alternatives, variations, and modifications are intended to fall within the scope of the following appended claims.

[0046] The Abstract is provided to comply with 37 C.F.R. Section 1.72(b) requiring an abstract that will allow the reader to ascertain the nature and gist of the technical disclosure. It is submitted with the understanding that it will not be used to limit or interpret the scope or meaning of the claims. The following claims are hereby incorporated into the detailed description, with each claim standing on its own as a separate embodiment.

CLAIMS

What is claimed is:

1. A method for operating a UE (user equipment) in an LTE (Long Term Evolution) network using TDD (time division duplex) mode, comprising:
5 receiving transmitted downlink resource allocations from an eNB (evolved Node B) in an enhanced physical downlink control channel (ePDCCH) made up of one or more indexed enhanced control channel elements (eCCEs) contained within indexed physical resource blocks (PRBs); and,
transmitting a hybrid-ARQ acknowledgement corresponding to a
10 detected ePDCCH allocating physical downlink shared channel (PDSCH) resources via a physical uplink control channel (PUCCH) resource that is implicitly indicated by the detected ePDCCH.
2. The method of claim 1 wherein the ePDCCH is detected in a downlink
15 subframe belonging to a specified bundling window of M indexed downlink subframes and wherein the PUCCH resource is defined by a PUCCH resource index $n_{PUCCH,i}^{(l)}$ that is a function of subframe index, lowest index of a PRB containing at least one eCCE in the detected ePDCCH, and lowest eCCE index.
- 20 3. The method of claim 1 wherein the ePDCCH is detected in a downlink subframe belonging to a specified bundling window of M indexed downlink subframes and wherein the PUCCH resource is defined by a PUCCH resource index $n_{PUCCH,i}^{(l)}$ and further comprising, for each subframe in the bundling window having an ePDCCH allocating downlink resources, calculating the
25 PUCCH resource index $n_{PUCCH,i}^{(l)}$ such that consecutive PUCCH resource indices are mapped to by the ePDCCHs ordered in accordance with an ordered list of the eCCEs making up the ePDCCHs where the eCCEs are ordered first according to subframe index, then according to PRB index, and then according to eCCE index.

30

4. The method of claim 2 further comprising, for each ePDCCH detected in a subframe $n-k_i$, calculating the PUCCH resource index $n_{PUCCH,i}^{(l)}$ for transmission in a subframe n as:

$$n_{PUCCH,i}^{(l)} = E \cdot \left(\sum_{l=0}^{i-1} N_{RB,ePDCCH}^l + (I_{PRB_ePDCCH}^{lowest_index} - N_{PRB,ePDCCH}^{offset}) \right) + n_{eCCE} + N_{ePUCCH}^{(l)}$$

5 where n is an integer, K is the set of downlink subframe indexes in the bundling window of subframe n , M is the number of downlink subframes in the bundling window, $k_i \in K$, $0 \leq i \leq M-1$, $N_{RB,ePDCCH}^l$ indicates the ePDCCH physical resource blocks (PRBs) reserved for use in transmitting the ePDCCH in a downlink subframe, $I_{PRB_ePDCCH}^{lowest_index}$ is the lowest PRB index including at least one
 10 eCCE of the ePDCCH detected in the subframe $n-k_i$, $N_{PRB,ePDCCH}^{offset}$ indicates the lowest PRB index for ePDCCH transmission, E is the number of eCCEs in one PRB, n_{eCCE} is the lowest index of the eCCE in the ePDCCH detected in subframe $n-k_i$, $0 \leq n_{eCCE} < E$, which is mapped to a PUCCH resource index $n_{PUCCH,i}^{(l)}$, and $N_{ePUCCH}^{(l)}$ indicates the PUCCH resources offset for the ePDCCH.

15

5. The method of claim 4 wherein the PUCCH resources offset $N_{ePUCCH}^{(l)}$ is a UE-specific parameter determined by the eNB.

6. The method of claim 1 wherein the ePDCCH is detected in a downlink
 20 subframe belonging to a specified bundling window of M indexed downlink subframes and wherein the PUCCH resource is defined by a PUCCH resource index $n_{PUCCH,i}^{(l)}$ and further comprising, for each subframe in the bundling window having an ePDCCH allocating downlink resources, calculating the PUCCH resource index $n_{PUCCH,i}^{(l)}$ such that consecutive PUCCH resource indices
 25 are mapped to by the ePDCCHs ordered in accordance with an ordered list of the eCCEs making up the ePDCCHs where the eCCEs are ordered first according to PRB index, then according to subframe index, and then according to eCCE index.

7. The method of claim 2 further comprising, for each ePDCCH detected in a subframe $n-k_i$, calculating the PUCCH resource index $n_{PUCCH,i}^{(l)}$ for transmission in a subframe n as:

$$n_{PUCCH,i}^{(l)} = E \cdot \Delta \cdot (I_{PRB_ePDCCH}^{lowest_index} - N_{PRB,ePDCCH}^{offset}) + n_{eCCE} + E \cdot i + N_{ePUCCH}^{(l)}$$

5 where n is an integer, K is the set of downlink subframe indexes in the bundling window of subframe n , M is the number of downlink subframes in the bundling window, $k_i \in K$, $0 \leq i \leq M-1$, $N_{PRB,ePDCCH}^i$ indicates the ePDCCH physical resource blocks (PRBs) reserved for use in transmitting the ePDCCH in a downlink subframe, $I_{PRB_ePDCCH}^{lowest_index}$ is the lowest PRB index including at least one
 10 eCCE of the ePDCCH detected in the subframe $n-k_i$, $N_{PRB,ePDCCH}^{offset}$ indicates the lowest PRB index for ePDCCH transmission, E is the number of eCCEs in one PRB, n_{eCCE} is the lowest index of the eCCE in the ePDCCH detected in subframe $n-k_i$, $0 \leq n_{eCCE} < E$, which is mapped to a PUCCH resource index $n_{PUCCH,i}^{(l)}$, $N_{ePUCCH}^{(l)}$ indicates the PUCCH resources offset for the ePDCCH, and Δ is a
 15 specified integer that depends upon the size of the bundling window.

8. The method of claim 7 wherein the PUCCH resources offset $N_{ePUCCH}^{(l)}$ is a UE-specific parameter determined by the eNB.

20 9. The method of claim 1 wherein the ePDCCH is detected in a downlink subframe belonging to a specified bundling window of M indexed downlink subframes and wherein the PUCCH resource is defined by a PUCCH resource index $n_{PUCCH,i}^{(l)}$ and further comprising, for each subframe in the bundling window having an ePDCCH allocating downlink resources, calculating the
 25 PUCCH resource index $n_{PUCCH,i}^{(l)}$ such that consecutive PUCCH resource indices are mapped to by the ePDCCHs ordered in accordance with an ordered list of the eCCEs making up the ePDCCHs where the eCCEs are ordered first according to slot number within the subframe, then according to subframe index, then according to PRB index, and then according to eCCE index.

10. The method of claim 2 further comprising, for each ePDCCH detected in a subframe $n-k_i$, calculating the PUCCH resource index $n_{PUCCH,i}^{(l)}$ for transmission in a subframe n by selecting a value p from $\{0,1\}$ that satisfies:

$$N_p \leq n_{eCCE} < N_{p+1} \text{ where } N_0 = 0, N_1 = 2, \text{ and } N_2 = 4$$

5 and calculating $n_{PUCCH,i}^{(l)}$ as:

$$n_{PUCCH,i}^{(l)} = E/2 \cdot (I_{PRB_ePDCCH}^{lowest_index} - N_{PRB,ePDCCH}^{offset}) + n_{eCCE} + F_1 + F_2 + \delta_i + N_{ePUCCH}^{(l)}$$

where n is an integer, K is the set of downlink subframe indexes in the bundling window of subframe n , M is the number of downlink subframes in the bundling window, $k_i \in K$, $0 \leq i \leq M-1$, $N_{RB,ePDCCH}^i$ indicates the ePDCCH physical

10 resource blocks (PRBs) reserved for use in transmitting the ePDCCH in a downlink subframe, $I_{PRB_ePDCCH}^{lowest_index}$ is the lowest PRB index including at least one eCCE of the ePDCCH detected in the subframe $n-k_i$, $N_{PRB,ePDCCH}^{offset}$ indicates the lowest PRB index for ePDCCH transmission, E is the number of eCCEs in one PRB, n_{eCCE} is the lowest index of the eCCE in the ePDCCH detected in subframe

15 $n-k_i$, $0 \leq n_{eCCE} < E$, which is mapped to a PUCCH resource index $n_{PUCCH,i}^{(l)}$,

$N_{ePUCCH}^{(l)}$ indicates the PUCCH resources offset for the ePDCCH, Δ is a specified integer that depends upon the bundling window size, and where:

where:

$$F_1 = \sum_{l=0}^{l=M-i-2} N_{RB,ePDCCH}^{M-l-1} \cdot N_p,$$

20 $F_2 = \sum_{j=0}^i N_{RB,ePDCCH}^j \cdot N_{p+1}$, and

$$\delta_i = (N_{RB,ePDCCH}^i - I_{PRB_ePDCCH}^{lowest_index} - 1) \cdot E/2 \cdot p.$$

11. The method of claim 10 wherein the PUCCH resources offset $N_{ePUCCH}^{(l)}$ is a UE-specific parameter determined by the eNB.

12. The method of claim 1 wherein the ePDCCH is detected in a downlink subframe belonging to a specified bundling window of M indexed downlink subframes and wherein the PUCCH resource is defined by a PUCCH resource index $n_{PUCCH,i}^{(l)}$ and further comprising, for each subframe in the bundling window having an ePDCCH allocating downlink resources, calculating the PUCCH resource index $n_{PUCCH,i}^{(l)}$ such that consecutive PUCCH resource indices are mapped to by the ePDCCHs ordered in accordance with an ordered list of the eCCEs making up the ePDCCHs where the eCCEs are ordered first according to PRB index, then according to slot number within the subframe, then according to subframe index, and then according to eCCE index.

13. The method of claim 2 further comprising, for each ePDCCH detected in a subframe n- k_i , calculating the PUCCH resource index $n_{PUCCH,i}^{(l)}$ for transmission in a subframe n by selecting a value p from {0,1} that satisfies:

$$N_p \leq n_{eCCE} < N_{p+1} \text{ where } N_0 = 0, N_1 = 2, \text{ and } N_2 = 4$$

and calculating $n_{PUCCH,i}^{(l)}$ as:

$$n_{PUCCH,i}^{(l)} = E \cdot \Delta \cdot (I_{PRB,ePDCCH}^{lowest_index} - N_{PRB,ePDCCH}^{offset}) + n_{eCCE} + (M - i - 1) \cdot N_p + i \times N_{p+1} + N_{ePUCCH}^{(l)}$$

where n is an integer, K is the set of downlink subframe indexes in the bundling window of subframe n, M is the number of downlink subframes in the bundling window, $k_i \in K, 0 \leq i \leq M-1$, $N_{RB,ePDCCH}^i$ indicates the ePDCCH physical resource blocks (PRBs) reserved for use in transmitting the ePDCCH in a downlink subframe, $I_{PRB,ePDCCH}^{lowest_index}$ is the lowest PRB index including at least one eCCE of the ePDCCH detected in the subframe n- k_i , $N_{PRB,ePDCCH}^{offset}$ indicates the lowest PRB index for ePDCCH transmission, E is the number of eCCEs in one PRB, n_{eCCE} is the lowest index of the eCCE in the ePDCCH detected in subframe n- $k_i, 0 \leq n_{eCCE} < E$, which is mapped to a PUCCH resource index $n_{PUCCH,i}^{(l)}$, $N_{ePUCCH}^{(l)}$ indicates the PUCCH resources offset for the ePDCCH, Δ is a specified integer that depends upon the bundling window size.

14. The method of claim 13 wherein the PUCCH resources offset $N_{ePUCCH}^{(l)}$ is a UE-specific parameter determined by the eNB.

15. The method of claim 1 wherein the ePDCCH is detected in a downlink subframe belonging to a specified bundling window of M indexed downlink subframes and wherein the PUCCH resource is defined by a PUCCH resource index $n_{PUCCH,i}^{(l)}$ and further comprising, for each subframe in the bundling window having an ePDCCH allocating downlink resources, calculating the PUCCH resource index $n_{PUCCH,i}^{(l)}$ such that consecutive PUCCH resource indices are mapped to by the ePDCCHs ordered in accordance with an ordered list of the eCCEs making up the ePDCCHs where the eCCEs are ordered first according to PRB index, then according to eCCE index, then according to subframe index.

16. The method of claim 2 further comprising, for each ePDCCH detected in a subframe $n-k_i$, calculating the PUCCH resource index $n_{PUCCH,i}^{(l)}$ for transmission in a subframe n as:

$$n_{PUCCH,i}^{(l)} = \Delta \cdot (E \cdot (I_{PRB_ePDCCH}^{lowest_index} - N_{PRB,ePDCCH}^{offset}) + n_{eCCE}) + i + N_{ePUCCH}^{(l)}$$

where n is an integer, K is the set of downlink subframe indexes in the bundling window of subframe n, M is the number of downlink subframes in the bundling window, $k_i \in K$, $0 \leq i \leq M-1$, $N_{PRB,ePDCCH}^i$ indicates the ePDCCH physical resource blocks (PRBs) reserved for use in transmitting the ePDCCH in a downlink subframe, $I_{PRB_ePDCCH}^{lowest_index}$ is the lowest PRB index including at least one eCCE of the ePDCCH detected in the subframe $n-k_i$, $N_{PRB,ePDCCH}^{offset}$ indicates the lowest PRB index for ePDCCH transmission, E is the number of eCCEs in one PRB, n_{eCCE} is the lowest index of the eCCE in the ePDCCH detected in subframe $n-k_i$, $0 \leq n_{eCCE} < E$, which is mapped to a PUCCH resource index $n_{PUCCH,i}^{(l)}$, $N_{ePUCCH}^{(l)}$ indicates the PUCCH resources offset for the ePDCCH, and Δ is a specified integer that depends upon the bundling window size.

17. The method of claim 16 wherein the PUCCH resources offset $N_{ePUCCH}^{(l)}$ is a UE-specific parameter determined by the eNB.
18. A method for operating an evolved Node B (eNB) in a in an LTE (Long Term Evolution) network using TDD (time division duplex) mode, comprising:
 5 transmitting data to a UE (user equipment) via a downlink physical downlink shared channel (PDSCH) in a subframe;
 allocating PDSCH resources to the UE for the subframe in an enhanced physical downlink control channel (ePDCCH) made up of one or more enhanced
 10 control channel elements (eCCEs) contained within physical resource blocks (PRBs) that indexed in a cell-specific manner;
 allocating a physical uplink control channel (PUCCH) resource to the UE for acknowledging the transmitted data by implicitly indicating the PUCCH resource in the ePDCCH.
19. The method of claim 18 wherein the PUCCH resource is defined by a PUCCH resource index $n_{PUCCH,i}^{(l)}$ that is a function of subframe index, lowest
 index of a PRB containing at least one eCCE of the detected ePDCCH, and
 lowest eCCE index of the detected ePDCCH.
20. The method of claim 19 wherein the PUCCH resource index $n_{PUCCH,i}^{(l)}$ is further a function of a PUCCH resources offset $N_{ePUCCH}^{(l)}$ that is determined by the eNB in a UE-specific manner.
21. A device operating as user equipment (UE) in an LTE network, comprising:
 an RF transceiver for providing an LTE air interface for communicating with a base station operating as an enhanced/evolved Node B (eNB); and
 processing circuitry to:
 30 receive transmitted downlink resource allocations from an eNB in an enhanced physical downlink control channel (ePDCCH) made up of one or more

indexed enhanced control channel elements (eCCEs) contained within indexed physical resource blocks (PRBs); and,

5 detect an ePDCCH allocating downlink resources in a subframe belonging to a specified bundling window of M indexed subframes and transmit a hybrid-ARQ acknowledgement corresponding to the detected ePDCCH via a physical uplink control channel (PUCCH) resource defined by a PUCCH resource index $n_{PUCCH,i}^{(l)}$ that is a function of subframe index, PRB index, and t eCCE index.

10 22. The device of claim 21 wherein the processing circuitry is further to, for each subframe in the bundling window having an ePDCCH allocating downlink resources, calculate the PUCCH resource index $n_{PUCCH,i}^{(l)}$ such that consecutive PUCCH resource indices are mapped to by ePDCCHs ordered in accordance with an ordered list of the eCCEs making up the ePDCCHs where the eCCEs are ordered first according to subframe index, then according to PRB index, and then according to eCCE index.

20 23. The device of claim 21 wherein the processing circuitry is further to, for each subframe in the bundling window having an ePDCCH allocating downlink resources, calculate the PUCCH resource index $n_{PUCCH,i}^{(l)}$ such that consecutive PUCCH resource indices are mapped to by ePDCCHs ordered in accordance with an ordered list of the eCCEs making up the ePDCCHs where the eCCEs are ordered first according to PRB index, then according to subframe index, and then according to eCCE index.

25 24. The device of claim 21 wherein the processing circuitry is further to, for each subframe in the bundling window having an ePDCCH allocating downlink resources, calculate the PUCCH resource index $n_{PUCCH,i}^{(l)}$ such that consecutive PUCCH resource indices are mapped to by ePDCCHs ordered in accordance with an ordered list of the eCCEs making up the ePDCCHs where the eCCEs are ordered first according to slot number within the subframe, then according to

subframe index, then according to PRB index, and then according to eCCE index.

25. The device of claim 21 wherein the processing circuitry is further to, for
5 each subframe in the bundling window having an ePDCCH allocating downlink
resources, calculate the PUCCH resource index $n_{PUCCH,i}^{(l)}$ such that consecutive
PUCCH resource indices are mapped to by ePDCCHs ordered in accordance
with an ordered list of the eCCEs making up the ePDCCHs where the eCCEs are
ordered first according to PRB index, then according to slot number within the
10 subframe, then according to subframe index, and then according to eCCE index.

26. The device of claim 21 wherein the processing circuitry is further to, for
each subframe in the bundling window having an ePDCCH allocating downlink
resources, calculate the PUCCH resource index $n_{PUCCH,i}^{(l)}$ such that consecutive
15 PUCCH resource indices are mapped to by ePDCCHs ordered in accordance
with an ordered list of the eCCEs making up the ePDCCHs where the eCCEs are
ordered first according to PRB index, then according to eCCE index, then
according to subframe index.

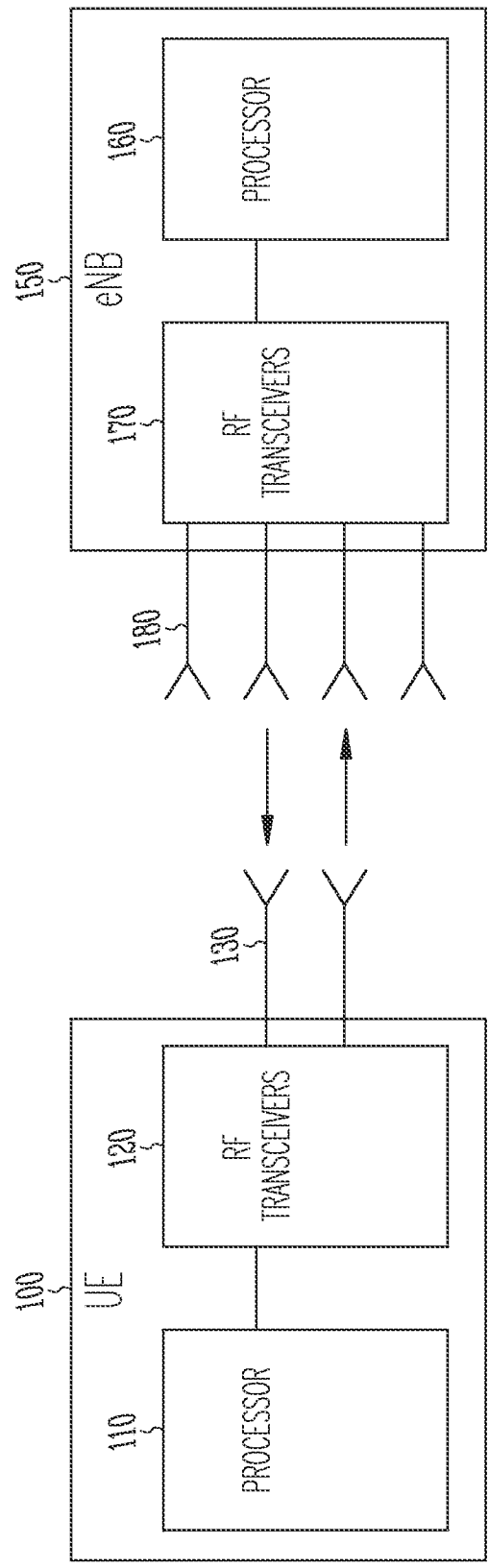


Fig. 1

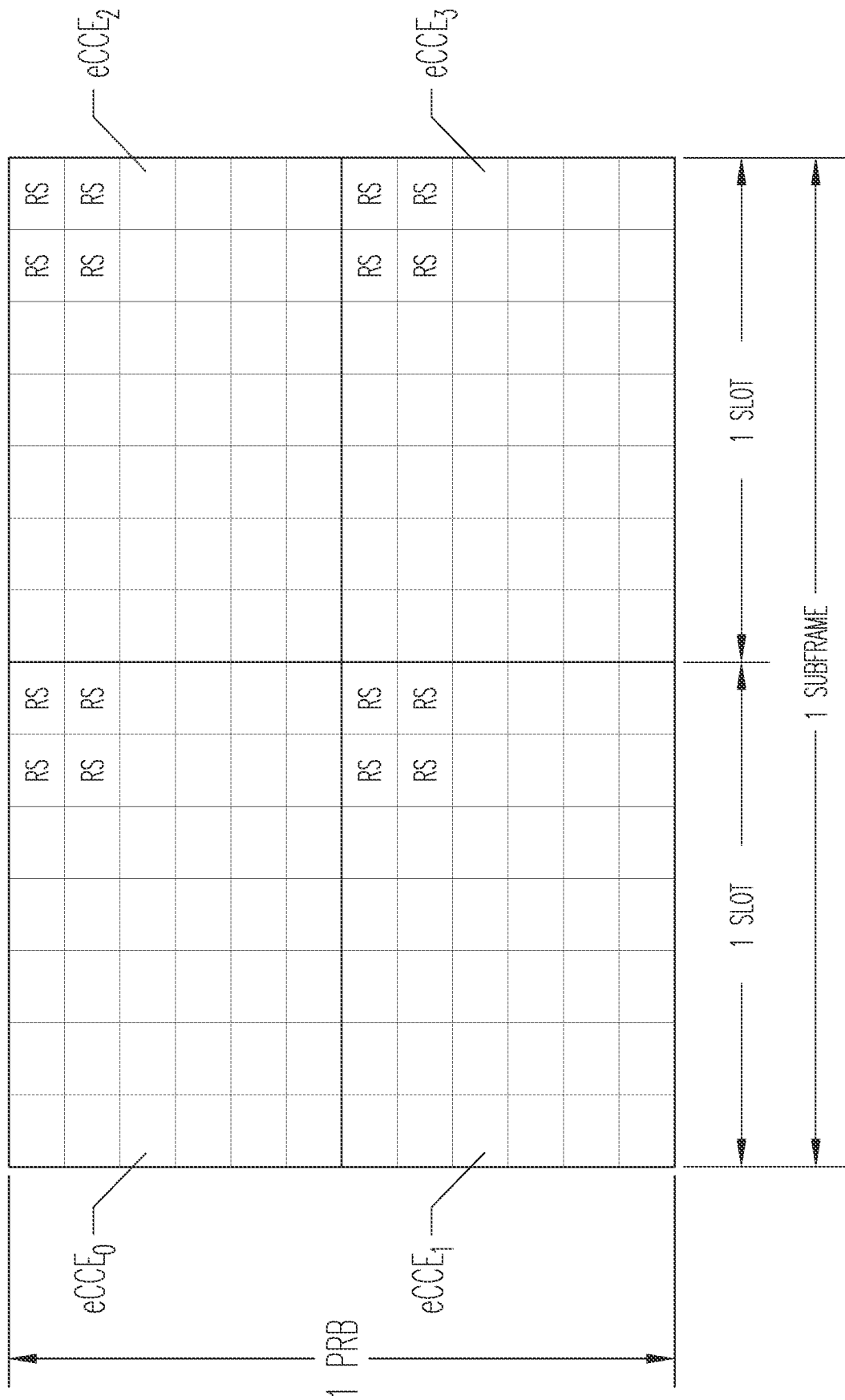


Fig. 2

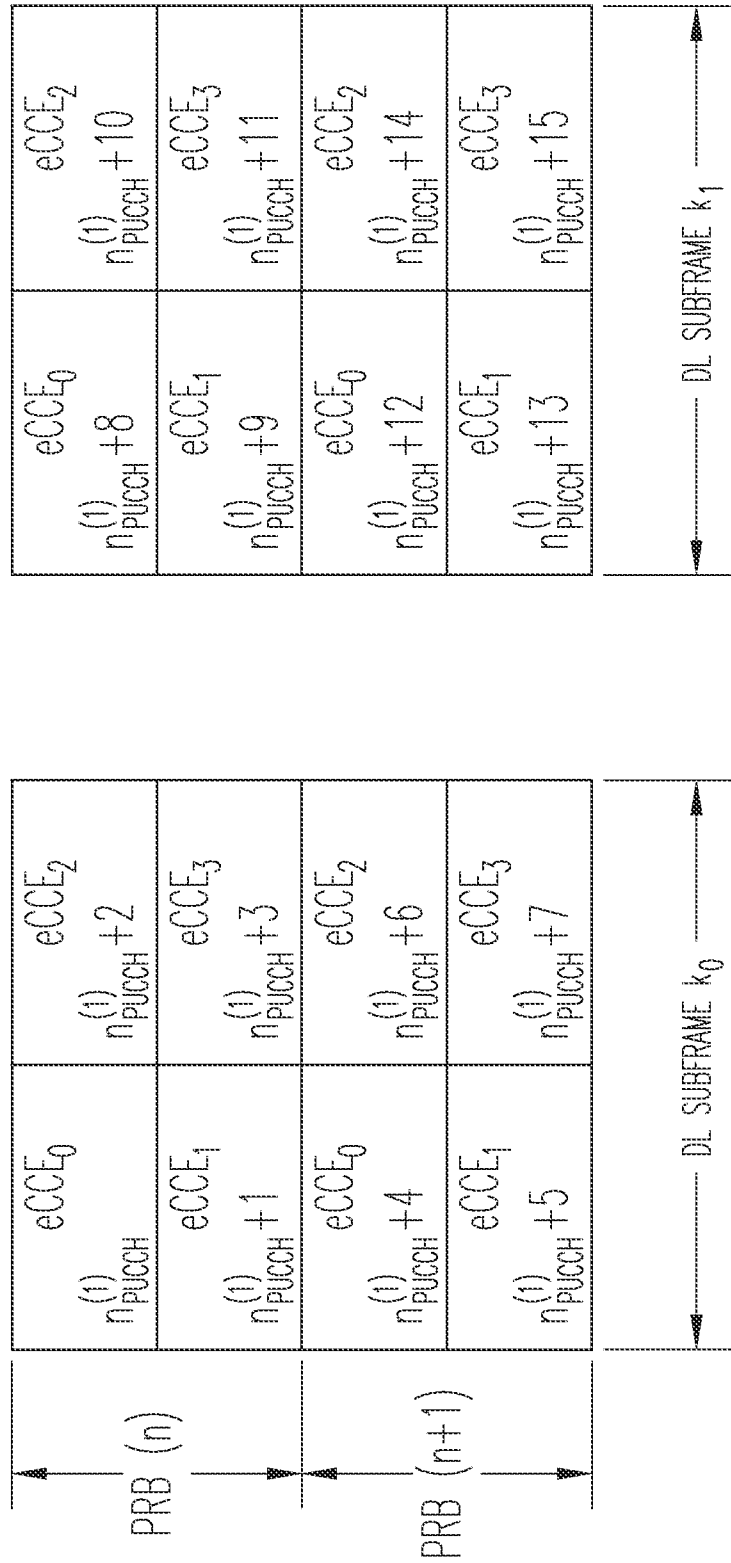


Fig. 3

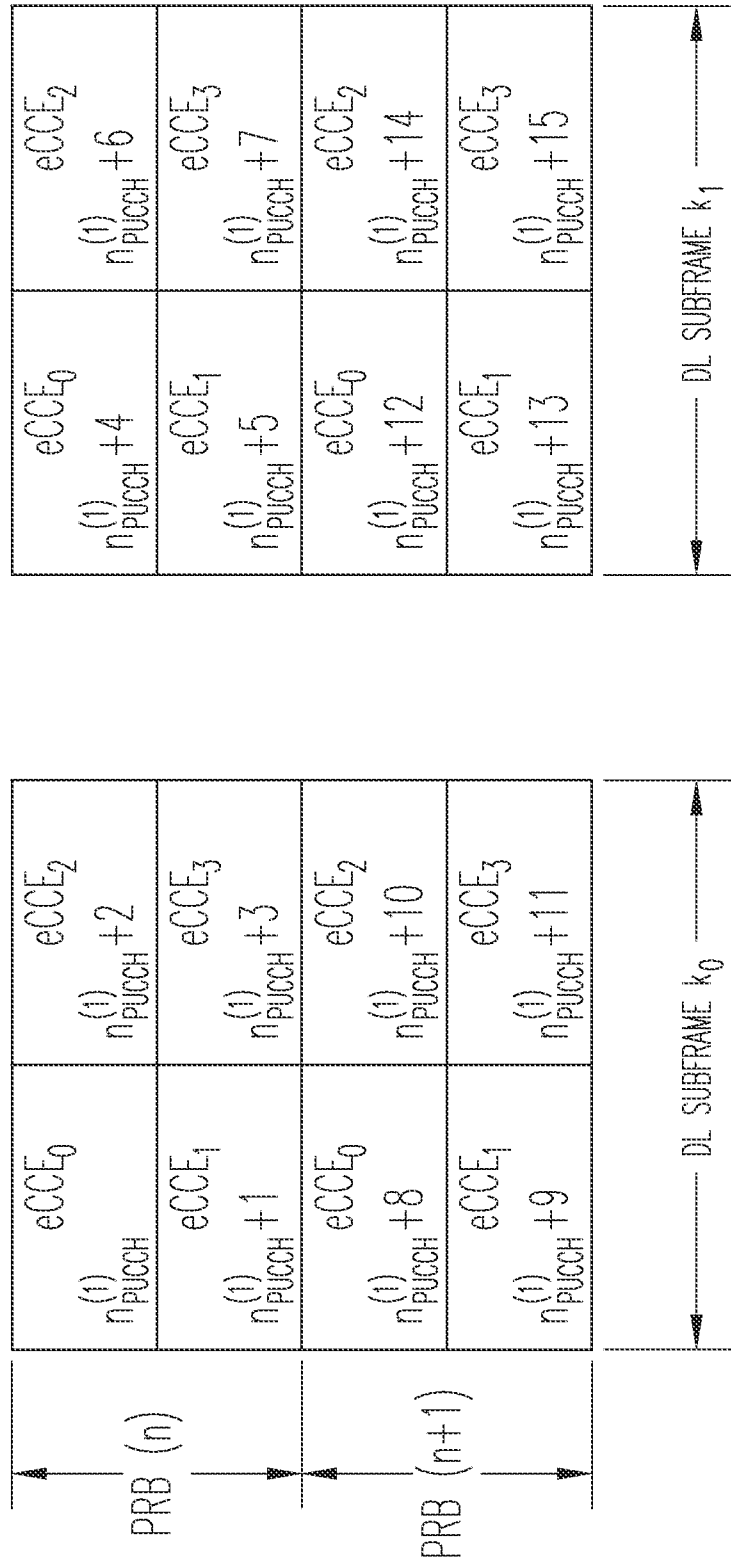


Fig. 4

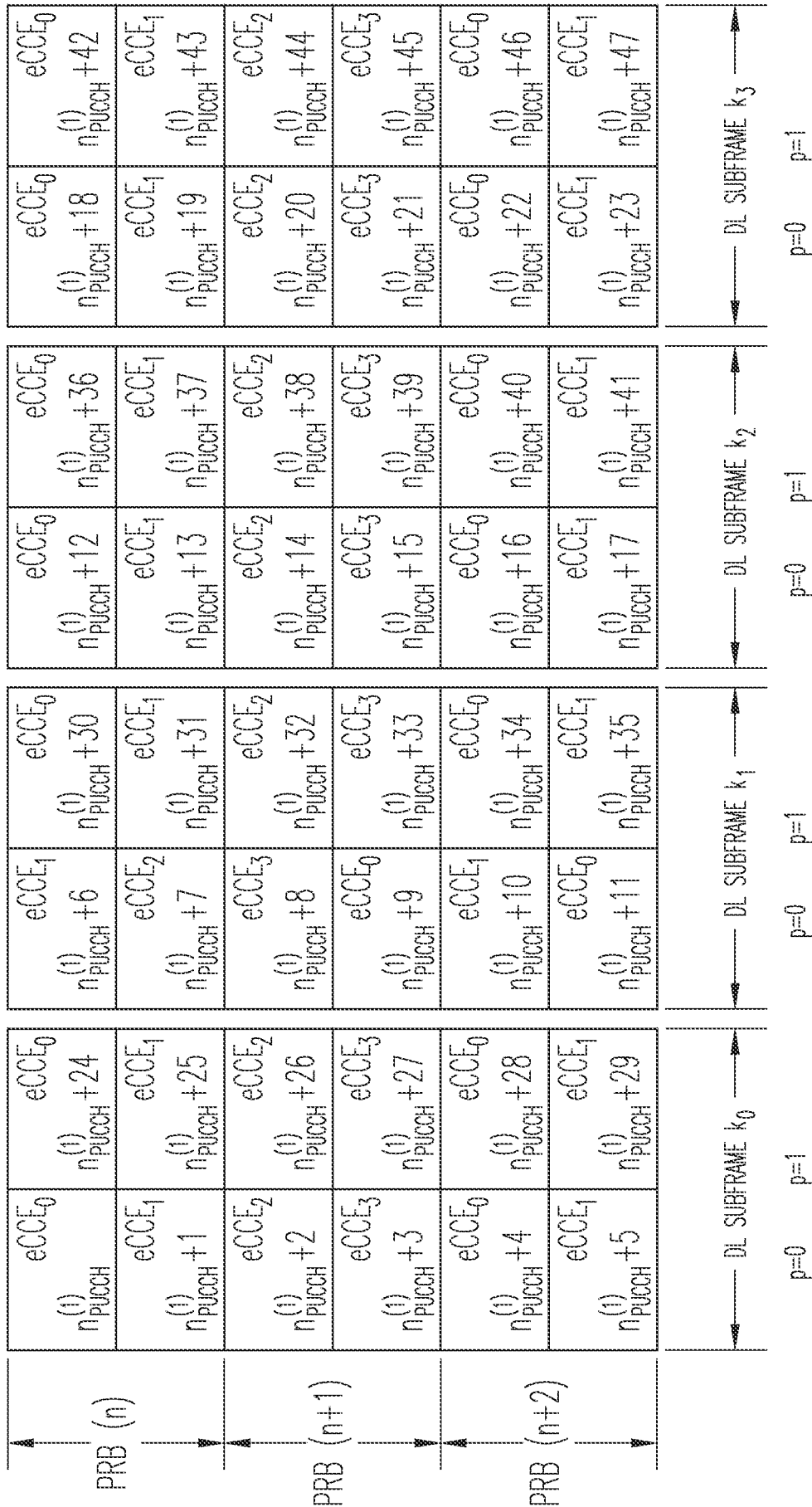


Fig. 5

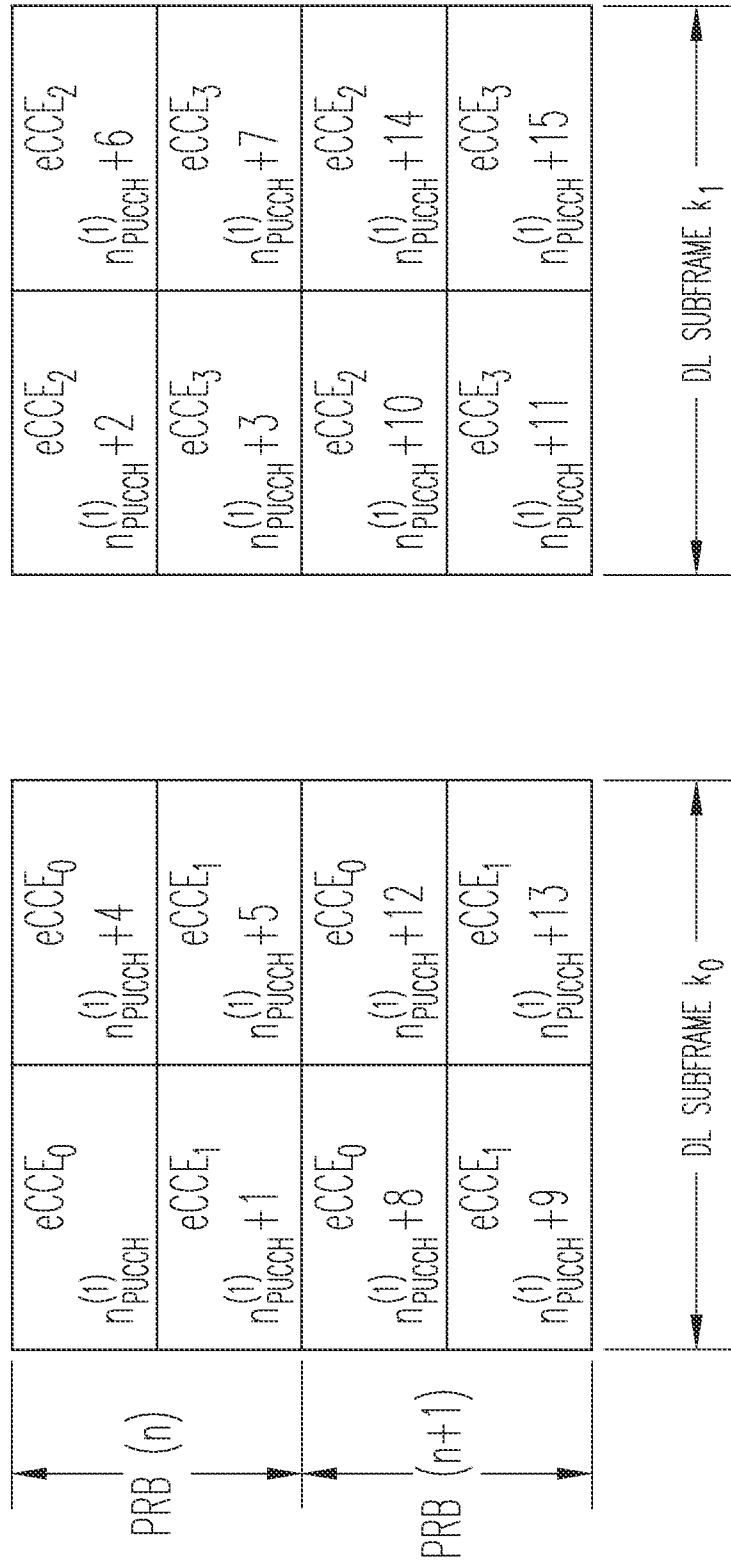


Fig. 6

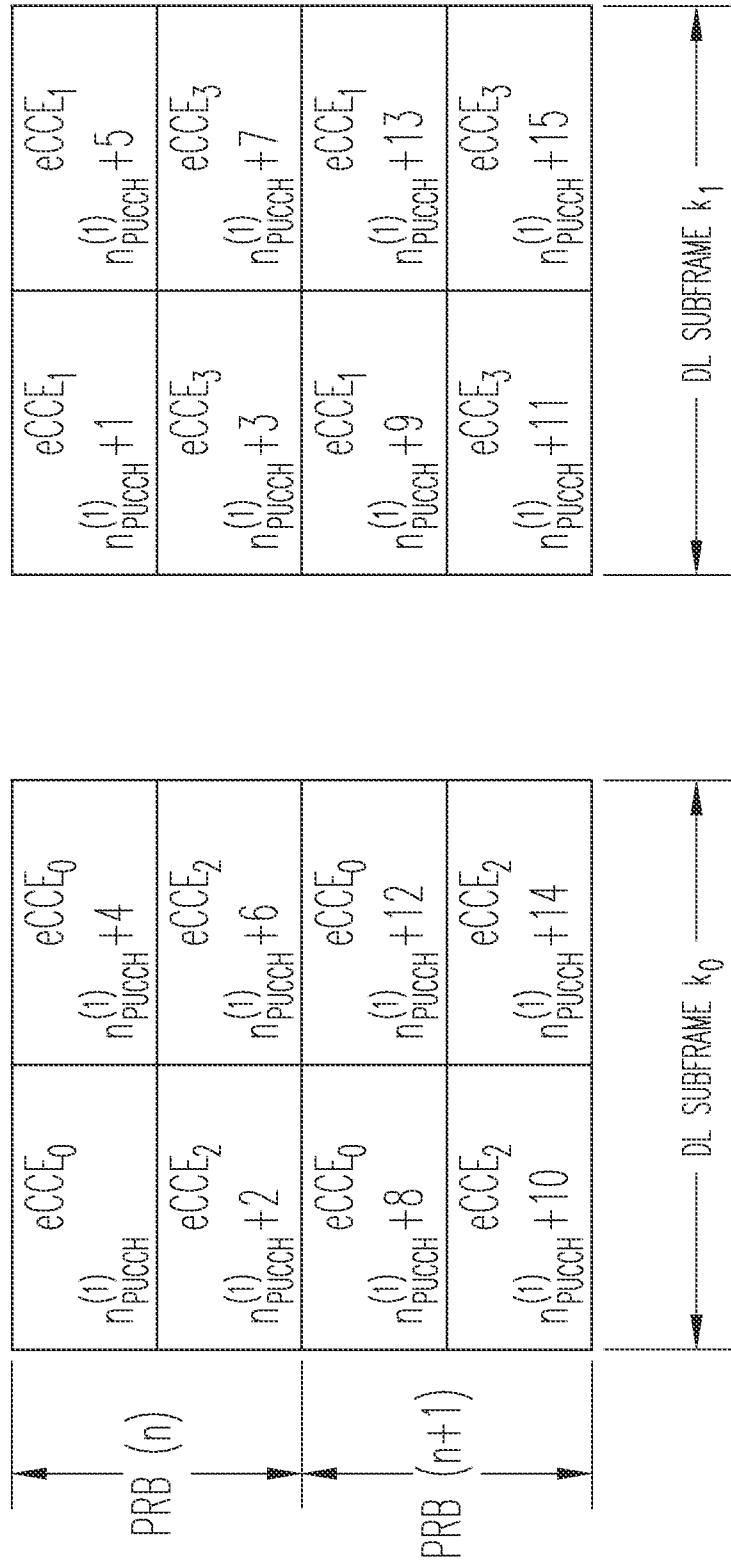


Fig. 7

INTERNATIONAL SEARCH REPORT

International application No.
PCT/US2013/027332**A. CLASSIFICATION OF SUBJECT MATTER****H04B 7/26(2006.01)i**

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHEDMinimum documentation searched (classification system followed by classification symbols)
H04L 01/18; H04L 01/18; H04L 12/56; H04B 07/212;Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched
Korean utility models and applications for utility models
Japanese utility models and applications for utility modelsElectronic data base consulted during the international search (name of data base and, where practicable, search terms used)
eKOMPASS(KIPO internal) & Keywords: ePDCCH, eCCE, PRB, H-ARQ, PUCCH resource offset, index, bundling window.**C. DOCUMENTS CONSIDERED TO BE RELEVANT**

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	KR 10-2011-0068839 A (LG ELECTRONICS INC.) 22 June 2011 See claims 1-2, 12-13; paragraphs 10-15; and figure 7.	1-26
A	KR 10-2009-0107559 A (LG ELECTRONICS INC.) 13 October 2009 See claims 1-3, 8-10; paragraphs 5-7, 15-16; and figure 4.	1-26
A	PANTECH, 'PUCCH resource allocation for E-PDCCH', R1-120329, 3GPP TSG RAN1 #68, Dresden, Germany, 6-10 February 2012 See page 1, lines 10-11; page 1, lines 21-26; and page 2, lines 3-4.	1-26
A	US 2012-0042221 A1 (KIM, SO YEON et al.) 16 February 2012 See claims 1-3; paragraphs 42-47; and figure 6.	1-26
A	US 2010-0208629 A1 (AHN, JOON KUI et al.) 19 August 2010 See claims 19-21; paragraphs 13-14, 70-77; and figure 6.	1-26

 Further documents are listed in the continuation of Box C. See patent family annex.

* Special categories of cited documents:

"A" document defining the general state of the art which is not considered to be of particular relevance

"E" earlier application or patent but published on or after the international filing date

"L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of citation or other special reason (as specified)

"O" document referring to an oral disclosure, use, exhibition or other means

"P" document published prior to the international filing date but later than the priority date claimed

"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention

"X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone

"Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art

"&" document member of the same patent family


Date of the actual completion of the international search

13 June 2013 (13.06.2013)

Date of mailing of the international search report

14 June 2013 (14.06.2013)

Name and mailing address of the ISA/KR


 Korean Intellectual Property Office
 189 Cheongsu-ro, Seo-gu, Daejeon Metropolitan City,
 302-701, Republic of Korea

Facsimile No. 82-42-472-7140

Authorized officer

KANG, Hee Gok

Telephone No. 82-42-481-8264



INTERNATIONAL SEARCH REPORT

Information on patent family members

International application No.

PCT/US2013/027332

Patent document cited in search report	Publication date	Patent family member(s)	Publication date
KR 10-2011-0068839 A	22.06.2011	EP 2514129 A2 US 2012-0300722 A1 WO 2011-074839 A2	24.10.2012 29.11.2012 23.06.2011
KR 10-2009-0107559 A	13.10.2009	CN102017506 A EP 2248294 A2 JP 2011-517168 A KR 10-0937299 B1 US 2010-0208629 A1 US 2011-0261729 A1 US 7957329 B2 WO 2009-116754 A2	13.04.2011 10.11.2010 26.05.2011 08.01.2010 19.08.2010 27.10.2011 07.06.2011 24.09.2009
US 2012-0042221 A1	16.02.2012	KR 10-2010-0117045 A WO 2010-123323 A2 WO 2010-123323 A3	02.11.2010 28.10.2010 27.01.2011
US 2010-0208629 A1	19.08.2010	CN102017506 A EP 2248294 A2 JP 2011-517168 A KR 10-0937299 B1 US 2011-261729 A1 US 7957329 B2 WO 2009-116754 A2 WO 2009-116754 A3	13.04.2011 10.11.2010 26.05.2011 18.01.2010 27.10.2011 07.06.2011 24.09.2009 23.12.2009