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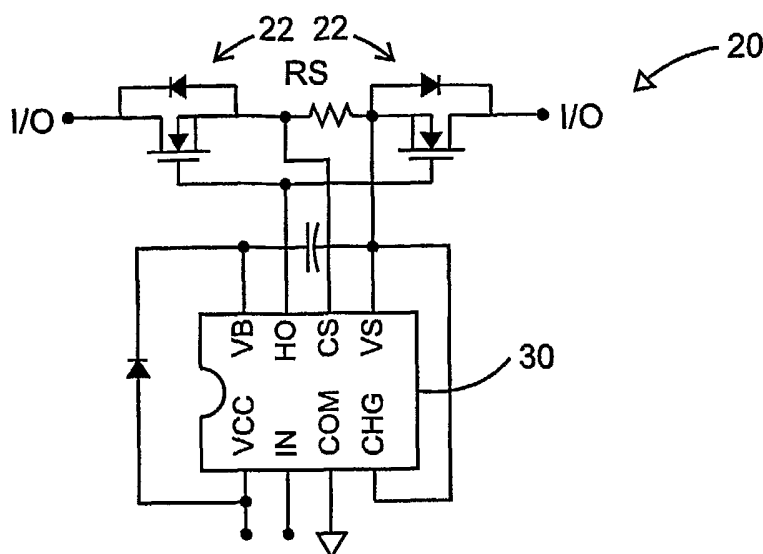
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(54) Title: CURRENT SENSING BI-DIRECTIONAL SWITCH AND PLASMA DISPLAY DRIVER CIRCUIT



(57) Abstract: A bi-directional switch (20) comprising first and second semiconductor switching devices (22), a current sensor (RS) connected in series with the switching devices (22), thereby forming a series circuit, a driver circuit (30) controlling the on/off operation of the first and second switching devices (22) are substantially simultaneously turned on and off, the driver circuit (30) turning the first and second switching devices (22) on in response to a control input and turning the first and second switching devices (22) off when current in the current sensor (RS) substantially drops to near a zero current. A discharge sustain driver circuit employing the bi-directional switches (20) for a plasma display panel (PDP) is also described.

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CURRENT SENSING BI-DIRECTIONAL
SWITCH AND PLASMA DISPLAY DRIVER CIRCUIT

CROSS REFERENCE TO RELATED APPLICATION

The present application claim the benefit and priority of U.S. Provisional Patent
5 application S.N. 60/475,180 filed May 30, 2003 entitled "CURRENT SENSING BI-
DIRECTIONAL SWITCH FOR PDP APPLICATIONS", the entire disclosure of which
is incorporated by reference herein.

BACKGROUND OF THE INVENTION

The present invention relates to switching circuits, and more particularly, to a
10 current sensing bi-directional switching circuit and even more particularly, to a current
sensing bi-directional switching circuit for plasma display applications. The present
invention relates to a current sensing bi-directional switch and a sustain driver circuit for
plasma display devices using the bidirectional switch.

Plasma display devices are gaining popularity because they are flat screen display
15 devices. Currently, plasma display panel (PDP) devices are used for many display
applications including television monitors and receivers and computer monitors. In a
plasma display device of the AC type, an AC voltage, typically of approximately 180
volts, is provided to the display device. When the display device discharges it can only
do so for a limited period of time. In order to sustain the discharge, an AC signal can be
20 provided to the PDP device to sustain the discharge. A PDP device is essentially
capacitive, so it is necessary to quickly provide the alternating current voltage to the PDP
to sustain the discharge. Accordingly, the PDP must be charged and discharged
repeatedly with the AC signal which reverses the voltage across the PDP at a periodic
rate.

25 Currently, typical PDP sustain drivers utilize at least two capacitors to store a
charge developed across the PDP from the B plus voltage and a number of transistor

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switches and diodes as well as at least two inductors to periodically reverse the charge across the PDP.

Typically, such PDP sustain drivers incorporate a full bridge driver, two inductors and two additional switching circuits connected to charge storage capacitors to store the
5 charge and allow it to be reversed.

With reference to Fig. 1, a typical prior art PDP sustain driver is shown. The PDP device, being primarily capacitive, is indicated by the capacitor C_p . The capacitor C_p is connected to the outputs of a full bridge driver 10 comprising transistors Q3, Q4, Q7 and Q8. The full bridge driver is connected between the B plus source, typically 170
10 to 180 volts DC and ground. The full bridge driver outputs, which are connected across the PDP device indicated by C_p , are also connected through inductors L1 and L2 to respective charge storage circuits 12 and 14. Once charge storage circuit 12 comprises transistors Q1 and Q2, diodes D1 and D2 and the charge storage capacitor C1. The other charge storage circuit 14 comprises transistors Q5 and Q6, diodes D3 and D4 and the
15 charge storage capacitor C2. In addition, diodes D5, D6, D7 and D8 are also needed. The circuit shown in Fig. 1 thus comprises eight transistors, eight diodes, two inductors and two charge storage capacitors.

In the circuit of Fig. 1, the AC plasma display panel (ACPDP) employs the full bridge driver 10 to alternately impose a positive and a negative voltage on the panel (C_p)
20 and sustain the image for a predetermined length of time. Since the PDP is a capacitive load, high peak currents are forced to flow in the switches comprising the full bridge, which can results in excessive losses, thereby reducing system efficiency. To reduce such losses and peak currents, the PDP sustain circuit as shown in Fig. 1 uses charge storage and recovery circuitry to reduce the peak currents.

25 With reference to Fig. 1, the cycle operates as follows: Initially, the panel C_p is charged in the positive direction as shown in Fig. 1 from the bus voltage source. Transistors Q2 and Q8 are initially turned on. The charge from C_p is transferred to capacitor C1 through inductor L1, diode D2, transistor Q2 and transistor Q8. Q2 and Q8 are then turned off. Q5 and Q4 are then turned on. With these transistors turned on the

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PDP indicated by C_p will now charge in the reverse direction from the charge stored on capacitor C_2 via transistor Q_5 , diode D_3 inductor L_2 and transistor Q_4 . C_p is now charged in the reverse direction and Q_5 is turned off. Transistor Q_7 is then turned on and capacitor C_p is charged to the full bus voltage through transistor Q_7 as well as transistor
5 Q_4 which is still turned on. Q_7 is then turned off after a predetermined time and the PDP C_p is now fully charged in the reverse direction. Transistor Q_6 is then turned on while transistor Q_4 is still on. The charge on C_p is transferred to capacitor C_2 via L_2 , D_4 , Q_6 and Q_4 .

Transistors Q_1 and Q_8 are then turned on. The charge present on C_1 is then
10 transferred to C_p thereby again charging the panel in the opposite direction. The charge on capacitor C_1 is transferred to C_p via transistor Q_1 , diode D_1 , inductor L_1 and transistor Q_8 . At this point transistor Q_4 is off. Transistor Q_1 is now turned off and Q_3 is turned on while Q_8 remains on, thereby fully charging the plasma display panel capacitance to the full bus voltage in the initial direction. Q_3 is then turned off after a
15 predetermined time and the cycle repeats again so that Q_2 and Q_8 are turned on transferring the charge to capacitor C_1 as previously described.

Components Q_1 , Q_2 , D_1 and D_2 serve as the bidirectional switch which transfers charge from C_p to C_1 and back to C_p . Similarly, components Q_5 , Q_6 , D_3 and D_4 serve to transfer the charge between C_p and C_2 . These transistors are driven by half bridge
20 drivers for example IR-2110 or IR-2113 half bridge drivers. The inductors L_1 and L_2 are required to ensure that most of the charge is transferred. In the absence of these inductors, only half of the charge will be transferred in either direction. Transferring most of the charge is highly desirable since a low voltage differential between C_p and the bus voltage will results in lower peak currents flowing through the full bridge switches,
25 reducing losses. The timing for the transfer is also critical. It has to be a sufficient length such that the current in the inductor is near zero, as this ensures that maximum charge is transferred in either direction. Figs. 2 and 3 show a simulation of major components in the prior art bidirectional switch. As apparent, the voltage across C_1 is at its maximum i.e., most of the charge has been transferred, when the voltage across C_p is

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minimum and the current in the inductor L1 is zero. Component and timing variations will inevitably cause a residual current to be present in the inductor at the end of the transfer period. The diodes D5, D6, D7 and D8 are included to dissipate this residual current but generate additional losses.

5 The circuit shown in Fig. 1 is complex and requires a significant number of components, as described, eight transistors, eight diodes, two storage capacitors and two inductors. The circuit is complex, expensive and suffers from unnecessary switching losses as a result of the large number of components.

It is desirable to provide a simpler, less expensive circuit that uses fewer
10 components and suffers from fewer losses.

It is also desirable to provide an improved bi-directional switch which can be used in a PDP sustain driver circuit as well as in other applications.

SUMMARY OF THE INVENTION

It is an object of the present invention to provide an improved bi-directional
15 switch and, in particular, a current sensing bi-directional switch.

It is yet still a further object of the present invention to provide an improved sustain driver for plasma display devices.

The above and other objects of the present invention are achieved by a bi-directional switch comprising: first and second semiconductor switching devices, a
20 current sensor connected in series with the switching devices, thereby forming a series circuit, a driver circuit controlling the on/off operation of the first and second switching devices such that the first and second switching devices are substantially simultaneously turned on and off, the driver circuit turning the first and second switching devices on in response to a control input and turning the first and second switching devices off when
25 current in the current sensor substantially drops to near a zero current.

The objects of the present invention are also achieved by a bi-directional switch comprising: at least one semiconductor switching device, a current sensor connected in series with the switching device, thereby forming a series circuit, a driver circuit

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controlling the on/off operation of the at least one switching device the driver circuit turning the switching device on in response to a control input and turning the switching device off when current in the current sensor substantially drops to near a zero current.

Further, the objects of the invention are also achieved by a discharge sustain
5 driver circuit for a plasma display device, the driver circuit comprising: a first transistor switching circuit for switching a DC bus voltage across the plasma display device, a storage capacitance, at least one inductor; and first and second bi-directional switching circuits coupled in series and being coupled to the first switching circuit to transfer charge from the plasma display device through the at least one inductor to the storage
10 capacitance, and back to the plasma display device; and a controller for the bi-directional switching circuits to control the bi-directional switching circuits so as to receive the charge on the storage capacitance and return the charge in an opposite charge direction to the plasma display device.

The objects of the invention are also achieved wherein the bi-directional
15 switching circuits each include a current sensor and each turn off when current through the switching circuit is approximately zero.

The objects of the invention are furthermore achieved by a method of operating a discharge sustain driver circuit for a plasma display device, the driver circuit comprising a first transistor switching circuit for switching a DC bus voltage across the plasma
20 display device, a storage capacitance, at least one inductor; and first and second bi-directional switching circuits coupled in series and being coupled to the first switching circuit to transfer charge from the plasma display device through the at least one inductance to the storage capacitor, and back to the plasma display device, and a controller for the bi-directional switching circuits to control the bi-directional switching
25 circuits so as to receive the charge on the storage capacitance and return the charge in an opposite charge direction to the plasma display device; and wherein the first switching comprises a full bridge switching circuit, the full bridge switching circuit comprising first and second series connected transistors connected across the DC bus and third and fourth series connected transistors connected across the DC bus; wherein the first and third

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transistors are high side connected and the second and fourth are low side connected; the plasma display device being connected across a common connection of the first and second transistors and a common connection of the third and fourth transistors, and wherein the first and second bi-directional switches are coupled in series together, and

5 wherein the storage capacitance and the at least one inductor are coupled in a series circuit across the second bi-directional switch, the method comprising: turning on the first and fourth transistors to substantially charge the display device to the bus voltage; turning off the first and fourth transistors when the display device has changed to substantially the bus voltage; turning on the first bi-directional switch to transfer the

10 charge on the display device to the storage capacitance; turning off the first bi-directional switch when the current through the switch is substantially zero; turning on the second bi-directional switch to reverse the charge across the storage capacitance; turning off the second bi-directional switch when the current therethrough is substantially zero; turning on the first bi-directional switch to transfer the reverse charge

15 on the storage capacitance to the display device; turning on the second and third transistors to fully charge the display device to substantially the bus voltage in the reverse direction; turning off the second and third transistors when the display device has charged to substantially the bus voltage; turning on the first bi-directional switch to transfer the reverse charge on the display device to the storage capacitance; turning off

20 the first bi-directional switch when the current through the switch is substantially zero; turning on the second bi-directional switch to again reverse the charge across the storage capacitance; turning off the second bi-directional switch when the current therethrough is substantially zero; turning on the first bi-directional switch to transfer the charge on the storage capacitance to the display device; and repeating the above steps as long as desired

25 to sustain a discharge in the display device.

Other features and advantages of the present invention will become apparent from the following description of the invention which refers to the accompanying drawings.

BRIEF DESCRIPTION OF THE DRAWING(S)

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The invention will now be described in greater detail in the following detailed description with reference to the drawings in which:

Fig. 1 shows a prior art PDP sustain driver circuit;

Fig. 2 shows a circuit used in simulating charge transfer from the PDP to a charge
5 storage capacitor for the circuit of Fig. 1;

Fig. 3 shows simulation results of transferring the charge from the PDP to the storage capacitor including the voltage across the storage capacitor, the voltage across the PDP C_p , current in the inductor L1 and starting pulse;

Fig. 4A shows the current sensing bi-directional switch according to the present
10 invention;

Fig. 4B shows waveforms for the circuit of Fig. 4A;

Fig. 5 shows block diagram for the bi-directional switch driver for the circuit of Fig. 4A;

Fig. 6 shows a PDP sustain circuit of the present invention using the bi-
15 directional switches of the present invention; and

Fig. 7 shows a variation of the circuit of Fig. 6 using only one inductor.

DETAILED DESCRIPTION OF THE INVENTION

With reference again to the drawings, Fig. 4A shows the current sensing bi-directional switch according to the present invention. In the embodiment shown, the bi-
20 directional switch 20 employs two common source, N channel MOSFETs 22 and a driver IC 30. Both N channel FETs are turned on and off simultaneously so the gates are commonly connected to the driver 30 output HO. The switch is activated externally through an ON pulse on the controller IN terminal. A series resistor RS of approximately 10 milli ohms is used to sense the current in the switch. The switch is turned off
25 automatically when the current approaches zero, indicating a complete transfer of charge from one input/output (I/O) to the other. The block diagram for the switch controller is shown in Fig. 5. Fig. 4A shows waveforms at the input IN, at the current sense input CS and at the output HO with respect to VS, the source voltage. The terminals of the

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controller 30 include VCC which is the input logic supply voltage, IN which is the logic input for the high side gate driver, COM which is the low side logic supply return, CHG which is a bootstrap capacitor charging input, VB which is the high side floating supply, HO which is the high side output, CS which is the high side current sense input and VS
5 which is the high side floating supply return.

With reference to Fig. 5, the controller block diagram utilizes mostly conventional circuitry including MOSFETs, Schmitt triggers, pulse generators, dv/dt filters, an RS latch, level shifters, comparators and amplifiers, and will not be described in detail herein. When an input is received as shown in Fig. 4A on the input IN, the
10 output HO will go high after a delay t_d (on). An exemplary wave form is shown in Fig. 4A for the current sense input CS, which is proportional to the current in resistor RS. When the current sense returns to zero, after a delay t_d (off), the output HO goes low turning off the MOSFETs 22. The output HO automatically shuts down at the next current zero crossing through the sense resistor in series with the MOSFETs. The output
15 HO can also be turned off by a logic zero at the IN terminal. This is shown in Fig. 4B at 25. With an input IN is received which goes to zero before the current sense input goes to zero, the HO output will go to zero as shown at 27. Otherwise, the output HO will go to zero at the next current zero crossing, as shown in Fig. 4B at 29.

Turning again to Fig. 4A, since the current can flow in both directions in the
20 switch, a level shift function circuit 35 is employed in the current sensing circuitry as shown in Fig. 5 to assist in detecting the zero crossing in both directions. This arrangement of the bi-directional switches in results in that the diodes D1, D2, D3 and D4 in series with the MOSFETs Q1, Q2, Q3 and Q4 in the circuit of Fig. 1 are not necessary. In addition, diodes D5, D6, D7 and D8 are also not necessary since the
25 residual current in the inductors L1 and L2 are low under all component/input pulse width variations. The only requirement is that the input pulse width be wider than that required to fully transfer the charge. Thus, the input pulse IN should be longer than the pulse width at the input CS, the zero crossing of which indicates when the charge has been fully transferred. See Fig. 4B, and in particular, see that IN from 31 to 33 is longer

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in time than pulse CS. Further, increased system efficiency is provided by the circuit of Fig. 4A due to the complete transfer of charge from the panel to the storage capacitor and back.

Fig. 6 shows a PDP sustain driver circuit utilizing the bi-directional switches according to the present invention. As shown, the sustain driver circuit employs a full bridge comprising transistors Q1, Q2, Q3 and Q4 and a single storage capacitor Cs, along with two bi-directional switches identified as BDS1 and BDS2 together with inductors L1 and L2. The circuit eliminates the diodes D1-D8 as well as one of the storage capacitors. In another embodiment described with reference to Fig. 7, only a single inductor is necessary.

With reference to Fig. 6, the operation of the circuit is as follows: initially, transistors Q3 and Q4 are turned on. This causes the display Cp panel to charge up through transistors Q3 and Q4 to the full bus voltage. Q3 and Q4 are then turned off. Bi-directional switch BDS1 is then turned on and charge is transferred from the display Cp to the storage capacitor Cs via BDS1 and inductor L1. BDS1 turns off automatically in accordance with Fig. 4B at the current zero crossing when the charge transferred to Cs is complete. BDS2 is then turned on and the charge in Cs flows through BDS2 into inductor L2. When the current in L2 and thus is BDS2 is zero, the charge cross the resonant circuit comprising Cs and L2 is reversed and BDS2 goes off. BDS1 is then turned on and the oppositely charged capacitor Cs now transfers its charge through BDS1 and L1 to Cp which is connected directly across the series circuit comprising BDS1, Cs and L1. Q1 and Q2 are then turned on and the reverse voltage across Cp further charges Cp to the full bus voltage. Q1 and Q2 are then turned off and BDS1 and BDS2 are again used to transfer the charge stored in Cp to Cs and reverse it. Hence, the oppositely charged voltage on Cp is transferred to Cs by turning on BDS1 which goes off once the charge has been fully transferred. BDS2 is then turned on to again reverse the charge on Cs. BDS2 goes off once the charge has been reversed and the charge is now provided across Cp again in the opposite direction. Then the cycle repeats, that is, Q3 and Q4 are

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turned on to charge C_p fully to the full bus voltage and the switches BDS1 and BDS2 are used to transfer the charge and reverse it.

The circuit according to Fig. 6 uses nine less components (the diodes D1 to D8 of Fig. 1 and one of the storage capacitors are eliminated than the original circuit of Fig. 1.

5 Furthermore, the switch losses are reduced since efficient charge transfer between C_p and C_s reduces two current in the full bridge.

Fig. 7 describes an alternative circuit using only a single inductor L1. Similarly to the circuit of Fig. 6, Q3 and Q4 first charge C_p to the full bus voltage, and are then turned off. BDS1 is then turned on to charge C_s . When the charge on C_p is transferred
10 to C_s , BDS1 turns off. BDS2 is then turned on and the charge on C_s is reversed through L1 and then BDS2 turns off. BDS1 is then turned on and the charge is transferred from the reversely charged C_s to C_p , thereby charging C_p oppositely. Q1 and Q2 are then turned on to fully charge C_p to the full bus voltage in the reverse direction. Then Q1 and Q2 are turned off and BDS1 is turned on to charge C_s . Once C_s has been fully charged
15 in the opposite direction, BDS1 turns off and BDS2 is turned on, thus reversing the charge again across C_s . BDS2 then turns off and BDS1 is turned on to charge C_p again in the original direction and the cycle repeats.

There has thus been described a current sensing bi-directional switch and an efficient sustain driver circuit for a plasma display device.

20 Although the present invention has been described in relation to particular embodiments thereof, many other variations and modifications and other uses will become apparent to those skilled in the art. Therefore, the present invention should be limited not by the specific disclosure herein, but only by the appended claims.

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WHAT IS CLAIMED IS:

1. A bi-directional switch comprising:
first and second semiconductor switching devices;
a current sensor connected in series with the switching devices, thereby forming
a series circuit;
5 a driver circuit controlling the on/off operation of the first and second switching
devices such that the first and second switching devices are substantially
simultaneously turned on and off, the driver circuit turning the first and second
switching devices on in response to a control input and turning the first and second
switching devices off when current in the current sensor substantially drops to near a
10 zero current.
2. The bi-directional switch of claim 1, wherein the current sensor
comprises a sensing resistor.
3. The bi-directional switch of claim 1, wherein the first and second
switching devices turn off on the first to appear of:
 - a) the control input changing state; or
 - b) the current in the current sensor substantially dropping to zero current.
- 5 4. The bi-directional switch of claim 1, wherein the driver circuit has a
current sense input coupled to the current sensor, and further comprising a level shifting
circuit to bi-directionally detect a current zero crossing in the current sensor.
5. A discharge sustain driver circuit for a plasma display device, the driver
circuit comprising:
a first transistor switching circuit for switching a DC bus voltage across the
plasma display device;
5 a storage capacitance;

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at least one inductor; and

first and second bi-directional switching circuits coupled in series and being coupled to the first switching circuit to transfer charge from the plasma display device through the at least one inductor to the storage capacitance, and back to the plasma

10 display device; and

a controller for the bi-directional switching circuits to control the bi-directional switching circuits so as to receive the charge on the storage capacitance and return the charge in an opposite charge direction to the plasma display device.

6. The discharge sustain driver circuit of claim 5, wherein the first transistor switching circuit comprises a full bridge transistor switching circuit.

7. The discharge sustain driver circuit of claim 5, wherein the storage capacitance comprises a single storage capacitor or a plurality of parallel connected storage capacitors.

8. The discharge sustain driver circuit of claim 5, wherein the bi-directional switching circuits each include a current sensor and each turn off when current through the switching circuit is approximately zero.

9. The discharge sustain driver circuit of claim 5, wherein each bi-directional switching circuit comprises two series connected semiconductor switches that are turned on substantially simultaneously.

10. The discharge sustain driver circuit of claim 9, further comprising a current sensor in series with the semiconductor switches.

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11. The discharge sustain driver circuit of claim 10, wherein the current sensor comprises a resistor.

12. The discharge sustain driver circuit of claim 5, wherein the at least one inductor comprises first and second inductors in series with both bi-directional switches, and the storage capacitance is connected in parallel with a series connection of one bi-directional switch and the second inductor.

13. The discharge sustain driver circuit of claim 5, wherein the at least one inductor is connected in a series circuit with the storage capacitance, and the series circuit of the storage capacitance and at least one inductor is connected in parallel with one of the bi-directional switches.

14. The discharge sustain driver circuit of claim 6, wherein the full bridge switching circuit comprises first and second series connected transistors connected across the DC bus and third and fourth series connected transistors connected across the DC bus; wherein the first and third transistors are high side connected and the second
5 and fourth are low side connected;

the plasma display device being connected across a common connection of the first and second transistors and a common connection of the third and fourth transistors, and wherein the first and second bi-directional switches are coupled in series together and in series with first and second inductors, the storage capacitance being connected
10 across a series connection of the second bi-directional switch and the second inductor;

the circuit operating according to the steps comprising:

turning on the first and fourth transistors to charge the display device substantially to the bus voltage;

turning off the first and fourth transistors when the display device has charged to
15 substantially the bus voltage;

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turning on the first bi-directional switch to transfer the charge on the display device to the storage capacitance;

turning off the first bi-directional switch when the charge transfer is substantially complete;

20 turning on the second bi-directional switch to reverse the charge across the storage capacitance;

turning off the second bi-directional switch when the current in the second bi-directional switch is substantially zero;

25 turning on the first bi-directional switch to transfer the reverse charge on the storage capacitance to the display device;

turning on the second and third transistors to further charge the display device substantially to the bus voltage in the reverse direction;

turning off the second and third transistors when the display device has charged to substantially the bus voltage;

30 turning on the first bi-directional switch to transfer charge from the display device to the storage capacitance;

turning on the second bi-directional switch to reverse the charge across the storage capacitance;

35 turning off the second bi-directional switch when the current in the second bi-directional switch is substantially zero; and

turning on the first bi-directional switch to transfer the again reversed charge on the storage capacitance to the display device; and repeating the above steps as long as desired to sustain a discharge in the display device.

15. The circuit of claim 14, wherein the bi-directional switches automatically turn off when the current through the respective switch is substantially zero.

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16. The circuit of claim 14, wherein all transistors are MOSFETs and the bi-directional switches employ MOSFETs.

17. The circuit of claim 6, wherein the full bridge switching circuit comprises first and second series connected transistors connected across the DC bus and third and fourth series connected transistors connected across the DC bus; wherein the first and third transistors are high side connected and the second and fourth are low
5 side connected;

the plasma display device being connected across a common connection of the first and second transistors and a common connection of the third and fourth transistors, and wherein the first and second bi-directional switches are coupled in series together, and wherein the storage capacitance and the at least one inductor are coupled in a series
10 circuit across the second bi-directional switch;

the circuit operating according to the steps comprising:

turning on the first and fourth transistors to substantially charge the display device to the bus voltage;

turning off the first and fourth transistors when the display device has charged to
15 substantially the bus voltage;

turning on the first bi-directional switch to transfer the charge on the display device to the storage capacitance;

turning off the first bi-directional switch when the current through the switch is substantially zero;

20 turning on the second bi-directional switch to reverse the charge across the storage capacitance;

turning off the second bi-directional switch when the current therethrough is substantially zero;

turning on the first bi-directional switch to transfer the reverse charge on the
25 storage capacitance to the display device;

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turning on the second and third transistors to fully charge the display device to substantially the bus voltage in the reverse direction;

turning off the second and third transistors when the display device has charged to substantially the bus voltage;

30 turning on the first bi-directional switch to transfer the reverse charge on the display device to the storage capacitance;

turning off the first bi-directional switch when the current through the switch is substantially zero;

35 turning on the second bi-directional switch to again reverse the charge across the storage capacitance;

turning off the second bi-directional switch when the current therethrough is substantially zero;

turning on the first bi-directional switch to transfer the charge on the storage capacitance to the display device; and

40 repeating the above steps as long as desired to sustain a discharge in the display device.

18. The circuit of claim 17, wherein the bi-directional switches automatically turn off when the current through the respective switch is substantially zero.

19. The circuit of claim 17, wherein all transistors are MOSFETs and the bi-directional switches employ MOSFETs.

20. The circuit of claim 5, wherein the first and second bidirectional switching circuits each comprise:

first and second semiconductor switching devices;

a current sensor connected in series with the switching devices, thereby forming a series circuit;

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a driver circuit controlling the on/off operation of the first and second switching devices such that the first and second switching devices are substantially simultaneously turned on and off, the driver circuit turning the first and second switching devices on in response to a control input and turning the first and second switching devices off when current in the current sensor substantially drops to near a zero current.

21. The circuit of claim 5, wherein the first and second bi-directional switching circuits each comprise:

at least one semiconductor switching device;

a current sensor connected in series with the switching device, thereby forming a series circuit;

a driver circuit controlling the on/off operation of the at least one switching device, the driver circuit turning the switching device on in response to a control input and turning the switching device off when current in the current sensor substantially drops to near a zero current.

22. A bi-directional switch comprising:

at least one semiconductor switching device;

a current sensor connected in series with the switching device, thereby forming a series circuit;

5 a driver circuit controlling the on/off operation of the at least one switching device the driver circuit turning the switching device on in response to a control input and turning the switching device off when current in the current sensor substantially drops to near a zero current.

23. The bi-directional switch of claim 22, wherein the current sensor comprises a sensing resistor.

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24. The bi-directional switch of claim 22, wherein the switching device turns off on the first to appear of:

- c) the control input changing state; or
- d) the current in the current sensor substantially dropping to zero current.

25. The bi-directional switch of claim 22, wherein the driver circuit has a current sense input coupled to the current sensor, and further comprising a level shifting circuit to bi-directionally detect a current zero crossing in the current sensor.

26. A discharge sustain driver circuit for a plasma display device, the driver circuit comprising:

a first transistor switching circuit for switching a DC bus voltage across the plasma display device;

5 a storage capacitance;

at least one inductor; and

first and second bi-directional switching circuits coupled in series and being coupled to the first switching circuit to transfer charge from the plasma display device through the at least one inductor to the storage capacitance, and back to the plasma

10 display device; and

a controller for the switching circuits to control the switching circuits so as to receive the charge on the storage capacitance and return the charge in an opposite charge direction to the plasma display device, the first and second bi-directional switching circuits turning off when a substantially zero current flows through the

15 respective bi-directional switching circuit..

27. A discharge sustain driver circuit for a plasma display device, the driver circuit comprising:

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a first transistor switching circuit for switching a DC bus voltage across the plasma display device;

5 a storage capacitance;

at least one inductor; and

first and second bi-directional switching circuits coupled in series and being coupled to the first switching circuit to transfer charge from the plasma display device through the at least one inductor to the storage capacitance, and back to the plasma

10 display device; and

a controller for the bi-directional switching circuits to control the bi-directional switching circuits so as to receive the charge on the storage capacitance and return the charge in an opposite charge direction to the plasma display device, further wherein the storage capacitance comprises a single storage capacitor or a plurality of parallel

15 connected storage capacitors.

28. A method of operating a discharge sustain driver circuit for a plasma display device, the driver circuit comprising a first transistor switching circuit for switching a DC bus voltage across the plasma display device, a storage capacitance, at least one inductor; and first and second bi-directional switching circuits coupled in

5 series and being coupled to the first switching circuit to transfer charge from the plasma display device through the at least one inductor to the storage capacitance, and back to the plasma display device; and a controller for the bi-directional switching circuits to control the bi-directional switching circuits so as to receive the charge on the storage capacitance and return the charge in an opposite charge direction to the plasma display

10 device and wherein the first switching circuit comprises a full bridge switching circuit, the full bridge switching circuit comprising first and second series connected transistors connected across the DC bus and third and fourth series connected transistors connected across the DC bus; wherein the first and third transistors are high side connected and the second and fourth are low side connected; the plasma display device being

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- 15 connected across a common connection of the first and second transistors and a common connection of the third and fourth transistors, and wherein the first and second bi-directional switches are coupled in series together and in series with first and second inductors, the storage capacitance being connected across a series connection of the second bi-directional switch and the second inductor;
- 20 the method comprising:
turning on the first and fourth transistors to charge the display device to substantially the bus voltage;
turning off the first and fourth transistors when the display device has charged to substantially the bus voltage;
- 25 turning on the first bi-directional switch to transfer the charge on the display device to the storage capacitance;
turning off the first bi-directional switch when the charge transfer is substantially complete;
turning on the second bi-directional switch to reverse the charge across the
- 30 storage capacitance;
turning off the second bi-directional switch when the current in the second bi-directional switch is substantially zero;
turning on the first bi-directional switch to transfer the reverse charge on the storage capacitance to the display device;
- 35 turning on the second and third transistors to further charge the display device substantially to the bus voltage in the reverse direction;
turning off the second and third transistors when the display device has charged to substantially the bus voltage;
turning on the first bi-directional switch to transfer charge from the display
- 40 device to the storage capacitance;
turning on the second bi-directional switch to reverse the charge across the storage capacitance;

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turning off the second bi-directional switch when the current in the second bi-directional switch is substantially zero; and

45 turning on the first bi-directional switch to transfer the again reversed charge on the storage capacitance to the display device; and repeating the above steps as long as desired to sustain a discharge in the display device.

29. The method of claim 28, wherein the bi-directional switches automatically turn off when the current through the respective switch is substantially zero.

30. A method of operating a discharge sustain driver circuit for a plasma display device, the driver circuit comprising a first transistor switching circuit for switching a DC bus voltage across the plasma display device, a storage capacitance, at least one inductor; and first and second bi-directional switching circuits coupled in
5 series and being coupled to the first switching circuit to transfer charge from the plasma display device through the at least one inductance to the storage capacitor, and back to the plasma display device, and a controller for the bi-directional switching circuits to control the bi-directional switching circuits so as to receive the charge on the storage capacitance and return the charge in an opposite charge direction to the plasma display
10 device; and wherein the first switching comprises a full bridge switching circuit, the full bridge switching circuit comprising first and second series connected transistors connected across the DC bus and third and fourth series connected transistors connected across the DC bus; wherein the first and third transistors are high side connected and the second and fourth are low side connected; the plasma display device being
15 connected across a common connection of the first and second transistors and a common connection of the third and fourth transistors, and wherein the first and second bi-directional switches are coupled in series together, and wherein the storage capacitance and the at least one inductor are coupled in a series circuit across the second bi-directional switch;

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- 20 the method comprising:
- turning on the first and fourth transistors to substantially charge the display device to the bus voltage;
- turning off the first and fourth transistors when the display device has changed to substantially the bus voltage;
- 25 turning on the first bi-directional switch to transfer the charge on the display device to the storage capacitance;
- turning off the first bi-directional switch when the current through the switch is substantially zero;
- turning on the second bi-directional switch to reverse the charge across the
- 30 storage capacitance;
- turning off the second bi-directional switch when the current therethrough is substantially zero;
- turning on the first bi-directional switch to transfer the reverse charge on the storage capacitance to the display device;
- 35 turning on the second and third transistors to fully charge the display device to substantially the bus voltage in the reverse direction;
- turning off the second and third transistors when the display device has charged to substantially the bus voltage;
- turning on the first bi-directional switch to transfer the reverse charge on the
- 40 display device to the storage capacitance;
- turning off the first bi-directional switch when the current through the switch is substantially zero;
- turning on the second bi-directional switch to again reverse the charge across the storage capacitance;
- 45 turning off the second bi-directional switch when the current therethrough is substantially zero;
- turning on the first bi-directional switch to transfer the charge on the storage capacitance to the display device; and

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repeating the above steps as long as desired to sustain a discharge in the display
50 device.

31. The method of claim 30, wherein the bi-directional switches
automatically turn off when the current through the respective switch is substantially
zero.

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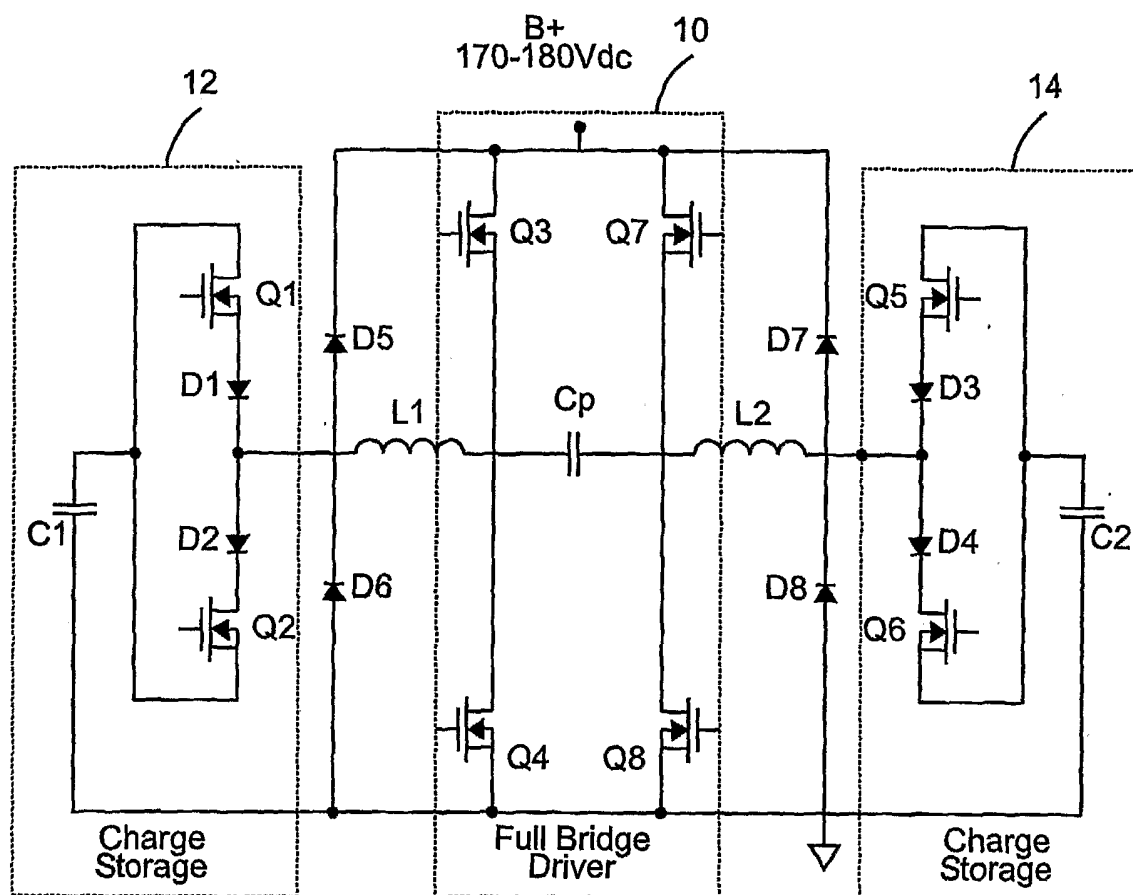


Figure 1
(Prior Art)

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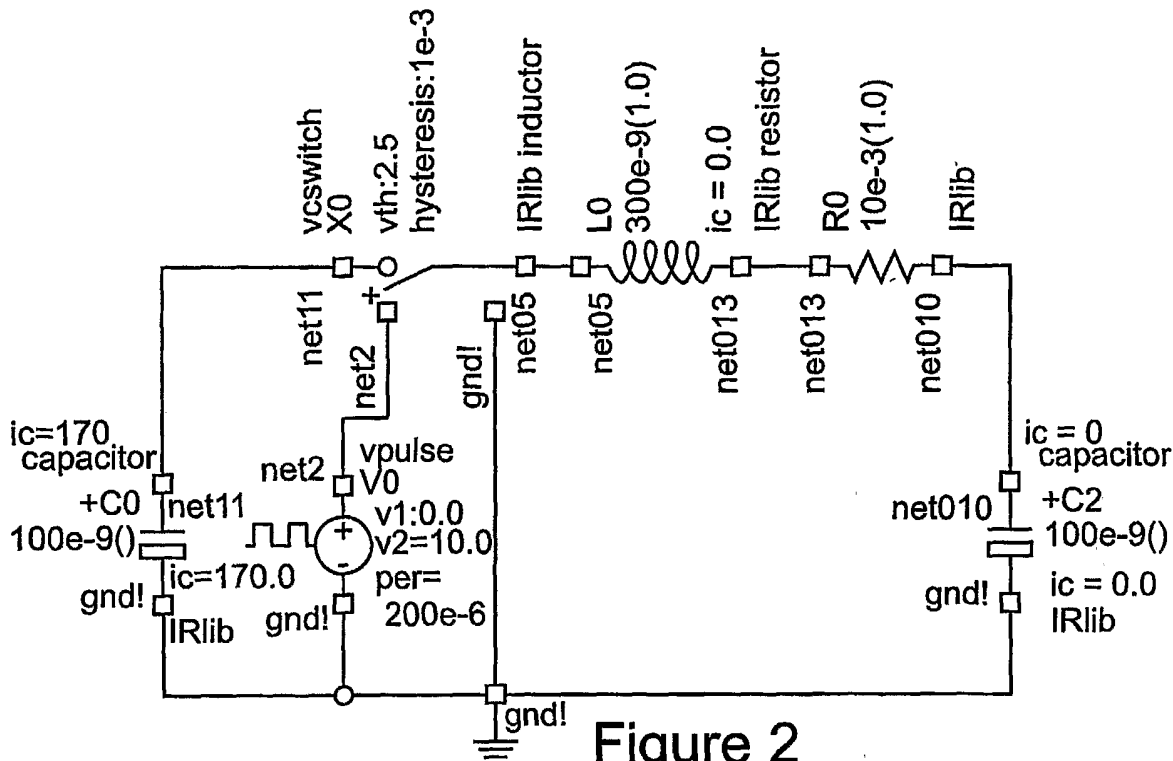
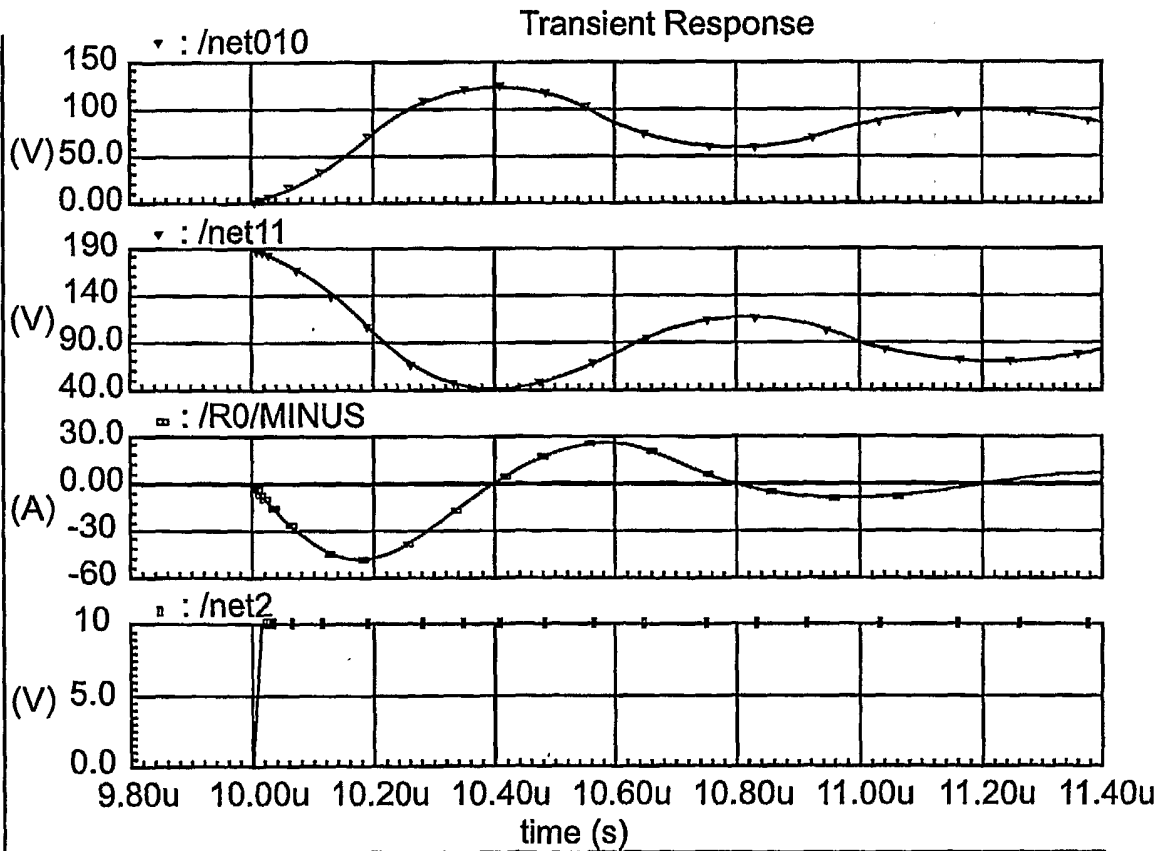


Figure 2



A:(10.4016u 1.04145)

Figure 3

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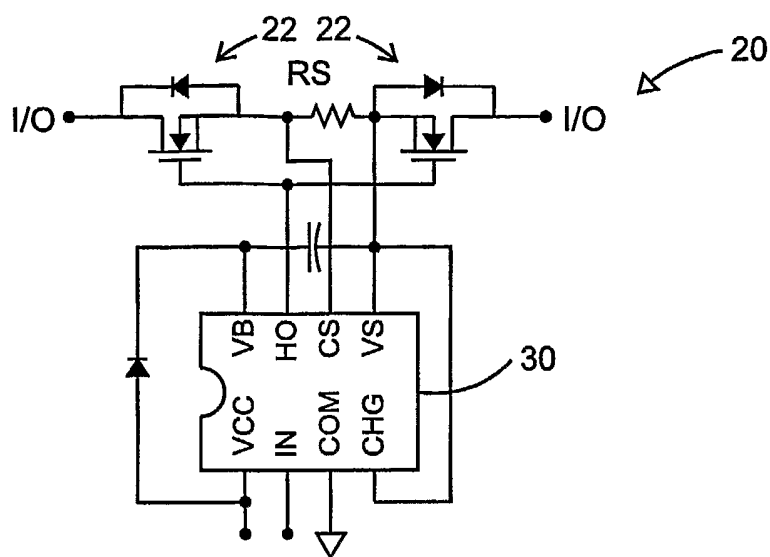


Figure 4A

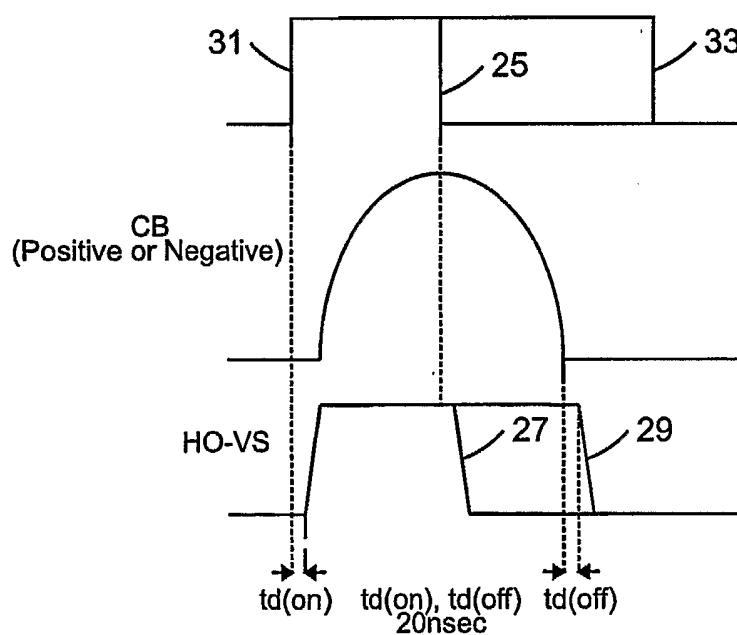


Figure 4B

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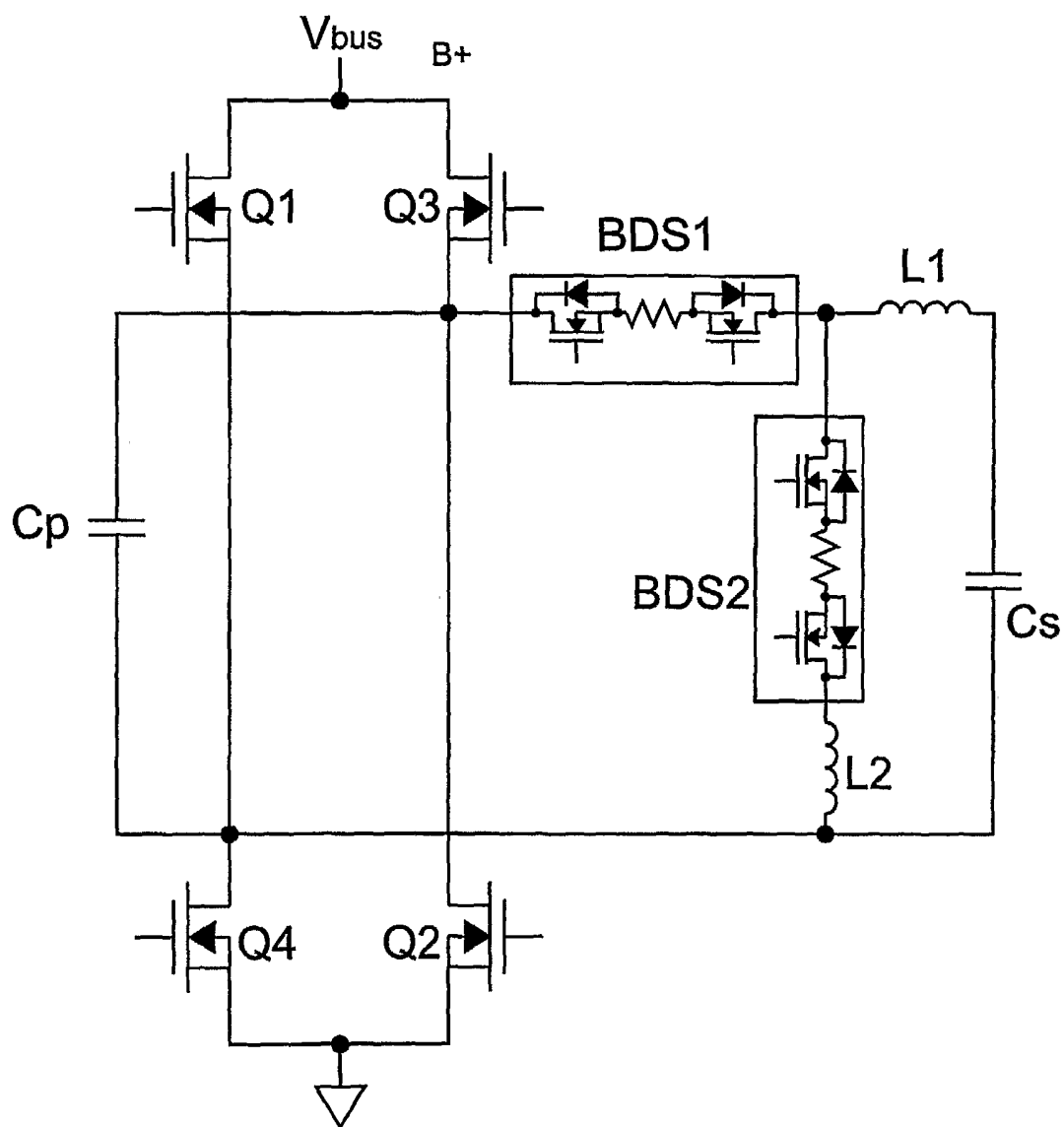


Figure 7

INTERNATIONAL SEARCH REPORT

International application No.

PCT/US04/16882

A. CLASSIFICATION OF SUBJECT MATTER

IPC(7) : G09G 3/28

US CL : 345/60-68

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

U.S. : 345/60-68

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)
Please See Continuation Sheet

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X,P	US 2003/0122735 A1 (HUANG ET AL.) 03 July 2003, figure 6, page 3.	1-31
X,P	US 6,583,575 B2 (ROH ET AL.) 24 June 2003, figure 3, column 6 and column 7.	1-31
X,P	US 6,768,270 B2 (CHAE) 27 July 2004, figure 3, column 5, lines 1-59.	1-31
X,P	US 6,674,417 B2 (HSU ET AL.) 06 January 2004, figure 4, column 4, lines 18-67.	1-31

☐ Further documents are listed in the continuation of Box C.

☐ See patent family annex.

* Special categories of cited documents:

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"P" document published prior to the international filing date but later than the priority date claimed

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"X"

document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone

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Date of the actual completion of the international search

16 August 2004 (16.08.2004)

Date of mailing of the international search report

13 SEP 2004

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INTERNATIONAL SEARCH REPORT

International application No.

PCT/US04/16882

Continuation of B. FIELDS SEARCHED Item 3:

EAST, database, US, US-PGPUB, EPO, JPO, DERWENT, IBM_TDB; search term: (bi adj directional adj switch\$3) and ((sustain\$3
adj driver) or (energy adj recovery))