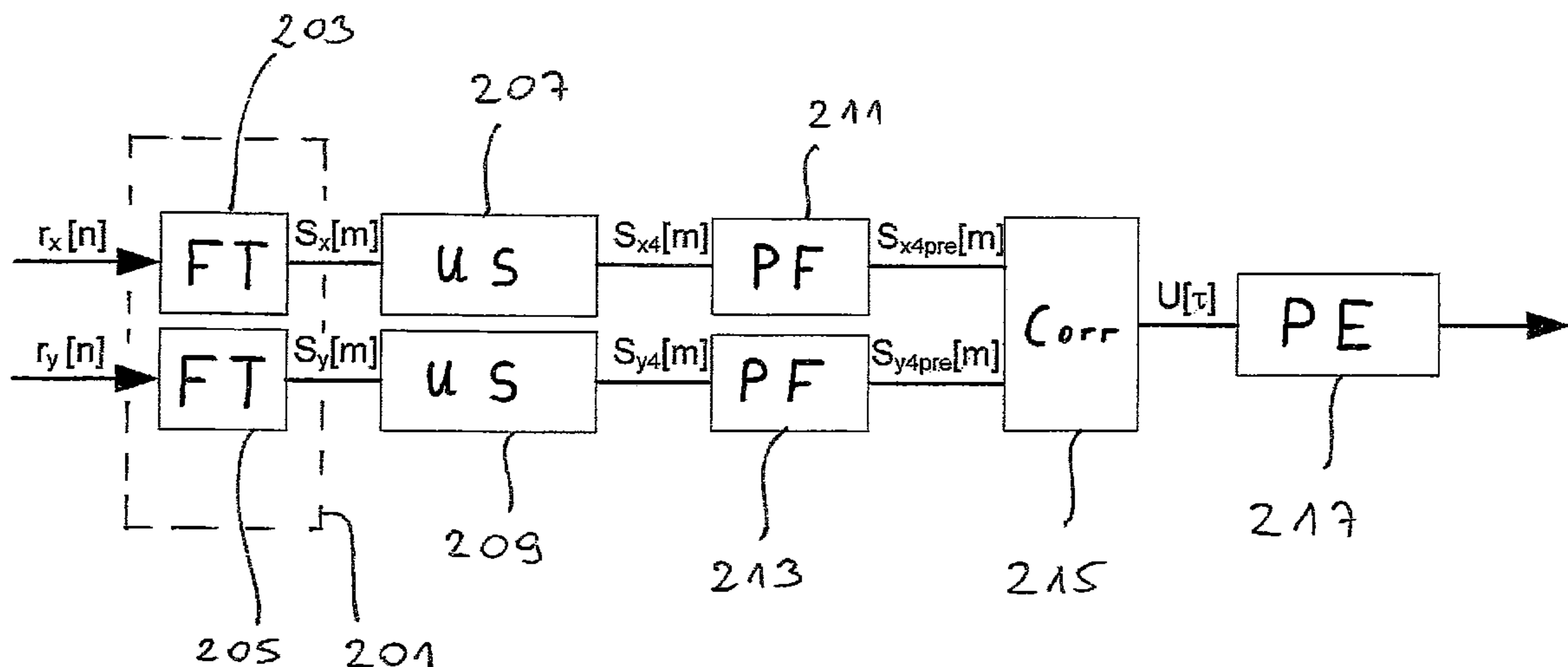




(86) Date de dépôt PCT/PCT Filing Date: 2009/08/24
(87) Date publication PCT/PCT Publication Date: 2011/03/03
(45) Date de délivrance/Issue Date: 2014/08/19
(85) Entrée phase nationale/National Entry: 2012/01/03
(86) N° demande PCT/PCT Application No.: CN 2009/073451
(87) N° publication PCT/PCT Publication No.: 2011/022869

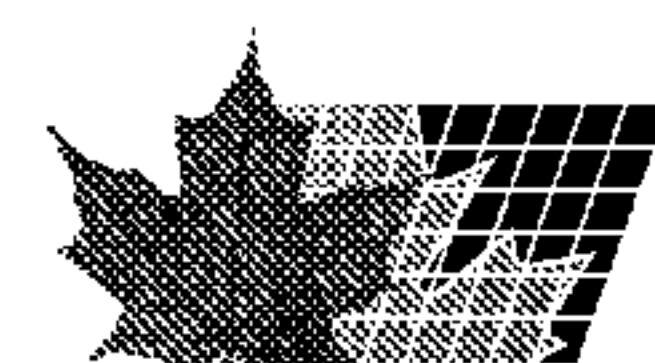
(51) Cl.Int./Int.Cl. *H04L 7/027* (2006.01)
(72) Inventeurs/Inventors:
HAUSKE, FABIAN NIKOLAUS, DE;
ZHAO, CHAN, CN
(73) Propriétaire/Owner:
HUAWEI TECHNOLOGIES CO., LTD., CN
(74) Agent: SMART & BIGGAR

(54) Titre : APPAREIL DE RECUPERATION D'HORLOGE
(54) Title: CLOCK RECOVERY APPARATUS



(57) Abrégé/Abstract:

The invention relates to a clock recovery apparatus being configured to recover clock information from an input signal. The clock recovery apparatus comprises Fourier transforming means (201) being configured to transform the input signal 5 into a frequency domain signal upon the basis of a Fourier transform, correlating means (215) being configured to correlate the frequency domain signal to obtain a correlation value associated with a certain frequency, and clock recovery means (217) being configured to determine whether the certain frequency corresponds to a clock frequency in order to recover the clock information. 10



(12) INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(19) World Intellectual Property Organization
International Bureau



(43) International Publication Date
3 March 2011 (03.03.2011)

(10) International Publication Number
WO 2011/022869 A1

(51) International Patent Classification:
H04L 7/027 (2006.01)

(21) International Application Number:
PCT/CN2009/073451

(22) International Filing Date:
24 August 2009 (24.08.2009)

(25) Filing Language: English

(26) Publication Language: English

(71) Applicant (for all designated States except US):
HUAWEI TECHNOLOGIES CO., LTD. [CN/CN];
Huawei Administration Building, Bantian, Longgang Dis-
trict, Shenzhen, Guangdong 518129 (CN).

(72) Inventors; and

(75) Inventors/Applicants (for US only): **HAUSKE, Fabian**
Nikolaus [DE/DE]; Huawei Technologies, Riesstr. 25,
80992 München (DE). **ZHAO, Chan** [CN/CN]; Huawei
Administration Building, Bantian, Longgang District,
Shenzhen, Guangdong 518129 (CN).

(74) Agent: **UNITALEN ATTORNEYS AT LAW**; 7th
Floor, Scitech Place, No.22, Jian Guo Men Wai Ave.,
Chao Yang District, Beijing 100004 (CN).

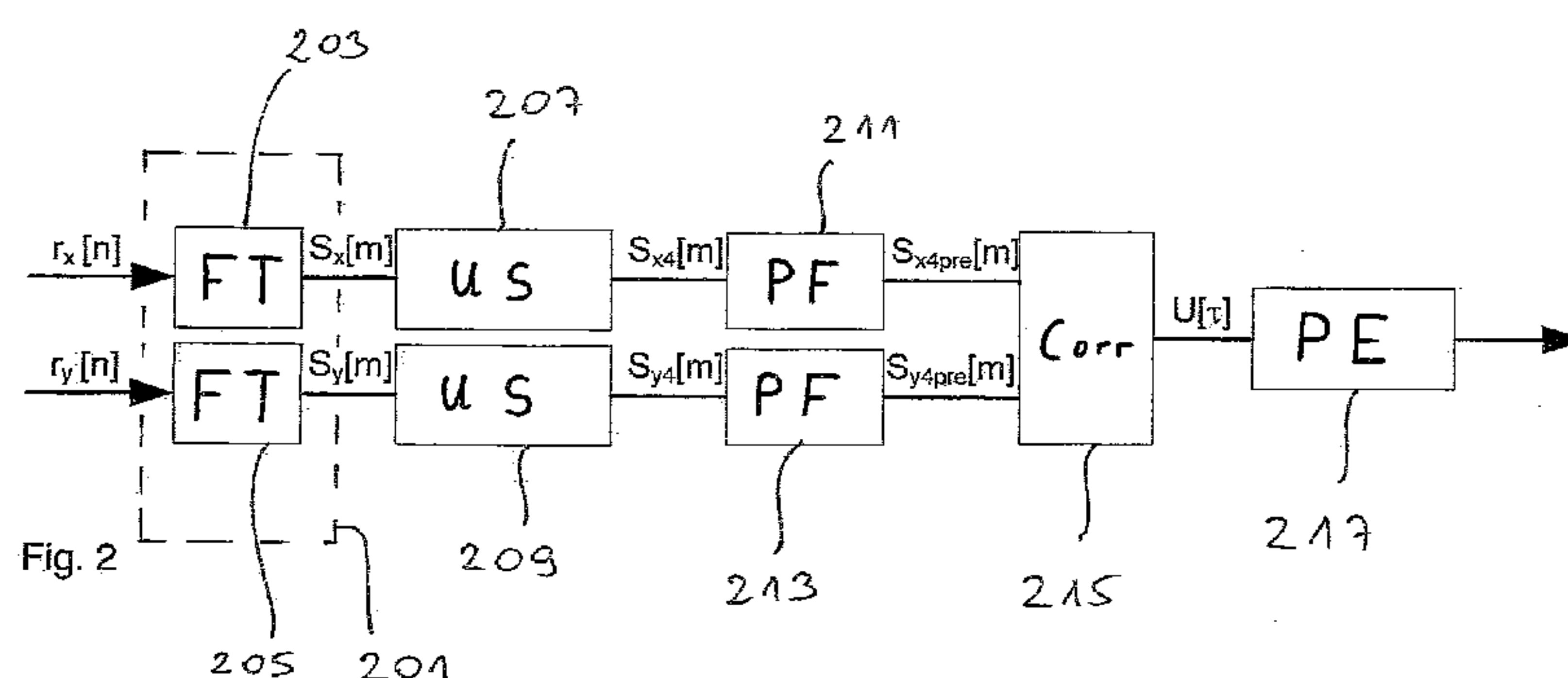
(81) Designated States (unless otherwise indicated, for every
kind of national protection available): AE, AG, AL, AM,
AO, AT, AU, AZ, BA, BB, BG, BH, BR, BW, BY, BZ,
CA, CH, CL, CN, CO, CR, CU, CZ, DE, DK, DM, DO,
DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, GT,
HN, HR, HU, ID, IL, IN, IS, JP, KE, KG, KM, KN, KP,
KR, KZ, LA, LC, LK, LR, LS, LT, LU, LY, MA, MD,
ME, MG, MK, MN, MW, MX, MY, MZ, NA, NG, NI,
NO, NZ, OM, PE, PG, PH, PL, PT, RO, RS, RU, SC, SD,
SE, SG, SK, SL, SM, ST, SV, SY, TJ, TM, TN, TR, TT,
TZ, UA, UG, US, UZ, VC, VN, ZA, ZM, ZW.

(84) Designated States (unless otherwise indicated, for every
kind of regional protection available): ARIPO (BW, GH,
GM, KE, LS, MW, MZ, NA, SD, SL, SZ, TZ, UG, ZM,
ZW), Eurasian (AM, AZ, BY, KG, KZ, MD, RU, TJ,
TM), European (AT, BE, BG, CH, CY, CZ, DE, DK, EE,
ES, FI, FR, GB, GR, HR, HU, IE, IS, IT, LT, LU, LV,
MC, MK, MT, NL, NO, PL, PT, RO, SE, SI, SK, SM,
TR), OAPI (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW,
ML, MR, NE, SN, TD, TG).

Published:

— with international search report (Art. 21(3))

(54) Title: Clock recovery apparatus



(57) Abstract: The invention relates to a clock recovery apparatus being configured to recover clock information from an input signal. The clock recovery apparatus comprises Fourier transforming means (201) being configured to transform the input signal 5 into a frequency domain signal upon the basis of a Fourier transform, correlating means (215) being configured to correlate the frequency domain signal to obtain a correlation value associated with a certain frequency, and clock recovery means (217) being configured to determine whether the certain frequency corresponds to a clock frequency in order to recover the clock information.

Clock recovery apparatus

FIELD OF THE INVENTION

5 The present invention relates to clock recovery in communication systems.

BACKGROUND OF THE INVENTION

10 In modern communication systems, timing recovery at a receiver is an essential issue for successful communication. Usually, at a transmitter, digital values sampled with a clock frequency are processed and transmitted e.g. via an optical signal to a remote receiver. During transmission, the optical signal is impaired by polarization effects, dispersion, attenuation, noise and other effects. At the receiver, the clock frequency forming a time basis need to be recovered in order to capture
15 the transmitted samples without introducing any further errors due to clock frequency mismatch. Time recovery errors may, in particular in optical transmission systems, further be introduced due to optical polarization effects like differential group delay (DGD), which may negatively affect the timing recovery accuracy. In particular, half-Baud DGD or multiples thereof in combination with certain states of
20 polarization (SOP) may impose critical conditions for timing recovery. The timing recovery should be also robust to all optical and electrical distortions.

In order to recover the time basis, square-timing recovery (STR) may be performed in time domain. Essentially, the STR approach applies a nonlinear operation, e.g. the square of the signal, to obtain nonlinear mixing products, i. e. the
25 clock tone. Prior to STR, a time domain filter which is a FIR filter (FIR: Finite Impulse Response) for enhancing spectral components around the clock frequency, i.e. the clock tone, may be applied. The filtered signal is then oversampled and squared to obtain the clock tone.

30

SUMMARY OF THE INVENTION

52663-24

2

An embodiment of the present invention may more efficiently recover time information from a received signal.

According to an aspect, there is provided a clock recovery apparatus being configured to recover clock information from an input signal, the apparatus
5 comprising: Fourier transforming means being configured to transform the input signal into a first frequency domain signal upon the basis of a Fourier transform by a first Fourier transformer and into a second frequency domain signal upon the basis of a Fourier transform by a second Fourier transformer; correlating means being configured to correlate the first frequency domain signal to obtain a first correlation
10 value associated with a first certain frequency and to correlate the second frequency domain signal to obtain a second correlation value associated with a second certain frequency; and clock recovery means being configured to determine whether the first or second certain frequency corresponds to a clock frequency in order to recover the clock information.

15 A further aspect provides a synchronization apparatus, comprising: analogue-to-digital converter means for sampling an analogue input signal with a sampling phase or frequency to obtain the input signal; or interpolation means for interpolating a digital signal with a sampling phase or frequency to obtain an interpolated signal forming an input signal; and a clock recovery apparatus as described herein, wherein
20 the clock recovery means is configured to change the sampling frequency or sampling phase of the analogue-to-digital converter means for synchronization.

There is also provided a method for recovering clock information from an input signal, the method comprising the steps of: transforming the input signal into a first frequency domain signal upon the basis of a Fourier transform and into a second
25 frequency domain signal upon the basis of a Fourier transform; correlating the frequency domain signal to obtain a first correlation value associated with a first certain frequency and correlating the second frequency domain signal to obtain a second correlation value associated with a second certain frequency; and

52663-24

2a

determining whether the first or second certain frequency corresponds to a clock frequency in order to recover the clock information.

In accordance with a still further aspect, there is provided a synchronization method, comprising the steps of: sampling an analogue input signal with a sampling phase or
5 frequency to obtain the input signal; recovering a clock information from an input signal as described above; and changing the sampling phase or the sampling frequency to synchronize with a frequency corresponding to the clock frequency.

According to one aspect, a clock recovery apparatus is provided which is configured to recover a clock information from an input signal, for example from a received
10 signal. The clock recovery apparatus comprises Fourier transforming means being configured to transform the input signal into a frequency domain signal upon the basis of the Fourier transform, correlating means being configured to correlate the frequency domain signal to obtain a correlation value associated with a certain frequency, and clock recovery means being configured to determine whether the
15 certain frequency corresponds to a clock frequency in order to recover the clock information. In order to perform the Fourier transform, the Fourier transforming means may employ a digital Fourier transformer, e.g. a digital fast Fourier transformer (FFT). The correlating means may be configured to correlate the frequency domain signal according to any digital correlation processing scheme to
20 obtain the correlation value. For example, a position of the correlation value indicated e.g. by an index among a plurality of correlation values may indicate the certain frequency in the frequency domain.

According to an implementation form, an estimation vector comprising correlation values may be defined by e.g. a cross-correlation between the signals according to
25 the x- and the y-polarization and/or by the auto-correlation of each signal according to the x- and the y-polarization in the frequency domain. Furthermore, the combination of the cross- and auto-correlation may also provide an estimation variable forming a correlation value for extracting the timing phase and/or timing frequency information. Also correlations of the combination of the polarization signals, e.g. the sum of the x-

52663-24

2b

polarization and the y-polarization, may provide estimation values being formed by the correlation values.

According to an implementation form, the correlation means may be configured to provide another correlation value upon the basis of the correlation of the frequency
5 domain signal, the other correlation value being associated with another certain frequency, wherein the clock recovery means is configured to determine whether

the other certain frequency corresponds to the clock frequency in order to recover the clock information. For example, the clock recovery means may compare the correlation value at the certain frequency and with the further correlation value at the further certain frequency in order to determine which correlation value is associated with a frequency corresponding to the clock frequency.

According to an implementation form, the clock recovery means may be configured to decide that the certain frequency corresponds to the clock frequency if the correlation value is greater than another correlation value. In order to make the decision, the clock recovery means may compare the correlation values or absolute values thereof or portions of the correlation values if the same are complex.

According to an implementation form, the clock recovery means may be configured to decide that the certain frequency corresponds to the clock frequency if the correlation value is greater than or equal to a certain threshold. By way of example, the clock recovery means may compare portions of the correlation value or an absolute value thereof to make the decision.

According to an implementation form, the clock recovery means may be configured to output a control signal indicating a change of a sampling frequency or sampling phase if the certain frequency does not correspond to the clock frequency. For example, the control signal may control a sampling frequency or phase of an analogue-to-digital converter using for sampling purposes. By way of example, this certain frequency does not correspond to the clock frequency if its value is e.g. lower than the certain threshold mentioned above or smaller than another correlation value associated with another certain frequency.

According to an implementation form, the correlation means may be configured to perform an auto-correlation of the frequency domain signal or of a portion thereof or to perform a cross-correlation of different portions of the frequency domain signal, in particular of different portions associated with different polarizations, to obtain the correlation value. By way of example, the correlation means may cross-

correlate frequency domain portions being associated with a x-polarization and with a y-polarization.

According to an implementation form, the input signal may be a digitalized version
5 of an optical signal having a first optical signal portion being associated with a first optical polarization, and a second optical signal portion being associated with a second optical polarization, wherein the frequency domain signal may comprise a first frequency domain signal portion being associated with the first polarization and a second frequency domain signal portion being associated with the second
10 optical polarization, and wherein the correlation means is configured to auto-correlate the first or the second frequency domain signal portion or to cross-correlate the first frequency domain signal portion and the second frequency domain signal portion to obtain the correlation value. In order to perform the respective correlation process, the correlation means may execute any correlation algo-
15 rithm providing correlation values.

According to an implementation form, the correlation means may be configured to weight at least a portion of the frequency domain signal or the correlation value with a weighting coefficient. The weighting coefficient may e.g. attenuate or amplify
20 certain correlation values, e.g. the outmost correlation values.

According to an implementation form, the apparatus may further comprise a filter, in particular an all-pass filter for chromatic dispersion filtering, which is configured to filter the frequency domain signal and to provide a filtered frequency domain
25 signal as the frequency domain signal to the correlating means. By way of example, the all-pass filter may perform the chromatic dispersion filtering in frequency domain.

According to an implementation form, the apparatus may further comprise an amplitude filter, in particular a band-pass filter, which is configured to filter the frequency domain signal and to provide a filtered frequency domain signal as the frequency domain signal to the correlating means. By way of example, the band-pass filter may perform the amplitude filtering in frequency domain.
30

According to an implementation form, the clock recovery means may further be configured to determining a clock phase upon a basis of the correlation value. For example, the clock phase may correspond to a phase of the correlation value if the same is a complex value. Thus, the clock phase may be determined by the real and the imaginary part of the correlation value.

According to an implementation form, the apparatus may further comprise oversampling means. The oversampling means may be configured to oversample the input signal prior to Fourier transformation. For example, the input signal may be oversampled four times by way of interpolation. Alternatively, oversampling means may be configured to oversample the signal after the Fourier transform in the frequency domain. For example, the frequency domain signal may be oversampled four times e.g. by zero-padding.

15

According to an implementation form, the apparatus may further comprise a Fourier transforming means being configured to transform the frequency domain signal into time domain to obtain a time domain signal for further processing.

According to another aspect, the invention provides a synchronization apparatus comprising analogue-to-digital converter means for sampling an analogue input signal with a sampling phase or frequency to obtain the input signal, and the inventive clock recovery apparatus which is configured to change the sampling phase or the sampling frequency of the analogue-to-digital converter means for synchronization. For example, the clock recovery apparatus may control the analogue-to-digital converter using the control signal mentioned above.

According to another aspect, the invention provides a synchronization apparatus comprising interpolation means for interpolating a digital signal with a sampling phase or frequency to obtain an interpolated signal, and the inventive clock recovery apparatus which is configured to change the sampling phase or the sampling frequency of the interpolation means for synchronization. For example, the clock

30

recovery apparatus may control the interpolation filter using the control signal mentioned above.

According to another aspect, the invention provides a method for recovering clock
5 information from an input signal. The method comprises transforming the input signal into a frequency domain signal upon the basis of the Fourier transform, correlating the frequency domain signal to obtain a correlation value associated with a certain frequency, and determining whether the certain frequency corresponds to a clock frequency in order to recover the clock information.

10 Further method steps are directly derivable from the functionality of the inventive clock recovery apparatus.

According to another aspect, the invention relates to a synchronization method
15 comprising sampling an analogue input signal with a sampling phase or frequency to obtain the input signal, recovering clock information from the input signal according to the method for recovering clock information, and changing the sampling phase for the sampling frequency to synchronize with a frequency corresponding to the clock frequency.

20 Further method steps are directly derivable from the functionality of the inventive synchronization apparatus.

BRIEF DESCRIPTION OF THE DRAWINGS

25 Below, embodiments will be described with respect to the following figures, in which:

Fig. 1 shows a synchronization apparatus;

30 Fig. 2 shows a synchronization apparatus;

Fig. 3 shows simulation results;

Fig. 4 shows simulation results;

Fig. 5 shows simulation results;

5

Fig. 6 shows simulation results; and

Fig. 7 shows a receiver comprising a synchronization apparatus.

10

DETAILED DESCRIPTION OF EMBODIMENTS OF THE INVENTION

Fig. 1 shows a synchronization apparatus comprising an analogue-to-digital converter (ADC) 101 having an output coupled to a clock recovery apparatus. The clock recovery apparatus comprises Fourier transforming means (FT) 103, e.g. a Fourier transformer, for providing a frequency domain signal, and, optionally, a pre-filter (PF) 105 for filtering the frequency domain signal. An output of the pre-filter 105 or an output of the Fourier transforming means 103 may be coupled to a clock recovery block (CRB) 107 comprising correlating means for correlating the frequency domain signal and clock recovery means for recovering the clock frequency. The clock recovery block 107 comprises an output coupled to a control input of the analogue-to-digital converter 101 for controlling its sampling phase or frequency. The clock recovery block 107 may further comprise another output for optional digital interpolation.

An output of the pre-filter 105 or an output of the Fourier transforming means 103 may be coupled to a Fourier transforming means 109 for transforming the frequency domain signal into time domain upon the basis of e.g. an inverse fast Fourier transform (IFFT).

The analogue-to-digital converter 101 may receive a signal corresponding to a single optical polarization or a plurality of distinct signals according to a plurality of

different polarizations, e.g. according to a x- and a y-polarization. Thus, the analogue-to-digital converter 101 may output samples time domain signals $r_x[n]$ and $r_y[n]$, wherein n is an index.

- 5 According to an implementation form, the digital implementation applies blocks of the sampled sequences $r_x[n]$ and $r_y[n]$ after ADC, transferred into the frequency domain (FD) as

$$S_x[m] = FFT\{\text{Re}\{r_x[n]\} + j \text{Im}\{r_x[n]\}\}$$

$$S_y[m] = FFT\{\text{Re}\{r_y[n]\} + j \text{Im}\{r_y[n]\}\}$$

10

with the FFT block size $m = 1, \dots, M$. The FD (Frequency Domain) representation of the signal may be extended to the double bandwidth, which relates to upsampling to 4 sps in the TD (Time Domain), to yield $S_{x,4}[m]$ and $S_{y,4}[m]$. After the digital pre-filter in by means of all-pass (APF) filtering and/or amplitude filtering (AF)

15

$$S_{x,4\text{pre}}[m] = S_{x,4}[m] \cdot H_{APF}[m] \cdot H_{AF}[m]$$

$$S_{y,4\text{pre}}[m] = S_{y,4}[m] \cdot H_{APF}[m] \cdot H_{AF}[m]$$

the ACF (Auto-Correlation Function) and the CCF (Cross-Correlation Function) may be calculated accordingly by

20

$$U_{xx}[\tau] = ACF\{S_{x,4\text{pre}}[m]\}$$

$$U_{yy}[\tau] = ACF\{S_{y,4\text{pre}}[m]\}$$

$$U_{xy}[\tau] = CCF\{S_{x,4\text{pre}}[m], S_{y,4\text{pre}}[m]\}$$

The ACF delivers a stable clock tone for channels without half-Baud DGD, the CCF delivers stable clock tones for half-Baud DGD and multiples of it.

Alternatively, the ACF and the CCF may be defined by convolution functions using
5 linear or circular convolutions.

The combination

$$U[\tau] = U_{xx}[\tau] + U_{yy}[\tau] + U_{xy}[\tau]$$

10 or

$$U[\tau] = ACF\{S_{x,4pre}[m] + S_{y,4pre}[m]\}$$

may respectively deliver a stable criterion for all channel conditions. From $U[\tau]$ the
15 clock tone is extracted to estimate the phase of the timing signal.

Also averaging over several FFT-blocks, averaging over several estimations and weighting functions $w[\tau]$ to improve the decision variable $U_{imp}[\tau] = w[\tau]U[\tau]$ may be employed.

20

Fig. 2 shows a corresponding clock recovery apparatus comprising Fourier transforming means 201 with a first Fourier transformer 203 and a second Fourier transformer 205. The outputs of the Fourier transforming means are coupled to optional upsampling means having a first upsampler (US) 207 associated with the
25 first Fourier transformer 203, and a second upsampler 209 associated with the second Fourier transformer 205. The outputs of the upsampling means are coupled to optional pre-filtering means comprising a first pre-filter (PF) 211 and a second pre-filter 213. The first pre-filter 211 is coupled to an output of the first upsampler 207, whereas the second pre-filter 213 is coupled to an output of the second

upsampler 209. The outputs of the pre-filtering means are coupled to correlation means 215 having an output coupled to an optional phase estimator (PE) 217.

As depicted in Fig. 2, the time domain signal having the components $r_x[n]$ and $r_y[n]$ is transferred into frequency domain using the Fourier transforming means 201.

The respective Fourier transformer 203 and 205 collectively provide a frequency domain signal having a first frequency domain signal portion associated with the x-polarization and a second frequency domain signal portion associated with the y-polarization. The frequency domain signals are subsequently upsampled to an equivalent of e.g. four samples per symbol in the time domain using the upsampling means. Subsequently, pre-filtering means may be employed in order to bandpass filter the respective upsampled signals around the Nyquist frequency in order to enhance the clock tone and/or in order to all-pass filter the respective upsampled signals to compensate for chromatic dispersion. Subsequently, e.g. a cross-correlation function between the signals associated with the x- and the y-polarization, and the auto-correlation function for e.g. each signal according to the x- and y-polarization may be computed using the correlation means 215. By way of example, all correlation functions may be summed up in order to obtain an average value.

Alternatively, the first and the second frequency domain signal provided by the first and second Fourier transformer 203, 205, may be superimposed to obtain a superimposed frequency domain signal which may further be processed as depicted in Fig. 2.

Fig. 3 shows simulation results demonstrating the result of the correlation performed by the correlation means 215, wherein correlation values according to e.g. a cross-correlation scheme corresponding to a cross-convolution of signals $X(f)$ and $X(-f)$ over frequency are depicted. By way of example, certain correlation values 301, 303 or 305 have magnitudes greater than other correlation values, so that the frequencies associated with that correlation values may be assumed to correspond to the clock frequencies used at a transmitter. By way of example, the

results shown in Fig. 3 are obtained for a differential group delay associated with 0 ps.

Fig. 4 shows an example of estimation variable $U[m] = U_{xx}[m] + U_{yy}[m] + U_{xy}[m] + U_{yx}[m]$ for an SOP with $\theta = 90^\circ$ and $\phi = 0^\circ$ with half-Baud DGD of 17 ps. By way of example, only the CCF part includes the clock tone.

Fig. 5 shows another example of estimation variable $U[\tau] = U_{xx}[\tau] + U_{yy}[\tau] + U_{xy}[\tau]$ for an SOP with $\theta = 90^\circ$ and $\phi = 0^\circ$ without DGD. In particular, in Fig. 5, absolute values of auto-correlation or cross-correlation values are depicted over normalized frequency, wherein f_{Ny} denotes the Nyquist frequency. The $CCF(X, X)$ refers to the $ACF(X)$. In contrast to Fig. 4, the clock tones are contained in the ACF components in Fig. 5.

Fig. 6 shows corresponding simulation results for a differential group delay of 17 ps.

As depicted in Figs. 3 to 6, the particular frequency associated with the clock frequency may be detected by determining the correlation value exceeding other correlation values in a plurality of correlation values.

According to an implementation form, a FD representation of a signal after an optional all-pass filter comprising CD compensation and/or an amplitude filter comprising band-pass filtering may be used for the estimation of the clock tones. The pre-filter can be simply implemented by a scalar vector multiplication which does not require FIR pre-filter (FIR: Finite Impulse Response), and the amplitude and phase transfer function of the pre-filter can be arbitrarily chosen for any, e.g. optimized, value. Upsampling can be achieved by zero-padding which is the least complexity approach and does not degrade the signal quality since an interpolation FIR filter is not required. The equivalent operation of squaring the signal in the time domain (TD) refers to a correlation of the signal in the FD. A combination of auto-

correlation of each polarization signal and cross-correlation of both polarization signals mitigates polarization effects, the half-Baud DGD in particular.

5 The closed loop bandwidth, essential for a fast tracking of time-varying timing phase variations, depends on the delay between the analogue-to-digital converter (ADC) and the feedback of the control signal. Extracting the timing phase from the FD signal, the time for processing the IFFT, the FIR filter for pre-filtering and interpolation is saved. This clearly increases the loop bandwidth and allows for faster tracking speeds mitigating more timing jitter and faster drifts. The inventive method
10 is suitable for any modulation format as the properties in the spectral domain are similar for all modulation formats.

Fig. 7 shows a receiver comprising an optical front end 701 coupled to a synchronization apparatus. The synchronization apparatus comprises optical-to-electrical
15 converting means and analogue-to-digital converting means 703, which are coupled to a clock recovery apparatus 705.

The optical front end 701 comprises an optical splitter 707 providing a signal component according to the x-polarization to a first 90° hybrid 709, and a second signal
20 according to the y-polarization to a second 90° hybrid 711. The 90° hybrids 709 and 711 respectively provide complex valued signals (each having a real and an imaginary value) to the analogue-to-digital converting means 703. Since the complex valued signals provided by the optical front end are optical signals, the analogue-to-digital converting means 703 may comprise, for each signal, an optical
25 converter 713 for converting the respective optical signal into an electrical signal, and an analogue-to-digital converter (ADC) 715 for converting the respective electrical signal into a digital signal. The digital-to-analogue converting means 703 provides an input signal to the clock recovery apparatus 705, wherein the input signal may comprise a real and an imaginary portion associated with the x-
30 polarization and a real and an imaginary portion associated with the y-polarization.

The clock recovery apparatus 705 may comprise a processor 717, the processor 717 comprising Fourier transforming means (FT) for transforming the time domain

signals into the frequency domain, and correlating means to correlate the frequency domain signals according to the above described principles.

5 The correlation values and/or the cross-correlation values provided by the correlating means implemented within the processing means 717 are provided to clock recovery means 719 for recovering the clock frequency from the correlation values. The clock recovery means 719 may output a control signal 720 which is provided to the analogue-to-digital converting means 703 in order to adjust a sampling frequency or sampling phase for more accurately capturing the clock frequency.

10

The clock recovery apparatus 705 may further optionally comprise a butterfly filter 721, carrier recovery means 723 and/or symbol estimation means 725.

52663-24

14

CLAIMS:

1. A clock recovery apparatus being configured to recover clock information from an input signal, the apparatus comprising:

5 Fourier transforming means being configured to transform the input signal into a first frequency domain signal upon the basis of a Fourier transform by a first Fourier transformer and into a second frequency domain signal upon the basis of a Fourier transform by a second Fourier transformer;

10 correlating means being configured to correlate the first frequency domain signal to obtain a first correlation value associated with a first certain frequency and to correlate the second frequency domain signal to obtain a second correlation value associated with a second certain frequency; and

clock recovery means being configured to determine whether the first or second certain frequency corresponds to a clock frequency in order to recover the clock information.

15 2. The apparatus according to claim 1, wherein the clock recovery means is configured to decide that the first certain frequency corresponds to the clock frequency if the first correlation value is greater than the second correlation value.

3. The apparatus according to any one of claims 1 and 2, wherein the clock recovery means is configured to decide that the certain frequency corresponds to the clock frequency if the correlation value is greater than or equal to a certain threshold.

4. The apparatus according to any one of claims 1 to 3, wherein the clock recovery means is configured to output a control signal indicating a change of a sampling frequency or sampling phase if the certain frequency does not correspond to the clock frequency.

25

52663-24

15

5. The apparatus according to any one of claims 1 to 4, wherein the correlation means is configured to perform an auto-correlation of the frequency domain signal or to perform a cross-correlation of different portions to obtain the correlation value.

5 6. The apparatus according to any one of claims 1 to 5, wherein the input signal is a digitalized version of an optical signal having a first optical signal portion being associated with a first optical polarization and a second optical signal portion being associated with a second optical polarization, wherein the frequency domain signal comprises a first frequency domain signal portion being associated with the
10 first polarization and a second frequency domain signal portion being associated with the second optical polarization, and wherein the correlation means is configured to auto-correlate the first or the second frequency domain signal portion or to cross-correlate the first frequency domain signal portion and the second frequency domain signal portion to obtain the correlation value.

15 7. The apparatus according to any one of claims 1 to 6, wherein the correlation means is configured to weight at least a portion of the frequency domain signal or the correlation value with a weighting coefficient.

8. The apparatus according to any one of claims 1 to 7, further comprising a filter being configured to filter the frequency domain signal and to provide a filtered
20 frequency signal as the frequency domain signal to the correlating means.

9. The apparatus according to any one of claims 1 to 8, wherein the clock recovery means is further configured to determine a clock phase upon the basis of the correlation value.

10. The apparatus according to any one of claims 1 to 9, further comprising
25 oversampling means being configured to oversample the input signal prior to the Fourier transformation.

52663-24

16

11. The apparatus according to any one of claims 1 to 10, further comprising a Fourier transforming means being configured to transform the frequency domain signal into time domain for further processing.

12. A synchronization apparatus, comprising:

5 analogue-to-digital converter means for sampling an analogue input signal with a sampling phase or frequency to obtain the input signal; or

interpolation means for interpolating a digital signal with a sampling phase or frequency to obtain an interpolated signal forming an input signal; and

10 the clock recovery apparatus according to any one of claims 1 to 11, wherein the clock recovery means is configured to change the sampling frequency or sampling phase of the analogue-to-digital converter means for synchronization.

13. A method for recovering clock information from an input signal, the method comprising the steps of:

15 transforming the input signal into a first frequency domain signal upon the basis of a Fourier transform and into a second frequency domain signal upon the basis of a Fourier transform;

correlating the frequency domain signal to obtain a first correlation value associated with a first certain frequency and correlating the second frequency domain signal to obtain a second correlation value associated with a second certain frequency; and

20 determining whether the first or second certain frequency corresponds to a clock frequency in order to recover the clock information.

14. A synchronization method, comprising the steps of:

sampling an analogue input signal with a sampling phase or frequency to obtain the input signal;

52663-24

17

recovering a clock information from an input signal according to claim 13; and

changing the sampling phase or the sampling frequency to synchronize with a frequency corresponding to the clock frequency.

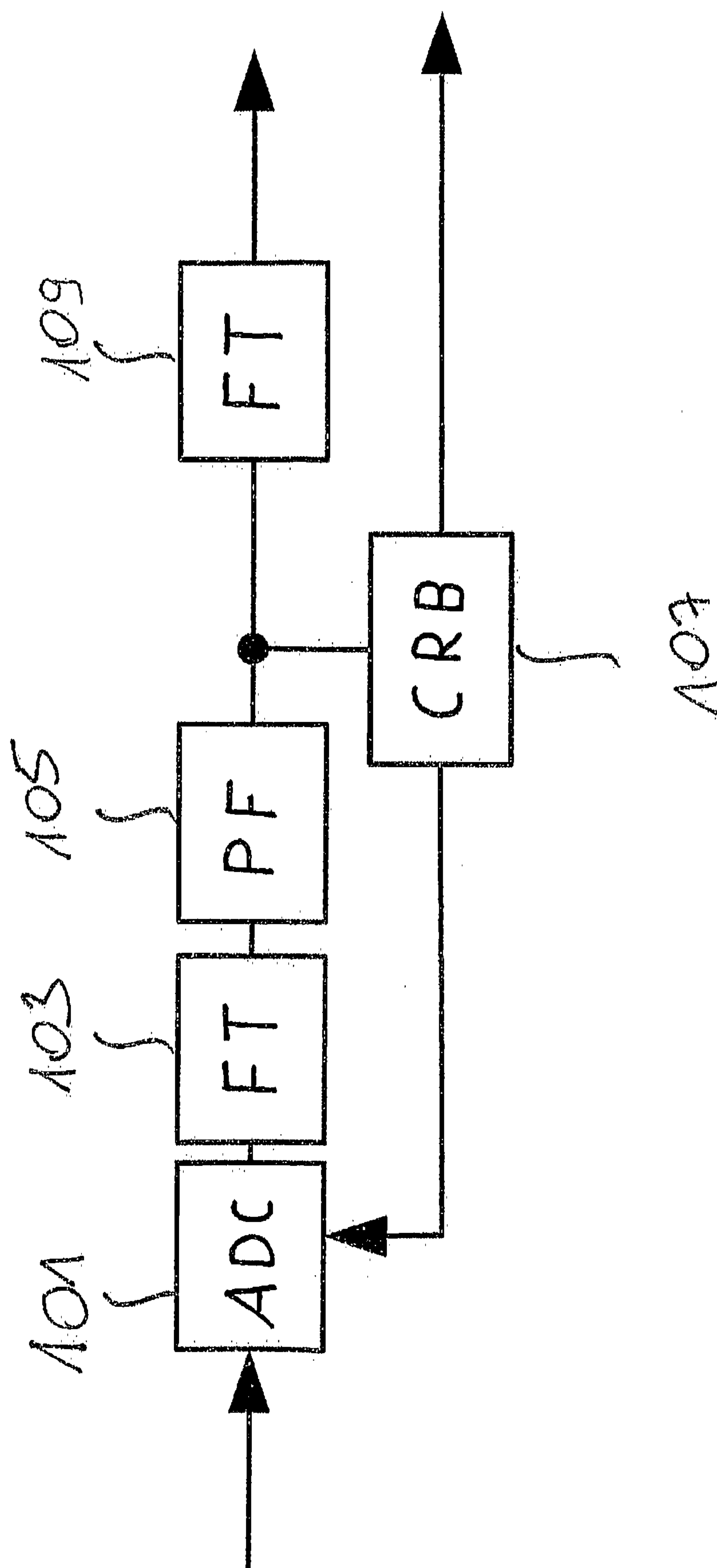


Fig. 1

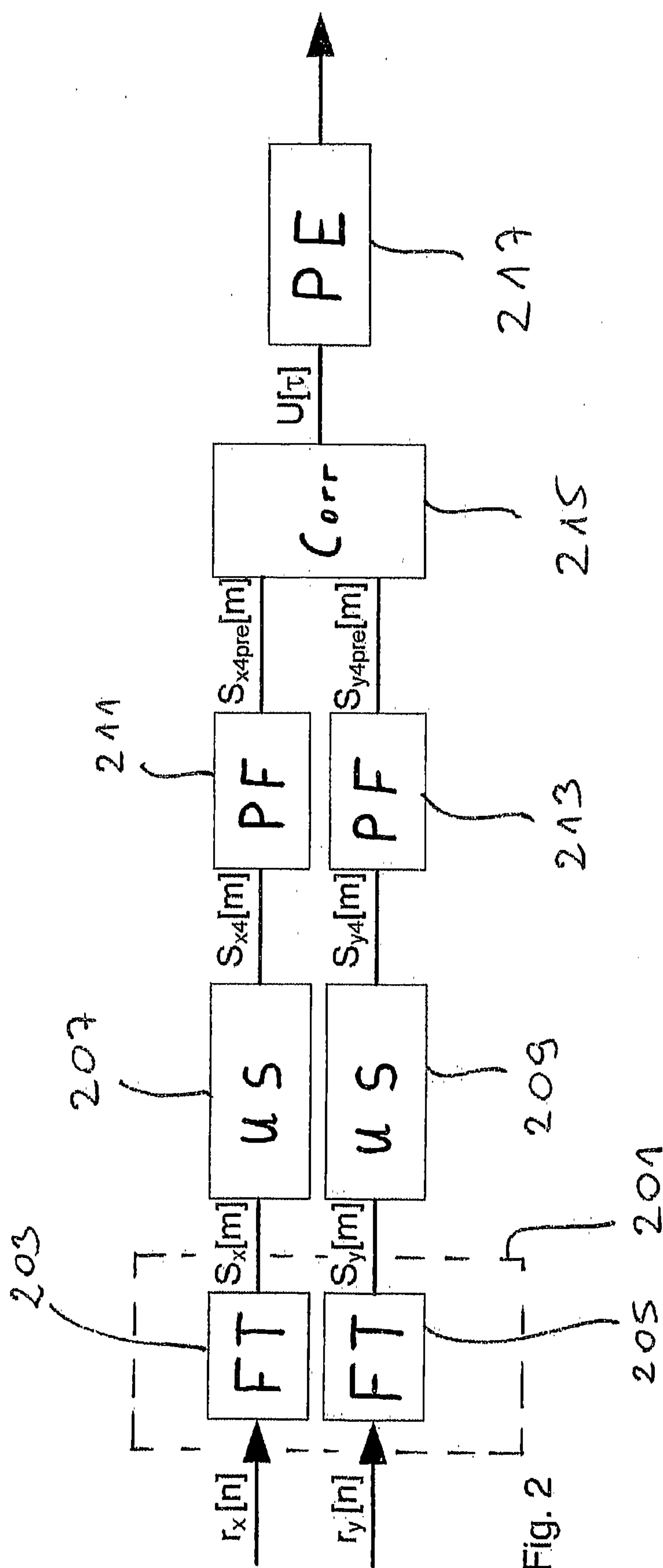


Fig. 2

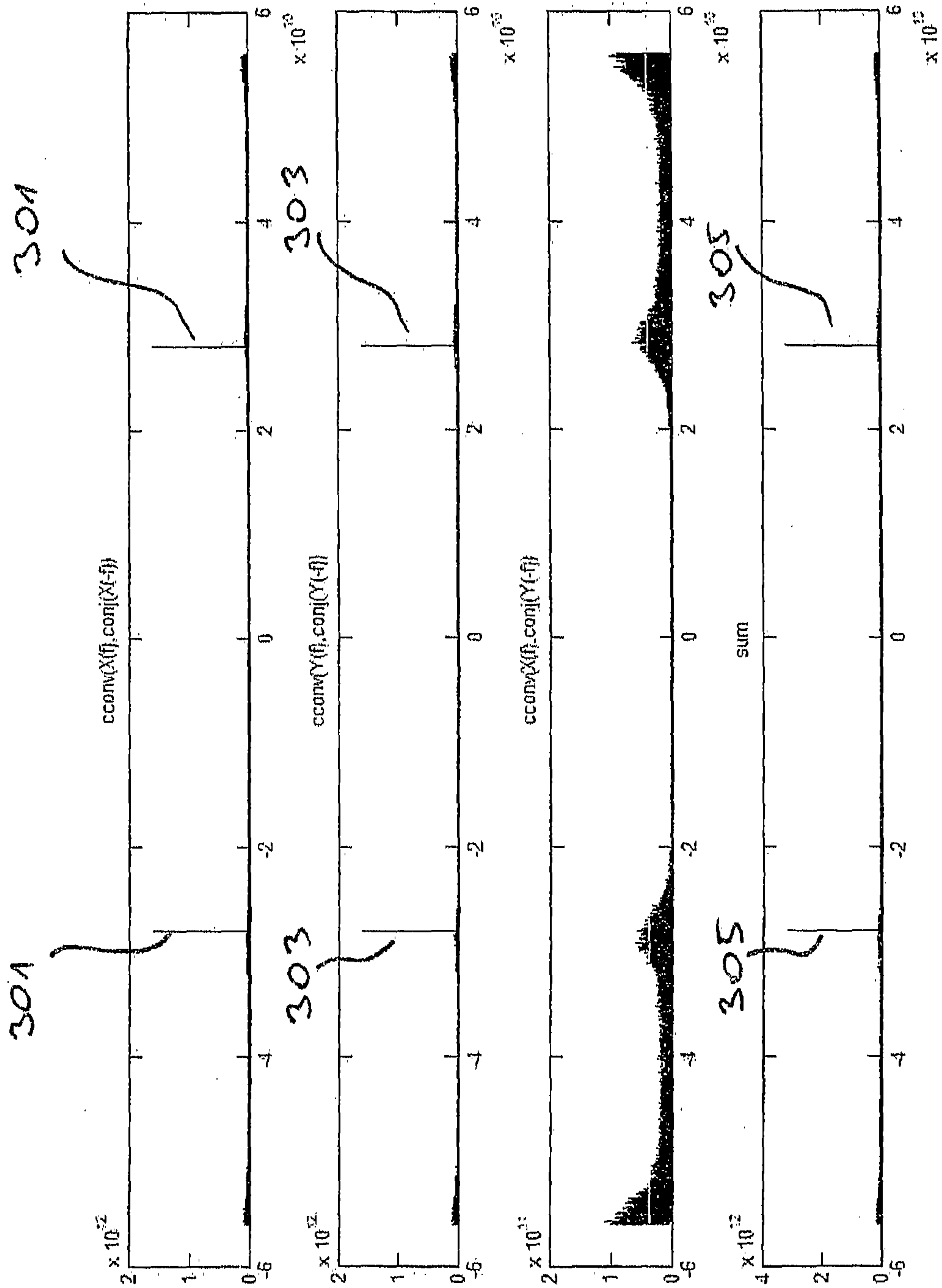


Fig. 3

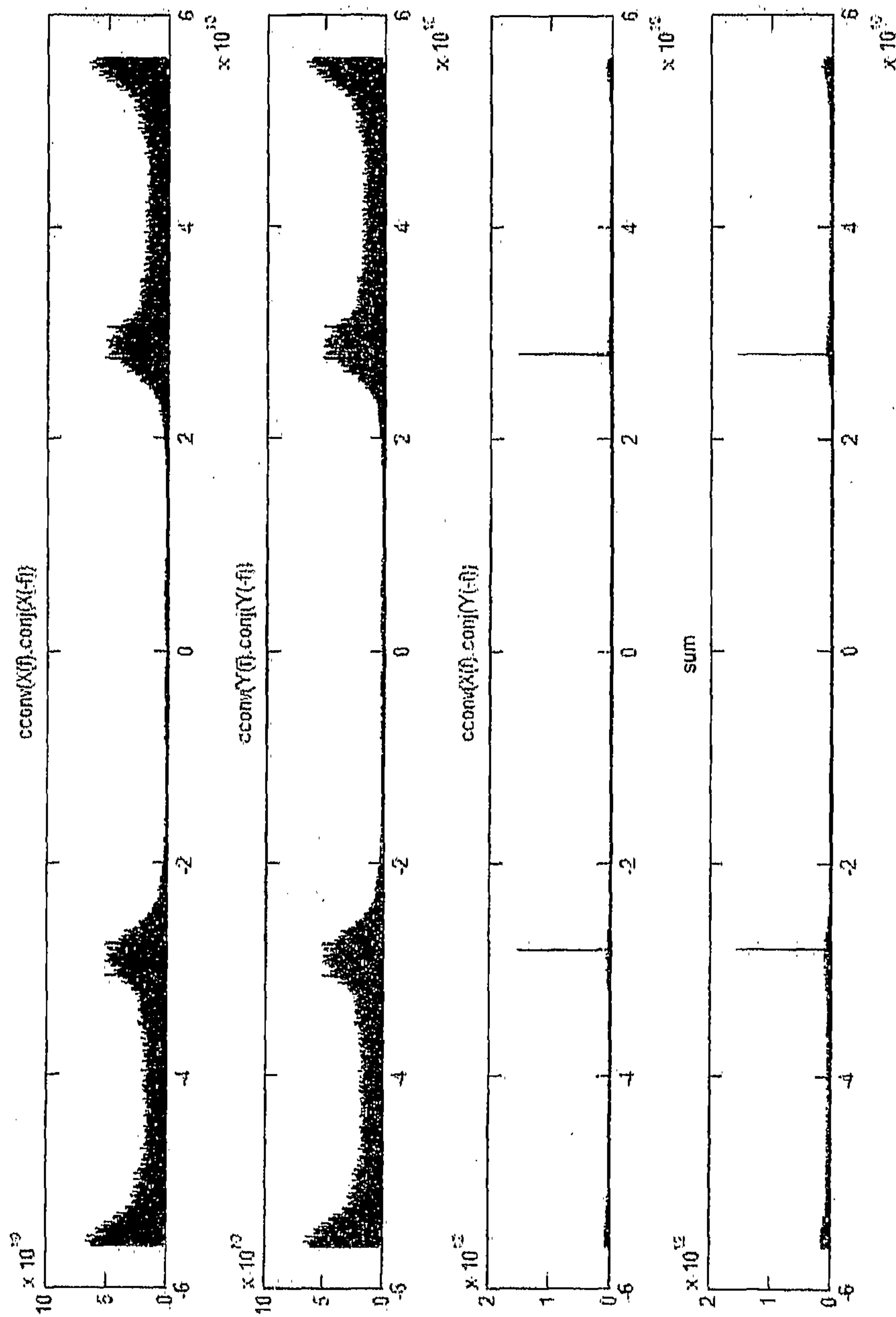


Fig. 4

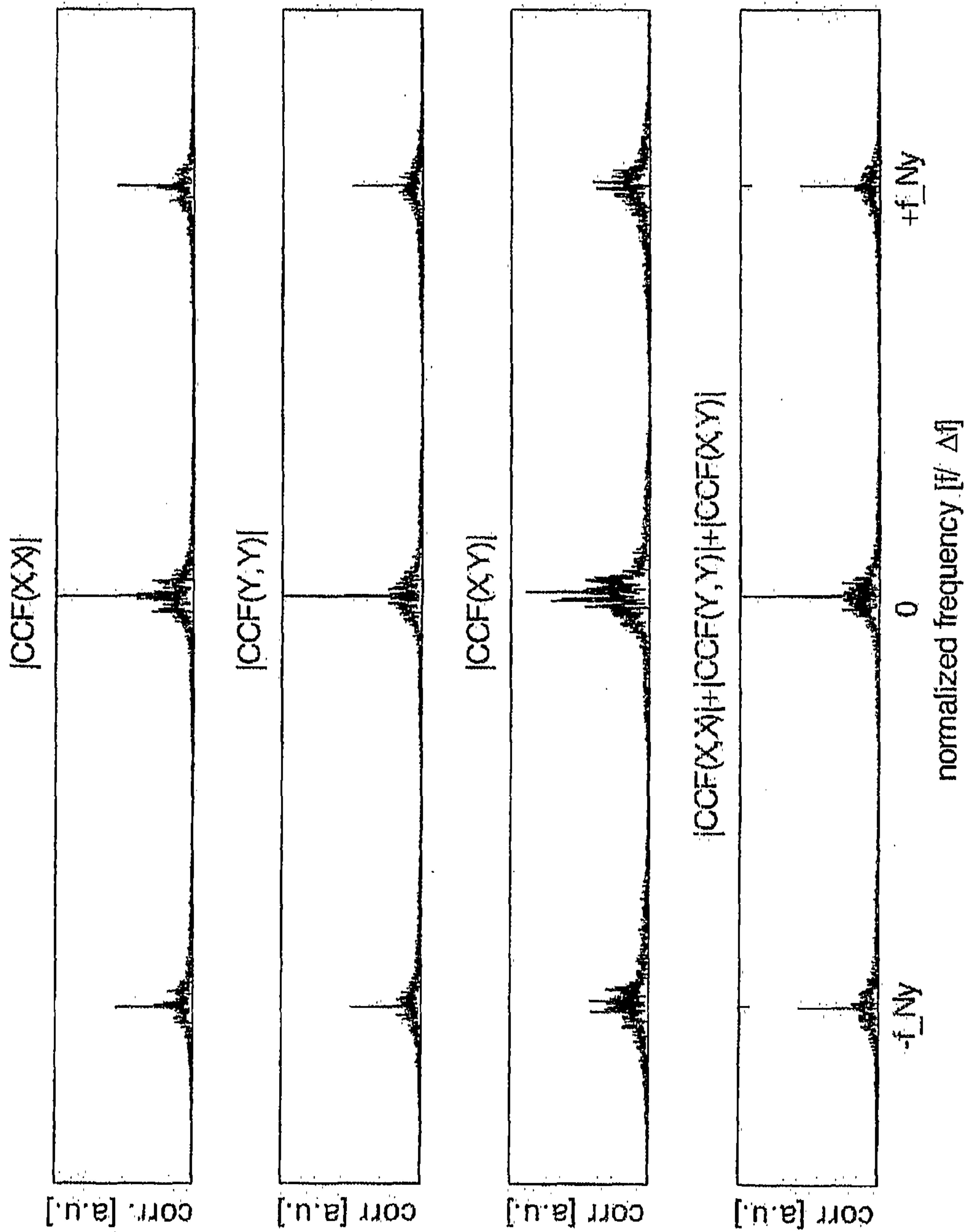


Fig. 5

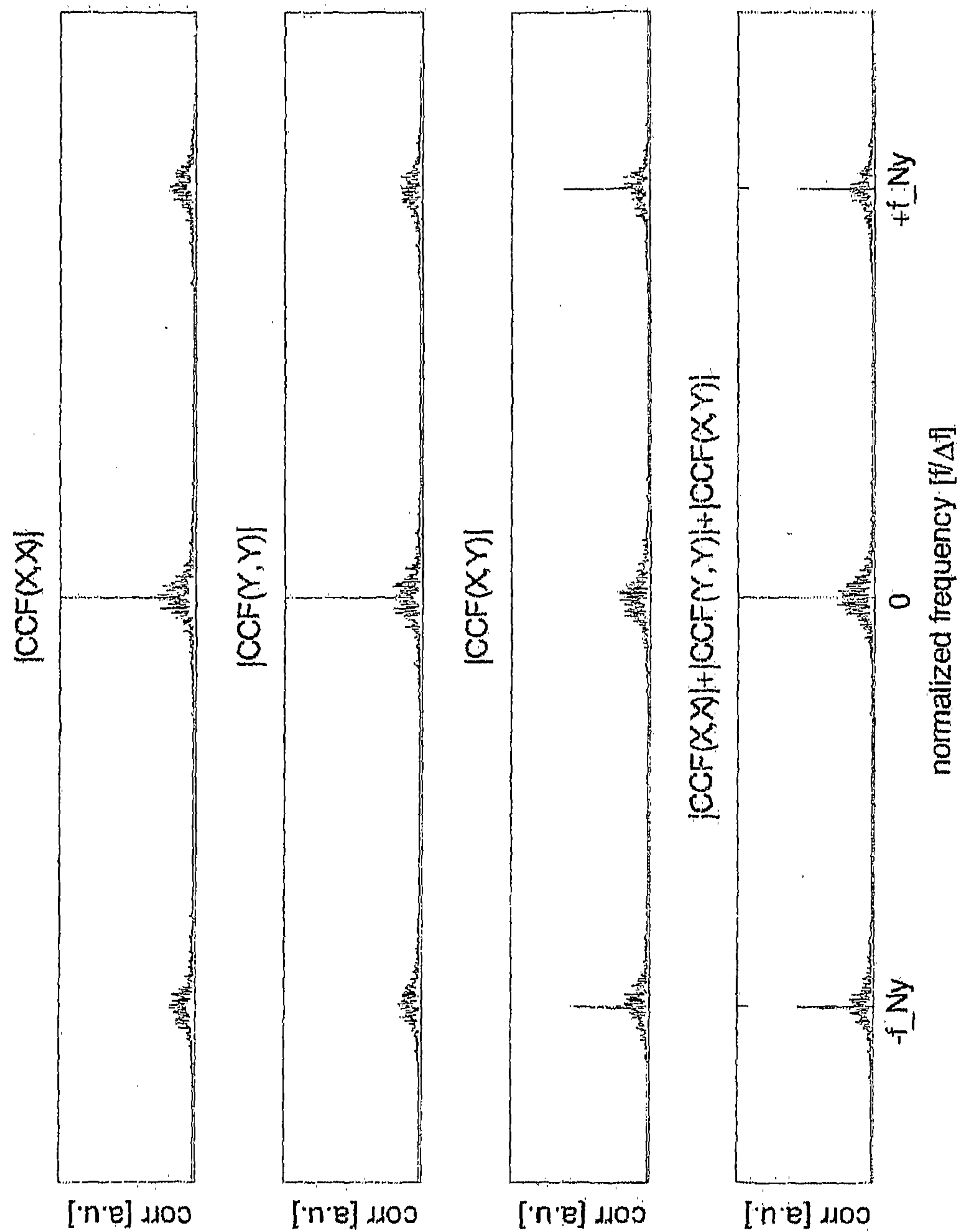


Fig. 6

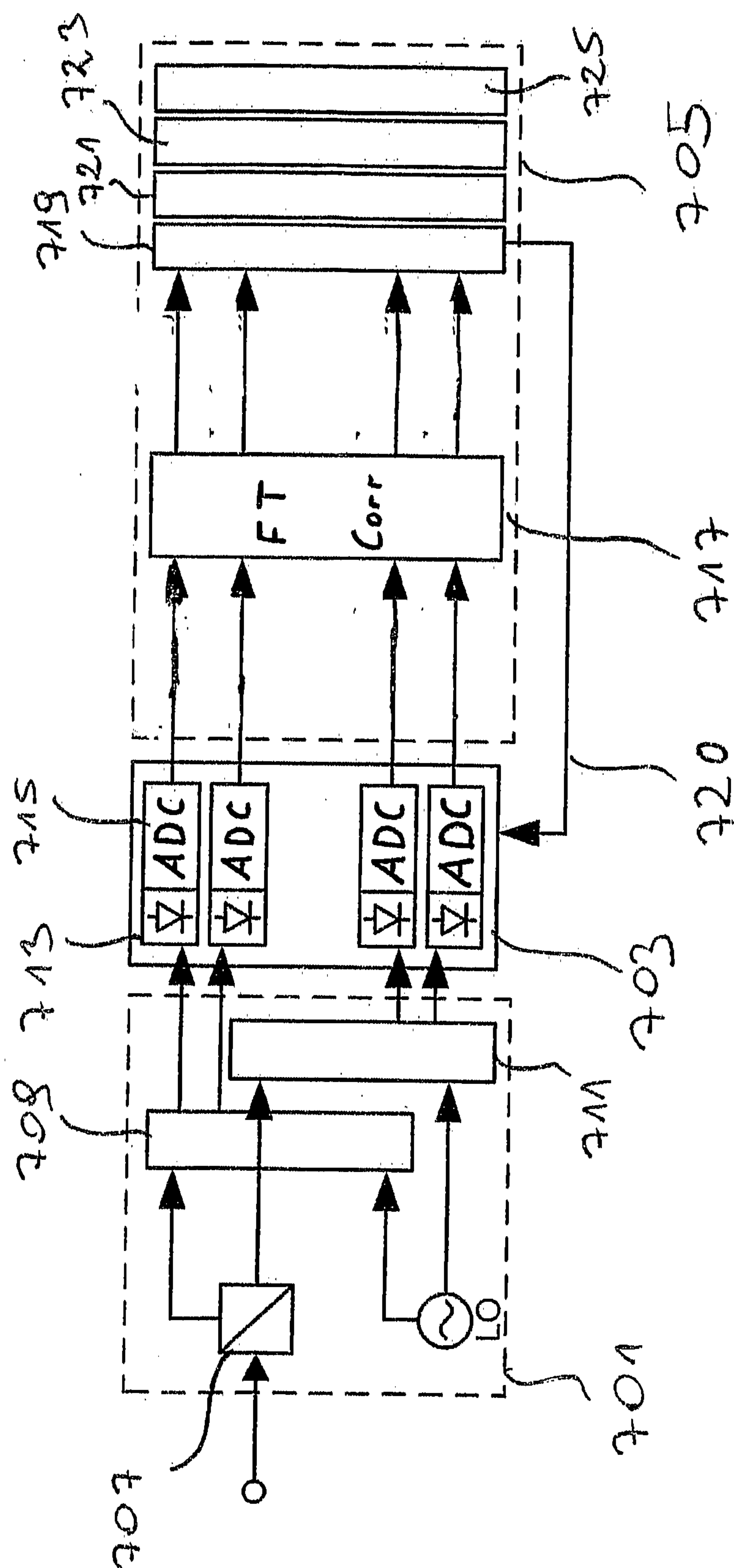


Fig. 7

