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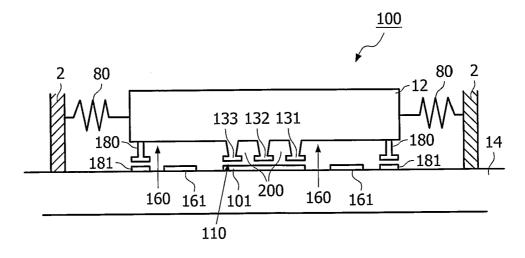
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(54) Title: ELECTRONIC DEVICE



(57) Abstract: The electronic device comprising a micro-electromechanical systems (MEMS) element at a first side of a substrate (14). The MEMS element comprises a first electrode (101) and a second electrode (11), that is part of a movable element and movable towards and from the first electrode between a first and a second position. The second electrode (11) is separated from the first electrode (101) by an air gap (110) in its first position. The movable element comprises a mechanical layer (12) and an intermediate layer, in which the second electrode (11) is defined. The second electrode (11) is constituted by a plurality of sections (131, 132, 133) in the intermediate layer, each of which is mechanically connected by a separate vertical interconnect (121, 122, 123) to the mechanical layer (12).



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Electronic device

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The invention relates to an electronic device comprising a microelectromechanical systems (MEMS) element, which MEMS element comprises a first electrode and a second electrode, that is part of a movable element and movable towards and from the first electrode, and that is separated from the first electrode by a gap in its opened position, which movable element comprises a mechanical layer and an intermediate layer, in which the second electrode is defined.

Microelectromechanical systems (MEMS) refer to a collection of microsensors and actuators, which can react to an environmental change under micro-circuit control. The integration of MEMS into traditional radio frequency (RF) circuits has resulted in systems with superior performance levels and lower manufacturing costs. The incorporation of MEMS based fabrication technologies into micro and millimetre wave systems offers viable routes to devices with MEMS actuators, antennas, switches and capacitors. The resultant systems operate with an increased bandwidth and increased radiation efficiency, reduced power consumption, and have considerable scope for implementation within the expanding area of wireless personal communication devices.

MEMS elements comprise a first and a second electrode of which the second electrode is movable to and from the first electrode between a first position and a second position. In the first position the MEMS element is opened and there is a gap, usually an air gap, between the first and the second electrode. In the second position, the MEMS element may be closed, such that there is no gap between the first and the second electrode, or the airgap has a minimum thickness. Such minimum thickness can be achieved with the provision of bumps with the desired thickness. A dielectric layer may be present on top of the first electrode, that is generally present on a substrate. This leads thereto that the first electrode does not make electrical contact with the second electrode in its closed position, but forms a capacitor therewith. The other electrode or electrodes may also be provided with dielectric layers or native oxides if so desired.

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The fact that the second electrode must be movable, but still be incorporated in a mechanically stable construction results therein that the devices are usually provided with a mechanical layer of sufficient thickness and mechanical stability. This layer may be a metal layer, but is alternatively a piezoelectric layer, such as is known from GB-A 2,353,410.

A device as described in the opening paragraph is known from WO-A 2004/54088, particularly Fig. 11 thereof. Both the intermediate layer and the mechanical layer are herein made of aluminium. These layers are mutually attached by a vertical interconnect with a diameter small than that of the second electrode in the intermediate layer.

It is a disadvantage of the known device that the capacitance density in a second position is less than was expected on the basis of the available surface area. Hence, the effective tuning range is also reduced, and with the resulting reduced tuning range the MEMS element is not very competitive to alternative solutions for tunable capacitors and switches. Such an alternative is in particular the use of discrete switches like pindiodes and pHEMT transistors, if needed in combination with discrete or thin-film capacitors.

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It is therefore an object of the invention to provide a device of the kind mentioned in the opening paragraph with an improved tuning range.

This object is achieved in that the second electrode is constituted by a plurality of sections in the intermediate layer, each of which is mechanically connected by a separate vertical interconnect to the mechanical layer. In other words, the second electrode is segmented.

It was found in experiments leading to the invention, that there is a stress difference built up between the mechanical layer and the intermediate layer, even in the case that these layers comprise the same metal or alloy. The stress difference is thought to result from inherent stresses in the layers. The consequence of this stress difference is that the movable element with the second electrode is distorted when it is displaced towards the first electrode. As a result, the second electrode is not planar anymore, but bent. This results that the minimum distance between the first and the second electrode is reached only on a limited surface area, and hence the capacitance density effectively decreases. Now by dividing the second electrode in the intermediate layer in a number of independent sections, the bending effect is considerably reduced, and also, the stress is reduced. As a consequence, the capacitance density is increased, even although the surface area of the second electrode is reduced.

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Consequently, the device of the invention has also a better thermal stability. It further may be miniaturized, as a consequence of the higher capacitance density and the better definition of the contact area.

In a preferred embodiment, the vertical interconnect has a smaller diameter than the section attached thereto in the intermediate layer. This reduces the stresses effectively even further. The smaller diameter is particularly present at the interface with the intermediate layer.

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It is an advantage of the presence of such vertical interconnects that it offers a larger design freedom. In fact, a separation can be made between a front end with the first and second electrode defining the electrical function and a back end providing the mechanical construction that acts as a support and the actuator of the second electrode. This allows that the front end is defined rather independently of the back end. In other words, if in the back end a metal layer is replaced by a ceramic layer, this does not need to give rise to major changes in the front end. And the back end may thus be optimised for the parameters of mechanical support, and package and manufacturability on an industrial scale. Particularly for a piezoelectric MEMS element, the back end may well be provided by assembly instead of by deposition of individual layers.

Preferably the diameter of the interconnect is only 0.75 times the diameter of the corresponding section. And more preferably, the diameter of the interconnect is at least 0.25 times the diameter of the corresponding section. In the case that the interconnect is ring-shaped, its diameter is defined as the diameter of the ring. In the case that the diameter of the interconnect is smaller than the stated 0.25 ratio, it is preffered that more then one virtual interconnect is used for a single section.

In a further embodiment, at least one through-hole is present in the movable element, which extends through the mechanical layer and one section in the intermediate layer. With this through-hole etchant is supplied effectively in order to remove a sacrificial layer between the first and the second electrode. Moreover, the presence of the through-hole in this area reduces the mass of the movable element in the area that should be moved. As such, it contributes to a reduction of the actuation voltage.

Suitably, the second electrode comprises etch holes that have a smaller diameter than that of the through-hole. It is considered advantageous that a pattern of etch holes is present in the second electrode. Due to the presence of the intermediate layer, the pattern of etch holes can be defined independently from that in the mechanical layer. Since the intermediate layer is substantially thinner than the mechanical layer, the resolution of the

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patterns can be higher, and the etch holes can be smaller. The intermediate layer preferably has only a fifth and more preferably only a tenth of the thickness of the mechanical layer.

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In another embodiment, the element is provided with a venting channel in its second, closed position, of which venting channel spaces between the sections and the interconnects are part. The fluid may be a gas, e.g. air or nitrogen. The venting channels run mainly parallel to the facing surfaces. The channels preferably end up in a vertical venting hole or at the side of the surface. The venting channels may extend into and along the substrate e.g. by partial removal of the substrate with a substrate transfer technique. Therewith the channels allow fluid to enter the area between the facing surface and damping of the movement of the moveable element to be controlled. Usually it is desired to increase the flow entering or exiting the area between the facing surfaces, to reduce such damping, and hence increase speed of opening and closing of the device. Much of the opening delay is concerned with the initial stage of opening, since the electrostatic attraction is greatest when the electrodes are closest. Usually the electrostatic force during opening is close to 0 because the actuation voltage is turned off. This is also where air damping is high because the air has least space to move. Similarly for device closing, the reduction in damping will be most pronounced at the final stage of closing for the same reasons. As this is where most of the closing delay occurs, the channels can enable a notable increase in closing speed. The benefits in terms of speed can be traded for other advantages such as reducing drive voltage or spring rate for a given speed, or using a higher operating pressure or higher viscosity fluid for a given speed for example. Another use for the channels could be to allow fluid to be forced in between the facing surfaces when closed, to help force them apart, to increase opening speed or overcome stiction.

In again another embodiment, the mechanical layer is provided with at least one movable section of which the second electrode is part, that is coupled to a main section of the movable element by a resilient coupling. The independently movable section allows movement of the movable element towards the substrate of the movable element in two modes. As a result, not the complete movable element, but only the independently movable section or alternatively only the main section of the movable element will be attached to the substrate. The problem of stiction is therewith reduced, in that first of all, the attached area is reduced and secondly, the resilient coupling provides an inherent counterforce.

Preferably, further having one or more flexible elements coupled to provide a force on the movable element dependent on the movement and being present between the movable element and one or more protrusions on a substrate. Such flexible elements or

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spring structures are known per se from US6,557,413. Therewith, the movable element is attached to the substrate only in limited areas, and may as a whole, be moved towards and from the first electrode more easily.

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In an even further embodiment, an anti-stiction bump is defined in the intermediate layer, and connected to the mechanical layer with a vertical interconnect. Such an anti-stiction bump is located opposite an area on the substrate that is free of any electrode. As such the bump will not be charged, and the bump is not prone to stiction. Preferably, the vertical interconnect of the bump has a diameter in a plane parallel to the intermediate layer, that is smaller than that of the bump surface in the intermediate layer. This has been found to provide a good bumping surface.

It is highly preferred if there is both a resilient coupling in the mechanical layer and an anti-stiction bump. It has turned out that the effective elastic constant of the spring structure gets higher when the anti-stiction bump is in contact with the substrate. As a result, the upwards force will be increased.

As will be understood, one or more actuation electrodes are needed in the event that the MEMS element does not have a sensing function, but must be actively addressed, i.e. as a switch or a tunable capacitor. The actuation electrode may be implemented in the first electrode, but preferably one or more independent actuation electrodes are present. In one advantageous embodiment of the invention, actuation electrodes are defined in the intermediate layer and are not divided into several sections. With this construction, the minimum distance between the actuation electrodes on the movable element and on the substrate is larger than the minimum distance between the first and second electrodes.

The MEMS element of the invention is very suitable for application as a capacitor, but also as a switch.

In a further embodiment, a thin-film capacitor is present adjacent to the MEMS element, said thin-film capacitor provided with a first electrode on the first side of the substrate and a second electrode in the intermediate layer, between which electrodes a layer of dielectric material is present. The device of the invention, with a metal layer on the substrate, an intermediate layer (generally of metal), and a mechanical layer (for instance of metal), allows the effective integration of thin film capacitors with MEMS elements. This is achieved by selectively etching away sacrificial layers. The selective etching can be a matter of flow of etchant. Preferably, the MEMS element is surrounded by any ring of non-etchable material to limit the flow of etchant. This ring is for instance the support through which the

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movable element is attached to the substrate, however it may be a separate ring as well, or part of the package of the MEMS element. In case that the mechanical layer is a metal, it is highly suitable for definition of inductors and the like elements.

The present device can be suitably made by etching away any sacrificial layers between the substrate, the intermediate layer and the mechanical layer with the help of etching. This is known per se in the art. Use is preferably made of dry etching, but wet etching could be applied alternatively with the desired result. A fluorine-containing dry etchant is preferred, and suitably the substrate is protected with a etch-stop layer that is resistant against such a fluorine-containing dry etchant.

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These and other aspects of the device of the invention will be further illustrated with respect to the drawings, in which:

Fig. 1 shows a diagrammatical view of a MEMS element;

Fig. 2 shows a detail of Fig. 1, which illustrates the invention

Fig. 3 shows in a diagrammatical, cross-sectional view, an embodiment of the device of the invention, and

Fig. 4 shows a top view of an embodiment.

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Equal reference numerals in different figures refer to equal constituents. The figures are not drawn to scale and only of a diagrammatical nature.

Fig. 1 shows a diagrammatical view of a MEMS element, and Fig. 2 shows a detail hereof. It comprises a movable element 100 with a second electrode 11, that is facing a first electrode 101 and separated therefrom by an air gap 110. The first electrode 101 is present on a substrate 14. Protrusions 2 are present on this substrate 14, to which the movable element 100 is mechanically connected with flexible elements 80. The movable element 100 has as its main layer a mechanical layer 12, which is designed to provide the mechanical stability. It is in this example a metal or alloy, but that is not necessary. An insulating layer may be used that solely has a support function. Alternatively, the mechanical layer may be a piezoelectric layer that may expand so as to provide any needed actuation. Generally, the mechanical layer 12 is a layer with a thickness in the order of one micron or more. The movable element 100 further comprises anti-stiction bumps 180, that define a minimal distance and support the planarity of the movable element 100. Non-charged counters 181 are

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present on the substrate 14. The movable element 100 comprises actuation electrodes 160 adjacent to the second electrode 11. The actuation electrodes 160 may be completely or substantially ring-shaped so as to circumfere the second electrode 11 and constitute effectively one electrode. Alternatively, more than one actuation electrode 160 is used. Counter electrodes 161 are present opposite to the actuation electrodes 160 on the substrate 14.

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According to the invention, the second electrode 11 comprises a plurality of independent sections 131, 132, 133, which are connected to the mechanical layer 12 with vertical interconnects 121, 122, 123. The result is a segmented second electrode 11. A venting channel 200 is defined by the areas in between of the vertical interconnects 121, 122, 123, when the element is closed and the second electrode 11

Fig. 3 shows an embodiment of the device of the invention. The device comprises a substrate 14, here of silicon with an oxide layer 141. Hereon, an etch-stop layer 18 and a base layer 10 are present. The etch-stop layer 18 is preferably an etch-stop layer against a fluorine plasma etchant. The etch-stop layer 18 preferably comprises any one of the Group IV-oxides, such as Al_2O_3 , HfO_2 , ZrO_2 and TiO_2 . In one preferred embodiment, the etch-stop layer 18 may comprise Al_2O_3 of a thickness of for instance 100 nm.

The first electrode 101 is defined in the base layer 10. It is separated from a second electrode 11 with an air gap 110. This air gap 110 has been formed by selective removal of a first sacrificial layer 16 and a second sacrificial layer 17. The sacrificial layers 16 and 17 may, for example, consist of Si, Si₃N₄, SiO₂, W, Mo, but also of any polymeric material, or any ferroelectric material. A mechanical layer 12 is present on top of the air gap 110 and the second sacrificial layer 17, as far as it has not been removed. Vertical interconnects 121, 122, 123 connect the different sections 131, 132, 133 of the second electrode 11 to the mechanical layer 12. The mechanical layer 12 and the base layer 10 consist of conductors, such as Al, Ni, Au, Cu or Pt. In this embodiment, aluminum conductors have been used for the base layer, and an alloy of Al_{.98}Cu_{.02} has been used for the mechanical layer. The first electrode 101 and/or the second electrode 11 may be provided with a native oxide layer, that is created by a treatment with a suitable plasma etch.

A mask 20 is applied on top of the mechanical layer 12. This mask 20 includes a window 21 to the sacrificial layer 17. For the mask 20, use is made of a polyimide with a thickness of about 5 μ m. This is suitable in view of the thickness of the mechanical layer 12, for instance 1 μ m, and offers sufficient protection against a plasma etchant, for instance a fluorine plasma. However, it is not excluded that the etching treatment may be carried out

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with wet-chemical etching. In that case, the etch-stop layer 18 need not to be present, or can be chosen differently.

After the etching step to remove the sacrificial layers 16,17 the mask 20 need not to be removed. In this example, it constitutes part of the movable element of the MEMS element. In addition, it may act as a passivation layer for other elements in the device. The mask 20 may further be used to provide further metal layer according to a desired pattern that can be used as contact pads, and as sealing ring for an hermetic package.

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Fig. 4 shows a top view of the movable element 100 in one embodiment. The movable element 100 is attached to protrusions 2 on the substrate 14 with flexible elements 80. Anti-pull in bumps or anti-stiction bumps 180 are present at the corners of the movable element 100. A U-shaped area of the movable element 100 is in use as the actuation electrode 160. Etch holes 21 are present herein. The second electrode 11 is located within the U-shaped actuation electrode 160. It is provided with etch holes 22. It is provided with a plurality of sections 131, 132 that define the actual electrode surface.

CLAIMS:

1. An electronic device comprising a micro-electromechanical systems (MEMS) element, which MEMS element comprises a first electrode and a second electrode, that is part of a movable element and movable towards and from the first electrode between a first and a second position, and that is separated from the first electrode by a gap in its first position, which movable element comprises a mechanical layer and an intermediate layer in which the second electrode is defined,

characterized in that the second electrode is constituted by a plurality of sections in the intermediate layer, each of which is mechanically connected by a separate vertical interconnect to the mechanical layer.

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- 2. An electronic device as claimed in Claim 1, wherein the vertical interconnect has a smaller diameter than the second electrode.
- 3. An electronic device as claimed in Claim 1, wherein at least one through-hole is present in the movable element, which extends through the mechanical layer and one section in the intermediate layer.
 - 4. An electronic device as claimed in Claim 3, wherein the element is provided with a venting channel in its second, closed position, of which venting channel spaces between the sections and the interconnects are part.
 - 5. An electronic device as claimed in Claim 1, wherein the mechanical layer is provided with at least one movable section of which the second electrode is part, that is coupled to a main section of the movable element by a resilient coupling.

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6. An electronic device as claimed in Claim 1 or 5, further having one or more flexible elements (80) coupled to provide a force on the movable element dependent on the movement and being present between the movable element and one or more protrusions on a substrate (14).

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- 7. An electronic device as claimed in Claim 1, 5 or 6, wherin at least one anti stiction bump is included.
- 5 8. An electronic device as claimed in Claim 1, 5, 6 or 7, wherin at least one actuation electrode is included.
 - 9. An electronic device as claimed in Claim 1, wherein the intermediate layer and the mechanical layer comprise different materials.
- 10. An electronic device as claimed in Claim 1, further comprising a thin-film capacitor adjacent to the MEMS element, said thin-film capacitor provided with a first electrode on the first side of the substrate and a second electrode in the intermediate layer, between which electrodes a layer of dielectric material is present.
- 11. An electronic device as claimed in Claim 1, wherein the mechanical layer comprises a metal.

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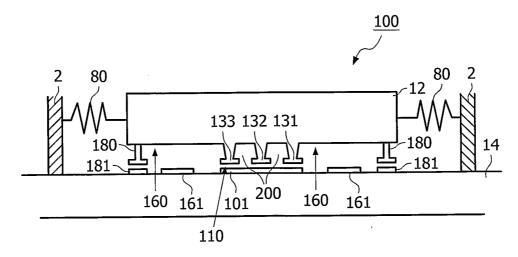


FIG. 1

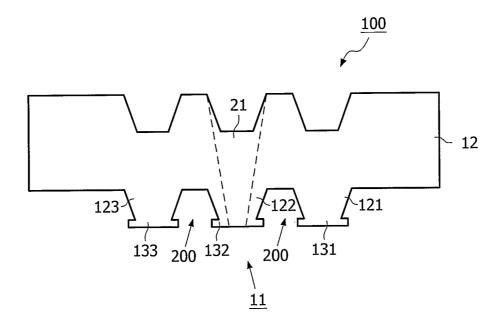


FIG. 2

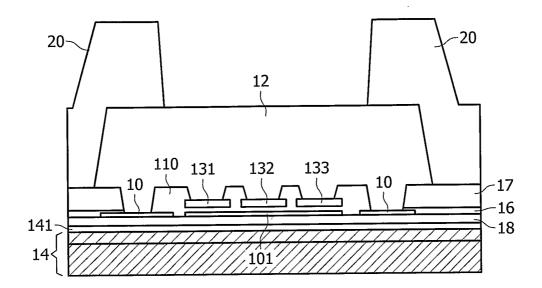


FIG. 3

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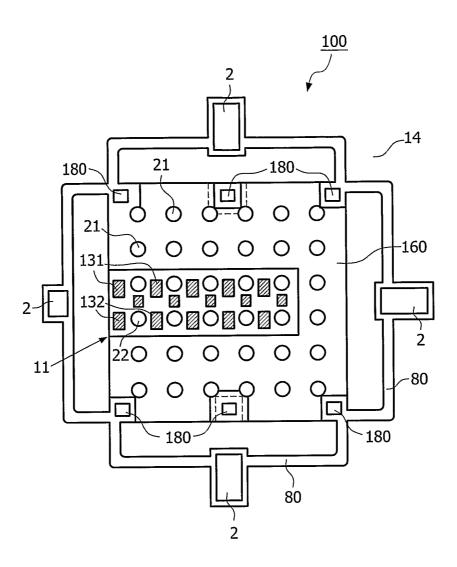


FIG. 4

INTERNATIONAL SEARCH REPORT

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a. classification of subject matter H01H59/00 According to International Patent Classification (IPC) or to both national classification and IPC B. FIELDS SEARCHED Minimum documentation searched (classification system followed by classification symbols) H01H Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched Electronic data base consulted during the international search (name of data base and, where practical, search terms used) EPO-Internal, PAJ C. DOCUMENTS CONSIDERED TO BE RELEVANT Category* Citation of document, with indication, where appropriate, of the relevant passages Relevant to claim No. US 5 454 904 A (GHEZZO ET AL) χ 1,5,6,11 3 October 1995 (1995-10-03) the whole document 2-4,7-10γ US 5 531 018 A (SAIA ET AL) 1,5,6,11 χ 2 July 1996 (1996-07-02) the whole document Υ PATENT ABSTRACTS OF JAPAN 2-4,9vol. 1997, no. 07, 31 July 1997 (1997-07-31) & JP 09 082199 A (OMRON CORP), 28 March 1997 (1997-03-28) abstract paragraph '0037!; figure 5 US 2002/179421 A1 (WILLIAMS BYRON L ET AL) 7,8,10 Υ 5 December 2002 (2002-12-05) the whole document Further documents are listed in the continuation of Box C. See patent family annex. Special categories of cited documents: "T" later document published after the international filing date or priority date and not in conflict with the application but "A" document defining the general state of the art which is not considered to be of particular relevance cited to understand the principle or theory underlying the invention *E* earlier document but published on or after the international *X* document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to filing date "L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another involve an inventive step when the document is taken alone "Y" document of particular relevance; the claimed invention citation or other special reason (as specified) cannot be considered to involve an inventive step when the document is combined with one or more other such docu-"O" document referring to an oral disclosure, use, exhibition or ments, such combination being obvious to a person skilled other means document published prior to the international filing date but later than the priority date claimed "&" document member of the same patent family Date of the actual completion of the international search Date of mailing of the international search report 1 February 2006 10/02/2006 Name and mailing address of the ISA/ Authorized officer European Patent Office, P.B. 5818 Patentlaan 2 NL - 2280 HV Rijswijk Tel. (+31-70) 340-2040, Tx. 31 651 epo nl, Ramírez Fueyo, M Fax: (+31-70) 340-3016

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