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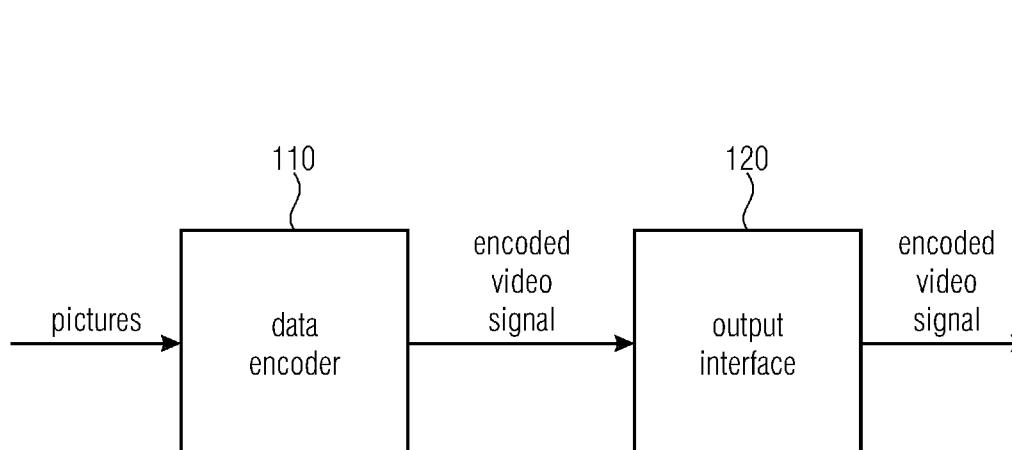


Fig. 1

(57) Abstract: A video encoder (101) according to embodiments is provided. The video encoder (101) is configured for encoding a plurality of pictures of a video by generating an encoded video signal, wherein each of the plurality of pictures comprises original picture data. The video encoder (101) comprises a data encoder (110) configured for generating the encoded video signal comprising encoded picture data, wherein the data encoder is configured to encode the plurality of pictures of the video into the encoded picture data. Moreover, the video encoder (101) comprises an output interface (120) configured for outputting the encoded picture data of each of the plurality of pictures. Furthermore, a video decoders, systems, methods for encoding and decoding, computer programs and encoded video signals according to embodiments are provided.



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**Encoder and Decoder, Encoding Method and Decoding Method  
with Profile and Level Dependent Coding Options**

Description

5

The present invention relates to video encoding and video decoding and, in particular, to an encoder and a decoder, to an encoding method and to a decoding method with profile and level dependent coding options.

10 H.265/HEVC (HEVC = High Efficiency Video Coding) is a video codec which already provides tools for elevating or even enabling parallel processing at an encoder and/or at a decoder. For example, HEVC supports a sub-division of pictures into an array of tiles which are encoded independently from each other. Another concept supported by HEVC pertains to WPP, according to which CTU-rows or CTU-lines of the pictures may be  
15 processed in parallel from left to right, e.g. in stripes, provided that some minimum CTU offset is obeyed in the processing of consecutive CTU lines (CTU = coding tree unit). It would be favorable, however, to have a video codec at hand which supports parallel processing capabilities of video encoders and/or video decoders even more efficiently.

20 In the following, an introduction to VCL partitioning according to the state-of-the-art is described (VCL = video coding layer).

Typically, in video coding, a coding process of picture samples requires smaller partitions, where samples are divided into some rectangular areas for joint processing such as  
25 prediction or transform coding. Therefore, a picture is partitioned into blocks of a particular size that is constant during encoding of the video sequence. In H.264/AVC standard fixed-size blocks of 16x16 samples, so called macroblocks, are used (AVC = Advanced Video Coding).

30 In the state-of-the-art HEVC standard (see [1]), there are Coded Tree Blocks (CTB) or Coding Tree Units (CTU) of a maximum size of 64 x 64 samples. In the further description of HEVC, for such a kind of blocks, the more common term CTU is used.

35 CTUs are processed in raster scan order, starting with the top-left CTU, processing CTUs in the picture line-wise, down to the bottom-right CTU.

The coded CTU data is organized into a kind of container called slice. Originally, in former video coding standards, slice means a segment comprising one or more consecutive

CTUs of a picture. Slices are employed for a segmentation of coded data. From another point of view, the complete picture can also be defined as one big segment and hence, historically, the term slice is still applied. Besides the coded picture samples, slices also comprise additional information related to the coding process of the slice itself which is 5 placed into a so-called slice header.

According to the state-of-the-art, a VCL (video coding layer) also comprises techniques for fragmentation and spatial partitioning. Such partitioning may, e.g., be applied in video coding for various reasons, among which are processing load-balancing in parallelization,

10 CTU size matching in network transmission, error-mitigation etc.

The object of the present invention is to provide improved concepts for video encoding and video decoding.

15 The object of the present invention is solved by the subject-matter of the independent claims.

Preferred embodiments are provided in the dependent claims.

20 In the following, embodiments of the present invention are described in detail with reference to the figures, in which:

Fig. 1 illustrates a video encoder according to an embodiment.

Fig. 2 illustrates a video decoder according to an embodiment.

Fig. 3 illustrates a system according to an embodiment.

Fig. 4 illustrates a viewport-dependent tile arrangements with overview.

Fig. 5 illustrates affine motion through sub-block divisioning.

Fig. 6 illustrates zooming motion with affine prediction.

Fig. 7 illustrates sub-block divisioning for affine prediction in different channels.

Fig. 8 shows a state-of-the art example bitstream order for such a case.

Fig. 9 illustrates a video encoder.

Fig. 10 illustrates a video decoder.

5 Fig. 11 illustrates the relationship between the reconstructed signal, i.e. the reconstructed picture, on the one hand, and the combination of the prediction residual signal as signaled in the data stream, and the prediction signal, on the other hand.

10

The following description of the figures starts with a presentation of a description of an encoder and a decoder of a block-based predictive codec for coding pictures of a video in order to form an example for a coding framework into which embodiments of the present invention may be built in. The respective encoder and decoder are described with respect 15 to Fig. 9 to Fig. 11. Thereinafter the description of embodiments of the concept of the present invention is presented along with a description as to how such concepts could be built into the encoder and decoder of Fig. 9 and Fig. 10, respectively, although the embodiments described with Fig. 1 to Fig. 3 and following, may also be used to form encoders and decoders not operating according to the coding framework underlying the 20 encoder and decoder of Fig. 9 and Fig. 10.

Fig. 9 shows a video encoder, an apparatus for predictively coding a picture 12 into a data stream 14 exemplarily using transform-based residual coding. The apparatus, or encoder, is indicated using reference sign 10. Fig. 10 shows a corresponding video decoder 20, i.e. 25 an apparatus 20 configured to predictively decode the picture 12' from the data stream 14 also using transform-based residual decoding, wherein the apostrophe has been used to indicate that the picture 12' as reconstructed by the decoder 20 deviates from picture 12 originally encoded by apparatus 10 in terms of coding loss introduced by a quantization of the prediction residual signal. Fig. 9 and Fig. 10 exemplarily use transform based 30 prediction residual coding, although embodiments of the present application are not restricted to this kind of prediction residual coding. This is true for other details described with respect to Fig. 9 and Fig. 10, too, as will be outlined hereinafter.

The encoder 10 is configured to subject the prediction residual signal to spatial-to-spectral 35 transformation and to encode the prediction residual signal, thus obtained, into the data stream 14. Likewise, the decoder 20 is configured to decode the prediction residual signal from the data stream 14 and subject the prediction residual signal thus obtained to spectral-to-spatial transformation.

Internally, the encoder 10 may comprise a prediction residual signal former 22 which generates a prediction residual 24 so as to measure a deviation of a prediction signal 26 from the original signal, i.e. from the picture 12. The prediction residual signal former 22 5 may, for instance, be a subtractor which subtracts the prediction signal from the original signal, i.e. from the picture 12. The encoder 10 then further comprises a transformer 28 which subjects the prediction residual signal 24 to a spatial-to-spectral transformation to obtain a spectral-domain prediction residual signal 24' which is then subject to quantization by a quantizer 32, also comprised by the encoder 10. The thus quantized 10 prediction residual signal 24" is coded into bitstream 14. To this end, encoder 10 may optionally comprise an entropy coder 34 which entropy codes the prediction residual signal as transformed and quantized into data stream 14. The prediction signal 26 is generated by a prediction stage 36 of encoder 10 on the basis of the prediction residual signal 24" encoded into, and decodable from, data stream 14. To this end, the prediction 15 stage 36 may internally, as is shown in Fig. 9, comprise a dequantizer 38 which dequantizes prediction residual signal 24" so as to gain spectral-domain prediction residual signal 24''", which corresponds to signal 24' except for quantization loss, followed by an inverse transformer 40 which subjects the latter prediction residual signal 24'''" to an inverse transformation, i.e. a spectral-to-spatial transformation, to obtain prediction 20 residual signal 24'''', which corresponds to the original prediction residual signal 24 except for quantization loss. A combiner 42 of the prediction stage 36 then recombines, such as by addition, the prediction signal 26 and the prediction residual signal 24''''' so as to obtain a reconstructed signal 46, i.e. a reconstruction of the original signal 12. Reconstructed signal 46 may correspond to signal 12'. A prediction module 44 of prediction stage 36 then 25 generates the prediction signal 26 on the basis of signal 46 by using, for instance, spatial prediction, i.e. intra-picture prediction, and/or temporal prediction, i.e. inter-picture prediction.

Likewise, decoder 20, as shown in Fig. 10, may be internally composed of components 30 corresponding to, and interconnected in a manner corresponding to, prediction stage 36. In particular, entropy decoder 50 of decoder 20 may entropy decode the quantized spectral-domain prediction residual signal 24" from the data stream, whereupon dequantizer 52, inverse transformer 54, combiner 56 and prediction module 58, interconnected and cooperating in the manner described above with respect to the 35 modules of prediction stage 36, recover the reconstructed signal on the basis of prediction residual signal 24" so that, as shown in Fig. 10, the output of combiner 56 results in the reconstructed signal, namely picture 12'.

Although not specifically described above, it is readily clear that the encoder 10 may set some coding parameters including, for instance, prediction modes, motion parameters and the like, according to some optimization scheme such as, for instance, in a manner optimizing some rate and distortion related criterion, i.e. coding cost. For example, 5 encoder 10 and decoder 20 and the corresponding modules 44, 58, respectively, may support different prediction modes such as intra-coding modes and inter-coding modes. The granularity at which encoder and decoder switch between these prediction mode types may correspond to a subdivision of picture 12 and 12', respectively, into coding segments or coding blocks. In units of these coding segments, for instance, the picture 10 may be subdivided into blocks being intra-coded and blocks being inter-coded. Intra-coded blocks are predicted on the basis of a spatial, already coded/decoded neighborhood of the respective block as is outlined in more detail below. Several intra-coding modes may exist and be selected for a respective intra-coded segment including directional or angular intra-coding modes according to which the respective segment is 15 filled by extrapolating the sample values of the neighborhood along a certain direction which is specific for the respective directional intra-coding mode, into the respective intra-coded segment. The intra-coding modes may, for instance, also comprise one or more further modes such as a DC coding mode, according to which the prediction for the respective intra-coded block assigns a DC value to all samples within the respective intra-coded segment, and/or a planar intra-coding mode according to which the prediction of 20 the respective block is approximated or determined to be a spatial distribution of sample values described by a two-dimensional linear function over the sample positions of the respective intra-coded block with driving tilt and offset of the plane defined by the two-dimensional linear function on the basis of the neighboring samples. Compared thereto, 25 inter-coded blocks may be predicted, for instance, temporally. For inter-coded blocks, motion vectors may be signaled within the data stream, the motion vectors indicating the spatial displacement of the portion of a previously coded picture of the video to which picture 12 belongs, at which the previously coded/decoded picture is sampled in order to obtain the prediction signal for the respective inter-coded block. This means, in addition to 30 the residual signal coding comprised by data stream 14, such as the entropy-coded transform coefficient levels representing the quantized spectral-domain prediction residual signal 24", data stream 14 may have encoded thereinto coding mode parameters for assigning the coding modes to the various blocks, prediction parameters for some of the blocks, such as motion parameters for inter-coded segments, and optional further 35 parameters such as parameters for controlling and signaling the subdivision of picture 12 and 12', respectively, into the segments. The decoder 20 uses these parameters to subdivide the picture in the same manner as the encoder did, to assign the same

prediction modes to the segments, and to perform the same prediction to result in the same prediction signal.

Fig. 11 illustrates the relationship between the reconstructed signal, i.e. the reconstructed picture 12', on the one hand, and the combination of the prediction residual signal 24''' as signaled in the data stream 14, and the prediction signal 26, on the other hand. As already denoted above, the combination may be an addition. The prediction signal 26 is illustrated in Fig. 11 as a subdivision of the picture area into intra-coded blocks which are illustratively indicated using hatching, and inter-coded blocks which are illustratively indicated not-hatched. The subdivision may be any subdivision, such as a regular subdivision of the picture area into rows and columns of square blocks or non-square blocks, or a multi-tree subdivision of picture 12 from a tree root block into a plurality of leaf blocks of varying size, such as a quadtree subdivision or the like, wherein a mixture thereof is illustrated in Fig. 11 in which the picture area is first subdivided into rows and columns of tree root blocks which are then further subdivided in accordance with a recursive multi-tree subdivisioning into one or more leaf blocks.

Again, data stream 14 may have an intra-coding mode coded thereinto for intra-coded blocks 80, which assigns one of several supported intra-coding modes to the respective intra-coded block 80. For inter-coded blocks 82, the data stream 14 may have one or more motion parameters coded thereinto. Generally speaking, inter-coded blocks 82 are not restricted to being temporally coded. Alternatively, inter-coded blocks 82 may be any block predicted from previously coded portions beyond the current picture 12 itself, such as previously coded pictures of a video to which picture 12 belongs, or picture of another view or an hierarchically lower layer in the case of encoder and decoder being scalable encoders and decoders, respectively.

The prediction residual signal 24''' in Fig. 11 is also illustrated as a subdivision of the picture area into blocks 84. These blocks might be called transform blocks in order to distinguish same from the coding blocks 80 and 82. In effect, Fig. 11 illustrates that encoder 10 and decoder 20 may use two different subdivisions of picture 12 and picture 12', respectively, into blocks, namely one subdivisioning into coding blocks 80 and 82, respectively, and another subdivision into transform blocks 84. Both subdivisions might be the same, i.e. each coding block 80 and 82, may concurrently form a transform block 84, but Fig. 11 illustrates the case where, for instance, a subdivision into transform blocks 84 forms an extension of the subdivision into coding blocks 80, 82 so that any border between two blocks of blocks 80 and 82 overlays a border between two blocks 84, or alternatively speaking each block 80, 82 either coincides with one of the transform blocks

84 or coincides with a cluster of transform blocks 84. However, the subdivisions may also be determined or selected independent from each other so that transform blocks 84 could alternatively cross block borders between blocks 80, 82. As far as the subdivision into transform blocks 84 is concerned, similar statements are thus true as those brought 5 forward with respect to the subdivision into blocks 80, 82, i.e. the blocks 84 may be the result of a regular subdivision of picture area into blocks (with or without arrangement into rows and columns), the result of a recursive multi-tree subdivisioning of the picture area, or a combination thereof or any other sort of blockation. Just as an aside, it is noted that blocks 80, 82 and 84 are not restricted to being of quadratic, rectangular or any other 10 shape.

Fig. 11 further illustrates that the combination of the prediction signal 26 and the prediction residual signal 24''' directly results in the reconstructed signal 12'. However, it should be noted that more than one prediction signal 26 may be combined with the prediction 15 residual signal 24''' to result into picture 12' in accordance with alternative embodiments.

In Fig. 11, the transform blocks 84 shall have the following significance. Transformer 28 and inverse transformer 54 perform their transformations in units of these transform blocks 84. For instance, many codecs use some sort of DST or DCT for all transform 20 blocks 84. Some codecs allow for skipping the transformation so that, for some of the transform blocks 84, the prediction residual signal is coded in the spatial domain directly. However, in accordance with embodiments described below, encoder 10 and decoder 20 are configured in such a manner that they support several transforms. For example, the 25 transforms supported by encoder 10 and decoder 20 could comprise:

- 25     ○ DCT-II (or DCT-III), where DCT stands for Discrete Cosine Transform
- DST-IV, where DST stands for Discrete Sine Transform
- DCT-IV
- DST-VII
- 30     ○ Identity Transformation (IT)

Naturally, while transformer 28 would support all of the forward transform versions of these transforms, the decoder 20 or inverse transformer 54 would support the corresponding backward or inverse versions thereof:

- 35     ○ Inverse DCT-II (or inverse DCT-III)
- Inverse DST-IV
- Inverse DCT-IV

- Inverse DST-VII
- Identity Transformation (IT)

5 The subsequent description provides more details on which transforms could be supported by encoder 10 and decoder 20. In any case, it should be noted that the set of supported transforms may comprise merely one transform such as one spectral-to-spatial or spatial-to-spectral transform.

10 As already outlined above, Fig. 9 to Fig. 11 have been presented as an example where the inventive concept described further below may be implemented in order to form specific examples for encoders and decoders according to the present application. Insofar, the encoder and decoder of Fig. 9 and Fig. 10, respectively, may represent possible implementations of the encoders and decoders described herein below. Fig. 9 and Fig. 10 are, however, only examples. An encoder according to embodiments of the 15 present application may, however, perform block-based encoding of a picture 12 using the concept outlined in more detail below and being different from the encoder of Fig. 9 such as, for instance, in that same is no video encoder, but a still picture encoder, in that same does not support inter-prediction, or in that the sub-division into blocks 80 is performed in a manner different than exemplified in Fig. 11. Likewise, decoders according to 20 embodiments of the present application may perform block-based decoding of picture 12' from data stream 14 using the coding concept further outlined below, but may differ, for instance, from the decoder 20 of Fig. 10 in that same is no video decoder, but a still picture decoder, in that same does not support intra-prediction, or in that same sub-divides picture 12' into blocks in a manner different than described with respect to Fig. 11 25 and/or in that same does not derive the prediction residual from the data stream 14 in transform domain, but in spatial domain, for instance.

30 In the following, a generic video encoder according to embodiments is described in Fig. 1, a generic video decoder according to embodiments is described in Fig. 2, and a generic system according to embodiments is described in Fig. 3.

Fig. 1 illustrates a generic video encoder 101 according to embodiments.

35 The video encoder 101 is configured for encoding a plurality of pictures of a video by generating an encoded video signal, wherein each of the plurality of pictures comprises original picture data.

The video encoder 101 comprises a data encoder 110 configured for generating the encoded video signal comprising encoded picture data, wherein the data encoder is configured to encode the plurality of pictures of the video into the encoded picture data.

5 Moreover, the video encoder 101 comprises an output interface 120 configured for outputting the encoded picture data of each of the plurality of pictures.

Fig. 2 illustrates a generic video decoder 151 according to embodiments.

10 The video decoder 151 is configured for decoding an encoded video signal comprising encoded picture data to reconstruct a plurality of pictures of a video.

The video decoder 151 comprises an input interface 160 configured for receiving the encoded video signal.

15 Moreover, the video decoder comprises a data decoder 170 configured for reconstructing the plurality of pictures of the video by decoding the encoded picture data.

Fig. 3 illustrates a generic system according to embodiments.

20 The system comprises the video encoder 101 of Fig. 1 and the video decoder 151 of Fig. 2.

25 The video encoder 101 is configured to generate the encoded video signal. The video decoder 151 is configured to decode the encoded video signal to reconstruct the picture of the video.

A first aspect of the invention is claimed in claims 1 to 15.

30 A second aspect of the invention is claimed in claims 16 to 30.

A third aspect of the invention is claimed in claims 31 to 45.

A fourth aspect of the invention is claimed in claims 46 to 58.

35 A fifth aspect of the invention is claimed in claims 59 to 71.

A sixth aspect of the invention is claimed in claims 72 to 90.

In the following, the first aspect of the invention is now described in detail.

5 In particular, the first aspect relates to Motion Vector wrap around.

When omnidirectional content is coded using certain projection formats such as the widely deployed ERF format (ERF = Equirectangular Projection), the samples on left and right boundary of the projected picture are actually neighboring in 3d-space after re-projection.

10 In order to leverage this characteristic in state-of-the-art video coding and prevent visual artefacts, the typical process of extrapolating image sample values on the left and right picture boundary is adjusted when motion compensated block-based inter-prediction reaches outside the picture boundary.

15 Instead of creating the traditional boundary padding sample values through perpendicular extrapolation of the last pixel column on the left and right side of the picture, a clipping operation is used in the derivation of reference sample positions that leads to a wrap around of motion vectors and reference blocks around a defined sample column as follows

20

```
xInti = sps_ref_wraparound_enabled_flag ?  
    ClipH( sps_ref_wraparound_offset, picW, ( xIntL + i - 3 ) ) :  
    Clip3( 0, picW - 1, xIntL + i - 3 )  
yInti = Clip3( 0, picH - 1, yIntL + i - 3 )
```

25

where ClipH ( o, W, x ) is defined as  

$$\begin{aligned} \text{mod}(x, o); & \text{ for } x < 0 \\ \text{mod}((x-W), o) + W - o; & \text{ for } x > W-1, \text{ or} \\ X; & \text{ otherwise} \end{aligned}$$

30

Clip3 is defined in [1] as:

$$\text{Clip3}(x, y, z) = \begin{cases} x & ; \quad z < x \\ y & ; \quad z > y \\ z & ; \quad \text{otherwise} \end{cases}$$

A problem arises when such content is further used in omnidirectional video services such as based on the OMAF viewport dependent profile using tiles (OMAF = Omnidirectional Media Format). In such services, a low-resolution variant of the ERP content version is 5 often mixed with other high-resolution fragments of the full content to create a viewport dependent stream. The necessity to arrange fragments of the high-resolution full scene and the low-resolution variant of the content onto a rectangular video frame, may lead to rotation of the low-resolution ERP content, by which the coding cannot leverage the above described characteristic to avoid visual artifacts. For instance, in Fig. 4, two arrangements 10 are showcased that feature a low-resolution ERP overview and high-resolution tiles. It can be seen that the ERP tile is either rotated or neighbored by tiles on the right side.

Therefore, the two tile arrangements illustrated in Fig. 4 pose requirements to the MV wrap around that are not satisfied by the state-of-the-art design.

15

Fig. 4 illustrates a viewport-dependent tile arrangements with overview according to an embodiment.

According to this aspect, the MV wrap around may, e.g., be adapted in the following ways to allow above tile arrangements. In one embodiment, a signaling `sps_ref_wraparound_idc` is used to differentiate the MV wrap around options to deal with 20 rotations as in the left-hand side of Fig. 4 as follows:

```
If ( sps_ref_wraparound_idc == 0 )
{
    xInti = ClipH( sps_ref_wraparound_offset, picW, ( xIntL + i - 3 ) )
25    yInti = Clip3( 0, picH - 1, yIntL + i - 3 )
}
else if ( sps_wraparound_idc == 1 )
{
    xInti = Clip3( 0, picW - 1, xIntL + i - 3 )
30    yInti = ClipH( sps_ref_wraparound_offset_second, picH, ( yIntL + i
        - 3 ) )
}
else if ( sps_wraparound_idc == 2 )
{
35    xInti = ClipH( sps_ref_wraparound_offset, picW, ( xIntL + i - 3 ) )
```

```
    yInti = ClipH( sps_ref_wraparound_offset_second, picH, ( yIntL + i  
    - 3 ) )  
}
```

5 In another embodiment, wrap around regions are defined to cover the scenario depicted on the right-hand side of Fig. 4.

In the following, the second aspect of the invention is now described in detail.

10

In particular, the second aspect relates to affine prediction runtime costs.

State of the art video codecs are typically designed to cover a broad range of applications, use cases and scenarios. E.g. a codec such as HEVC includes techniques targeted at 15 streaming, broadcast, real-time conversational or surveillance services. Each of those services typically operates at different resolutions that depend on the service constraints: broadcast typically uses higher resolutions than conversational and so on. While the numerous individual tools that a codec comprises are tuned to suit this broad range of associated requirements, there may be losses in efficiency or increased complexity at 20 each individual operation point.

For instance, video coding traditionally employs a translation-only motion model, i.e. rectangular blocks are displaced according to a two-dimensional motion vector to form a motion-compensated predictor for the current block. Such a model cannot express 25 rotational or zooming motion that is common in many video sequences and hence, there have been efforts to slightly extend the traditional translational motion model, e.g. as referred to as affine motion in JEM (JEM = Joint Exploration Model). As illustrated in Fig. 5, in this affine motion model, two (or three) so called control point motion vectors per block ( $v_0, v_1, \dots$ ) are used to model rotational and zooming motion. The control point 30 motion vectors are chosen from MV candidates of the adjacent blocks. The resulting motion compensated predictor is derived via computing a sub-block based motion vector field and rely on traditional translational motion compensation of rectangular sub-blocks.

Fig. 5 illustrates affine motion through sub-block divisioning. In particular, Fig. 5 exemplarily illustrates the resulting sub-block reference or predictor (dashed red) for the top-right sub-block (solid red).

While Fig. 5 represents a more or less simple rotational motion, depending on the control point motion vectors, it may happen that the individual sub-block references span over a wide distance in the picture, e.g. such as when representing a zooming motion. In such a case, the resulting sub-block references could be for instance be positioned as illustrated 5 in Fig. 6, exemplarily shown with a smaller block size that results in 2x2 sub-blocks.

In particular, Fig. 6 illustrates zooming motion with affine prediction.

In such a case, the decoder is required to fetch the individual sub-block references out of a greater total picture area than is covered by the complete current block. In order to cost-efficiently implement such an affine prediction mode, it is required to coordinate fetching of the individual sub-block references on currently available hardware. Such a coordination, can for instance be the joint fetching of the continuous reference picture area that comprises all sub-block references. Hence, extreme zooming motion can be expensive to implement on decoder side as efficient reference fetching strategies are precluded.

In order to limit worst case reference picture access without sacrificing substantial coding gains of the affine prediction mode, approaches are proposed in the state of the art 10 limiting the spread of the predicted block in the reference picture.

According to this aspect, such restrictions may, e.g., be derived depending on the allowed maximal memory bandwidth. This can be either specified in terms of the overall maximal memory bandwidth (e.g. with the specification of the level limit), approximately limiting the 15 memory bandwidth overhead (defining a maximal increase of the required reference memory access relative to usual translational prediction) or explicitly signaled by the encoder in high level parameters. The methods might be mixed.

In one embodiment, the limit on reference sub-block spread is enforced through 20 guaranteeing that a bounding box around the resulting sub-block reference blocks does not exceed a threshold. Such a threshold can be derived from block-size, picture resolution, the maximal allowed bandwidth or explicitly signaled in the high level parameters.

25 In another embodiment, the limit on reference sub-block spread is dependent on the coded picture resolution or defined by a level limit.

In the following, the third aspect of the invention is now described in detail.

In particular, the third aspect relates to affine prediction chroma handling.

Coding efficiency of the affine prediction model described above is anti-proportional to sub-block size as smaller sub-blocks allow to better approximate the affine motion with the underlying translatory block-based motion model. However, the fine granular reference picture access comes along at significant runtime expense.

When luma and chroma components of a video are unequally sampled, e.g. such as in the predominant consumer video format 4:2:0, prediction/reference blocks are typically sampled in the same ratio. For instance, when a 16x16 luma block is predicted through traditional methods, such as translatory motion compensated block-based prediction, the respective chroma block of 8x8 is predicted using the same luma block motion vector.

In context of the above affine prediction model, as the luma sub-block are already very fine granular or small, the state-of-the-art solution for handling chroma block bases chroma processing on the same luma block size regardless of format subsampling. Hence, 4x4 sub-blocks in the luma channel in affine prediction lead to 4x4 sub-block in the chroma channel which are thereby fewer in number in chroma channels than in luma channels as illustrated in Fig. 7.

Fig. 7 illustrates sub-block divisioning for affine prediction in different channels.

Hence, a way to derive the motion information for the corresponding chroma blocks is needed. Different flavors for solutions exists. For instance, the simplest state-of-the-art solution is to average the motion information mvLX[ ] of all four corresponding luma sub-blocks, e.g. as follows:

```
mvAvgLX = ( mvLX[ ( xSbIdx >> 1 << 1 ) ][ ( ySbIdx>>1<<1 ) ] +
            mvLX[ ( xSbIdx >> 1 << 1 ) + 1 ][ ( ySbIdx >> 1 << 1 ) ] +
            mvLX[ ( xSbIdx >> 1 << 1 ) ][ ( ySbIdx >> 1 << 1 ) + 1 ] +
            mvLX[ ( xSbIdx >> 1 << 1 ) + 1 ][ ( ySbIdx>>1<<1 ) + 1 ] + 2 ) >> 2
```

Another option is to only average the corresponding top-left and the bottom-right luma motion vector as follows:

```
mvAvgLX = ( mvLX[ ( xSbIdx >> 1 << 1 ) ][ ( ySbIdx>>1<<1 ) ] +
            mvLX[ ( xSbIdx >> 1 << 1 ) + 1 ][ ( ySbIdx>>1<<1 ) + 1 ] + 1 ) >> 1
```

Furthermore, symmetric rounding can be introduced as follows:

```

mvAvgLX = ( mvLX[ ( xSbIdx >> 1 << 1 ) ][ (ySbIdx>>1<<1) ] +
5      mvLX[ ( xSbIdx >> 1 << 1 ) + 1 ][ (ySbIdx>>1<<1) + 1 ] )
mvAvgLX[ 0 ] = ( mvAvgLX[ 0 ] >= 0 ?
                  ( mvAvgLX[ 0 ] + 1 ) >> 1 :
                  - ( ( - mvAvgLX[ 0 ] + 1 ) >>1 ) )
mvAvgLX[ 1 ] = ( mvAvgLX[ 1 ] >= 0 ?
10     ( mvAvgLX[ 1 ] + 1 ) >> 1 :
                  - ( ( - mvAvgLX[ 1 ] + 1 ) >> 1 ) )

```

However, a problem arises when chroma formats different from 4:2:0 are used where additional averaging schemes beyond the above state-of-the-art flavors are desirable. For 15 instance, when 4:4:4 chroma format is used, averaging should be avoided as each 4x4 chroma sub-block corresponds to exactly one 4x4 luma sub-block and the 4:2:2 chroma format requires averaging only in one dimension. Hence, according to this aspect, the correct luma blocks to involve in the averaging operation are derived through introducing the chroma subsampling ratio into the derivation of mvAvgLX. In one embodiment the 20 derivation is realized as follows

```

mvAvgLX = ( mvLX[ ( xSbIdx >> SubWidthC << SubWidthC ) ][ (ySbIdx>>
SubHeightC
25      << SubHeightC ) + SubWidthC ][ (ySbIdx>> SubWidthC << SubWidthC ) +
SubHeightC ] )
mvAvgLX[ 0 ] = ( mvAvgLX[ 0 ] >= 0 ?
                  ( mvAvgLX[ 0 ] + 1 ) >> 1 :
                  - ( ( - mvAvgLX[ 0 ] + 1 ) >>1 ) )
mvAvgLX[ 1 ] = ( mvAvgLX[ 1 ] >= 0 ?
30     ( mvAvgLX[ 1 ] + 1 ) >> 1 :
                  - ( ( - mvAvgLX[ 1 ] + 1 ) >> 1 ) )

```

where SubWidthC and SubHeightC are derived from the applied chroma format as follows:

Chroma Format	SubWidthC	SubHeightC
4:2:0	1	1
4:2:2	1	0
4:4:4	0	0

In the following, the fourth aspect of the invention is now described in detail.

5 In particular, the fourth aspect relates to interpolation filter overhead

Motion compensated inter-prediction can use sample values at sub-sample positions (e.g. half-pel, quarter-pel) which are interpolated through convolutional filters from sample values at integer sample positions. State-of-the-art codecs generally use a fixed tap filter

10 kernel, e.g. 8-tap, to generate such sub-pel sample values.

However, when sub-block based inter-prediction such as affine prediction or DMVR (decoder-side motion vector refinement) lead to relatively small luma and chroma sub-blocks such as of size 4x4, a memory bandwidth problem occurs. With a decreasing 15 (sub-)block size in relation to a fixed interpolation filter size, the overhead in terms of the total number of accessed samples for the generated reference luma sub-pel samples of the (sub-)block increases. Therefore, it has been proposed to use relatively shorter tap filter kernels for sub-pel interpolation of small sub-blocks such as 4x4 which shows a minimal coding penalty that decreases with increasing content resolution.

20

According to this aspect the selection of shorter tap interpolation filters for sub-pel interpolation is adapted dependent on the resolution of the coded content in order to benefit from the gains in terms of peak memory bandwidth reduction for high resolution content without sacrificing coding efficiency with lower resolution content.

25

In one embodiment, several sub-pel interpolation filters are available (e.g. bilinear, 4-tap, 6-tap, 8-tap) and the content resolution and block size are used to determine the applicable filter.

30 An implementation is made more expensive by every increase of the amount of separate filters that need to be implemented and kept at hand in a encoder or decoder. It is therefore advantageous to reduce the amount of separate filters that are to be implemented in parallel in a encoder or decoder.

For instance, it is state-of-the-art that the chroma components of a video signal use spatial subsampling, e.g. when using the 4:2:0 chroma format. In such a case, the chroma component typically also uses a shorter-tap sub-pel-interpolation filter than the luma component. Hence, such shorter-tap sub-pel interpolation filter need to be held ready by decoder/encoder implementations in addition to the traditional longer-tap sub-pel interpolation filter kernel used for luma components. In an embodiment, a reuse of the shorter-tap sub-pel interpolation filter is introduced in cases where sub-block processing requires a reduced-tap filter for reduction of peak memory bandwidth, e.g. dependent on coded picture size or by encoder choice and bitstream indication.

In the following, the fifth aspect of the invention is now described in detail.

In particular, the fifth aspect relates to bitstream property description and profile set operations

Typically, video streams are accompanied by signalling that describes their characteristics and allows identifying the type of capabilities required of a video decoder so that the bitstream can be decoded.

State-of-the-art video coding standards, such as AVC (Advanced Video Coding), HEVC include profiles and level indications into parameter sets in the bitstream that allow identifying which tools need to be supported and e.g. the resolutions limitations of a video coding sequence, so that it can be easily identified whether a decoder can decode a video sequence.

In terms of profiles, one could identify profiles that only support Intra coding tools, or Intra and Inter tools but only 8-bit configurations, or 10-bit configurations, layered or non-layered coding, etc.

In terms of levels (more related to bitrate, resolution and/or framerate) one could differentiate 1080p@30Hz, 4Kp@30 Hz, 4Kp@60 Hz, 8Kp@30 Hz, etc.

That information is typically used in video streaming systems for capability exchange so that the characteristics are exposed and different receivers can match their capabilities to the requirements to decode the offered bitstreams.

For instance, in DASH (Dynamic Adaptive Streaming over HTTP), the Media Presentation Description (MPD) may comprise several videos offered for the same content with corresponding different parameters. This is done by indicating in an attribute called @codec="xxx" where it is indicated, the codec, profile, level and so on. Similarly, in RTP

5 streaming (RTP = Realtime Transport Protocol) there is a Session Description Protocol (SDP), with which operation points for a session are negotiated. Then, the parties involved in the session agree on which codec, profile and level should be used to establish a session. Also, in Transport Stream, MPEG2-TS (MPEG = Moving Picture Experts Group; TS = Transport Stream) (heavily used in broadcast transmission) there are means to 10 expose these values to the end-device. This is done by including the corresponding video descriptor in a program table that identifies the codec, profile and level of a program (i.e. for instance a TV channel) and thus the adequate channel can be picked that is decodable by the end-device.

15 Currently, the properties included in the bitstream only apply to a Coded Video Sequence (CVS). A CVS starts with the first IRAP (Intra Random Access Point) and ends with an IRAP that has one of the following properties:

- It is an IDR (Instantaneous Decoder Refresh)
- It is a BLA (Broken Link Access) (only relevant for HEVC)
- It has an End Of Sequence (EOS) SEI message (SEI = Supplemental Enhancement Information).

20 In most of the video services a “session” may comprise several temporally continuous CVSs and therefore the parameters indicated into the parameter sets for AVC and HEVC are not sufficient, since they only describe the first CVS of all of the CVSs involved into the “session”. This issue has been solved in MPEG2-TS by the following text:

30 *“NOTE – In one or more sequences in the AVC video stream the level may be lower than the level signalled in the AVC video descriptor, while also a profile may occur that is a subset of the profile signalled in the AVC video descriptor. However, in the entire AVC video stream, only tools shall be used that are included in the profile signalled in the AVC video descriptor, if present. For example, if the main profile is signalled, then the baseline profile may be used in some sequences, but only using those tools that are in the main profile. If the sequence parameter sets in an AVC video stream signal different profiles, and no additional constraints are signalled, then the stream may need examination to determine which profile, if any, the entire stream conforms to. If an AVC video descriptor is to be associated with an AVC video stream that does not conform to a single profile,*

*then the AVC video stream must be partitioned into two or more sub-streams, so that AVC video descriptors can signal a single profile for each such sub-stream."*

In other words, the profile and level indicated in the descriptor is a profile that is a 5 superset of any profile used in the CVSs of the AVC video stream and the level is the highest level of any CVS.

In a similar manner, the profile and level indicated in MPD (Media Presentation Description) or SDP can be "higher" (meaning profile superset or more tools required than 10 the profile of each CVS) than the ones in the video bitstreams.

It would be desirable that the bitstreams themselves included such a signalling so that it is clear from the beginning which is the set of required tools to decode all the CVSs of a video "session".(Here, the term video session also, for example, comprises a TV channel 15 program). The signalling consists of a parameter set that applies to several CVSs, i.e. to a bitstream which

There are two cases of especial interest:

20 • CVSs with profiles for which there is no profile that is a superset of both at the same time.  
• CVSs that might change over time with respect to the number of layers.

One option would be to signal all tools required for decoding all CVS with a per tool flag, 25 but this would lead to a non-desirable solution since it is typically more convenient to work based on profiles which are easier to compare than multiple tools which can lead to an extremely high number of combinations.

More than one profile active within a bitstream could lead to the four following case. The 30 tools needed to decode the bitstream correspond to:

• The intersection of two profiles, where no profile exist that corresponds to the intersection.  
• The intersection of two profiles, where one profile is the intersection of two profiles.  
35 • The union of two profiles, where no profile exists that corresponds to the intersection.  
• The union of two profiles, where a profile exists that corresponds to the intersection.

For the cases that there is a profile that corresponds to all tools needed, a single profile indication would be enough.

5 However, for the case that no profile exists to describe all tools necessary, more than one profile should be signalled and intersection or union should be signalled to indicate whether supporting of a single profile is enough to decode the bitstream (intersection) or both profiles need to be supported to be able to decode the bitstream.

10

In the following, the sixth aspect of the invention is now described in detail.

In particular, the sixth aspect relates to buffering Period SEI and Picture Timing SEI message parsing dependencies

15

In existing designs like HEVC, syntax elements in Buffering Period SEI message (SEI = Supplemental Enhancement Information), Picture Timing SEI message and Decoding Unit SEI message depend on syntax elements that are carried in the SPS (Sequence Parameter Set). This dependency includes the number of bits which are used to code 20 specific syntax elements and also presence of syntax elements.

However, it is not desirable to put all Buffering Period/Picture Timing/ Decoding Unit related information into the same syntax location because:

25

1) Syntax elements currently in the SPS are useful for session/capability negotiation, which is done by exchanging parameter sets. Thus they cannot solely be signalled in the Buffering Period SEI message.

30

2) Syntax elements in the Buffering Period SEI message cannot be moved to the SPS, because activating a new SPS is only possible at IRAP pictures (IRAP = Intra Random Access Point) , while Buffering Periods can start at any picture associated with temporal sublayer 0. It is also undesirable to require activation of new parameter sets at every buffering period.

35

The above parsing dependency leads to a startup problem: Before the first VCL NAL unit of the video bitsream is parsed, there is no active SPS. Since SEI messages are sent before the first VCL NAL unit (NAL = network abstraction layer), they will arrive before the SPS is activated.

Also, in out-of-band signaling of parameter sets, a in-the-middle device such as a splicer interested in understanding the HRD parameters of a bitstream, e.g. for splicing bitstreams, may not be able to parse the respective SEI messages, because the relate 5 parameter sets are not included in the bitstream.

Also for a simple in-the-middle device such as a splicer it may be hard to parse through the whole SPS until it reaches the HRD parameters in VUI.

10 Also, the order of NAL units is not explicitly constrained to require the SPS to be send before the Buffering Period SEI message that refers to the SPS. In such situations, a decoder may have to store the respective SEI messages in bitstream format (without being able to parse it), until the SPS arrives and is activated. Only then, the SEI messages can be parsed.

15 Fig. 8 shows a state-of-the art example bitstream order for such a case.

In an embodiment, parsing of the respective SEI messages is made independent of the SPS in the following way:

20 All syntax elements, which

- specify the length of another syntax element in the respective SEI messages, or
- are not required for session/capability negotiation (sub-picture clock tick divisor)

25 are moved into the Buffering Period SEI message.

The length of all syntax elements in the Buffering Period SEI is completely independent of other syntax structures. The length of syntax elements in the Timing SEI message and 30 Decoding Unit SEI message may be dependent on syntax elements in the Buffering Period SEI message, but not dependent on any parameter set values.

The presence flags for specific HRD parameters (NAL, VCL, DU, etc.), the count of CPB parameters (vui\_cpb\_cnt\_minus1[i] and bp\_cpb\_cnt\_minus1) and other syntax sections 35 are duplicated in parameter sets and the Buffering Period SEI message, and this aspect comprises to constrain the value of these duplicated syntax elements to be equal in both locations.

Picture Timing SEI message and Decoding Unit SEI message are made independent by moving all syntax elements, that indicate presence of other syntax elements into the respective SEI messages (e.g. frame\_field\_info\_present\_flag), or duplicating it.

5 As part of this aspect, to achieve bitstream conformance, the values of above syntax elements shall be equal to the respective values in the parameter sets.

As part of this aspect, tick\_devisors (indicating the sub-picture timing granularity) in buffering periods are to have the same value unless concatenation\_flag is equal to 1 (i.e.

10 splicing occurred).

As already outlined, in embodiments, it is proposed to add HRD parameters, Buffering Period, Picture Timing, and Decoding Unit information SEI messages to the VVC specification. General syntax and semantics are based on the corresponding HEVC

15 structures. Parsing of SEI messages is made possible independent of the presence of the SPS.

As outlined above, for checking bitstream conformance, a buffering model is required. The VVC buffering model may, e.g., be based on the Hypothetical Reference Decoder (HRD)

20 specification that was used in HEVC with some simplifications.

HRD parameters are present in the SPS VUI. Buffering Period SEI, Picture Timing SEI and Decoding Unit information SEI messages were added to the Access Units that they referred to.

25

In HEVC a parsing dependency exists between SPS and the HRD related SEI messages. Although the semantics allowed only referring to a SPS that is active in the same Access unit, the bitstream order allowed situations, where a SEI could arrive before the SPS. In such cases the coded SEI messages needed to be stored until the correct SPS arrives.

30

Another situation, in which this parsing dependency is problematic, are network elements and splicers, especially, when parameter sets are exchanged out of band (e.g. in session negotiation). In such cases, the parameter sets may not be included in the bitstream at all, so that the network element or splicer could not decode the buffering/timing information at 35 all. It still may be interesting to these elements, to recognize values like decoding times and frame/field information.

In embodiments, it is proposed to make parsing of Buffering Period, Picture Timing, and Decoding Unit information SEI messages independent from the SPS VUI/HRD parameters by:

5

- Moving all syntax elements that indicate the length or presence of other syntax elements from VUI into the Buffering Period SEI message.#
- Duplicating syntax elements, that are required in both locations. The values of these syntax elements are constrained to be identical.

10

Design decisions:

15

- Not all syntax elements should be moved from VUI to the Buffering Period SEI, since parameter sets are usually exchanged for session negotiation. So parameters that are important for session negotiation are kept in VUI.
- Moving all Buffering Period parameters into SPS is not possible, since Buffering Periods may start at non-IRAP pictures, where activating a new SPS would not be possible.
- A dependency between Buffering Period SEI and (Picture Timing SEI or Decoding Unit SEI) is not considered harmful, because there is also a logical dependency between these messages.

20

A syntax example based on HEVC syntax is given below.

25

The following syntax diagrams are based on the HEVC syntax with changes marked in red. Semantics are generally moved to the new locations of the syntax elements (with adaptation to the syntax element name). Whenever syntax elements are duplicated, a bitstream constraint is added that these syntax elements shall have the same value.

## VUI/HRD parameters:

...	
<b>vui_frame_field_info_present_flag</b>	
...	
<b>vui_timing_info_present_flag</b>	u(1)
if( vui_timing_info_present_flag ) {	
<b>vui_num_units_in_tick</b>	u(32)
<b>vui_time_scale</b>	u(32)
<b>vui_poc_proportional_to_timing_flag</b>	u(1)
if( vui_poc_proportional_to_timing_flag )	
<b>vui_num_ticks_poc_diff_one_minus1</b>	ue(v)
<b>vui_hrd_parameters_present_flag</b>	u(1)
if( vui_hrd_parameters_present_flag )	
<b>hrd_parameters( 1, sps_max_sub_layers_minus1 )</b>	
}	
...	u(1)

hrd_parameters( commonInfPresentFlag, maxNumSubLayersMinus1 ) {	Descriptor
if( commonInfPresentFlag ) {	
vui_nal_hrd_parameters_present_flag	u(1)
vui_vcl_hrd_parameters_present_flag	u(1)
if( vui_nal_hrd_parameters_present_flag    vui_vcl_hrd_parameters_present_flag ) {	
vui_sub_pic_hrd_params_present_flag	u(1)
if( sub_pic_hrd_params_present_flag ) {	
tick_divisor_minus2	u(8)
du_cpb_removal_delay_increment_length_minus1	u(5)
sub_pic_cpb_params_in_pic_timing_sei_flag	u(1)
dpb_output_delay_du_length_minus1	u(5)
}	
bit_rate_scale	u(4)
cpb_size_scale	u(4)
if( vui_sub_pic_hrd_params_present_flag )	
cpb_size_du_scale	u(4)
initial_cpb_removal_delay_length_minus1	u(5)
au_cpb_removal_delay_length_minus1	u(5)
dpb_output_delay_length_minus1	u(5)
}	
}	
for( i = 0; i <= maxNumSubLayersMinus1; i++ ) {	
fixed_pic_rate_general_flag[ i ]	u(1)
if( !fixed_pic_rate_general_flag[ i ] )	
fixed_pic_rate_within_cvs_flag[ i ]	u(1)
if( fixed_pic_rate_within_cvs_flag[ i ] )	
elemental_duration_in_tc_minus1[ i ]	ue(v)
else	
low_delay_hrd_flag[ i ]	u(1)
if( !low_delay_hrd_flag[ i ] )	
vui_cpb_cnt_minus1[ i ]	ue(v)
if( nal_hrd_parameters_present_flag )	
sub_layer_hrd_parameters( i )	
if( vcl_hrd_parameters_present_flag )	
sub_layer_hrd_parameters( i )	
}	
}	

Buffering Period SEI message:

```

buffering_period( payloadSize ) {
    bp_seq_parameter_set_id
    bp_nal_hrd_parameters_present_flag
    bp_vcl_hrd_parameters_present_flag
    if( bp_nal_hrd_parameters_present_flag || bp_vcl_hrd_parameters_present_flag ) {
        bp_sub_pic_hrd_params_present_flag
        if( bp_sub_pic_hrd_params_present_flag ) {
            tick_divisor_minus2
            du_cpb_removal_delay_increment_length_minus1
            sub_pic_cpb_params_in_pic_timing_sei_flag
            dpb_output_delay_du_length_minus1
        }
        initial_cpb_removal_delay_length_minus1
        au_cpb_removal_delay_length_minus1
        dpb_output_delay_length_minus1
    }
    if( !bp_sub_pic_hrd_params_present_flag )
        irap_cpb_params_present_flag
    if( irap_cpb_params_present_flag ) {
        cpb_delay_offset
        dpb_delay_offset
    }
    concatenation_flag
    au_cpb_removal_delay_delta_minus1
    bp_cpb_cnt_minus1
    if( bp_nal_hrd_parameters_present_flag NalHrdBpPresentFlag ) {
        for( i = 0; i < bp_cpb_cnt_minus1 + 1 CpbCnt; i++ ) {
            nal_initial_cpb_removal_delay[ i ]
            nal_initial_cpb_removal_offset[ i ]
            if( sub_pic_hrd_params_present_flag || irap_cpb_params_present_flag ) {
                nal_initial_alt_cpb_removal_delay[ i ]
                nal_initial_alt_cpb_removal_offset[ i ]
            }
        }
    }
    if( bp_vcl_hrd_parameters_present_flag VclHrdBpPresentFlag ) {
        for( i = 0; i < bp_cpb_cnt_minus1 + 1 CpbCnt; i++ ) {
            vcl_initial_cpb_removal_delay[ i ]
            vcl_initial_cpb_removal_offset[ i ]
            if( sub_pic_hrd_params_present_flag || irap_cpb_params_present_flag ) {
                vcl_initial_alt_cpb_removal_delay[ i ]
                vcl_initial_alt_cpb_removal_offset[ i ]
            }
        }
    }
    if( payload_extension_present() )
        use_alt_cpb_params_flag
}

```

## Picture Timing SEI message:

5

pic_timing( payloadSize ) {	Descriptor
frame_field_info_present_flag	
if( frame_field_info_present_flag ) {	
pic_struct	u(4)
source_scan_type	u(2)
duplicate_flag	u(1)
}	
if( CpbDpbDelaysPresentFlag ) {	
au_cpb_removal_delay_minus1	u(v)
pic_dpb_output_delay	u(v)
if( sub_pic_hrd_params_present_flag )	
pic_dpb_output_du_delay	u(v)
if( sub_pic_hrd_params_present_flag && sub_pic_cpb_params_in_pic_timing_sei_flag ) {	&&
num_decoding_units_minus1	ue(v)
du_common_cpb_removal_delay_flag	u(1)
if( du_common_cpb_removal_delay_flag )	
du_common_cpb_removal_delay_increment_minus1	u(v)
for( i = 0; i <= num_decoding_units_minus1; i++ ) {	
num_nalus_in_du_minus1[ i ]	ue(v)
if( !du_common_cpb_removal_delay_flag && i < num_decoding_units_minus1 )	
du_cpb_removal_delay_increment_minus1[ i ]	u(v)
}	
}	
}	
}	

Decoding Unit Information SEI message:

decoding_unit_info( payloadSize ) {	Descriptor
<b>decoding_unit_idx</b>	ue(v)
if( !sub_pic_cpb_params_in_pic_timing_sei_flag )	
<b>du_spt_cpb_removal_delay_increment</b>	u(v)
<b>dpb_output_du_delay_present_flag</b>	u(1)
if( dpb_output_du_delay_present_flag )	
<b>pic_spt_dpb_output_du_delay</b>	u(v)
}	

5

Although some aspects have been described in the context of an apparatus, it is clear that these aspects also represent a description of the corresponding method, where a block or device corresponds to a method step or a feature of a method step. Analogously, aspects described in the context of a method step also represent a description of a corresponding

10 block or item or feature of a corresponding apparatus. Some or all of the method steps may be executed by (or using) a hardware apparatus, like for example, a microprocessor, a programmable computer or an electronic circuit. In some embodiments, one or more of the most important method steps may be executed by such an apparatus.

15 Depending on certain implementation requirements, embodiments of the invention can be implemented in hardware or in software or at least partially in hardware or at least partially in software. The implementation can be performed using a digital storage medium, for example a floppy disk, a DVD, a Blu-Ray, a CD, a ROM, a PROM, an EPROM, an EEPROM or a FLASH memory, having electronically readable control signals stored 20 thereon, which cooperate (or are capable of cooperating) with a programmable computer system such that the respective method is performed. Therefore, the digital storage medium may be computer readable.

25 Some embodiments according to the invention comprise a data carrier having electronically readable control signals, which are capable of cooperating with a programmable computer system, such that one of the methods described herein is performed.

30 Generally, embodiments of the present invention can be implemented as a computer program product with a program code, the program code being operative for performing

one of the methods when the computer program product runs on a computer. The program code may for example be stored on a machine readable carrier.

Other embodiments comprise the computer program for performing one of the methods

5 described herein, stored on a machine readable carrier.

In other words, an embodiment of the inventive method is, therefore, a computer program having a program code for performing one of the methods described herein, when the computer program runs on a computer.

10

A further embodiment of the inventive methods is, therefore, a data carrier (or a digital storage medium, or a computer-readable medium) comprising, recorded thereon, the computer program for performing one of the methods described herein. The data carrier, the digital storage medium or the recorded medium are typically tangible and/or 15 non-transitory.

20

A further embodiment of the inventive method is, therefore, a data stream or a sequence of signals representing the computer program for performing one of the methods described herein. The data stream or the sequence of signals may for example be configured to be transferred via a data communication connection, for example via the 25 Internet.

25

A further embodiment comprises a processing means, for example a computer, or a programmable logic device, configured to or adapted to perform one of the methods described herein.

A further embodiment comprises a computer having installed thereon the computer program for performing one of the methods described herein.

30

A further embodiment according to the invention comprises an apparatus or a system configured to transfer (for example, electronically or optically) a computer program for performing one of the methods described herein to a receiver. The receiver may, for example, be a computer, a mobile device, a memory device or the like. The apparatus or system may, for example, comprise a file server for transferring the computer program to 35 the receiver.

In some embodiments, a programmable logic device (for example a field programmable gate array) may be used to perform some or all of the functionalities of the methods

described herein. In some embodiments, a field programmable gate array may cooperate with a microprocessor in order to perform one of the methods described herein. Generally, the methods are preferably performed by any hardware apparatus.

5 The apparatus described herein may be implemented using a hardware apparatus, or using a computer, or using a combination of a hardware apparatus and a computer.

The methods described herein may be performed using a hardware apparatus, or using a computer, or using a combination of a hardware apparatus and a computer.

10

The above described embodiments are merely illustrative for the principles of the present invention. It is understood that modifications and variations of the arrangements and the details described herein will be apparent to others skilled in the art. It is the intent, therefore, to be limited only by the scope of the impending patent claims and not by the 15 specific details presented by way of description and explanation of the embodiments herein.

References

[1] ISO/IEC, ITU-T. High efficiency video coding. ITU-T Recommendation H.265 | ISO/IEC 23008 10 (HEVC), edition 1, 2013; edition 2, 2014.

Claims

1. A video decoder (151) for decoding an encoded video signal comprising encoded picture data to reconstruct a plurality of pictures of a video, wherein the video decoder (151) comprises:

5 an input interface (160) configured for receiving the encoded video signal, and

10 a data decoder (170) configured for reconstructing the plurality of pictures of the video by decoding the encoded picture data,

15 wherein the data decoder (170) is configured to determine a reconstruct sample of a subsequent picture of the plurality of pictures depending on a current picture of the plurality of pictures, wherein the data decoder (170) is configured to determine a reference position of a reference sample within the current picture,

wherein the data decoder (170) is configured to decode a value of a wraparound parameter encoded within the encoded video signal,

20 wherein the data decoder (170) is configured to determine the reference position of the reference sample depending on a position of the reconstruct sample, depending on a motion vector and depending on the value of the wraparound parameter,

25 wherein the data decoder (170) is configured to determine the reconstruct sample of the subsequent picture depending on said reference sample being located at the reference position.

30 2. A video decoder (151) according to claim 1,

35 wherein the current picture has a first vertical border at a lower-bound x-value, wherein the current sample has a second vertical border at an upper-bound x-value, the upper-bound x-value being greater than the lower-bound x-value,

wherein the current picture has a first horizontal border at a lower-bound y-value, wherein the current sample has a second horizontal border at an upper-bound y-value, the upper-bound y-value being greater than the lower-bound y-value, and

wherein the data decoder (170) is configured to determine the reference position depending on at least one of the lower-bound x-value, the upper-bound x-value, the lower-bound y-value and the upper-bound y-value.

5 3. A video decoder (151) according to claim 2,

wherein the reference position has an x-value and a y-value,

10 wherein the data decoder (170) is configured to determine an intermediate position having an x-value and a y-value depending on the position of the reconstruct sample to determine and depending on the motion vector,

15 wherein, if the x-value of the intermediate position is smaller than the lower-bound x-value or greater than the upper-bound x-value, the data decoder (170) is configured to determine the reference position depending on the x-value of the intermediate position and depending on the value of the wraparound parameter such that the x-value of the reference position is greater than or equal to the lower-bound x-value and such that the x-value of the reference position is smaller than or equal to the upper-bound x-value, and

20 wherein, if the y-value of the intermediate position is smaller than the lower-bound y-value or greater than the upper-bound y-value, the data decoder (170) is configured to determine the reference position depending on the y-value of the intermediate position and depending on the value of the wraparound parameter such that the y-value of the reference position is greater than or equal to the lower-bound y-value and such that the y-value of the reference position is smaller than or equal to the upper-bound y-value.

25 4. A video decoder (151) according to claim 3,

30 wherein if the x-value of the intermediate position is greater than or equal to the lower-bound x-value and if the if the x-value of the intermediate position is smaller than or equal to the upper-bound x-value, the data decoder (170) is configured to determine the reference position such that the x-value of the reference position is equal to the x-value of the intermediate position,

35 wherein if the y-value of the intermediate position is greater than or equal to the lower-bound y-value and if the if the y-value of the intermediate position is smaller

than or equal to the upper-bound y-value, the data decoder (170) is configured to determine the reference position such that the y-value of the reference position is equal to the y-value of the intermediate position.

5 5. A video decoder (151) according to claim 3 or 4,

wherein, if the wraparound\_parameter has a first value, and if the y-value of the intermediate position is smaller than the lower-bound y-value, the data decoder (170) is configured to determine the reference position such that the y-value of the 10 reference position is equal to the lower-bound y-value,

wherein, if the wraparound\_parameter has the first value, and if the y-value of the intermediate position is greater than the upper-bound y-value, the data decoder (170) is configured to determine the reference position such that the y-value of the 15 reference position is equal to the upper-bound y-value, and

wherein, if the wraparound\_parameter has a second value, and if the y-value of the intermediate position is smaller than the lower-bound y-value or is greater than the upper-bound y-value, the data decoder (170) is configured to determine the 20 reference position such that the y-value of the reference position is greater than the lower-bound y-value and smaller than the upper-bound y-value.

6. A video decoder (151) according to 5,

25 wherein, if the wraparound\_parameter has a third value, and if the y-value of the intermediate position is smaller than the lower-bound y-value or is greater than the upper-bound y-value, the data decoder (170) is configured to determine the reference position such that the y-value of the reference position is greater than the lower-bound y-value and smaller than the upper-bound y-value

30 wherein, if the wraparound\_parameter has the second value, and if the x-value of the intermediate position is smaller than the lower-bound x-value, the data decoder (170) is configured to determine the reference position such that the x-value of the reference position is equal to the lower-bound x-value,

35 wherein, if the wraparound\_parameter has the second value, and if the x-value of the intermediate position is greater than the upper-bound x-value, the data

decoder (170) is configured to determine the reference position such that the x-value of the reference position is equal to the upper-bound x-value, and

5 wherein, if the wraparound\_parameter has the first value or the third value, and if the x-value of the intermediate position is smaller than the lower-bound x-value or is greater than the upper-bound x-value, the data decoder (170) is configured to determine the reference position such that the x-value of the reference position is greater than the lower-bound x-value and smaller than the upper-bound x-value.

10 7. A video decoder (151) according to claim 6,

15 wherein, if the wraparound\_parameter has the first value or the third value, and if the x-value of the intermediate position is smaller than the lower-bound x-value or is greater than the upper-bound x-value, the data decoder (170) is configured to determine the reference position depending on a first offset parameter, and

20 wherein, if the wraparound\_parameter has the second value or the third value, and if the y-value of the intermediate position is smaller than the lower-bound y-value or is greater than the upper-bound y-value, the data decoder (170) is configured to determine the reference position depending on a second offset parameter.

8. A video decoder (151) according to claim 7,

25 wherein the data decoder (170) is configured to determine the reference position based on:

```
If ( sps_ref_wraparound_idc == 0 )
{
    xInti =
        ClipH( sps_ref_wraparound_offset, picW, ( xIntL + i - 3 ) )
    yInti = Clip3( 0, picH - 1, yIntL + i - 3 )
}
else if ( sps_wraparound_idc == 1 )
{
    xInti = Clip3( 0, picW - 1, xIntL + i - 3 )
    yInti = ClipH( sps_ref_wraparound_offset_second, picH, ( yIntL
        + i - 3 ) )
}
```

```
else if ( sps_wraparound_idc == 2 )
{
    xInti =
        ClipH( sps_ref_wraparound_offset, picW, ( xIntL + i - 3 ) )
5        yInti = ClipH( sps_ref_wraparound_offset_second, picH, ( yIntL
+ i - 3 ) )
}
```

wherein `sps_ref_wraparound_idc` indicates the wraparound parameter with 0 being the first value, with 1 being the second value and with 2 being the third value of the wraparound parameter,

wherein `xInti` indicates the x-value of the reference position, wherein `yInti` indicates the y-value of the reference position,

15 wherein `xIntL` indicates the x-value of the intermediate position, wherein `yIntL` indicates the y-value of the intermediate position,

20 wherein `picW - 1` indicates the upper-bound x-value and 0 indicates the lower-bound x-value,

wherein `picH - 1` indicates the upper-bound y-value and 0 indicates the lower-bound y-value,

25 wherein `sps_ref_wraparound_offset` indicates the first offset parameter,

wherein `sps_ref_wraparound_offset_second` indicates the second offset parameter,

30 wherein

ClipH ( o, W, x ) is defined as  
mod(x,o); for x<0  
mod((x-W),o)+W-o; for x > W-1, or  
35 x; otherwise

wherein mod is a modulo operation.

9. A video encoder (101) for encoding a plurality of pictures of a video by generating an encoded video signal, wherein each of the plurality of pictures comprises original picture data, wherein the video encoder (101) comprises:

5 a data encoder (110) configured for generating the encoded video signal comprising encoded picture data, wherein the data encoder (110) is configured to encode the plurality of pictures of the video into the encoded picture data, and

10 an output interface (120) configured for outputting the encoded picture data of each of the plurality of pictures,

wherein the data encoder (110) is configured to generate the encoded video signal comprising an encoding of a motion vector and an encoding of a value of a wraparound parameter.

15

10. A video encoder (101) according to claim 9,

wherein the data decoder (170) is configured to generate the encoded video signal comprising an encoding of a first offset parameter and/or of a second offset 20 parameter such that the decoder is able to determine the reference position of the reference sample within the current picture depending on the first offset parameter and/or the second offset parameter.

25

11. A method for decoding an encoded video signal comprising encoded picture data to reconstruct a plurality of pictures of a video, wherein the method for decoding comprises:

receiving the encoded video signal, and

30

reconstructing the plurality of pictures of the video by decoding the encoded picture data,

wherein the reconstructing the plurality of pictures comprises decoding a value of a wraparound parameter encoded within the encoded video signal,

35

wherein reconstructing the plurality of pictures comprises determining a reconstruct sample of a subsequent picture of the plurality of pictures depending on a current picture of the plurality of pictures, wherein the method comprises determining a

reference position of a reference sample within the current picture, wherein determining the reference position of the reference sample is conducted depending on a position of the reconstruct sample, depending on a motion vector and depending on the value of the wraparound parameter, and

5

wherein determining the reconstruct sample of the subsequent picture is conducted depending on said reference sample being located at the reference position.

10 12. A method for encoding a plurality of pictures of a video by generating an encoded video signal, wherein each of the plurality of pictures comprises original picture data, wherein the method comprises:

15 generating the encoded video signal comprising encoded picture data, wherein generating the encoded video signal comprises encoding the plurality of pictures of the video into the encoded picture data, and

outputting the encoded picture data of each of the plurality of pictures,

20 wherein generating the encoded video signal is conducted such that the encoded video signal comprises an encoding of a motion vector and an encoding of a value of a wraparound parameter.

25 13. A computer program for implementing the method of claim 11 or 12 when being executed on a computer or signal processor.

14. An encoded video signal encoding a picture, wherein the encoded video signal comprises encoded picture data encoding the picture,

30 wherein the encoded video signal comprises an encoding of a motion vector and an encoding of a value of a wraparound parameter such that a decoder is able to determine a reference position of a reference sample within a current picture of the plurality of pictures depending on a position of a reconstruct sample within a subsequent picture of the plurality of pictures, depending on the motion vector and depending on the value of the wraparound parameter.

35 15. A system comprising:

the video encoder (101) according to claim 9 or 10, and

the video decoder (151) according to one of claims 1 to 8,

5 wherein the video encoder (101) configured to generate the encoded video signal, and

wherein the video decoder (151) is configured to decode the encoded video signal to reconstruct the picture of the video.

10

16. A video encoder (101) for encoding a plurality of pictures of a video by generating an encoded video signal, wherein each of the plurality of pictures comprises original picture data, wherein the video encoder (101) comprises:

15

a data encoder (110) configured for generating the encoded video signal comprising encoded picture data, wherein the data encoder (110) is configured to encode the plurality of pictures of the video into the encoded picture data, and

20

an output interface (120) configured for outputting the encoded picture data of each of the plurality of pictures,

25

wherein the data encoder (110) is configured to generate the encoded video signal by encoding restriction information depending on an allowed maximum reference picture memory bandwidth or depending on an allowed maximal bounding box around resulting reference sub blocks or depending on a limit of a reference sub-block spread.

17. A video encoder (101) according to claim 16,

30

wherein the data encoder (110) is configured to encode the restriction information which comprises an indication of a limit for a maximal bandwidth overhead by defining a maximal increase of required reference memory access relative to usual translational prediction.

35

18. A video encoder (101) according to claim 16 or 17,

wherein the data encoder (110) is configured to encode the restriction information which comprises a threshold for a size of a bounding box around sub-block reference blocks.

5 19. A video encoder (101) according to claim 18,

wherein the threshold depends on a picture resolution and/or on a maximal allowed bandwidth.

10 20. A video encoder (101) according to one of claims 16 to 19,

wherein the data encoder (110) is configured to encode the restriction information which comprises a limit on a reference sub-block spread.

15 21. A video decoder (151) for decoding an encoded video signal comprising encoded picture data to reconstruct a plurality of pictures of a video, wherein the video decoder (151) comprises:

an input interface (160) configured for receiving the encoded video signal, and

20 a data decoder (170) configured for reconstructing the plurality of pictures of the video by decoding the encoded picture data,

25 wherein the data decoder (170) is configured to decode from the encoded video signal restriction information depending on an allowed maximum memory bandwidth or depending on an allowed maximal bounding box around resulting reference sub blocks or depending on a limit of a reference sub-block spread, and

30 wherein the data decoder (170) is configured to reconstruct the plurality of pictures of the video depending on the restriction information.

22. A video decoder (151) according to claim 21,

35 wherein the data decoder (170) is configured to decode from the encoded video signal the restriction information which comprises an indication of a limit for a maximal bandwidth overhead by defining a maximal increase of required reference memory access relative to usual translational prediction.

23. A video decoder (151) according to claim 21 or 22,

wherein the data decoder (170) is configured to decode from the encoded video signal the restriction information which comprises a threshold for a size of a bounding box around sub-block reference blocks.

24. A video decoder (151) according to claim 23,

wherein the threshold depends on a picture resolution and/or on a maximal allowed bandwidth.

25. A video decoder (151) according to one of claims 21 to 24,

wherein the data decoder (170) is configured to decode from the encoded video signal the restriction information which comprises a limit on a reference sub-block spread which is dependent on a coded picture resolution or which is dependent on a level limit.

26. A method for encoding a plurality of pictures of a video by generating an encoded video signal, wherein each of the plurality of pictures comprises original picture data, wherein the method comprises:

generating the encoded video signal comprising encoded picture data, wherein generating the encoded video signal comprises encoding the plurality of pictures of the video into the encoded picture data, and

outputting the encoded picture data of each of the plurality of pictures,

wherein generating the encoded video signal comprises encoding restriction information depending on an allowed maximum memory bandwidth or depending on an allowed maximal bounding box around resulting reference sub blocks or depending on a limit of a reference sub-block spread.

27. A method for decoding an encoded video signal comprising encoded picture data to reconstruct a plurality of pictures of a video, wherein the method comprises:

receiving the encoded video signal, and

reconstructing the plurality of pictures of the video by decoding the encoded picture data,

wherein the method further comprises decoding from the encoded video signal restriction information depending on an allowed maximum memory bandwidth or depending on an allowed maximal bounding box around resulting reference sub blocks or depending on a limit of a reference sub-block spread, and

wherein reconstructing the plurality of pictures of the video is conducted depending on the restriction information.

28. A computer program for implementing the method of claim 26 or 27 when being executed on a computer or signal processor.

15 29. An encoded video signal encoding a picture, wherein the encoded video signal comprises encoded picture data encoding the picture,

wherein the encoded video signal comprises restriction information depending on an allowed maximum memory bandwidth or depending on an allowed maximal bounding box around resulting reference sub blocks or depending on a limit of a reference sub-block spread.

30. A system comprising:

25 the video encoder (101) according to claim 16 or 20, and

the video decoder (151) according to one of claims 21 to 25,

wherein the video encoder (101) configured to generate the encoded video signal, and

wherein the video decoder (151) is configured to decode the encoded video signal to reconstruct the picture of the video.

35 31. A video decoder (151) for decoding an encoded video signal comprising encoded picture data to reconstruct a plurality of pictures of a video, wherein the video decoder (151) comprises:

an input interface (160) configured for receiving the encoded video signal, and  
a data decoder (170) configured for reconstructing the plurality of pictures of the  
video by decoding the encoded picture data,

5

wherein the data decoder (170) is configured to decode chroma format information  
encoded within the encoded video signal,

10

wherein the data decoder (170) is configured to determine motion information for a  
chroma subblock of a plurality of chroma subblocks from motion information of one  
or more of a plurality of luma subblocks depending on the chroma format  
information,

15

wherein the data decoder (170) is configured to reconstruct the plurality of pictures  
depending on the motion information of the one or more of the luma subblocks and  
depending on the motion information for the chroma subblock.

32. A video decoder (151) according to claim 31,

20

wherein the data decoder (170) is configured to determine a motion vector for said  
chroma subblock as the motion information for the chroma subblock,

25

wherein, if the chroma format information indicates a first format, the data decoder  
(170) is configured to determine the motion vector for said chroma subblock  
depending on a motion vector of exactly one of the luma subblocks, and

30

wherein, if the chroma format information indicates a format being different from  
the first format, the data decoder (170) is configured to determine the motion  
vector for said chroma subblock depending on each motion vector of at least two  
of the luma subblocks.

33. A video decoder (151) according to claim 32,

35

wherein, if the chroma format information indicates a second format, being different  
from the first format, the data decoder (170) is configured to determine the motion  
vector for said chroma subblock depending on each motion vector of exactly two of  
the luma subblocks.

34. A video decoder (151) according to claim 33,

5 wherein, if the chroma format information indicates the second format, the data decoder (170) is configured to determine the motion vector for said chroma subblock by averaging the motion vectors of said exactly two of the luma subblocks, or by symmetrically rounding the motion vectors of said exactly two of the luma subblocks.

10 35. A video decoder (151) according to claim 33 or 34,

15 wherein a luma block comprises at least four of the luma subblocks, wherein each row of two or more rows of the luma block comprises at least two of the at least four of the luma subblocks, wherein each column of two or more columns of the luma block comprises two or more of the at least four of the luma subblocks,

15 wherein, if the chroma format information indicates the second format, the data decoder (170) is configured to determine the motion vector for said chroma subblock depending on said exactly two of the at least four of the luma subblocks.

20 36. A video decoder (151) according to claim 35,

25 wherein a first row of the two or more rows comprises an upper-left luma subblock, wherein a second row of the two or more rows comprises a bottom-right luma subblock, wherein the upper-left luma subblock and the bottom-right luma subblock are diagonally arranged with respect to each other, and

30 wherein, if the chroma format information indicates the second format, the data decoder (170) is configured to determine the motion vector for said chroma subblock depending on the motion vector of the upper-left luma subblock and depending on the motion vector of the bottom-right luma subblock.

37. A video decoder (151) according to claim 35 or 36,

35 wherein, if the chroma format information indicates a third format, being different from the first format and being different from the second format, the data decoder (170) is configured to determine the motion vector for said chroma subblock depending on each motion vector of exactly two of the luma subblocks from exactly one row of the two or more rows of the luma block; or the data decoder

(170) is configured to determine the motion vector for said chroma subblock depending on each motion vector of exactly two of the luma subblocks from exactly one column of the two or more columns of the luma block.

5 38. A video decoder (151) according to one of claims 35 to 27

wherein, if the chroma format information indicates the third format, the data decoder (170) is configured to determine the motion vector for said chroma subblock depending on each motion vector of exactly two of the luma subblocks 10 from exactly one row of the two or more rows of the luma block; and

wherein, if the chroma format information indicates a fourth format, being different from the third format and being different from the first format and being different from the first format, the data decoder (170) is configured to determine the motion 15 vector for said chroma subblock depending on each motion vector of exactly two of the luma subblocks from exactly one column of the two or more columns of the luma block.

39. A video decoder (151) according to claim 38,

20

wherein the data decoder (170) is configured to determine the motion vector for said chroma subblock based on:

```
mvAvgLX = ( mvLX[ ( xSbIdx >> SubWidthC << SubWidthC ) ]  
25           [ (ySbIdx>> SubHeightC << SubHeightC ) ] +  
             mvLX[ ( xSbIdx >> SubWidthC << SubWidthC ) + SubWidthC ]  
             [ (ySbIdx>> SubHeightC << SubHeightC ) + SubHeightC ] )
```

wherein `mvAvgLX` is the motion vector for said chroma subblock,

30

wherein `mvLX[][]` is the motion vector of one of the luma subblocks,

wherein `xSbIdx` is an x-index of said one of the luma subblocks,

35

wherein `ySbIdx` is an y-index of one of the luma subblocks,

wherein `mvAvgLX[ 0 ]` is defined according to

```

mvAvgLX[ 0 ] = ( mvAvgLX[ 0 ] >= 0 ?
( mvAvgLX[ 0 ] + 1) >> 1 :
- ( ( - mvAvgLX[ 0 ] + 1 ) >>1 ) )

```

5

wherein `mvAvgLX[ 1 ]` is defined according to

```

mvAvgLX[ 1 ] = ( mvAvgLX[ 1 ] >= 0 ?
( mvAvgLX[ 1 ] + 1) >> 1 :
- ( ( - mvAvgLX[ 1 ] + 1 ) >> 1 ) .

```

10

wherein `SubWidthC` and `SubHeightC` are defined according to:

Chroma Format	SubWidthC	SubHeightC
4:2:0	1	1
4:2:2	1	0
4:4:4	0	0

15 40. A video encoder (101) for encoding a plurality of pictures of a video by generating an encoded video signal, wherein each of the plurality of pictures comprises original picture data, wherein the video encoder (101) comprises:

20 a data encoder (110) configured for generating the encoded video signal comprising encoded picture data, wherein the data encoder (110) is configured to encode the plurality of pictures of the video into the encoded picture data, and

an output interface (120) configured for outputting the encoded picture data of each of the plurality of pictures,

25

wherein the data encoder (110) is configured to generate the encoded video signal such that the encoded video signal comprises an encoding of chroma format information and an encoding of motion information of one or more of a plurality of luma subblocks.

30

41. A method for decoding an encoded video signal comprising encoded picture data to reconstruct a plurality of pictures of a video, wherein the method comprises:

for receiving the encoded video signal, and

reconstructing the plurality of pictures of the video by decoding the encoded picture data,

5

wherein reconstructing the plurality of pictures comprises decoding chroma format information encoded within the encoded video signal,

10

wherein reconstructing the plurality of pictures comprises determining motion information for a chroma subblock of a plurality of chroma subblocks from motion information of one or more of a plurality of luma subblocks depending on the chroma format information,

15

wherein reconstructing the plurality of pictures is conducted depending on the motion information of the one or more of the luma subblocks and depending on the motion information for the chroma subblock.

20

42. A method for encoding a plurality of pictures of a video by generating an encoded video signal, wherein each of the plurality of pictures comprises original picture data, wherein the method comprises:

generating the encoded video signal comprising encoded picture data, wherein generating the encoded video signal comprises encoding the plurality of pictures of the video into the encoded picture data, and

25

outputting the encoded picture data of each of the plurality of pictures,

30

wherein generating the encoded video signal is conducted such that the encoded video signal comprises an encoding of chroma format information and an encoding of motion information of one or more of a plurality of luma subblocks.

43. A computer program for implementing the method of claim 41 or 42 when being executed on a computer or signal processor.

35

44. An encoded video signal encoding a picture, wherein the encoded video signal comprises encoded picture data encoding the picture,

wherein the encoded video signal further comprises an encoding of chroma format information and an encoding of motion information of one or more of a plurality of luma subblocks.

5 45. A system comprising:

the video encoder (101) according to claim 40, and

the video decoder (151) according to one of claims 31 to 39,

10 wherein the video encoder (101) configured to generate the encoded video signal, and

wherein the video decoder (151) is configured to decode the encoded video signal  
15 to reconstruct the picture of the video.

46. A video decoder (151) for decoding an encoded video signal comprising encoded picture data to reconstruct a plurality of pictures of a video, wherein the video decoder (151) comprises:

20 an input interface (160) configured for receiving the encoded video signal, and

25 a data decoder (170) configured for reconstructing the plurality of pictures of the video by decoding the encoded picture data,

wherein the data decoder (170) is configured to select an interpolation filter from two or more interpolation filters as a selected interpolation filter depending on a resolution of coded content of the encoded picture data and/or depending on a block size of the encoded picture data, and

30 wherein the data decoder (170) is configured to reconstruct the plurality of pictures using the selected interpolation filter.

47. A video decoder (151) according to claim 46,

35 wherein the two or more interpolation filters are two or more tab interpolation filters for sub-pel interpolation.

48. A video decoder (151) according to claim 46 or 47,

wherein the two or more interpolation filters comprise a bilinear interpolation filter, a 4-tab interpolation filter, a 6-tab interpolation filter and an 8-tab interpolation filter.

5

49. A video decoder (151) according to one of claims 46 to 48,

wherein the two or more interpolation filters comprise a chroma sub-pel-interpolation filter suitable for chroma sub-pel interpolation and suitable for luma sub-pel interpolation.

10

50. A video encoder (101) for encoding a plurality of pictures of a video by generating an encoded video signal, wherein each of the plurality of pictures comprises original picture data, wherein the video encoder (101) comprises:

15

a data encoder (110) configured for generating the encoded video signal comprising encoded picture data, wherein the data encoder (110) is configured to encode the plurality of pictures of the video into the encoded picture data, and

20

an output interface (120) configured for outputting the encoded picture data of each of the plurality of pictures,

25

wherein the data encoder (110) is configured to select an interpolation filter from two or more interpolation filters as a selected interpolation filter depending on a resolution of coded content of the encoded picture data and/or depending on a block size of the encoded picture data, and wherein the data encoder (110) is configured to generate the encoded video signal such that the encoded video signal comprises an encoding of an indication that indicates the selected interpolation filter, or

30

wherein the data encoder (110) is configured to generate the encoded video signal such that the encoded video signal encodes an indication that indicates the two or more interpolation filters.

35

51. A video encoder (101) according to claim 50,

wherein the two or more interpolation filters are two or more tab interpolation filters for sub-pel interpolation.

52. A video encoder (101) according to claim 50 or 51,

5 wherein the two or more interpolation filters comprise a bilinear interpolation filter, a 4-tab interpolation filter, a 6-tab interpolation filter and an 8-tab interpolation filter.

53. A video encoder (101) according to one of claims 50 to 52,

10 wherein the two or more interpolation filters comprise a chroma sub-pel-interpolation filter suitable for chroma sub-pel interpolation and suitable for luma sub-pel interpolation.

15 54. A method for decoding an encoded video signal comprising encoded picture data to reconstruct a plurality of pictures of a video, wherein the method comprises:

16 for receiving the encoded video signal, and

20 reconstructing the plurality of pictures of the video by decoding the encoded picture data,

25 wherein reconstructing the plurality of pictures comprises selecting an interpolation filter from two or more interpolation filters as a selected interpolation filter depending on a resolution of coded content of the encoded picture data and/or depending on a block size of the encoded picture data, and

30 55. wherein reconstructing the plurality of pictures is conducted using the selected interpolation filter.

35 55. A method for encoding a plurality of pictures of a video by generating an encoded video signal, wherein each of the plurality of pictures comprises original picture data, wherein the method comprises:

generating the encoded video signal comprising encoded picture data, wherein generating the encoded video signal comprises encoding the plurality of pictures of the video into the encoded picture data, and

36 outputting the encoded picture data of each of the plurality of pictures,

wherein generating the encoded video signal comprises selecting an interpolation filter from two or more interpolation filters as a selected interpolation filter depending on a resolution of coded content of the encoded picture data and/or depending on a block size of the encoded picture data, and wherein  
5 generating the encoded video signal such that the encoded video signal comprises an encoding of an indication that indicates the selected interpolation filter, or

wherein generating the encoded video signal is conducted such that the encoded video signal comprises an encoding of an indication that indicates the two or more  
10 interpolation filters.

56. A computer program for implementing the method of claim 54 or 55 when being executed on a computer or signal processor.

15 57. An encoded video signal encoding a picture, wherein the encoded video signal comprises encoded picture data encoding the picture,

wherein the encoded video signal comprises an indication that indicates a selected  
20 interpolation filter that depends a resolution of coded content of the encoded picture data and/or depending on a block size of the encoded picture data, or

wherein the encoded video signal comprises an indication that indicates two or more interpolation filters.

25 58. A system comprising:

the video encoder (101) according to one of claims 50 to 53, and

the video decoder (151) according to one of claims 46 to 49,

30 wherein the video encoder (101) configured to generate the encoded video signal, and

wherein the video decoder (151) is configured to decode the encoded video signal  
35 to reconstruct the picture of the video.

59. A video encoder (101) for encoding a plurality of pictures of a video by generating an encoded video signal, wherein each of the plurality of pictures comprises original picture data, wherein the video encoder (101) comprises:

5 a data encoder (110) configured for generating the encoded video signal comprising encoded picture data, wherein the data encoder (110) is configured to encode the plurality of pictures of the video into the encoded picture data, and

10 an output interface (120) configured for outputting the encoded picture data of each of the plurality of pictures,

15 wherein a video session comprises a plurality of coded video sequences, wherein a CVS profile of a plurality of video profiles is assigned to each coded video sequence of the plurality of coded video sequences, wherein the CVS profile of said coded video sequence defines one or more tools of a plurality of tools that are needed to decode said coded video sequence, wherein each of the plurality of video profiles defines at least one tool that is needed to decode at least one of the plurality of coded video sequences,

20 wherein the data encoder (110) is configured to determine a session profile for the video session depending on the CVS profiles of the plurality of coded video sequences of the video session, wherein the session profile defines at least one tool from the plurality of tools that is needed to decode the plurality of coded video sequences of the video session,

25 wherein the data encoder (110) is configured to generate the encoded video signal such that the encoded video signal comprises an encoding of an indication that indicates the session profile,

30 wherein the data encoder (110) is configured to determine the session profile from the plurality of video profiles such that the session profile comprises all tools of the plurality of tools that are needed to decode all coded video sequences of the video session.

35 60. A video encoder (101) according to claim 59,

wherein the data encoder (110) is configured to determine the session profile such that the session profile is a union of at least two of the plurality of video profiles, if

there exists no video profile among the plurality of video profiles that comprises all tools of the plurality of tools that are needed to decode all coded video sequences of the video session.

5 61. A video encoder (101) according to claim 59,

wherein the data encoder (110) is configured to determine exactly one video profile from the plurality of video profiles as the session profile, if there exists a video profile among the plurality of video profiles that comprises all tools of the plurality 10 of tools that are needed to decode all coded video sequences of the video session, wherein said video profile is the exactly one session profile.

62. A video encoder (101) according to claim 59,

15 wherein the data encoder (110) is configured to determine the session profile such that the session profile is an intersection of at least two of the plurality of video profiles, if there exists no video profile among the plurality of video profiles that comprises all tools of the plurality of tools that are needed to decode all coded video sequences of the video session.

20 63. A video decoder (151) for decoding an encoded video signal comprising encoded picture data to reconstruct a plurality of pictures of a video, wherein the video decoder (151) comprises:

25 an input interface (160) configured for receiving the encoded video signal, and

a data decoder (170) configured for reconstructing the plurality of pictures of the video by decoding the encoded picture data,

30 wherein the data decoder (170) is configured to decode from the encoded video signal a session profile for a video session comprising a plurality of coded video sequences, wherein each of a plurality of video profiles defines at least one tool from a plurality of tools that is needed to decode at least one of the plurality of coded video sequences, wherein the session profile defines at least one tool from the plurality of tools that is needed to decode the plurality of coded video 35 sequences of the video session,

wherein the data decoder (170) is configured to reconstruct the plurality of pictures of the video depending on the session profile.

64. A video decoder (151) according to claim 63,

5

wherein the session profile is a union of at least two of the plurality of video profiles.

65. A video decoder (151) according to claim 63,

10

wherein the data decoder (170) is configured to determine exactly one video profile from the plurality of video profiles as the session profile, if there exists a video profile among the plurality of video profiles that comprises all tools of the plurality of tools that are needed to decode all coded video sequences of the video session.

15

66. A video decoder (151) according to claim 63,

wherein the session profile is an intersection of at least two of the plurality of video profiles.

20

67. A method for encoding a plurality of pictures of a video by generating an encoded video signal, wherein each of the plurality of pictures comprises original picture data, wherein the method comprises:

25

generating the encoded video signal comprising encoded picture data, wherein generating the encoded video signal comprises encoding the plurality of pictures of the video into the encoded picture data, and

outputting the encoded picture data of each of the plurality of pictures,

30

wherein a video session comprises a plurality of coded video sequences, wherein a CVS profile of a plurality of video profiles is assigned to each coded video sequence of the plurality of coded video sequences, wherein the CVS profile of said coded video sequence defines one or more tools of a plurality of tools that are needed to decode said coded video sequence, wherein each of the plurality of video profiles defines at least one tool that is needed to decode at least one of the plurality of coded video sequences,

35

wherein the method comprises determining a session profile for the video session depending on the CVS profiles of the plurality of coded video sequences of the video session, wherein the session profile defines at least one tool from the plurality of tools that are needed to decode the plurality of coded video sequences of the video session,

wherein generating the encoded video signal is conducted such that the encoded video signal comprises an encoding of an indication that indicates the session profile,

wherein determining the session profile from the plurality of video profiles is conducted such that the session profile comprises all tools of the plurality of tools that are needed to decode all coded video sequences of the video session.

15 68. A method for decoding an encoded video signal comprising encoded picture data to reconstruct a plurality of pictures of a video, wherein the method comprises:

for receiving the encoded video signal, and

20 reconstructing the plurality of pictures of the video by decoding the encoded picture data,

wherein the method comprises decoding from the encoded video signal a session profile for a video session comprising a plurality of coded video sequences, wherein each of a plurality of video profiles defines at least one tool from a plurality of tools that is needed to decode at least one of the plurality of coded video sequences, wherein the session profile defines at least one tool from the plurality of tools that is needed to decode the plurality of coded video sequences of the video session,

30 wherein reconstructing the plurality of pictures of the video is conducted depending on the session profile.

35 69. A computer program for implementing the method of claim 67 or 68 when being executed on a computer or signal processor.

70. An encoded video signal encoding a picture, wherein the encoded video signal comprises encoded picture data encoding the picture,

wherein a video session comprises a plurality of coded video sequences, wherein a CVS profile of a plurality of video profiles is assigned to each coded video sequence of the plurality of coded video sequences, wherein the CVS profile of said coded video sequence defines one or more tools of a plurality of tools that are needed to decode said coded video sequence, wherein each of the plurality of video profiles defines at least one tool that is needed to decode at least one of the plurality of coded video sequences,

10 wherein the session profiles for the video session depends on the CVS profiles of the plurality of coded video sequences of the video session,

wherein the encoded video signal comprises an encoding of an indication that indicates the session profile,

15 wherein the session profile comprises all tools of the plurality of tools that are needed to decode all coded video sequences of the video session.

71. A system comprising:

20 the video encoder (101) according to one of claims 59 to 62, and

the video decoder (151) according to one of claims 63 to 66,

25 wherein the video encoder (101) configured to generate the encoded video signal, and

wherein the video decoder (151) is configured to decode the encoded video signal to reconstruct the picture of the video.

30 72. A video encoder (101) for encoding a plurality of pictures of a video by generating an encoded video signal, wherein each of the plurality of pictures comprises original picture data, wherein the video encoder (101) comprises:

35 a data encoder (110) configured for generating the encoded video signal comprising encoded picture data, wherein the data encoder (110) is configured to encode the plurality of pictures of the video into the encoded picture data, and

an output interface (120) configured for outputting the encoded picture data of each of the plurality of pictures,

5 wherein the data encoder (110) is configured to generate the encoded video signal by encoding a sequence parameter set and by encoding a supplemental enhancement information message, wherein the sequence parameter set relates to two or more of the plurality of pictures and comprise a first group of one or more syntax elements of a plurality of syntax elements, and wherein the supplemental enhancement information message relates to exactly one of the plurality of 10 pictures and comprises a second group of one or more syntax elements of the plurality of syntax elements,

15 wherein the data encoder (110) is configured to generate the sequence parameter set and the supplemental enhancement information message such that the sequence parameter set does not comprise at least one syntax element of the second group of syntax elements of the supplemental enhancement information message, and such that the sequence parameter set does not comprise length information for said at least one syntax element of the second group of syntax 20 elements of the supplemental enhancement information message.

20 73. A video encoder (101) according to claim 72,

25 wherein the data encoder (110) is configured to generate the sequence parameter set and the supplemental enhancement information message such that the sequence parameter set does not comprise at least one further syntax element of the second group of syntax elements of the supplemental enhancement information message, and such that the sequence parameter set does not 30 comprise presence information for said at least one further syntax element of the second group of syntax elements of the supplemental enhancement information message.

74. A video encoder (101) according to claim 72 or 73,

35 wherein the data encoder (110) is configured to generate the sequence parameter set such that the sequence parameter set comprises at least one first syntax element of the plurality of syntax elements that comprises session negotiation information and/or capability negotiation information,

wherein the data encoder (110) is configured to generate the supplemental enhancement information message such that the supplemental enhancement information message does not comprise said at least one first syntax element that comprises the session negotiation information and/or the capability negotiation information,

5 wherein the data encoder (110) is configured to generate the supplemental enhancement information message such that the supplemental enhancement information message comprises at least one second syntax element of the plurality of syntax elements that does not comprise the session negotiation information and/or the capability negotiation information, and

10 15 wherein the data encoder (110) is configured to generate the sequence parameter set such that the sequence parameter set does not comprise said at least one second syntax element that does not comprise session negotiation information and/or capability negotiation information.

75. A video encoder (101) according to one of claims 72 to 74,

20 wherein the data encoder (110) is configured to generate the sequence parameter set such that the sequence parameter set comprises all syntax elements of the plurality of syntax elements that comprise session negotiation information and/or capability negotiation information,

25 wherein the data encoder (110) is configured to generate the supplemental enhancement information message such that the supplemental enhancement information message does not comprise said any of the plurality of syntax elements that comprises the session negotiation information and/or the capability negotiation information,

30 35 wherein the data encoder (110) is configured to generate the supplemental enhancement information message such that the supplemental enhancement information message comprises all syntax elements of the plurality of syntax elements that do not comprise the session negotiation information and/or the capability negotiation information, and

wherein the data encoder (110) is configured to generate the sequence parameter set such that the sequence parameter set does not comprise any of the plurality of

syntax elements that does not comprise session negotiation information and/or capability negotiation information.

76. A video encoder (101) according to one of claims 72 to 75,

5

wherein the supplemental enhancement information message is a Buffering Period supplemental enhancement information message, or is a Timing supplemental enhancement information message, or is a Decoding Unit supplemental enhancement information message.

10

77. A video encoder (101) according to one of claims 72 to 76,

15

wherein, for at least one syntax element of the plurality of syntax elements, the data encoder (110) is configured to generate the sequence parameter set and the supplemental enhancement information message such that the sequence parameter set comprises said at least one syntax element and such that the supplemental enhancement information message also comprises said syntax element, and

20

wherein the data encoder (110) is configured to generate the sequence parameter set and the supplemental enhancement information message such that for each of the plurality of syntax elements that is present in the sequence parameter set and that is also present in the supplemental enhancement information message, a value of said syntax element is equal in the sequence parameter set and in the supplemental enhancement information message.

25

78. A video encoder (101) according to one of claims 72 to 77,

30

wherein the data encoder (110) is configured to generate the sequence parameter set and the supplemental enhancement information message such that a sub-picture timing granularity is equal in the sequence parameter set and in the supplemental enhancement information message, if a concatenation flag is not set to 1.

35

79. A video decoder (151) for decoding an encoded video signal comprising encoded picture data to reconstruct a plurality of pictures of a video, wherein the video decoder (151) comprises:

an input interface (160) configured for receiving the encoded video signal, and  
a data decoder (170) configured for reconstructing the plurality of pictures of the  
video by decoding the encoded picture data,

5

wherein the data decoder (170) is configured to decode from the encoded video  
signal a sequence parameter set and a supplemental enhancement information  
message, wherein the sequence parameter set relates to two or more of the  
plurality of pictures and comprise a first group of one or more syntax elements of a  
plurality of syntax elements, and wherein the supplemental enhancement  
information message relates to exactly one of the plurality of pictures and  
comprises a second group of one or more syntax elements of the plurality of  
syntax elements, wherein the sequence parameter set does not comprise at least  
10 one syntax element of the second group of syntax elements of the supplemental  
enhancement information message, and wherein the sequence parameter set  
does not comprise length information for said at least one syntax element of the  
second group of syntax elements of the supplemental enhancement information  
message,

15

20 wherein the data decoder (170) is configured to reconstruct the plurality of pictures  
of the video depending on the sequence parameter set and depending on the  
supplemental enhancement information message.

80. A video decoder (151) according to claim 79,

25

wherein the sequence parameter set does not comprise at least one further syntax  
element of the second group of syntax elements of the supplemental enhancement  
information message, and wherein the sequence parameter set does not comprise  
presence information for said at least one further syntax element of the second  
30 group of syntax elements of the supplemental enhancement information message.

81. A video decoder (151) according to claim 79 or 80,

35

wherein the sequence parameter set comprises at least one first syntax element of  
the plurality of syntax elements that comprises session negotiation information  
and/or capability negotiation information,

wherein the supplemental enhancement information message does not comprise said at least one first syntax element that comprises the session negotiation information and/or the capability negotiation information,

5 wherein the supplemental enhancement information message comprises at least one second syntax element of the plurality of syntax elements that does not comprise the session negotiation information and/or the capability negotiation information, and

10 wherein the sequence parameter set does not comprise said at least one second syntax element that does not comprise session negotiation information and/or capability negotiation information.

82. A video decoder (151) according to one of claims 79 to 81,

15 wherein the sequence parameter set comprises all syntax elements of the plurality of syntax elements that comprise session negotiation information and/or capability negotiation information,

20 wherein the supplemental enhancement information message does not comprise said any of the plurality of syntax elements that comprises the session negotiation information and/or the capability negotiation information,

25 wherein the supplemental enhancement information message comprises all syntax elements of the plurality of syntax elements that do not comprise the session negotiation information and/or the capability negotiation information, and

30 wherein the sequence parameter set does not comprise any of the plurality of syntax elements that does not comprise session negotiation information and/or capability negotiation information.

83. A video decoder (151) according to one of claims 79 to 82,

35 wherein the supplemental enhancement information message is a Buffering Period supplemental enhancement information message, or is a Timing supplemental enhancement information message, or is a Decoding Unit supplemental enhancement information message.

84. A video decoder (151) according to one of claims 79 to 83,

5       wherein, for at least one syntax element of the plurality of syntax elements, the sequence parameter set comprises said at least one syntax element and such that the supplemental enhancement information message also comprises said syntax element, and

10      wherein, for each of the plurality of syntax elements that is present in the sequence parameter set and that is also present in the supplemental enhancement information message, a value of said syntax element is equal in the sequence parameter set and in the supplemental enhancement information message.

85. A video decoder (151) according to one of claims 79 to 84,

15      wherein a sub-picture timing granularity is equal in the sequence parameter set and in the supplemental enhancement information message, if a concatenation flag is not set to 1.

20      86. A method for encoding a plurality of pictures of a video by generating an encoded video signal, wherein each of the plurality of pictures comprises original picture data, wherein the method comprises:

25      generating the encoded video signal comprising encoded picture data, wherein generating the encoded video signal comprises encoding the plurality of pictures of the video into the encoded picture data, and

outputting the encoded picture data of each of the plurality of pictures,

30      wherein generating the encoded video signal is conducted by encoding a sequence parameter set and by encoding a supplemental enhancement information message, wherein the sequence parameter set relates to two or more of the plurality of pictures and comprise a first group of one or more syntax elements of a plurality of syntax elements, and wherein the supplemental enhancement information message relates to exactly one of the plurality of pictures and comprises a second group of one or more syntax elements of the plurality of syntax elements,

wherein the method comprises generating the sequence parameter set and the supplemental enhancement information message such that the sequence parameter set does not comprise at least one syntax element of the second group of syntax elements of the supplemental enhancement information message, and such that the sequence parameter set does not comprise length information for said at least one syntax element of the second group of syntax elements of the supplemental enhancement information message.

5 87. A method for decoding an encoded video signal comprising encoded picture data  
10 to reconstruct a plurality of pictures of a video, wherein the method comprises:

for receiving the encoded video signal, and

15 reconstructing the plurality of pictures of the video by decoding the encoded picture data,

wherein the method comprises decoding from the encoded video signal a sequence parameter set and a supplemental enhancement information message, wherein the sequence parameter set relates to two or more of the plurality of pictures and comprise a first group of one or more syntax elements of a plurality of syntax elements, and wherein the supplemental enhancement information message relates to exactly one of the plurality of pictures and comprises a second group of one or more syntax elements of the plurality of syntax elements, wherein the sequence parameter set does not comprise at least one syntax element of the second group of syntax elements of the supplemental enhancement information message, and wherein the sequence parameter set does not comprise length information for said at least one syntax element of the second group of syntax elements of the supplemental enhancement information message.

20 30 wherein reconstructing the plurality of pictures of the video is conducted depending on the sequence parameter set and depending on the supplemental enhancement information message.

25 88. A computer program for implementing the method of claim 86 or 87 when being  
35 executed on a computer or signal processor.

89. An encoded video signal encoding a picture, wherein the encoded video signal comprises encoded picture data encoding the picture,

5 wherein the encoded video signal comprises an encoding of a sequence parameter set and an encoding of a supplemental enhancement information message, wherein the sequence parameter set relates to two or more of the plurality of pictures and comprise a first group of one or more syntax elements of a plurality of syntax elements, and wherein the supplemental enhancement information message relates to exactly one of the plurality of pictures and comprises a second group of one or more syntax elements of the plurality of syntax elements,

10

15 wherein the sequence parameter set does not comprise at least one syntax element of the second group of syntax elements of the supplemental enhancement information message, and wherein the sequence parameter set does not comprise length information for said at least one syntax element of the second group of syntax elements of the supplemental enhancement information message.

90. A system comprising:

20 the video encoder (101) according to one of claims 72 to 78, and

25 the video decoder (151) according to one of claims 79 to 85,

wherein the video encoder (101) configured to generate the encoded video signal, and

25 wherein the video decoder (151) is configured to decode the encoded video signal to reconstruct the picture of the video.

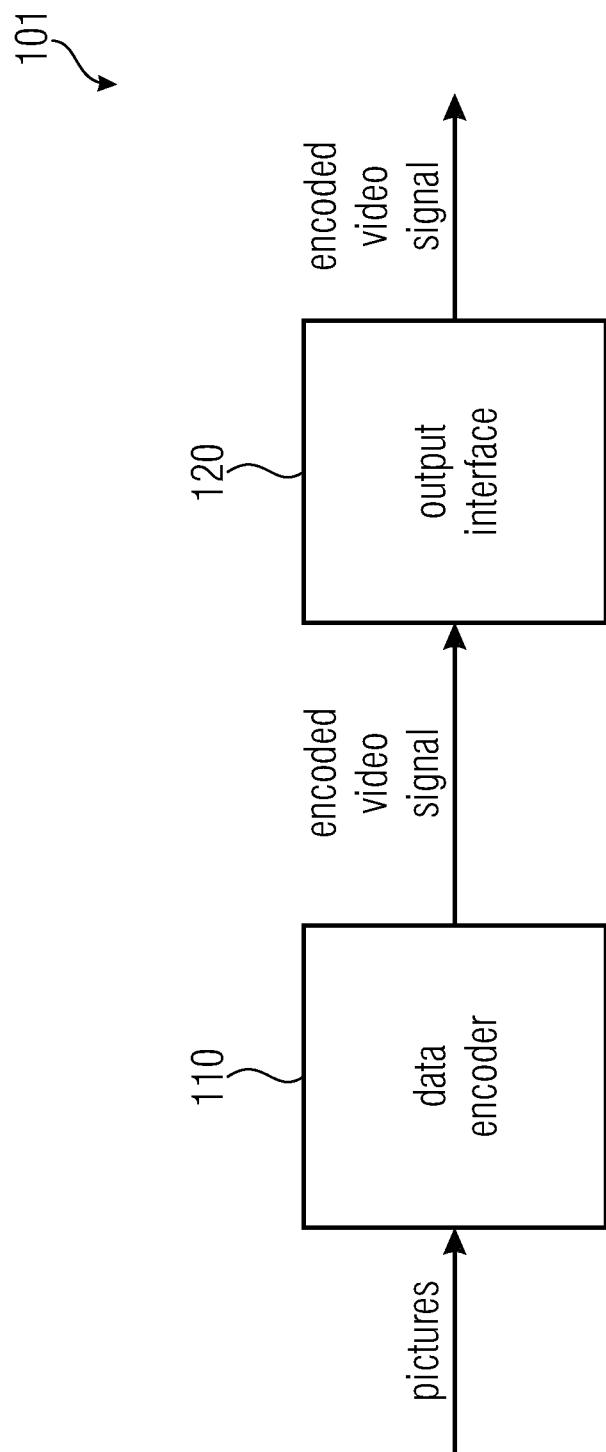


Fig. 1

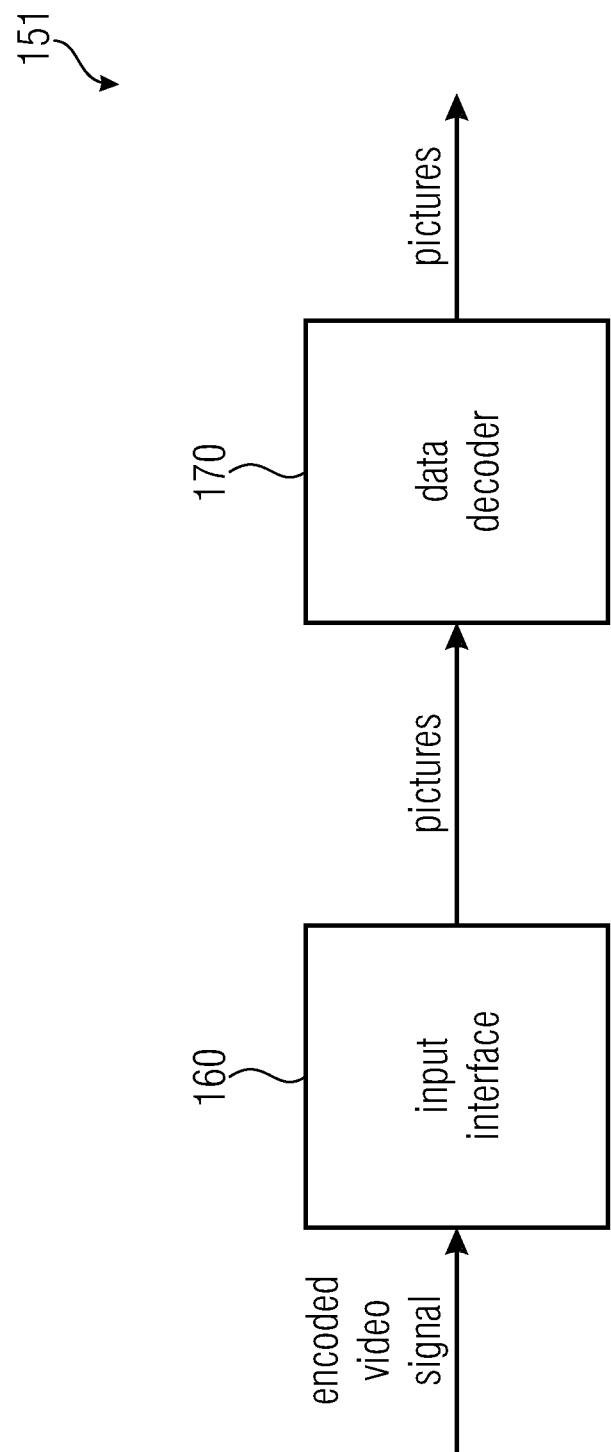


Fig. 2

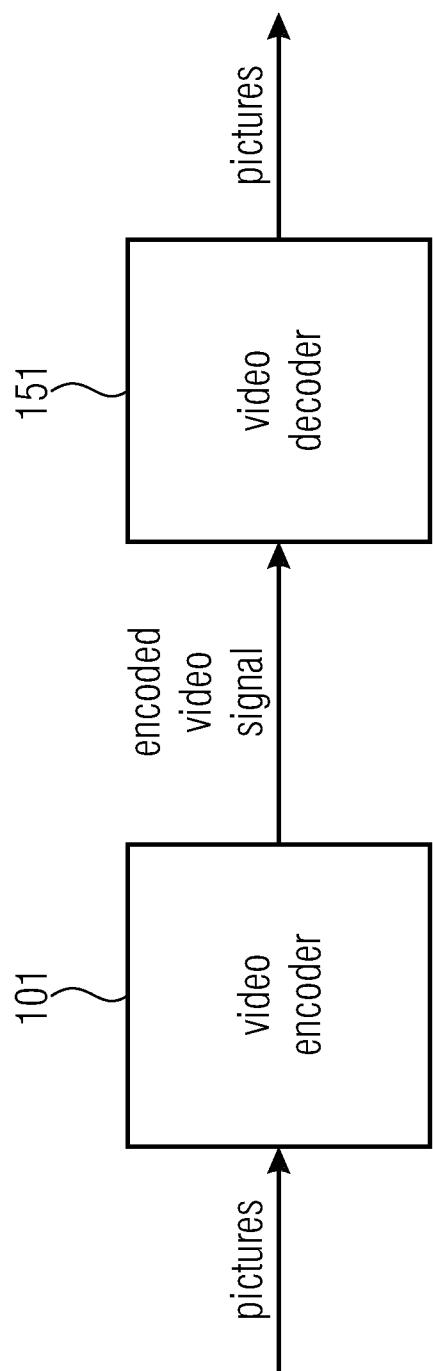


Fig. 3

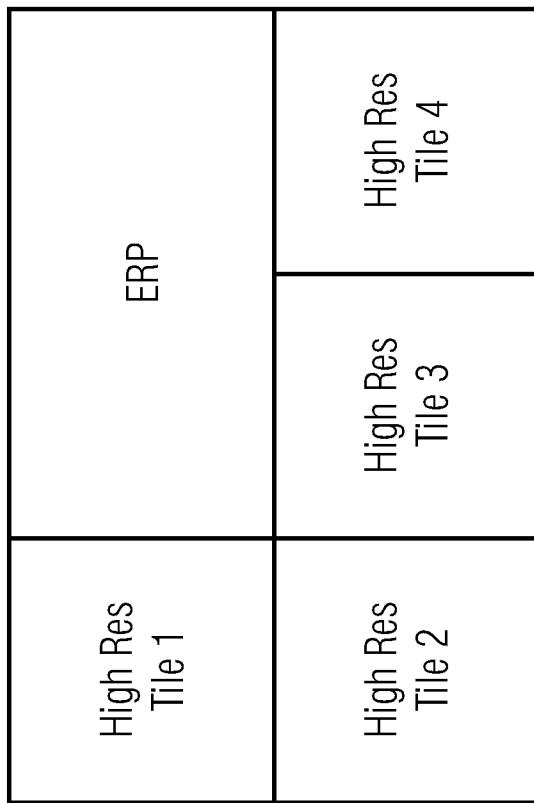


Fig. 4

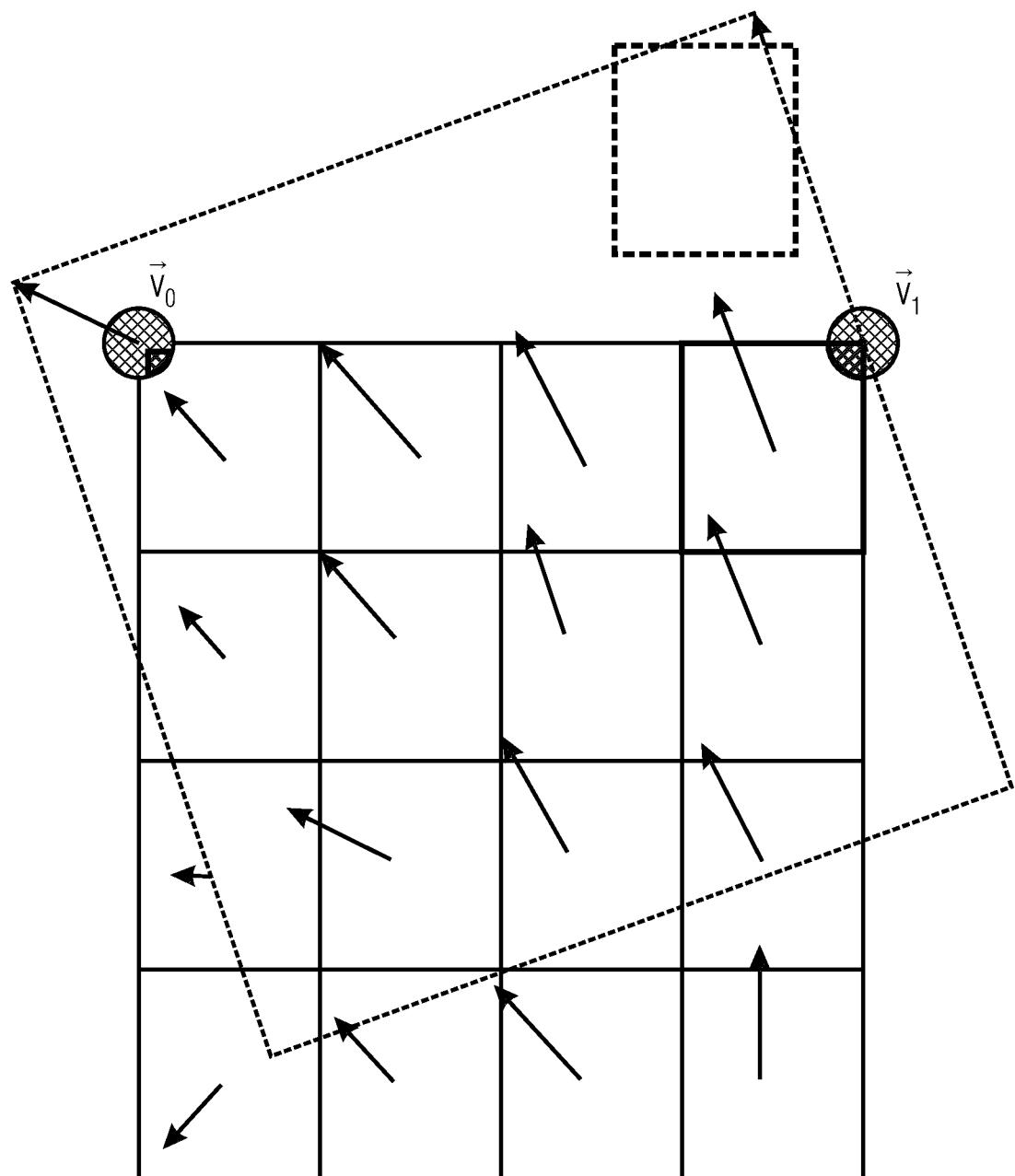


Fig. 5

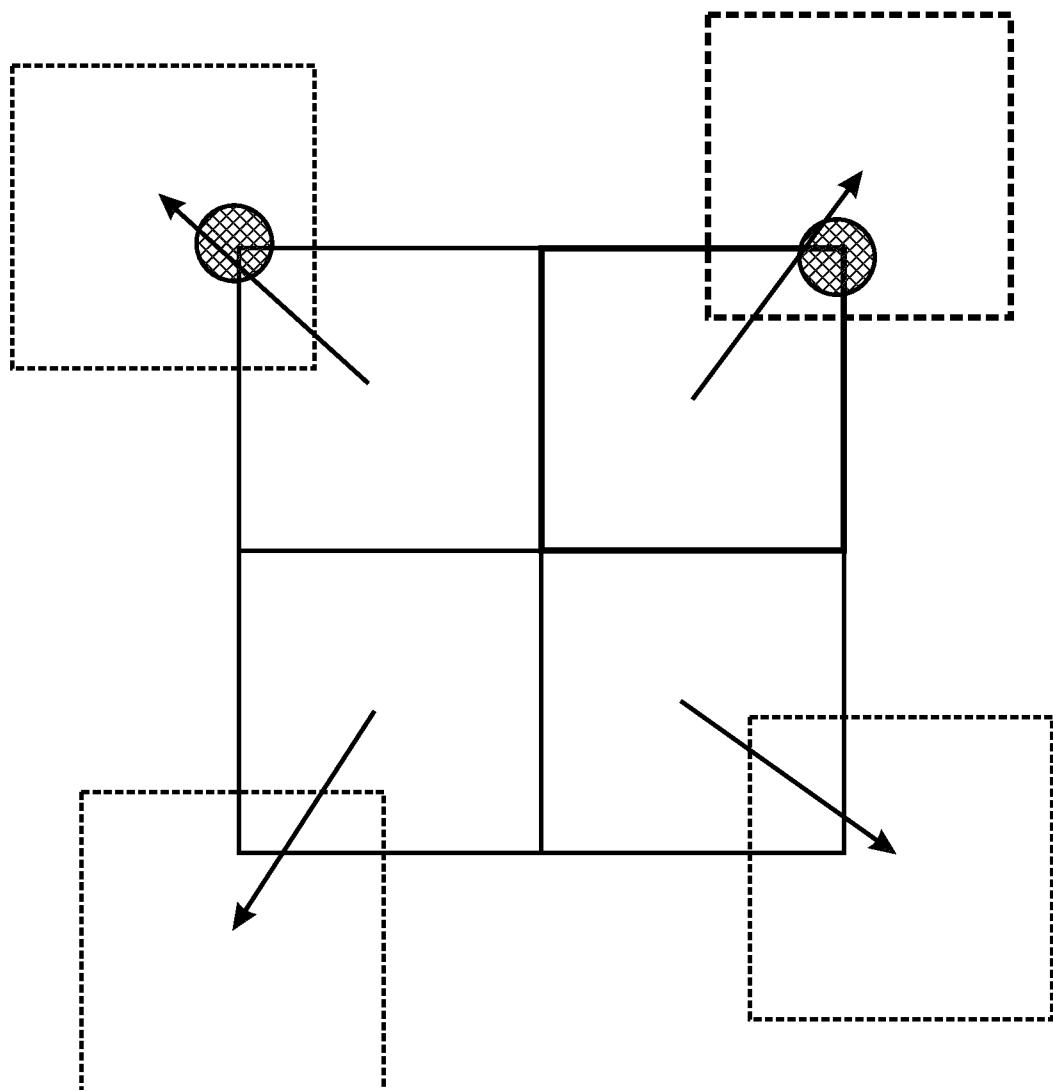


Fig. 6

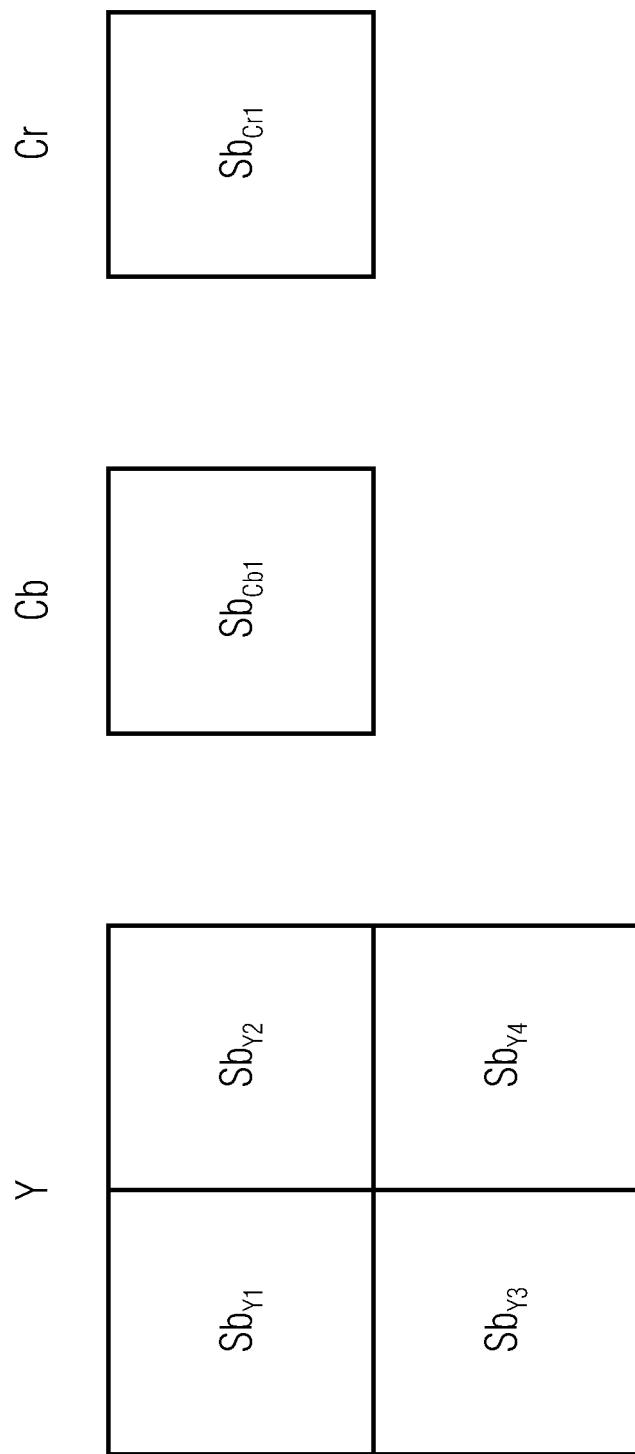


Fig. 7

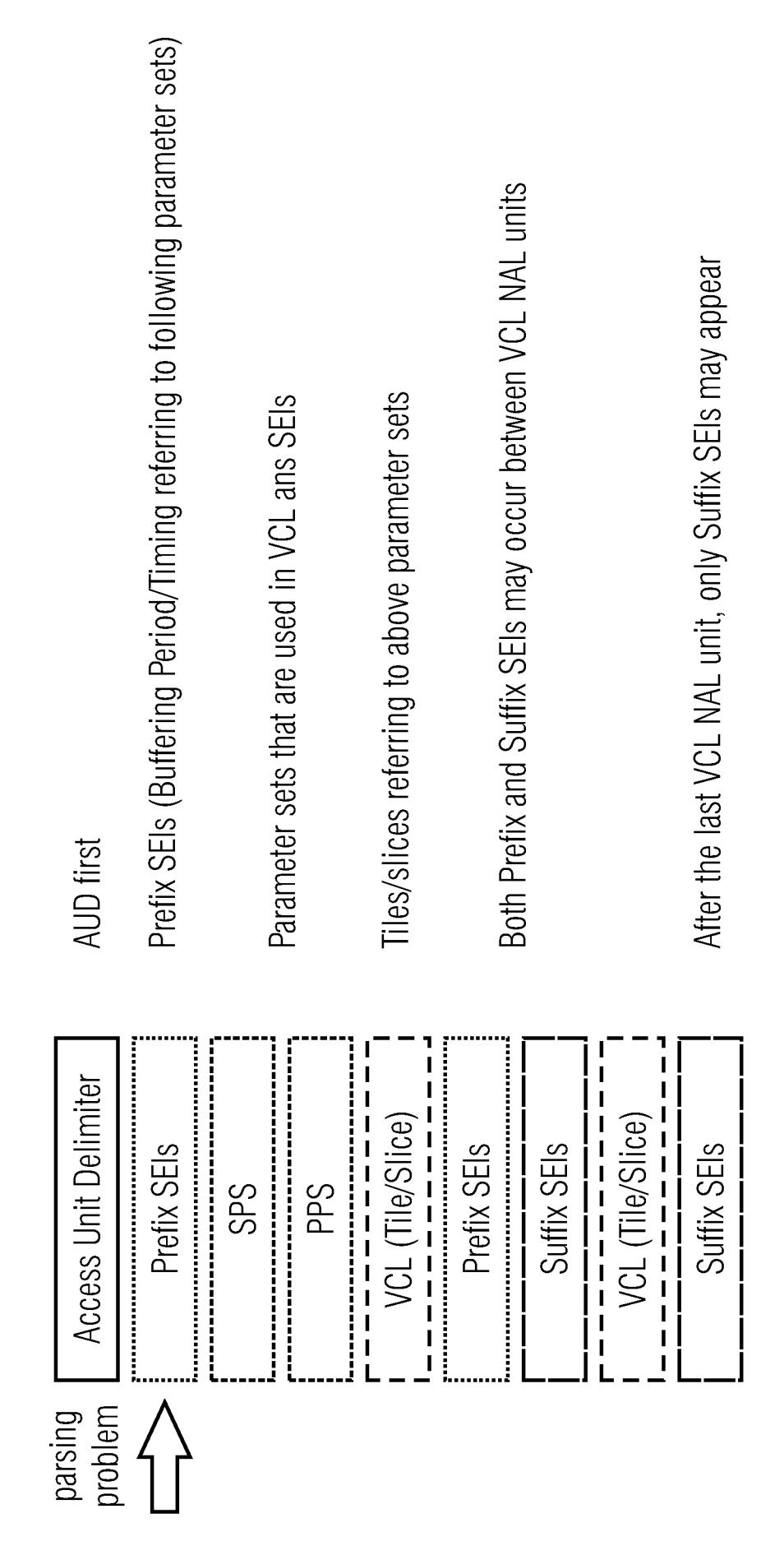


Fig. 8

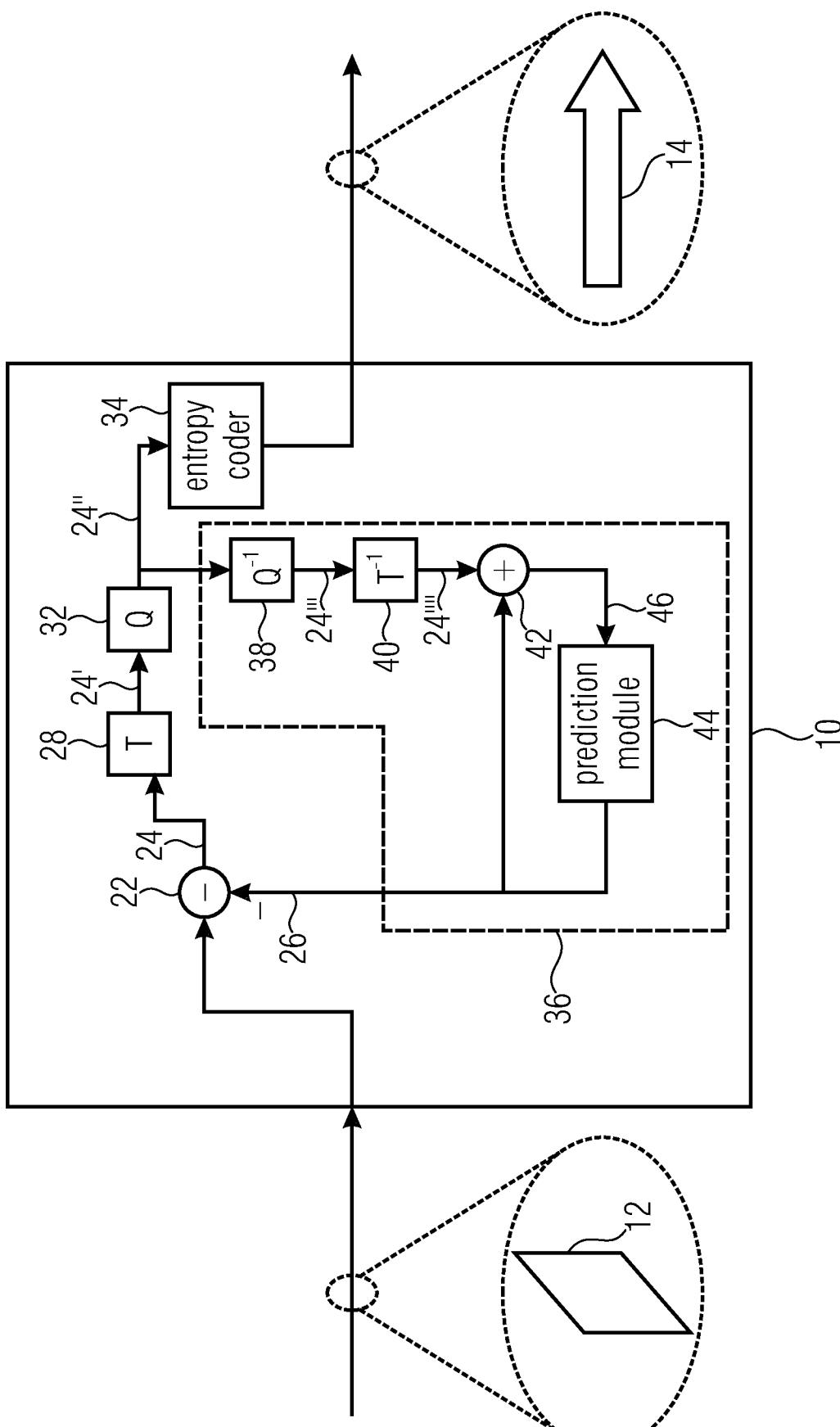


Fig. 9

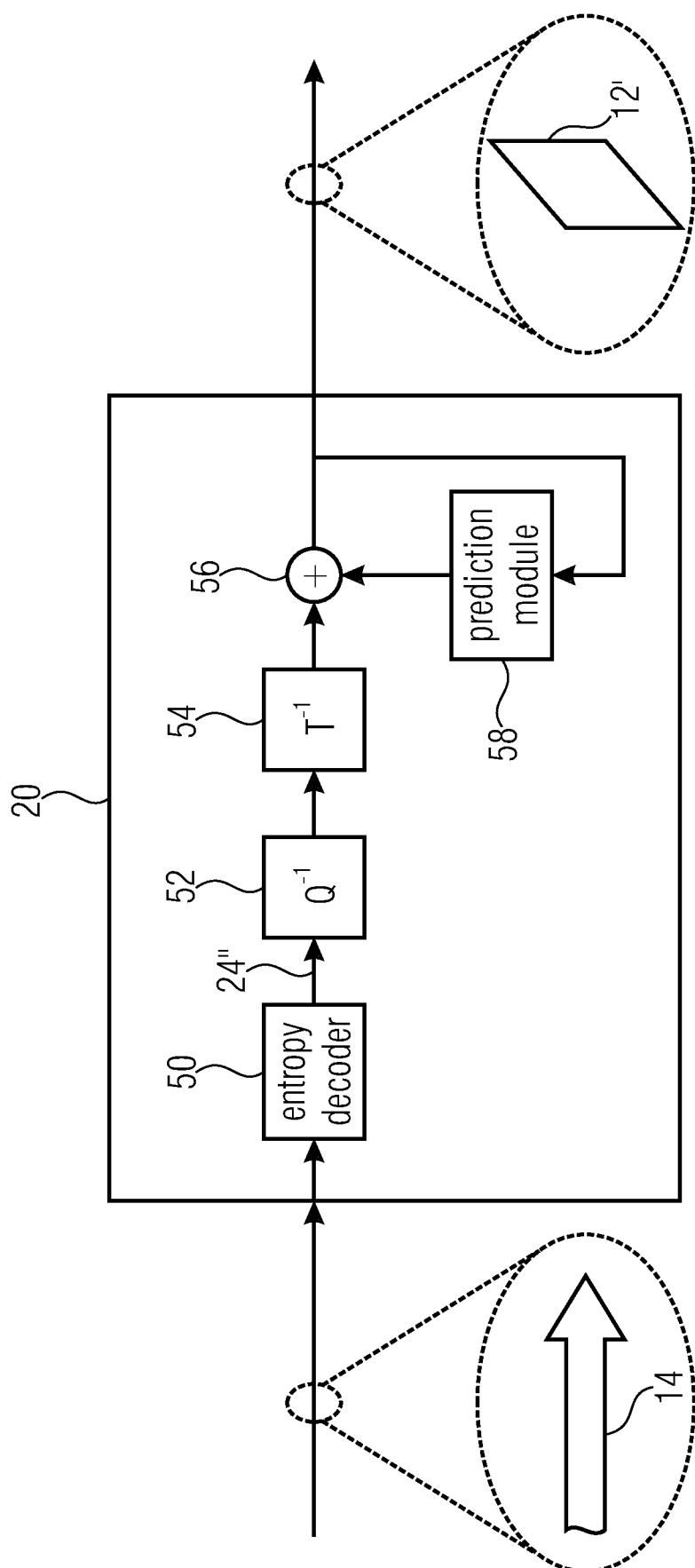


Fig. 10

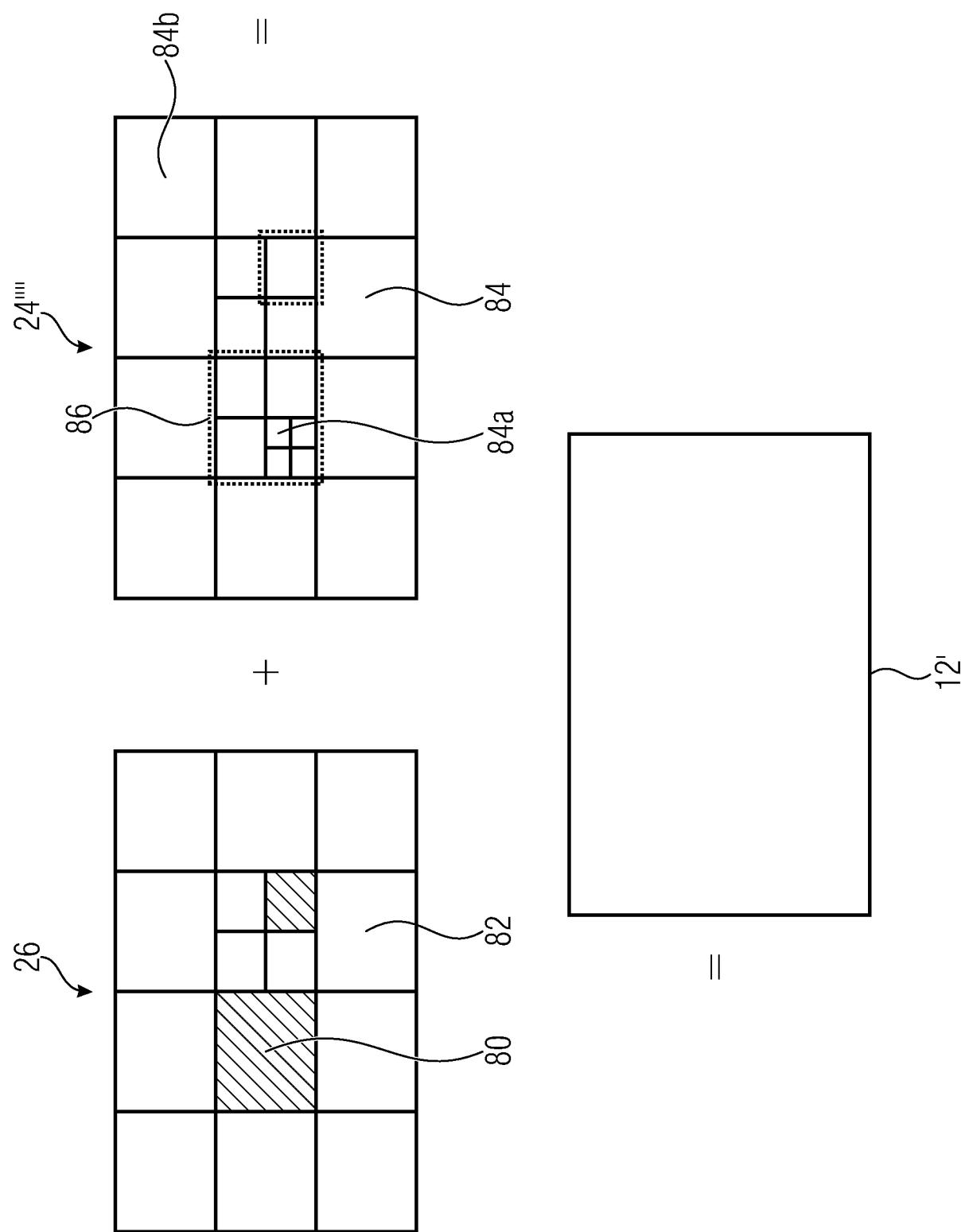


Fig. 11

# INTERNATIONAL SEARCH REPORT

International application No  
PCT/EP2020/056355

A. CLASSIFICATION OF SUBJECT MATTER				
INV.	H04N19/117	H04N19/136	H04N19/186	H04N19/43
	H04N19/54	H04N19/563	H04N19/59	H04N19/597
	H04N19/82	H04N21/00	H04N19/172	H04N19/176

According to International Patent Classification (IPC) or to both national classification and IPC

## B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

H04N

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

EPO-Internal, WPI Data, COMPENDEX, INSPEC, IBM-TDB

## C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	<p>BROSS B ET AL: "Versatile Video Coding (Draft 4)", 125. MPEG MEETING; 20190114 - 20190118; MARRAKECH; (MOTION PICTURE EXPERT GROUP OR ISO/IEC JTC1/SC29/WG11), ,</p> <p>no. m46627 9 March 2019 (2019-03-09), XP030215564, Retrieved from the Internet: URL:<a href="http://phenix.int-evry.fr/mpeg/doc_end_user/documents/125_Marrakech/wg11/m46627-JVET-M1001-v6-JVET-M1001-v6.zip">http://phenix.int-evry.fr/mpeg/doc_end_user/documents/125_Marrakech/wg11/m46627-JVET-M1001-v6-JVET-M1001-v6.zip</a> JVET-M1001-v6.docx [retrieved on 2019-03-09] * sections 5.8, 7.3.2.1 and 8.5.7.3.2 * * see page 3 of editorial notes - 25th black bullet point * * section 7.4.3.1 *; page 63</p>	1-15

Further documents are listed in the continuation of Box C.

See patent family annex.

\* Special categories of cited documents :

- "A" document defining the general state of the art which is not considered to be of particular relevance
- "E" earlier application or patent but published on or after the international filing date
- "L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)
- "O" document referring to an oral disclosure, use, exhibition or other means
- "P" document published prior to the international filing date but later than the priority date claimed

"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention

"X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone

"Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art

"&" document member of the same patent family

Date of the actual completion of the international search	Date of mailing of the international search report
6 May 2020	15/07/2020

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Ferré, Pierre

## INTERNATIONAL SEARCH REPORT

International application No
PCT/EP2020/056355

C(Continuation). DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	<p>-----</p> <p>HANHART (INTERDIGITAL) P ET AL: "CE13: PERP with horizontal geometry padding of reference pictures (Test 3.3)", 12. JVET MEETING; 20181003 - 20181012; MACAO; (THE JOINT VIDEO EXPLORATION TEAM OF ISO/IEC JTC1/SC29/WG11 AND ITU-T SG.16 ), , no. JVET-L0231 11 October 2018 (2018-10-11), XP030195383, Retrieved from the Internet: URL:<a href="http://phenix.int-evry.fr/jvet/doc_end_user/documents/12_Macao/wg11/JVET-L0231-v4.zip">http://phenix.int-evry.fr/jvet/doc_end_user/documents/12_Macao/wg11/JVET-L0231-v4.zip</a> JVET-L0231_r2.docx [retrieved on 2018-10-11] the whole document</p> <p>-----</p>	1-15
A	<p>-----</p> <p>LIN JIAN-LIANG ET AL: "Efficient Projection and Coding Tools for 360° Video", IEEE JOURNAL ON EMERGING AND SELECTED TOPICS IN CIRCUITS AND SYSTEMS, IEEE, PISCATAWAY, NJ, USA, vol. 9, no. 1, 1 March 2019 (2019-03-01), pages 84-97, XP011714059, ISSN: 2156-3357, DOI: 10.1109/JETCAS.2019.2899660 [retrieved on 2019-03-08]</p> <p>* section III.B.1 *</p> <p>-----</p>	1-15
A	<p>-----</p> <p>HE YUWEN ET AL: "Motion compensated prediction with geometry padding for 360 video coding", 2017 IEEE VISUAL COMMUNICATIONS AND IMAGE PROCESSING (VCIP), IEEE, 10 December 2017 (2017-12-10), pages 1-4, XP033325766, DOI: 10.1109/VCIP.2017.8305088</p> <p>* section II *</p> <p>-----</p>	1-15
A	<p>-----</p> <p>WO 2018/170279 A1 (VID SCALE INC [US]) 20 September 2018 (2018-09-20) paragraph [0044] - paragraph [0045]</p> <p>-----</p>	1-15

# INTERNATIONAL SEARCH REPORT

International application No.  
PCT/EP2020/056355

## Box No. II Observations where certain claims were found unsearchable (Continuation of item 2 of first sheet)

This international search report has not been established in respect of certain claims under Article 17(2)(a) for the following reasons:

1.  Claims Nos.:  
because they relate to subject matter not required to be searched by this Authority, namely:
  
2.  Claims Nos.:  
because they relate to parts of the international application that do not comply with the prescribed requirements to such an extent that no meaningful international search can be carried out, specifically:
  
3.  Claims Nos.:  
because they are dependent claims and are not drafted in accordance with the second and third sentences of Rule 6.4(a).

## Box No. III Observations where unity of invention is lacking (Continuation of item 3 of first sheet)

This International Searching Authority found multiple inventions in this international application, as follows:

see additional sheet

1.  As all required additional search fees were timely paid by the applicant, this international search report covers all searchable claims.
  
2.  As all searchable claims could be searched without effort justifying an additional fees, this Authority did not invite payment of additional fees.
  
3.  As only some of the required additional search fees were timely paid by the applicant, this international search report covers only those claims for which fees were paid, specifically claims Nos.:
  
4.  No required additional search fees were timely paid by the applicant. Consequently, this international search report is restricted to the invention first mentioned in the claims; it is covered by claims Nos.:

1-15

### Remark on Protest

- The additional search fees were accompanied by the applicant's protest and, where applicable, the payment of a protest fee.
- The additional search fees were accompanied by the applicant's protest but the applicable protest fee was not paid within the time limit specified in the invitation.
- No protest accompanied the payment of additional search fees.

**FURTHER INFORMATION CONTINUED FROM PCT/ISA/ 210**

This International Searching Authority found multiple (groups of) inventions in this international application, as follows:

1. claims: 1-15

    how to avoid visual artefacts

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2. claims: 16-30

    how to reduce computational costs

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3. claims: 31-45

    how to derive motion vector for chroma blocks

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4. claims: 46-58

    how to reduce interpolation filter overhead

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5. claims: 59-71

    how to define bitstream property description and profile set operations

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6. claims: 72-90

    how to avoid parsing dependencies

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**INTERNATIONAL SEARCH REPORT**

Information on patent family members

International application No

PCT/EP2020/056355

Patent document cited in search report	Publication date	Patent family member(s)		Publication date
WO 2018170279	A1 20-09-2018	CN 110651476 A	EP 3596926 A1	03-01-2020

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