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(54) Title: ARCHITECTURE TO SUPPORT MULTIPLE CONCURRENT EXECUTION CONTEXTS ON A PROCESSOR

(57) Abstract: Multithreading permits execution of instructions across multiple hardware contexts without software context switching. This may result in lower power consumption, increased throughput, and higher performance. The invention describes an architecture whereby a multithreading processor may be initialized and controlled by threads running on the processor.

INTERNATIONAL SEARCH REPORT

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A. CLASSIFICATION OF SUBJECT MATTER
 IPC 7 G06F9/46

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)
 IPC 7 G06F

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)
 EPO-Internal, WPI Data

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	WO 99/21089 A (INTERNATIONAL BUSINESS MACHINES CORPORATION) 29 April 1999 (1999-04-29) abstract page 9, line 1 - line 20 page 11, line 18 - line 34 page 19, line 20 - line 26	1-27
A	US 5 968 157 A (JOY ET AL) 19 October 1999 (1999-10-19) abstract column 1, line 63 - line 67 column 2, line 65 - column 3, line 6 column 5, line 28 - column 6, line 24	1-27
A	US 5 669 002 A (BUCH ET AL) 16 September 1997 (1997-09-16) abstract column 2, line 1 - line 23	1-27

Further documents are listed in the continuation of box C.
 Patent family members are listed in annex.

° Special categories of cited documents :

A document defining the general state of the art which is not considered to be of particular relevance *E* earlier document but published on or after the international filing date *L* document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified) *O* document referring to an oral disclosure, use, exhibition or other means *P* document published prior to the international filing date but later than the priority date claimed	*T* later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention *X* document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone *Y* document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art. *&* document member of the same patent family
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Date of the actual completion of the international search	Date of mailing of the international search report
17 May 2005	25/05/2005

Name and mailing address of the ISA European Patent Office, P.B. 5818 Patentlaan 2 NL - 2280 HV Rijswijk Tel. (+31-70) 340-2040, Tx. 31 651 epo nl, Fax: (+31-70) 340-3016	Authorized officer <p style="text-align: center; font-weight: bold;">Archontopoulos, E</p>
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INTERNATIONAL SEARCH REPORT

Information on patent family members

Intern application No
PCT/US 03/17221

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