BIDIRECTIONAL BATTERY POWER INVERTER

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Related U.S. Patent Documents
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Field of Classification Search
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See application file for complete search history.

ABSTRACT

Disclosure is a bidirectional battery power inverter comprising a DC-DC converter circuit element (3) to which the battery (2) can be connected in order to generate an AC output voltage from a battery (2) voltage in a discharging mode while charging the battery (2) in a charging mode. The inverter further comprises an HF transformer which forms a resonant circuit along with a resonant capacitor (6). In order to increase the efficiency of said battery power inverter, the transformer is provided with two windings (11, 12) with a center tap (20) on the primary side, said center tap (20) being connected to a power electronic center-tap connection with semiconductor switches (21, 31) while a winding (13) to which the resonant capacitor (6) is serially connected is provided on the secondary side.

36 Claims, 7 Drawing Sheets
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BIDIRECTIONAL BATTERY POWER INVERTER

Matter enclosed in heavy brackets [ ] appears in the original patent but forms no part of this reissue specification; matter printed in italics indicates the additions made by reissue.

This application claims Priority from German Application No. DE 10 2005 023 290.6 filed on 20 May 2005.

BACKGROUND OF THE INVENTION

1. Field of the Invention

The present invention relates to a bidirectional battery power inverter as set forth in the preamble of claim 1 and claim 2.

2. Description of the Prior Art

Bidirectional power inverters serve on the one side to convert a DC battery voltage from 12 volt into a 50 Hz or 60 Hz AC voltage of e.g., 230 volt, on the other side however also to charge the battery from the AC voltage. The energy flows both from the battery to a DC-AC converter and from the DC-AC converter to the battery. The problem arising here is that the conduction losses are quite high at such small DC voltages.

A power inverter with a half-bridge array on the primary side and a full bridge array on the secondary side is known from the document EP 0 820 893 A2.

A bidirectional DC-DC converter is known from EP 1 458 084 A2. It includes a bridge circuit on the input side, a bridge circuit on the output side and a transformer connected between said two circuits, said transformer having a resonant capacitor and a resonant inductor.

A DC-AC converter having a transformer, which comprises two windings with center tap on the primary side, is connected to a power electronic midpoint circuit with semiconductor switches and comprises a winding on the secondary side, is shown and described in U.S. Pat. No. 6,507,503 B2.

Further converter circuits have been disclosed in the document DE 40 13 506 A1 and in U.S. 2003/0142513 A1.

BRIEF SUMMARY OF THE INVENTION

It is the object of the invention to provide a high efficiency power inverter.

This object is achieved by a power inverter having the characterizing features of claim 1 in connection with the features of the preamble.

The invention allows benefiting from all the advantages of resonant switching without the tradeoff of high conduction losses on the undervoltage side. A high switch frequency can be utilized hereby. As a result, a planar transformer, which is provided in accordance with a preferred embodiment, may be utilized.

The present invention combines the advantages of low conduction losses and the possibility of utilizing a planar transformer without the trade-off of an unfavorable high apparent power of the transformer and switch overvoltages on the primary side, a restricted operating range and high conduction and switch losses on the low voltage side. Put another way, this means that with the topology of the invention, as contrasted to prior art, a very low-loss, fully resonant switching is made possible on the high current side by the fact that the adjustment stage (boost-buck chopper) is activated in operation ranges in which the voltage needs to be adapted.

In accordance with the invention, there is provided that a DC-AC converter circuit element is provided, said converter circuit element lying on the output side of the inverter and comprising a boost-buck chopper that is connected between the DC-DC converter and the DC-AC converter circuit element. As a result, switching losses due to non-optimal operation in which the DC-DC converter cannot be switched resonantly and an unfavorable current/voltage configuration in the optimal point of operation are avoided.

By virtue of the fact that a resonant switching element is distributed in circuit parts with staggered clocking, the capacitor load is low and the HF ripple current in the DC source is low as well.

 Appropriately, the clock rate of the half-bridge circuit is less than the resonant frequency that is determined by the transformer leakage inductance and the resonant capacitor, depending on whether there is provided one such capacitor or two capacitors connected in series. The resonant frequency is obtained from the leakage inductance of the transformer and the series resonant capacitance or from the leakage inductance of the transformer and the series connected capacitors.

Selecting this clock rate, the semiconductors are switched both on and off with zero current.

If the clock rate of the half-bridge circuit is less than the resonant frequency obtained from the leakage inductance of the transformer and the series resonant capacitance, switching on should always occur at zero current since in this case the switching losses are low or inexistent. In accordance with another advantageous developed implementation of the invention, there is provided a synchronous actuation of the boost-buck chopper and of the resonant converter. The synchronous actuation has the advantage that the effective current load is minimized in the capacitors of the power inverter.

A particular effect of benefit is achieved if the transformer is configured to be a planar transformer, said planar transformer being provided with two printed circuit boards. Both the core of the transformer and the printed circuit boards are housed in a housing. A cast housing is manufacturable at lower cost since projections needed in the cast housing of a planar transformer are easier to manufacture. A planar transformer with two printed circuit boards is moreover manufacturable at a lower cost than a planar transformer having a large printed circuit board. A large printed circuit board is more expensive than two small ones.

Other advantageous implementations of the invention are described in the subordinate claims.

An exemplary embodiment is explained in closer detail with reference to the drawings, other advantageous developed implementations of the invention and advantages thereof being described.

BRIEF DESCRIPTION OF THE DRAWING

Fig. 1 shows a circuit diagram of a DC-DC converter of the invention,

Fig. 2 shows an equivalent circuit diagram of the transformer HFT,

Fig. 3 shows a circuit diagram of a DC-DC converter of the invention with two HFT transformers,

Fig. 4 shows a sectional view of a planar transformer and

Fig. 5 shows another view of the planar transformer.

Fig. 6 shows a circuit diagram of a DC-DC converter of the invention with half-bridge capacitors as the resonant elements;
FIG. 7 shows transformer current and transformer voltage on the secondary side of the transformer.

In the Figs., like elements bear the same reference numerals.

DETAILED DESCRIPTION OF THE INVENTION

FIG. 1 shows a power inverter 1 of the invention, for example for supplying AC consumers in island networks. Said power inverter is connected to a battery 2. The power inverter 1 includes one DC-DC converter circuit element 3 with a capacitor 4 connected in parallel to the battery 2 and one HF transformer HFT.

The power inverter 1 further includes a DC-AC converter circuit element 5 that lies on the output side of the power inverter 1 and a boost-buck chopper 8 that is connected between the DC-DC converter circuit element 3 and the DC-AC converter circuit element 5. The DC-AC converter circuit element 5 is configured to be monophase.

The inverter 1 is configured to be a bidirectional battery power inverter and serves for generating an AC output voltage from the battery voltage in a discharge mode of operation and for charging the battery 2 in the charge mode. Together with a resonant capacitor 6, the HF transformer HFT forms a resonant circuit.

FIG. 2 shows an equivalent circuit diagram of the transformer HFT. In accordance with the invention, the transformer HFT has, on the primary side, two windings 11, 12 with a center tap 20, said center tap being connected to a power electronic midpoint circuit with semiconductor switches 21, 31, a winding 13 to which the resonant capacitor 6 is connected in series being provided on the secondary side. The semiconductor switches 21, 31 form a midpoint circuit.

FIG. 2 shows the leakage inductances 15, 16 on the primary side and the leakage inductance 17 on the secondary side. The core of the transformer bears the reference numeral 14. The center tap is indicated at 20. The reference numeral 19 designates an additional inductance.

The semiconductor switches 21, 31 switch alternately and generate the alternating voltage needed for voltage transformation, said alternating voltage being converted back into a direct voltage on the secondary side by the bridge-connected semiconductor elements 41, 51, 61, 71 and being smoothed by a filter capacitor 7. The DC-DC converter element works in a constant point of operation so that its input and output voltage are in a fixed relation dictated by the transformation ratio of the transformer.

Due to the resonant mode of operation, high switching frequencies of more than 50 kHz are appropriate so that a planar transformer may be utilized. It only makes sense to utilize planar transformers at frequencies in excess of 50 kHz.

Preferably, the resonant frequency of the alternating voltage circuit, which is composed of leakage inductances of the transformer HFT and a series resonant capacitance 6 (FIG. 1), is higher than the clock frequency of the semiconductor switch. As a result, the semiconductor switches 21, 31 can be switched on and off with zero current.

There is provided the use of the boost-buck chopper 8 for allowing using the circuit for strongly fluctuating battery voltages. Thanks to the boost-buck chopper 8, the intermediate voltage is prevented from dropping to such an extent at small battery voltages that no output nominal voltage is achieved. If the pulse width in the DC-DC converter were reduced in the charging mode at low battery voltage, the resonant mode of operation could no longer be ensured. As a result, non-optimal operation would cause switch losses to occur. A boost-buck chopper 8 is utilized in particular, said boost-buck chopper adapting the variable battery voltage to a constant voltage at the capacitor 19. The boost-buck chopper 8 consists of a choke 18, the switch elements 141 and 151 and the capacitor 19. If the input voltage (battery voltage) exceeds e.g., 12 V, the voltage at the capacitor 19 increases proportionally with the input voltage. In this case, the boost-buck chopper needs no longer be closed.

As shown in FIG. 3, the transformer circuit can be made from two circuit parts with two HF transformers HFT1 and HFT2. The arrays are thereby provided with staggered clocking so that the capacitor load and the HF ripple current in the DC source are lower. The two circuit parts shown in FIG. 3 are configured to be half-bridge arrays. In a configuration with half-bridges, the transformation ratio of the transformer is only half as high. A smaller transformer transformation ratio is advantageous because then the leakage inductance 15 and 16 transformed from the overvoltage side by the transformation ratio will not become too high.

A half-bridge array needs less semiconductor switches and involves less cost as a result thereof.

The planar transformer 29 shown in FIG. 4 is embedded in an aluminium cast housing 24 and comprises two printed circuit boards 22, 23. The power semiconductors can be configured to be built in SMD construction. As shown in FIG. 5, the transformer comprises a primary winding 26 and a secondary winding 27 that are arranged on a printed circuit board 25. The choke core 30 is provided with an air gap and is also integrated in the printed circuit board 25. It is only magnetized by the secondary winding 27, not by the primary winding 26.

In FIG. 6, there is shown a design of the circuit in which the capacitors 34, 35 form a resonant circuit together with the leakage inductance of HFT. With such a design, the parasite capacitors of the semiconductor switches 21, 31 are discharged to particularly low values by the currents in the main inductance of the transformer prior to switching them on. Smaller switch-on losses occur as a result thereof. The transformer currents and the voltages occurring on the secondary side in this implementation correspond to the illustration shown in FIG. 7. FIG. 7 shows the current 60 and the voltage 50 on the secondary side of the transformer.

The DC-AC converter 5 can be configured to be a H-bridge for converting a one-phase AC voltage or a three-phase bridge for converting a three-phase AC voltage.

An arrangement in which the DC-AC converter 5 is always operated in such a manner that the voltage at the capacitor 19 is always higher than the peak value of the nominal voltage may also be advantageous. As a result, in case the battery is being charged, the DC-AC converter can be operated in a PFC (Power Factor Correction) mode of operation and sinusoidal current in phase with the mains voltage can be tapped from the mains. In case the battery is being discharged, a sinusoidal AC voltage can be generated at any form of current.

In order to be capable of supplying any load (including asymmetric load and unipolar loads) in the case of three-phase converters, a connected N can be added as the fourth phase and the entire circuit can be implemented so that the capacitor 19 consists of two capacitors with a grounded mid-point, each capacitor being charged or discharged separately by a resonant converter using a boost-buck chopper connected downstream thereof.

The semiconductor elements 21, 31, 41, 51, 61, 71, 141, 151 may be configured to be MOSFETs, IGBTs, bipolar transistors or GTOs. The parallel diodes can be separate components or consist of parasite diodes of the MOSFETs utilized. The DC source 2 can be a battery, a fuel cell, a generator-supplied DC intermediate circuit or a double-layer capacitor (Ultracap).
The resonant converter can be advantageously utilized in automobiles in order to allow for energy exchange between different DC sources such as traction battery, double-layer capacitor, auxiliary operating battery and so on. The direction of the energy flow may thereby be reversed while accelerating and braking.

The planar transformer may be advantageously implemented in such a manner that the secondary leakage inductance is increased by integrating an additional choke into the secondary transformer winding. This may occur in the manner shown in FIG. 5.

As contrasted with the half- and full bridge circuits, the semiconductor voltages of the semiconductors 21, 31, 221 and 231 are not limited to the capacitor voltage of the capacitor 4. As contrasted with normal midpoint circuits, the capacitor voltage is not limited to the double of the capacitor voltage of capacitor 4; the voltage drop above the primary side portion of the resonant inductance of the transformers HFT 1 and HFT 21 is added thereon. This effect can only be controlled if the substantial portion of the resonant inductance is displaced for its major part onto the secondary side. This is achieved by the transformer structure described and/or by additional resonant inductances 10 on the secondary side.

LIST OF NUMERALS

1 power inverter
2 battery
3 DC-DC converter circuit element
4 capacitor
5 DC-AC converter circuit element
6 resonant capacitor
7 filter capacitor
8 boost-buck converter
9 additional leakage inductance
10 primary side windings
11 secondary side winding
12 transformer core
13 leakage inductance
14 choke
15 capacitor
16 center tap
17 semiconductor switch
18 printed circuit board
19 aluminum cast housing
20 printed circuit board
21 primary winding
22 secondary winding
23 resonant capacitor
24 semiconductor switch
25 planar transformer
26 choke core
27 semiconductor switch
28 half-bridge capacitors
29 half-bridge capacitors as resonant elements
30 DC voltage of the battery in a discharge mode of operation and
31 for charging the battery generating an DC output voltage from an AC voltage in a charge mode of operation, [said] the power inverter [including comprising:
32 a DC-DC converter circuit element comprising a resonant circuit comprising an HF transformer [that forms a resonant circuit together with] and a resonant capacitor, [characterized in that] wherein the HF transformer comprises two primary windings with a center tap on its primary side, [said] the center tap being connected to a power electronic midpoint circuit with semiconductor switches, a secondary winding to which the resonant capacitor is connected in series being provided on the a secondary side.
33 [that] a DC-AC converter circuit element is provided which lies disposed on an output side of the power inverter [and comprises:
34 a boost or buck converter that is connected between the DC-DC converter circuit element and the DC-AC converter circuit element; and
35 that] a resonant switching element is being distributed in circuit parts having a staggered clock clocking rate, [the] a resonant frequency of the alternating voltage a resonant circuit being higher than [the] a clock frequency of a half-bridge.
36 2. The bidirectional battery power inverter as set forth in claim 1, wherein [characterized in that] the DC-DC converter circuit element comprises a half-bridge.
37 3. The bidirectional battery power inverter as set forth in claim 1, wherein [characterized in that] the transformer is configured to be comprised a planar transformer.
38 4. The bidirectional battery power inverter as set forth in claim 1, [characterized in that] wherein the primary windings of the transformer are only [wound about] wound about a transformer core while the secondary winding is [wound about] wound around the transformer core and [about] an additional choke core.
39 5. A bidirectional battery power inverter with a DC-DC converter circuit element, to which a battery may be connected, for generating an AC output voltage from a [battery] DC voltage of the battery in a discharge mode of operation and for charging the battery generating a DC output voltage from an AC voltage in a charge mode of operation, wherein [said] the power inverter [including comprising:
40 a resonant circuit comprising an HF transformer that forms a resonant circuit together with] and resonant capacitors, [characterized in that] wherein the HF transformer comprises two primary windings with a center tap on its primary side, [said] the center tap being connected to a power electronic midpoint circuit with semiconductor switches, and [one] wherein the HF transformer further comprises a secondary winding on the a secondary side, wherein the winding being connected to the resonant capacitors at a convergence point.
41 [that] a DC-AC converter circuit element is provided which lies disposed on an output side of the power inverter and comprises comprising a boost or buck converter that is connected between the DC-DC converter circuit element and the DC-AC converter circuit element; and
42 [that] a resonant switching element is distributed in circuit parts having a staggered clock rate.
43 6. The bidirectional battery power inverter as set forth in claim 5, wherein [said] the DC-DC converter circuit element comprises a half-bridge.
7. The bidirectional battery power inverter as set forth in claim 5, characterized in that wherein the transformer [is configured to be] comprises a planar transformer.

8. The bidirectional battery power inverter as set forth in claim 5, characterized in that wherein the primary windings of the transformer are only [configured about the] wound around a transformer core while the secondary winding is [configured about] wound around the transformer core and [configured about] an additional choke core.

9. A bidirectional battery power inverter for [bidirectionally] converting electrical power between a DC side and an AC side of the inverter, wherein the DC side being connectable to a battery, the inverter comprising:
   a DC/DC converter comprising a power electronic midpoint circuit coupled to a primary winding of an HF transformer,
   and bridge-connected semiconductor elements coupled to a secondary winding of the HF transformer through a capacitance,
   wherein the capacitance in conjunction with a leakage inductance of the HF transformer defines a resonant frequency[,] and
   a DC/AC converter connected to the DC/DC converter, the DC/AC converter comprising a boost-buck chopper [for avoiding voltage-dropping, so that] configured to adjust a voltage to an output nominal voltage [is achieved], wherein the power electronic midpoint circuit is configured to [be operated] operate at an operation frequency lower than the resonant frequency in order to allow for a substantially zero current switching of the first and second power electronic midpoint circuit achieved circuits, and wherein the coupling capacitances are [each] formed by two capacitors connected in series, [one] a tap of each of the secondary windings [of each] of the first and second HF transformers being connected to midpoints of the corresponding capacitors.

10. The bidirectional battery power inverter of claim 9, wherein the primary winding comprises a center tap being connected to a first battery terminal, and wherein the power electronic midpoint circuit comprises two switches being connected to a second battery terminal.

11. The bidirectional battery power inverter of claim 9, wherein the capacitance comprises two capacitors connected in series between connection terminals, and wherein the secondary winding is connected to a midpoint of the two capacitors.

12. The bidirectional battery power inverter of claim 9, wherein the DC/DC converter further comprises a second power electronic midpoint circuit coupled to a primary winding of a second HF transformer, and second bridge-connected semiconductor elements coupled to a secondary winding of the second HF transformer through a second capacitance.

13. The bidirectional battery power inverter of claim 12, wherein the power electronic midpoint circuit and the second power electronic midpoint circuit are configured to be operated at the same operation frequency by an offset clocking.

14. The bidirectional battery power inverter of claim 9, wherein the bridge-connected semiconductor elements are [configured to form] a half bridge.

15. The bidirectional battery power inverter of claim 9, wherein the bridge-connected semiconductor elements are [configured to form] a full bridge.

16. The bidirectional battery power inverter of claim 9, wherein the HF transformer is a planar transformer.

17. The bidirectional battery power inverter of claim 9, wherein the primary winding of the HF transformer is only [configured about] wound around a transformer core, and the secondary winding of the HF transformer is [configured about] wound around an additional choke core.

18. A bidirectional [battery] power inverter for bidirectionally converting electrical power between a DC side and an AC side of the inverter, wherein the DC side being connectable to a battery, the inverter comprising:
   a DC/DC converter comprising a first and a second power electronic midpoint circuit coupled to corresponding primary windings of a first HF transformer and a second HF transformer,
   the DC/DC converter further comprising first and second bridge-connected semiconductor elements coupled to corresponding secondary windings of the first and second HF transformers through corresponding coupling capacitances defining a resonant frequency in conjunction with a leakage inductance of the HF transformers; and
   a DC/AC converter connected to the DC/DC converter, the DC/AC converter comprising a boost-buck chopper for avoiding voltage-fluctuation, so that an output nominal voltage is achieved, wherein the first and second power electronic midpoint circuit is configured to be operated at an operation frequency lower than the resonant frequency in order to allow for a substantially zero current switching of the first and second power electronic midpoint circuit achieved circuits, and wherein the coupling capacitances are [each] formed by two capacitors connected in series, [one] a tap of each of the secondary windings [of each] of the first and second HF transformers being connected to midpoints of the corresponding capacitors.
US RE45,069 E

1. A bidirectional DC-DC converter comprising:
an input comprising a first input terminal and a second input terminal;
an output comprising a first output terminal and a second output terminal;
a first power electronic midpoint circuit comprising a first switch and a second switch;
a first transformer comprising a primary side and a secondary side, the primary side connected to the first power electronic midpoint circuit, wherein a first terminal of the first switch of the first power electronic midpoint circuit is connected to a first terminal of the primary side of the first transformer, wherein a first terminal of the second switch of the first power electronic midpoint circuit is connected to a second terminal of the primary side of the first transformer, wherein a second terminal of the first switch of the first power electronic midpoint circuit and a second terminal of the second switch of the first power electronic midpoint circuit are connected to the second output terminal, and wherein the first input terminal is connected to a midpoint terminal of the primary side of the first transformer;
a circuit comprising switches and capacitors, wherein a first terminal of a first switch of the circuit and a first terminal of a first capacitor of the circuit are connected to the first output terminal, wherein a second terminal of a second switch of the circuit and a second terminal of a second capacitor of the circuit are connected to the second output terminal, wherein a first transformer of the circuit and a first terminal of the first capacitor of the circuit are connected to a midpoint terminal of the primary side of the first transformer; and
a boost-buck chopper element connected to the first output terminal and the second output terminal.

2. The bidirectional DC-DC converter as set forth in claim 1, wherein a capacitance of the capacitor and a leakage inductance of the transformer defines a resonant frequency, and wherein the DC-DC converter element is configured to be operated at an operation frequency which is lower than the resonant frequency.

3. A device for bidirectionally converting electrical power between a first side and a second side comprising:
a DC-DC converter element comprising:
a midpoint circuit configured to convert a first DC voltage to a second DC voltage and further configured to provide the first AC voltage to a transformer, the midpoint circuit connected to a primary side of the transformer, wherein the primary comprises two windings with a center tap and the midpoint circuit comprises two switches;
a bridge circuit connected to a secondary side of the transformer, the bridge circuit configured to convert a transformed first AC voltage to a second DC voltage, the bridge comprising a capacitor connected to a winding of the secondary side of the transformer; and
a boost-buck chopper element connected to the DC-DC converter element, wherein a capacitance of the capacitor and a leakage inductance of the transformer defines a resonant frequency, and wherein the DC-DC converter element is configured to be operated at an operation frequency which is lower than the resonant frequency.

4. The device as set forth in claim 3, wherein the bridge circuit comprises a full bridge connected through the capacitor to the winding of the secondary side.

5. The device as set forth in claim 3, wherein the bridge circuit comprises a half bridge and a resonant capacitor circuit.

6. The device as set forth in claim 3, wherein the two switches of the midpoint circuit are configured to be switched on and off with zero current.

7. The device as set forth in claim 3, wherein the boost-buck chopper element comprising a choke and a further capacitor.

8. The device as set forth in claim 3, wherein boost-buck chopper element is configured to adapt a variable first AC voltage to a constant DC voltage.

9. The device as set forth in claim 3, further comprising a DC-AC inverter element connected to the boost-buck chopper.

10. A bidirectional DC-DC converter comprising:
an input comprising a first input terminal and a second input terminal;
an output comprising a first output terminal and a second output terminal;
a first power electronic midpoint circuit comprising a first switch and a second switch;
a first transformer comprising a primary side and a secondary side, the primary side connected to the first power electronic midpoint circuit, wherein a first terminal of the first switch of the first power electronic midpoint circuit is connected to a first terminal of the primary side of the first transformer, wherein a first terminal of the second switch of the first power electronic midpoint circuit is connected to a second terminal of the primary side of the first transformer, wherein a second terminal of the first switch of the first power electronic midpoint circuit and a second terminal of the second switch of the first power electronic midpoint circuit are connected to the second output terminal, and wherein the first input terminal is connected to a midpoint terminal of the primary side of the first transformer; and
a boost-buck chopper element connected to the first output terminal and the second output terminal.
A circuit comprising switches and capacitors, wherein a first terminal of a first switch of the circuit and a first terminal of a first capacitor of the circuit are connected to the first output terminal, wherein a second terminal of a second switch of the circuit and a second terminal of a second capacitor of the circuit are connected to the second output terminal, wherein a second terminal of the first switch of the circuit and a first terminal of the second switch of the circuit are connected to a first terminal of the secondary side of the first transformer through a first resonant capacitor, wherein a second terminal of the first capacitor of the circuit and a first terminal of the second capacitor of the circuit are connected to a second terminal of the secondary side of the first transformer, wherein a first terminal of a third switch of the circuit is connected to the first output terminal and a second terminal of a fourth switch is connected to the second output terminal, wherein a second terminal of the third switch of the circuit and a first terminal of the fourth switch of the circuit are connected to a first terminal of the secondary side of the second transformer through a second resonant capacitor, and wherein the second terminal of the first capacitor of the circuit and the first terminal of the second capacitor of the circuit are connected to a second terminal of the secondary side of the second transformer, and wherein the first and second power electronic midpoint circuits are configured to be operated at an operation frequency which is lower than a resonant frequency; and a boost-buck chopper element connected to the first output terminal and the second output terminal of the DC-DC converter element, the boost-buck chopper configured to avoid voltage fluctuations.

34. The bidirectional device as set forth in claim 33, further comprising a DC-AC inverter element connected to the boost-buck chopper.

35. A device comprising:
   a bidirectional DC-DC converter including a resonant circuit comprising a transformer and resonant capacitors, wherein the transformer comprises a primary side with a center tap, the center tap being connected to a power electronic midpoint circuit comprising semiconductor switches, and wherein the transformer further comprises a secondary winding on a secondary side, the secondary winding being connected to the resonant capacitors at a convergence point; and
   a boost or buck chopper connected to the DC-DC converter.

36. The device as set forth in claim 35, further comprising a half-bridge circuit.

* * * * *
It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In the Claims

In Col. 7, line 1, claim 7, delete “inserter” and insert --inverter--.