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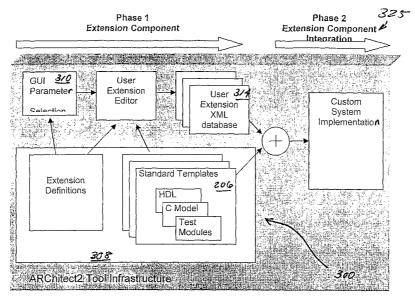
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(54) Title: IMPROVED COMPUTERIZED EXTENSION APPARATUS AND METHODS



(57) Abstract: Apparatus and methods for integrated circuit (IC) design (fig 1-14), including the configuration and addition of extensions to the design (fig 1-14). In one exemplary embodiment, a computer program rendered in an object-oriented language implementing the aforementioned methods for automatically adding user-customized extensions to digital processors is disclosed (fig 1-14). The program comprises an extension tool which is adapted for varying levels of abstraction, and to significantly automate the creation and generation of various different extension types including for example ALUs, condition codes, and registers (fig 1-14). A markup language (e.g., XML) database of abstracted extension components is utilized to permit ready addition and modification of extensions, as well as applicability of the extensions across different target architectures (fig 1-14).



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For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.

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Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched			
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EAST			
C. DOC	UMENTS CONSIDERED TO BE RELEVANT	. , , , , , , , , , , , , , , , , , , ,	·
Category *			Relevant to claim No.
X	US PUB 2002/0156929 (HEKMATPOUR) 24 October 2002 (24.10.2002) fig 1-8		1-41
11	05 1 05 2002/0100725 (ALEXANIA CON 2 1 000000 2002 (2 112012002) 11g 1 0		- 1.2
X	US PUB 2003/0009658 (CHEN et al) 09 January 2003 (09.01.2003) fig 1-20		1-41
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