



US00D613889S

(12) **United States Design Patent**
Liu

(10) **Patent No.:** **US D613,889 S**

(45) **Date of Patent:** **** Apr. 13, 2010**

(54) **LIGHT EMITTING DIODE BULB**

D99,637 S *	5/1936	Abramson	D26/4
D370,282 S *	5/1996	Grether	D26/4
D381,441 S *	7/1997	Cheung	D26/4

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* cited by examiner

(**) Term: **14 Years**

Primary Examiner—Marcus A Jackson

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(21) Appl. No.: **29/346,256**

(57) **CLAIM**

(22) Filed: **Oct. 29, 2009**

The ornamental design for a light emitting diode bulb, as shown.

(30) **Foreign Application Priority Data**

Aug. 7, 2009 (CN) 2009 3 0311340.6

DESCRIPTION

(51) **LOC (9) Cl.** **26-06**

FIG. 1 is a perspective view of a light emitting diode bulb showing our new design.

(52) **U.S. Cl.** **D26/4**

FIG. 2 is a front elevational view thereof.

(58) **Field of Classification Search** D26/1-4;
313/313, 315, 316, 317, 318, 493; 315/52,
315/53, 56, 57, 58

FIG. 3 is a rear elevational view thereof.

FIG. 4 is a left-side, elevational view thereof.

See application file for complete search history.

FIG. 5 is a right-side, elevational view thereof.

(56) **References Cited**

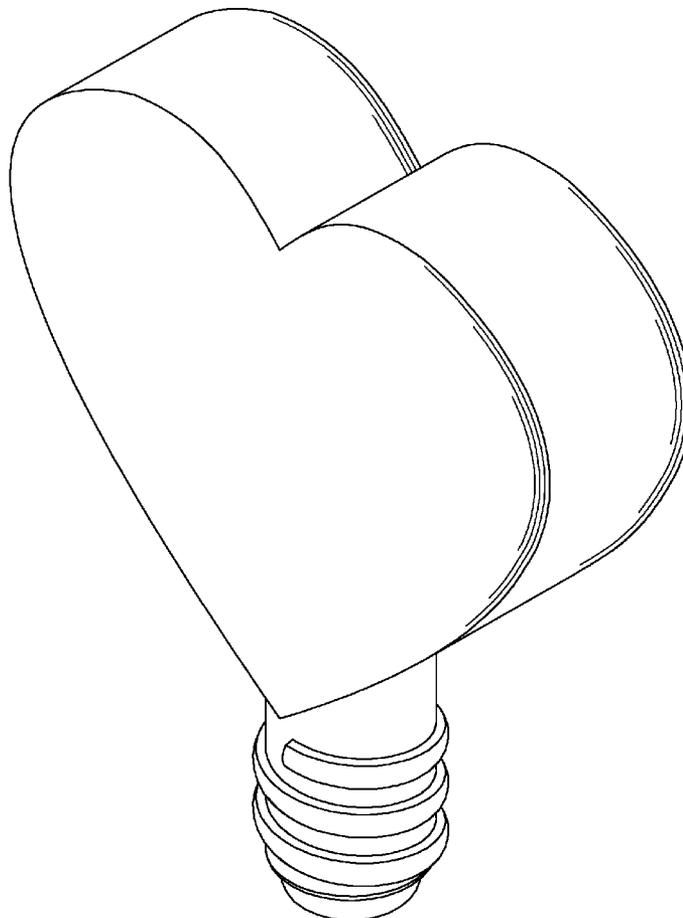
FIG. 6 is a top plan view thereof; and,

FIG. 7 is a bottom plan view thereof.

U.S. PATENT DOCUMENTS

D94,890 S * 3/1935 Graf D26/4

1 Claim, 7 Drawing Sheets



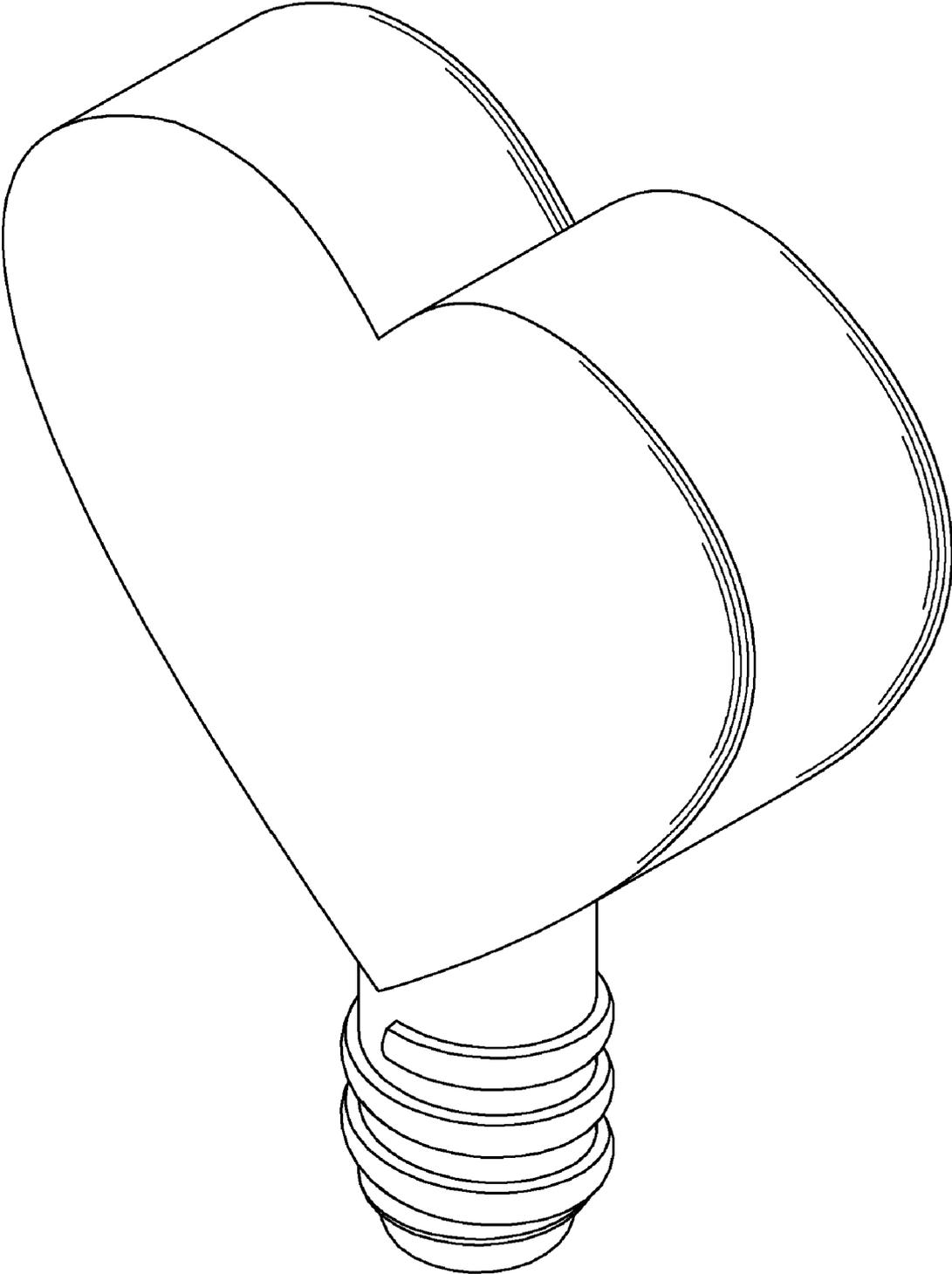


FIG. 1

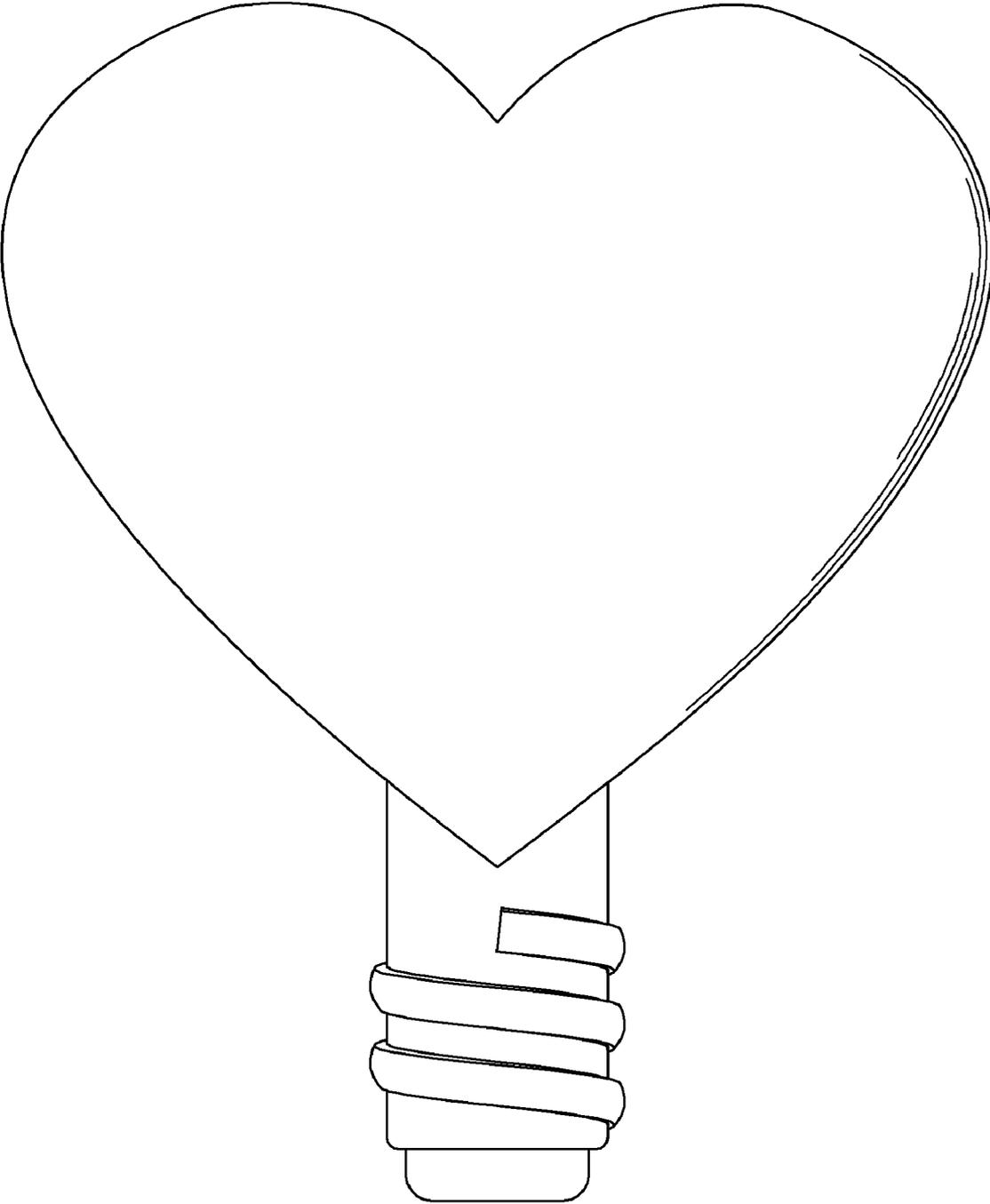


FIG. 2

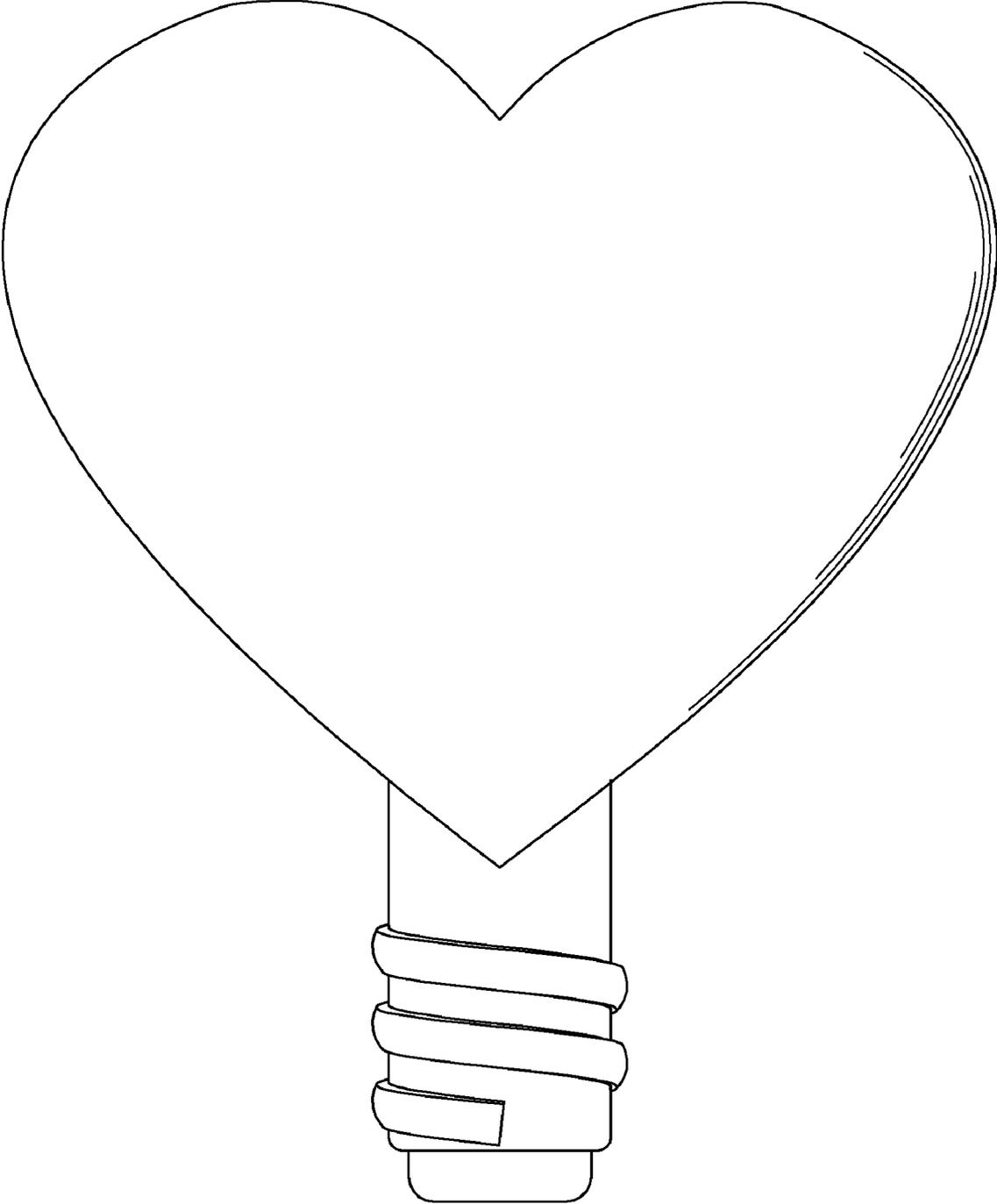


FIG. 3

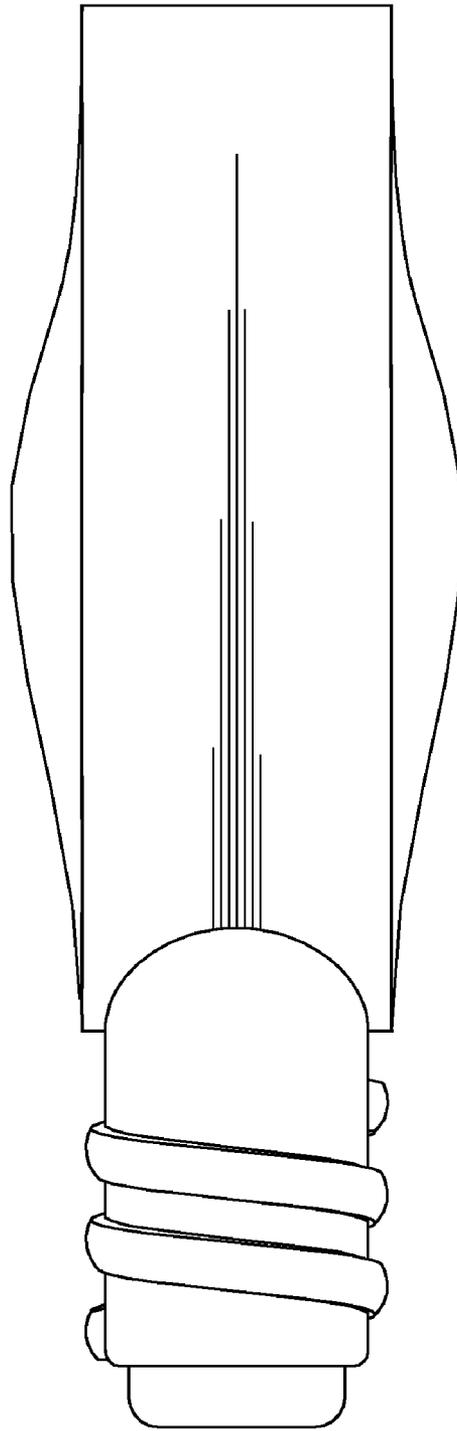


FIG. 4

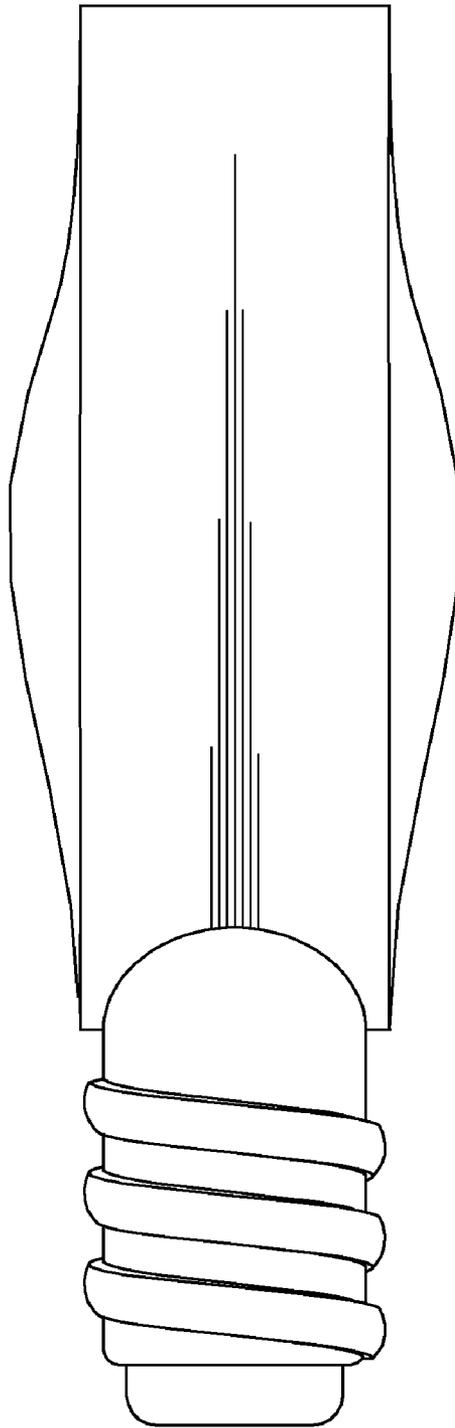


FIG. 5

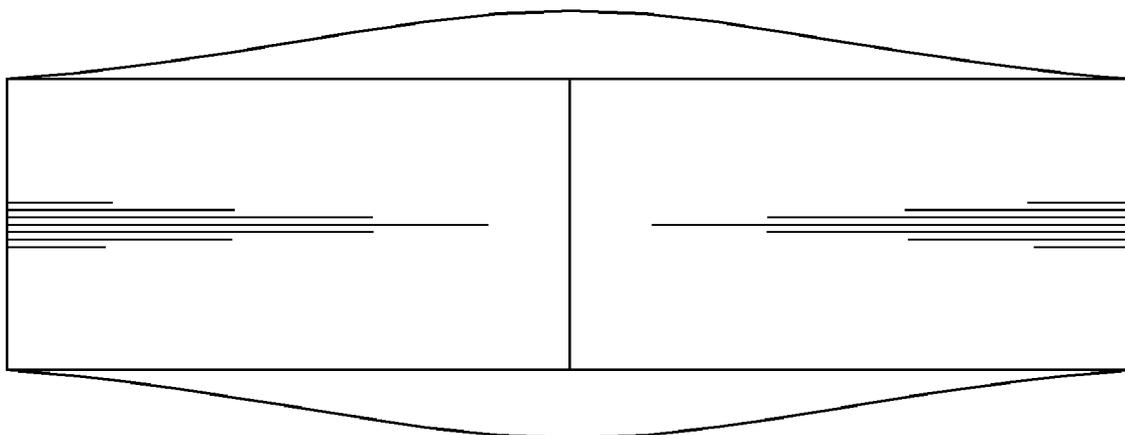


FIG. 6

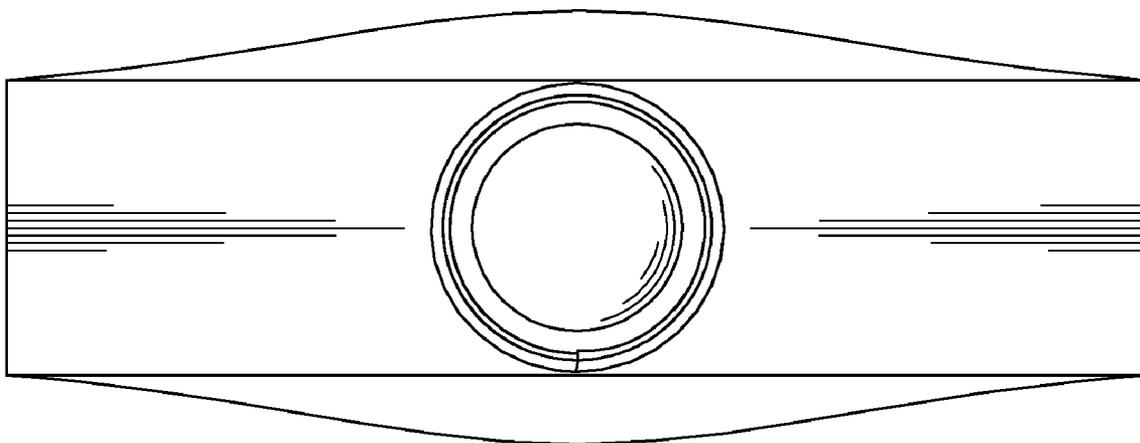


FIG. 7