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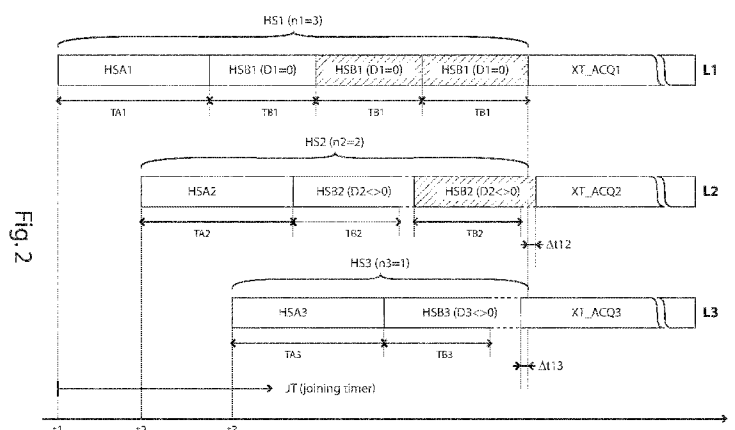
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(54) Title: TIME-ALIGNMENT OF CROSSTALK ACQUISITION PHASES BETWEEN MULTIPLE JOINING LINES



(57) Abstract: The present invention relates to an access node (100) for providing broadband communication services to subscribers. In accordance with an embodiment of the invention, the access node comprises a first transceiver unit (101) for initializing a first communication channel (CH1) over a first communication line (L1) and a second transceiver unit (102) for subsequently initializing a second communication channel (CH2) over a second communication line (L2), which initialization of the first and second communication channels respectively comprising first and second handshake phases (HS1, HS2) whereby peer transceiver units exchange capabilities and select a common mode of operation, first and second crosstalk channel acquisition phases (XT_ACQ1, XT_ACQ2) from the first and second communication lines into further active communication lines (Lx), and further first and second generic initialization phases. The access node further comprises a line initialization controller (111) configured to stretch the execution of the first handshake phase with respect to the execution of the second handshake phase so as to shorten a time difference (Δt_{12}) between respective entries into the first and second crosstalk channel acquisition phases. The present invention also relates to a method for initializing communication channels over communication lines.

**TIME-ALIGNMENT OF CROSSTALK ACQUISITION PHASES BETWEEN
MULTIPLE JOINING LINES**

Technical Field of the Invention

The present invention relates to access nodes providing
5 broadband communication services to subscribers.

Technical Background of the Invention

Crosstalk (or inter-channel interference) is a major
source of channel impairment for Multiple Input Multiple Output
(MIMO) communication systems, such as Digital Subscriber Line
10 (DSL) communication systems.

As the demand for higher data rates increases, DSL
systems are evolving toward higher frequency bands, wherein
crosstalk between neighboring transmission lines (that is to
say, transmission lines that are in close vicinity such as
15 twisted copper pairs in a cable binder) is more pronounced (the
higher frequency, the more coupling).

A MIMO system can be described by the following linear
model:

$$\mathbf{Y}(f) = \mathbf{H}(f)\mathbf{X}(f) + \mathbf{Z}(f) \quad (1)$$

20 wherein the N-component complex vector $\mathbf{X}$, respectively $\mathbf{Y}$,
denotes a discrete frequency representation of the symbols
transmitted over, respectively received from, the N channels,
wherein the $N \times N$ complex matrix $\mathbf{H}$ is referred to as the channel
matrix: the (i,j) -th component of the channel matrix $\mathbf{H}$ describes
25 how the communication system produces a signal on the i -th
channel output in response to a symbol being transmitted to the
 j -th channel input; the diagonal elements of the channel matrix
describe direct channel coupling, and the off-diagonal elements
of the channel matrix describe inter-channel coupling (also
30 referred to as the crosstalk coefficients),
and wherein the N-component complex vector $\mathbf{Z}$ denotes additional
noise present over the N channels, such as alien interference,
thermal noise and Radio Frequency Interference (RFI).

Different strategies have been developed to mitigate
35 crosstalk and to maximize effective throughput, reach and line
stability. These techniques are gradually evolving from static
or dynamic spectral management techniques to multi-user signal
coordination (or vectoring).

One technique for reducing inter-channel interference
40 is joint signal precoding: the transmit data symbols are jointly

passed through a precoding matrix before being transmitted over the respective communication channels. The precoding matrix is such that the concatenation of the precoder and the communication channel results in little or no interference at the receiver. This is achieved by adding to the original signal an anti-phase signal that is the inverse of an estimate of the aggregate crosstalk signal.

A further technique for reducing inter-channel interference is joint signal post-processing: the received data symbols are jointly passed through a crosstalk cancellation matrix before being detected. The crosstalk cancellation matrix is such that the concatenation of the post-coder and the communication channel results in little or no interference at the receiver. This is achieved by subtracting from the received signal an estimate of the aggregate crosstalk signal.

Signal vectoring is typically performed at a traffic aggregation point, whereat all the data symbols that are to be concurrently transmitted and/or received are available. Signal precoding is particularly suited for downstream communication, while signal post-processing is particularly suited for upstream communication.

The choice of the vectoring group, that is to say the set of communication lines, the signals of which are jointly processed, is rather critical for achieving good crosstalk cancellation performances. Within that group, each communication line is considered as a disturbing line inducing crosstalk into the other communication lines of the group, and the same communication line is considered as a victim line receiving crosstalk from the other communication lines of the group. Crosstalk from lines that do not belong to the vectoring group is treated as alien noise and is not canceled.

Ideally, the vectoring group should match the whole set of communication lines that physically and noticeably interact with each other. Yet, limited vectoring capabilities and/or specific network topologies may prevent such an exhaustive approach, in which case the vectoring group would include a subset only of all the physically interacting lines, thereby yielding limited crosstalk cancellation performances.

The performance of signal precoding and post-processing depends critically on the component values of the precoding and cancellation matrix respectively, which component values are to

be computed and updated according to the actual (and varying) crosstalk coupling functions between the respective communication channels.

A prior art method for estimating the crosstalk
5 coefficients comprises the steps of:

- simultaneously transmitting a plurality of mutually orthogonal crosstalk pilot sequences of length L through respective ones of a plurality of disturber channels,
- measuring errors induced over a victim channel while the pilot
10 sequences are being transmitted,
- correlating the error measurements with respective ones of the plurality of crosstalk pilot sequences, thereby yielding a plurality of correlated error measurements,
- estimating the crosstalk coefficients from the plurality of
15 disturber channels into the victim channel based on respective ones of the plurality of correlated error measurements.

That is, transceiver units send mutually orthogonal downstream and/or upstream pilot signals. Error samples, measuring both interference and noise over the victim channel,
20 are fed back to a Vectoring Control Entity (VCE). Error samples contain both amplitude and phase information on a per-tone basis, or on a per-group-of-tones basis. The error samples are correlated with a given pilot sequence in order to obtain the crosstalk contribution from a specific line. To reject the
25 crosstalk contribution from the other lines, i.e. in order to fulfill the orthogonality requirement, a multiple of L error samples shall be collected and processed. The crosstalk estimates are used for updating the precoding and/or cancellation matrix. The process can be repeated as needed to
30 obtain more and more accurate estimates.

The orthogonality requirement further implies that the length L of the pilot sequences is lower-bounded by the size of the vectoring group: the more channels, the more lengthy the pilot sequences, the longer the estimation of the crosstalk
35 coefficients.

This prior art method has been adopted by the International Telecommunication Union (ITU) for use with VDSL2 transceivers, and is described in the recommendation entitled
"Self-FEXT Cancellation (vectoring) For Use with VDSL2
40 Transceivers", ref. G.993.5 (04/2010).

In this recommendation, it is currently envisaged that the pilot signals would be sent on the so-called SYNC symbols, which occur periodically after every 256 DATA symbols.

On a given disturber line, a representative subset of the active carriers (or tones) of the SYNC symbol are 4-QAM modulated by the same pilot digit (+1 or -1) from a given pilot sequence, and thus all transmit one of two complex constellation points, either '1 + j' corresponding to '+1', or '-1 - j' corresponding to '-1'. The remaining carriers of the SYNC symbol keeps on carrying the typical SYNC-FLAG for EOC message acknowledgment.

On a given victim line, error samples are measured and reported for a specific SYNC symbol to the VCE for further crosstalk estimation.

In G.993.5, it is further assumed that the access node transmits and receives the SYNC symbols over the vectored lines synchronously (super frame alignment) so as pilot signal transmission and error measurements occur simultaneously.

If a line comes into service (e.g., after modem start-up at subscriber premises), the crosstalk coefficients from the new joining line into the already active lines need to be estimated first, and the precoder and/or post-coder be updated accordingly, before the new joining line can transmit at full power over the DATA symbols, else the raising interference may bring about a line retrain on a few active lines (if the newly induced interference exceeds the configured noise margin). Similarly, the crosstalk coefficients from the already active lines into the joining line need to be estimated first, and the precoder and/or post-coder be updated accordingly, before the new joining line starts determining respective carrier bit loadings and gains so as to take full profit from the vectoring gains.

G.993.5 defines new crosstalk acquisition phases during the VDSL2 initialization procedure for acquiring the crosstalk coefficients from the new joining line into the active lines, and vice-versa.

A first crosstalk acquisition phase is carried out after the HANDSHAKE phase, whereby peer transceiver units acknowledges their mutual presence, exchange their respective capabilities and agree on a common mode of operation, and the CHANNEL DISCOVERY phase, during which peer transceiver units

exchange basic communication parameters through the SOC channel while transmitting at full power within the assigned communication band. The first crosstalk acquisition phase is termed O-P-VECTOR 1 and R-P-VECTOR 1 for downstream and upstream communication respectively, and aims at estimating the downstream and upstream crosstalk coefficients from the initializing line into the already active lines. O-P-VECTOR 1 and R-P-VECTOR 1 signals comprise SYNC symbols only, which are aligned with the SYNC symbols of the active lines, and thus do not impair communication over the active lines. O-P-VECTOR 1 is followed by O-P-VECTOR 1-1; R-P-VECTOR 1 is followed by R-P-VECTOR 1-1 and R-P-VECTOR 1-2.

A second crosstalk acquisition phase is carried out after the CHANNEL TRAINING phase takes place, that is to say after the time equalizer and/or the echo canceler have been adjusted, and before the CHANNEL ANALYSIS AND EXCHANGE phase, that is to say before Signal to Noise and Interference Ratio (SNIR) is measured and corresponding bit loading and gain values are determined for the respective carriers. The second crosstalk acquisition phase is termed O-P-VECTOR 2-1 and R-P-VECTOR 2 for downstream and upstream communication respectively, and aims at estimating the crosstalk coefficients from the already active lines into the initializing line.

A clause in § 10.3 of G.993.5 ITU recommendation states that *"if several lines are initialized simultaneously, the initialization procedures of these lines have to be aligned in time, so that all lines pass the vectoring-related phases simultaneously (see clauses 10.3.3.6 and 10.4.3.9)"*. Further in § 10.3.3.6 op. cit., the following further technical details are mentioned in case multiple lines are initialized:

- *"The downstream crosstalk channels from the initializing lines into the active lines of the vector group should be estimated simultaneously by insuring that O-P-VECTOR 1 signals are sent on all initialization lines during the estimation. This can be done by controlling the end and the start of O-P-VECTOR 1 in each line.*
- *The upstream crosstalk channels between the initializing lines and the active lines of the vector group should be estimated simultaneously by insuring that R-P-VECTOR 1 signals are sent on all initialization lines during the*

estimation. This can be done by controlling the end of R-P-VECTOR 1 with the O-P-SYNCHRO V1 signal in each line."

One option would be to require that lines in a vectoring group are always activated sequentially. However, this
5 may lead to a denial of service for any further lines that want to join after a single line is being initialized.

The contribution entitled "*G.vector: Facilitating simultaneous initialization of multiple joining lines*" from Alcatel-Lucent, and submitted during the ITU session that took
10 place in Atlanta in February 2009, ref. 09AG-044, proposes the following algorithm for optimizing simultaneous joining of multiple lines:

- *"The VCE always maintains a queue of lines that have requested joining, but have not yet joined.*
- 15 - *When ready, e.g., when a previous set of lines have finished joining, the VCE starts initialization for all lines in the queue (thus emptying the queue).*
- *Initialization proceeds independently until it reaches the O-P-VECTOR 1 state. Each line will stay in the O-P-VECTOR 1*
20 *state until receiving OP-SYNCHRO V1 signal from its respective VTU-O (under VCE control). The VCE maintains lines in O-P-VECTOR 1 until all crosstalk coefficients targeted by this estimation stage, on all lines, have been estimated with sufficient accuracy. Then VCE sends release signal allowing*
25 *all lines in the group to exit O-P-VECTOR 1, and enter CHANNEL-DISCOVERY."*

This prior art method is disadvantageous in that the transceiver units shall remain in O-P-VECTOR 1 state till all
simultaneously joining lines reaches O-P-VECTOR 1. Then, a full
30 pilot sequence transmission shall take place, meaning at least L further SYNC symbols shall be awaited, to acquire the very first crosstalk estimates. Nonetheless, the O-P-VECTOR 1 phase is upper-bounded in time by G.993.5 recommendation: O-P-VECTOR 1 shall be less than 1024 SYNC symbols long, meaning $1024 \times 257 \times$
35 $250 \times 10^{-6} \approx 65\text{s}$. Queuing the lines in this phase directly impacts the amount of remaining time for crosstalk estimation, and thus the accuracy of the crosstalk estimation process, and eventually the performances of vectoring.

Summary of the Invention

It is an object of the present invention to improve parallel joining of multiple lines into a group of vectored communication lines.

5 In accordance with a first aspect of the invention, an access node for providing broadband communication services to subscribers, and comprising a first transceiver unit for initializing a first communication channel over a first communication line and a second transceiver unit for
10 subsequently initializing a second communication channel over a second communication line, which initialization of said first and second communication channels respectively comprising first and second handshake phases whereby peer transceiver units exchange capabilities and select a common mode of operation,
15 first and second crosstalk channel acquisition phases from said first and second communication lines into further active communication lines, and further first and second generic initialization phases. The access node further comprises a line initialization controller configured to stretch the execution of
20 said first handshake phase with respect to the execution of said second handshake phase so as to shorten a time difference between respective entries into said first and second crosstalk channel acquisition phases.

In accordance with another aspect of the invention, a
25 method for initializing communication channels over communication lines comprises the step of initializing a first communication channel over a first communication line and subsequently initializing a second communication channel over a second communication line, which initialization of said first
30 and second communication channels respectively comprising first and second handshake phases whereby peer transceiver units exchange capabilities and select a common mode of operation, first and second crosstalk channel acquisition phases from said first and second communication lines into further active
35 communication lines, and further first and second generic initialization phases. The method further comprises the step of stretching the execution of said first handshake phase with respect to the execution of said second handshake phase so as to shorten a time difference between respective entries into said
40 first and second crosstalk channel acquisition phases.

By controlling the period of time over which the handshake procedure spans, one closely controls the time at which a communication line enters the crosstalk acquisition phase. If the time difference between the respective entries
5 into the crosstalk acquisition phase is minimized or at least shortened, then the time available for crosstalk estimation is increased, leading to better vectoring performances.

The execution of the first handshake procedure can be stretched while the execution of the second handshake procedure
10 is not so as the first communication line enters the crosstalk acquisition phase when the second communication line is expected to do so, or alternatively both the first and second handshake procedures can be stretched so as the first and second communication lines enter the crosstalk acquisition phase at a
15 further time, e.g. when a third communication line is expected to enter the crosstalk acquisition phase or alternatively at an arbitrary time scheduled by the access node.

The first communication line may indifferently enter the crosstalk acquisition phase just before or just after the
20 second communication line does so.

The minimum time difference that can be reached between respective entries into the crosstalk acquisition phase depends on the exact technique that is used for stretching the execution of the handshake procedure as it will be set forth in the
25 following.

In one embodiment of the invention, stretching the execution of said first handshake phase with respect to the execution of said second handshake phase is achieved by re-iterating through a particular message exchange during said
30 first handshake phase with a number of iterations of said particular message exchange during said first handshake phase greater than a respective number of iterations of said particular message exchange during said second handshake phase.

By repeating a specific message exchange (or message
35 round) of the handshake phase, which repetition being allowed by the standard, one can postpone the entry into the crosstalk acquisition phase by a certain amount of time without the peer transceiver unit detecting a handshake failure and rebooting (meaning some additional random time that would ultimately
40 depend on each and every customer product manufacturer), thereby

reducing by the same amount the initial time difference between the respective entries into the crosstalk acquisition phase.

One can repeat that specific message exchange over the first communication line, and possibly over the second
5 communication line too. Yet, the number of iterations over the first communication line shall be higher than the number of iterations over the second communication line.

In one embodiment of the invention, said particular message exchange comprises the exchange of a Capability List
10 Request CLR message, a Capability List CL message and a Mode Selection MS message, and re-iteration through said particular message exchange is achieved by issuing a Request REQ-CLR message for a Capability List Request CLR message.

This embodiment relies on a provision of the ITU
15 recommendation entitled "Handshake Procedures for Digital Subscriber Line (DSL) Transceivers", ref. G.994.1 (02/2007), allowing a transceiver unit to request again capabilities from a peer transceiver unit instead of acknowledging the selected mode of operation.

20 In one embodiment of the invention, stretching the execution of said first handshake phase with respect to the execution of said second handshake phase is achieved by intentionally delaying response messages during said first handshake phase by an amount of time averagely greater than a
25 respective amount of time used during said second handshake phase.

By voluntarily delaying (yet still within the timeout boundaries) response messages, such as CL or MS or ACK messages, to request messages, such as CLR or MS or MP or MR message, one
30 can also postpone the entry into the crosstalk acquisition phase over the respective communication line.

In one embodiment of the invention, response messages during said second handshake phase are intentionally delayed by an amount of time averagely greater than a respective amount of
35 time used during said first handshake phase.

This embodiment is particularly advantageous when used over the second communication line in combination with the repetition of a particular message exchange over the first communication line, as it provides fine-tuning of the time
40 alignment of the first and second crosstalk acquisition phases while the loop back through the particular message exchange

provides coarse time alignment. This embodiment is particularly advantageous as the time difference between the respective entries into the crosstalk acquisition phase can be almost reduced to zero.

5 In one embodiment of the invention, stretching the execution of said first handshake phase is conditioned by initialization of said first and second communication channels being requested no longer than a joining time period apart.

10 So as to not unduly delay initialization of a communication channel, a joining timer is started upon joining of the first communication line and new initialization requests over further inactive communication lines are awaited. After the timer expiry, any new joining line will have to wait till the communication lines that are being initialized complete their
15 initialization and enter show time.

 In one embodiment of the invention, duration of said joining time period is dynamically adjusted in dependence of an operational context.

20 For instance, a long timer value would be used for restart or reboot of the access node as many lines are expected to join in a very short time period, whereas a shorter timer value would be used for restart or reboot of a line termination card, whereas an even shorter timer value would be used during normal operation of the access node (so called tracking mode).

25 Further characterizing embodiments are mentioned in the appended claims.

Brief Description of the Drawings

30 The above and other objects and features of the invention will become more apparent and the invention itself will be best understood by referring to the following description of an embodiment taken in conjunction with the accompanying drawings wherein:

- fig. 1 represents a communication system comprising an access node according to the present invention,
- 35 - fig. 2 represents joining communication lines being initialized according to the present invention.

Detailed Description of the Invention

40 There is seen in fig. 1 a Digital Subscriber Line Access Multiplexer (DSLAM) 100 located at a central office or at a remote location closer to subscriber premises, and comprising the following noticeable functional blocks:

- G.993.5 compliant transceiver units 101 (or VTUC1), 102 (or VTUC2) and 103 (or VTUC3),
- a line initialization controller 111 (or CTRL),

The line initialization controller 111 is coupled to
5 the transceivers units 101, 102 and 103.

The DSLAM 100 is coupled to Customer Premises Equipment (CPE) via Unshielded Twisted Pairs (UTP), such as CAT3 cables. Presently, the first transceiver unit 101 is coupled to a first remote transceiver unit 201 (or VTUR1) via a first subscriber
10 line L1; the second transceiver unit 102 is coupled to a second remote transceiver unit 202 (or VTUR2) via a second subscriber line L2; and the third transceiver unit 103 is coupled to a third remote transceiver unit 203 (or VTUR3) via a third subscriber line L3. The remote transceiver units 201, 202 and
15 203 form part of e.g. a modem, a gateway, a router, a set top box, a laptop, etc.

The subscriber lines L1, L2 and L3 are bundled in a cable binder 301 together with further subscriber lines, and induce crosstalk into each other as they are in close vicinity
20 over whole or part of their length. Presently, crosstalk mostly reduces to Far-End Crosstalk (FEXT): some substantial amount of the signal transmitted by a transceiver unit (the disturber) couples into a neighboring line and impairs reception of the direct signal transmitted over that neighboring line at a remote
25 transceiver unit (the victim). For instance, the downstream signal transmitted by the VTUC 101 over line L1 couples into line L2 and is detected as noise by the VTUR 202. Also, the upstream signal transmitted by the VTUR 203 over line L3 couples into line L1 and is detected as noise by the VTUC 101.

The DSLAM 100 further includes a precoder and/or a post-coder (not shown) for downstream and/or upstream crosstalk cancellation. Presently, the subscriber lines L1, L2 and L3 form part of the same vectoring group, which may comprises further subscriber lines, and the precoder and/or post-coder are
35 configured to cancel downstream and/or upstream crosstalk between lines of the vectoring group.

Typically, the frequency samples of a downstream data symbol are forwarded to the precoder by a transceiver unit, and crosstalk-compensated samples are returned by the precoder to
40 the transceiver unit for Inverse Discrete Fourier Transform (IDFT), Digital to Analog Conversion (DAC) and further

transmission over the subscriber line. Similarly, the frequency samples of a received upstream data symbol are forwarded to the post-coder by a transceiver unit, and (almost) crosstalk-free samples are returned by the post-coder to the transceiver unit
5 for detection and demodulation.

Primarily, the transceiver units 101, 102 and 103 are configured to terminate the subscriber lines L1, L2 and L3 respectively, and to initialize and operate DSL communication channels CH1, CH2 and CH3 respectively. So are the remote
10 transceiver units 201, 202 and 203.

The transceiver units 101, 102 and 103 are further configured to notify the line initialization controller 111 about a new line starting up (joining line), being on behalf of the transceiver unit at the central office or the transceiver
15 unit at the customer premises, and further to carry out the DSL initialization procedure after approval from the line initialization controller 111.

The communication channels CH1, CH2 and CH3 comprise a downstream data communication path and an upstream data
20 communication path using distinct downstream and upstream frequency bands (frequency division multiplexing). Respective bit loadings and gains for downstream and upstream carriers are determined and agreed upon during line initialization, thereby yielding a total downstream data rate and a total upstream data
25 rate.

The DSL initialization procedure comprises a handshake phase, multiple crosstalk acquisition phases, a channel discovery phase, a channel training phase, and a channel analysis and exchange phase.

30 The handshake phase is described in G.994.1, and makes use of one or more predefined set of carriers (so-called signaling family) depending on the one or more specific annex of the recommendation being supported. Those pre-defined carrier sets comprise very few carriers only (typically 2 or 3), thereby
35 causing little interference on neighboring lines.

The handshake procedure comprises a first sub-phase A during which peer transceiver units acknowledge their mutual presence by exchanging probe signals and acquire clock synchronization for symbol rate and carrier frequencies, and a
40 second sub-phase B during which peer transceiver units exchange their respective capabilities and agree on a common mode for

training and operation. A successful completion of the handshake phase will lead to the first crosstalk acquisition phase O-P-VECTOR 1.

5 All messages in the handshake phase are sent with the one or more limited set of carriers. All carrier frequencies within a carrier set, and all carrier sets, are simultaneously modulated with the same data bits using Differential Phase Shift Keying (DPSK). The transmit point is rotated 180° from the previous point if the transmit bit is a 1, and the transmit
10 point is left unchanged if the transmit bit is a 0.

Initially, the VTUR is in state R-SILENT0 transmitting silence, and the VTUC is in state C-SILENT1 transmitting silence.

For duplex mode of operation, and in the event of the
15 VTUR initiating the initialization procedure, the first handshake sub-phase A starts with the VTUR transmitting a R-TONES-REQ signal from one or both of its signaling family with phase reversals every 16 ms. When this has been detected by the VTUC, the VTUC shall respond by transmitting a C-TONES signal
20 from one or both of its signaling family. When this has been detected by the VTUR, the VTUR shall transmit silence (R-SILENT1) for 50 to 500 ms and shall then transmit a R-TONE1 signal from only one signaling family. When the VTUC has detected R-TONE1 signal, it shall respond by transmitting GALF
25 (0x81 = one complement of 0x7E) on modulated carriers (C-GALF1). When the VTUR has detected GALF, it shall respond by transmitting FLAGS (0x7E) on modulated carriers (R-FLAG1). When the VTUC has detected FLAGS, it shall respond by transmitting FLAGS (C-FLAG1). When the VTUR has detected FLAGS, it shall
30 enter the sub-phase B by initiating the first message transaction.

In the event of the VTUC initiating the initialization procedure, the first handshake sub-phase A starts with the VTUC directly transmitting C-TONES and keeps on as aforementioned.

35 Slightly different timing and signals are defined for half-duplex mode of operation.

The second handshake sub-phase B starts with the VTUR sending a CAPABILITY LIST REQUEST CLR message conveying the capabilities of the VTUR (that is to say, a list of possible
40 modes of operation), and whereby the VTUR further requests the VTUC capabilities. The VTUC replies with a CAPABILITY LIST CL

message conveying the VTUC capabilities. The VTUR acknowledges the good receipt of the CL message by returning an ACK(1) acknowledgment.

The sub-phase B carries on by either the VTUR or the
5 VTUC selecting a common mode of operation according to the advertised capabilities. This is achieved by issuing a MODE SELECT MS message conveying the selected mode of operation, and by returning an ACK(1) acknowledgment. Typically, the VTUR selects the most appropriate mode of operation at once and
10 issues the MS message. Yet, the VTUR can request the VTUC to select a particular mode of operation by issuing a MODE REQUEST MR message, or can propose a particular mode of operation while leaving the final decision to the VTUC by issuing a MODE PROPOSAL MP message. Once a particular mode of operation is
15 acknowledged, the VTUC and the VTUR enter the O-P-VECTOR 1 and R-P-VECTOR 1 crosstalk acquisition phases respectively.

G.994.1 defines a provision for re-iterating through the sub-phase B by allowing the VTUC to respond to the MS message with a REQUEST - CAPABILITY LIST REQUEST REQ-CLR message
20 requesting the VTUR to proceed again with CLR/CL/ACK(1) message exchange and further MS/MR/MP/ACK(1) message exchange, and without returning to the initial transaction state (R-SILENT0).

The line initialization controller 111 is further configured to control the DSL initialization procedure over each
25 subscriber line, and more specifically to supply the VTUCs 101, 102 and 103 with a number n of iterations for the second handshake sub-phase B to be carried out, as well as with an additional delay D for answering any message requiring a specific response or acknowledgment from the VTUC during the
30 sub-phase B, such as a CLR or MS or MR or MP message.

The VTUCs 101, 102 and 103 are further configured to measure the execution times TA and TB of the first and second handshake sub-phases A and B respectively (not including the configured additional delay D, if any), and to report the so-
35 measured execution time to the line initialization controller 111. By so doing, the execution time spread from different CPE manufacturers and/or implementations is accounted for, and the accuracy of O-P-VECTOR 1 time-alignment is improved.

Although the initialization controller 111 has been
40 depicted as a central unit within the access node 100, it can be partly or wholly distributed across the VTUCs 101, 102 and 103.

An operation of the DSLAM 100 now follows with reference to fig. 2.

Initially, the subscriber lines L1, L2 and L3 are inactive (L3 state). It is further assumed that one or more
5 particular subscriber line Lx is already active (see Lx in fig. 1), meaning that a DSL communication channel is established (L0 state) over that subscriber line. The one or more line Lx form part of the same vectoring group as lines L1, L2 and L3.

At time t1, the VTUC 101 detects a request over the
10 subscriber line L1 whereby the DSL communication channel CH1 is to be initialized. This request is a R-TONES-REQ signal issued by the VTUR 201. Alternatively, the initialization of the DSL channel CH1 can be triggered by the DSLAM 100 in case of e.g. system reboot, power failure, line card failure, management
15 commands, etc.

The VTUC 101 notifies the line initialization controller 111 about the joining event for line L1 (see join_req1 in fig. 1). Thereupon, the line initialization controller 111 allows line L1 to start up (see allow1 in fig. 1)
20 and, as the line L1 is the very first line to be initialized, starts a joining timer JT and waits for any new joining event for further line of the same vectoring group.

The line initialization controller 111 further configures the VTUC 101 with a number n1 of iterations of sub-
25 phases B to be carried out over line L1 (see iteration_HSB=n1 in fig. 1), and with a timer D1 for voluntarily delaying handshake response messages over line L1 (see resp_delay=D1 in fig. 1).

n1 is chosen according to the maximum time one can afford waiting before O-P-VECTOR 1 crosstalk acquisition phase
30 takes place. Presently, and still with reference to fig. 2, n1 = 3 and D1 = 0 (i.e., reply without delay).

n1 may also be adjusted according to the operational context of the DSLAM 100 at time t1: a large value would be used for system initialization, another intermediate value would be
35 used for partial initialization, and a small value would be used for normal operation.

Upon approval from the line initialization controller 111, the VTUC 101 performs a handshake procedure HS1 comprising a first sub-phase HSA1 and a number n1 of second sub-phases
40 HSB1. Re-iteration through the sub-phase HSB1 is achieved by issuing a REQ-CLR message upon receipt of a MS message from the

VTUR 201. For the last iteration, the VTUC 101 acknowledges the MS message from the VTUR 201 and enters O-P-VECTOR 1 crosstalk acquisition phase XT_ACQ1.

The value of the joining timer JT is set according to
5 the following formula:

$$JT = TA1 + n1 \cdot TB1 - \tilde{TA} - \tilde{TB} \quad (2),$$

wherein TA1 and TB1 respectively denote average time durations of the sub-phases HSA1 and HSB1 as measured by the VTUC 101 over the line L1 and reported to the line initialization controller
10 111 (see duration_HSA=TA1 and duration_HSB=TB1 in fig. 1),
and wherein $\tilde{TA}$ and $\tilde{TB}$ respectively denote default average time durations of the first and second handshake sub-phases A and B as assumed by the line initialization controller 111.

By so doing, a line joining at time $t1 + JT$ (i.e., the
15 maximum joining time) would enter O-P-VECTOR 1 almost simultaneously with line L1.

The execution of the handshake phases over the lines, the initialization of which is requested between $t1$ and $t1 + JT$, is controlled and adjusted as it will be set forth further for
20 lines L2 and L3.

If a line joins after the joining timer JT expires, then the line initialization controller 111 postpones the initialization of the DSL channel over that line till all the pending initializations complete and initializing lines enter
25 showtime (L0 state). Consequently, the VTUC does not answer the R-TONES-REQ signal and stays in C-SILENT1 state, and the VTUR keeps on repeating the R-TONES-REQ signal until receiving the C-TONES signal from the VTUC.

At subsequent times $t2$ and $t3$, requests for line
30 initialization are received over lines L2 and L3 respectively with $t1 \leq t2 \leq t3 \leq t1 + JT$.

The VTUCs 102 and 103 notify the line initialization controller 111 about the joining events for lines L2 and L3 respectively (see join_req2 and join_req3 in fig. 1). Thereupon,
35 the line initialization controller 111 allows lines L2 and L3 to start up (see allow2 and allow3 in fig. 1) as the joining timer JT is still running.

The line initialization controller 111 configures the VTUCs 102 and 103 with numbers $n2$ and $n3$ of iterations of the
40 sub-phase B (see iteration_HSB= $n2$ and iteration_HSB= $n3$ in fig.

1), and with response delays D2 and D3 (see resp_delay=D2 and resp_delay=D3 in fig. 1).

Upon approval from the line initialization controller 111, the VTUC 102 performs a handshake procedure HS2 comprising
 5 a first sub-phase HSA2 and the number n2 of second sub-phase HSB2, and the VTUC 103 performs a handshake procedure HS3 comprising a first sub-phase HSA3 and the number n3 of second sub-phase HSB3. After successful completion of the handshake procedures HS2 and HS3, the VTUCs 102 and 103 enter O-P-VECTOR 1
 10 crosstalk acquisition phases XT_ACQ2 and XT_ACQ3 respectively.

The values n2 and n3 are determined so as the following inequalities are conformed with:

$$TA1+n1\cdot TB1-TB2 \leq t2-t1+TA2+n2\cdot TB2 \leq TA1+n1\cdot TB1 \quad (3),$$

$$TA1+n1\cdot TB1-TB3 \leq t3-t1+TA3+n3\cdot TB3 \leq TA1+n1\cdot TB1 \quad (4),$$

15 wherein TA2 and TB2 respectively denote average time durations of the sub-phases HSA2 and HSB2 as measured by the VTUC 102 over the line L2 and reported to the line initialization controller 111 (see duration_HSA=TA2 and duration_HSB=TB2 in fig. 1), and wherein TA3 and TB3 respectively denote average time
 20 durations of the sub-phases HSA3 and HSB3 as measured by the VTUC 103 over the line L3 and reported to the line initialization controller 111 (see duration_HSA=TA3 and duration_HSB=TB3 in fig. 1).

By so doing, one brings the entries into XT_ACQ2 and
 25 XT_ACQ3 as close as possible to, yet before, the entry into XT_ACQ1. Presently, and still with reference to fig. 2, n1 = 2 and n3 = 1 (i.e., no repetition).

The fine-tuning of O-P-VECTOR 1 time alignment across lines L1, L2 and L3 is achieved by adjusting the handshake
 30 response delays D2 and D3 that are used over lines L2 and L3 during HSB2 and HSB3 respectively

For instance, assuming that r response messages are issued by the VTUC during one iteration of the sub-phase HSB, one computes the average response delays D2 and D3 that shall be
 35 voluntarily added over lines L2 and L3 respectively before issuing a response message as:

$$D2 = \frac{(TA1+n1\cdot TB1-(t2-t1+TA2+n2\cdot TB2))}{n2\cdot r} \quad (5),$$

$$D3 = \frac{(TA1+n1\cdot TB1-(t3-t1+TA3+n3\cdot TB3))}{n3\cdot r} \quad (6).$$

Yet, the so-computed response delays D2 and D3 shall be lower than the applicable timeout value (minus some margin to account for the transmission and processing latency).

By so doing, one can achieve almost perfect time-
5 alignment between the respective O-P-VECTOR 1 phases (see Δt_{12} and Δt_{13} that reduce almost to zero in fig. 2).

The respective duration of the sub-phases A and B as reported by the VTUC are stored in a local data repository of the DSLAM 100, and used later on when lines are initialized.
10 Those values can be stored in association with a particular product ID or alike, and can be an average of the last measured values for a particular product ID.

If no value is available for a specific line (e.g., because that line starts for the very first time), then the
15 default values $\tilde{T}_A$ and $\tilde{T}_B$ are assumed. As soon as the first measured values are available, they substitute for the default values, and the number of iterations and/or the response delay to be used over that line are updated accordingly.

The responses messages over line L1 may also be delayed
20 (i.e., $D1 \neq 0$) so as the lines L1, L2 and L3 enter O-P-VECTOR 1 at an arbitrary scheduled time.

After O-P-VECTOR 1 completion, the crosstalk coefficients from the joining lines L1, L2 and L3 into the active line Lx are acquired and used to initialize the
25 corresponding coefficients of the precoding and/or cancellation matrix. From that time onwards, downstream and/or upstream crosstalk from the new joining lines L1, L2 and L3 is canceled over the active line Lx, and the lines L1, L2 and L3 can enter the CHANNEL DISCOVERY PHASE without impairing communication over
30 the active line Lx.

In an alternative embodiment of the present invention, responses are not delayed ($D1 = D2 = D3 = 0$), and the line initialization controller 111 performs coarse alignment only by re-iterating through the second handshake sub-phase B. The
35 formulas (4) and (5) for computing n_2 and n_3 shall be advantageously replaced by:

$$n_2 = i, \min_{i \in \{1, n_1-1\}} |(TA_1 + n_1 \cdot TB_1) - (t_2 - t_1 + TA_2 + i \cdot TB_2)| \quad (7),$$

$$n_3 = i, \min_{i \in \{1, n_1-1\}} |(TA_1 + n_1 \cdot TB_1) - (t_3 - t_1 + TA_3 + i \cdot TB_3)| \quad (8).$$

In still an alternative embodiment of the present invention, the line initialization controller 111 does not re-iterate through the second handshake sub-phase B ($n1 = n2 = n3 = 1$), but only adjusts the response delays D1, D2 and D3 such that
5 D1 $\geq$ D2 $\geq$ D3, thereby shortening (within the timeout boundaries) the time differences Δt_{12} and Δt_{13} between the respective entries into O-P-VECTOR 1.

In still an alternative embodiment of the present invention, the line initialization controller 111 executes the
10 handshake procedure over line L3 at a nominal pace ($D3 = 0$) and without re-iterating through sub-phase B ($n3 = 1$). The number of iterations and the response delays over lines L1 and L2 are adjusted so as lines L1 and L2 enter XT_ACQ1 and XT_ACQ2 when line L3 is expected to enter XT_ACQ3.

15 In still an alternative embodiment, the number of iterations and/or the response delays are dynamically adjusted based on the respective measured time shifts between alike handshake transactions (e.g., the receipt of the MS message from the VTUR). The VTUC 101, 102 and 103 are then further configured
20 to report those specific handshake milestones to the line initialization controller 111 so as it can dynamically adjust the number of iterations and the response delays accordingly. For instance, if a certain handshake milestone is detected over line L1 before being detected over line L2, then D1 is increased
25 with respect to D2. If line L1 is unable to catch up with line L2 (because response delays are upper-bounded by the timeout value), then another message round is requested for both lines L1 and L2 so as to give another opportunity for line L1 to catch up with line L2.

30 Although the present invention has been described with exhaustive reference to G.993.5 DSL technology, it is similarly applicable to further wireline access technologies.

It is to be noticed that the term 'comprising' should not be interpreted as being restricted to the means listed
35 thereafter. Thus, the scope of the expression 'a device comprising means A and B' should not be limited to devices consisting only of components A and B. It means that with respect to the present invention, the relevant components of the device are A and B.

It is to be further noticed that the term 'coupled' should not be interpreted as being restricted to direct connections only. Thus, the scope of the expression 'a device A coupled to a device B' should not be limited to devices or
5 systems wherein an output of device A is directly connected to an input of device B, and/or vice-versa. It means that there exists a path between an output of A and an input of B, and/or vice-versa, which may be a path including other devices or means.

The description and drawings merely illustrate the
10 principles of the invention. It will thus be appreciated that those skilled in the art will be able to devise various arrangements that, although not explicitly described or shown herein, embody the principles of the invention and are included within its spirit and scope. Furthermore, all examples recited
15 herein are principally intended expressly to be only for pedagogical purposes to aid the reader in understanding the principles of the invention and the concepts contributed by the inventor(s) to furthering the art, and are to be construed as being without limitation to such specifically recited examples
20 and conditions. Moreover, all statements herein reciting principles, aspects, and embodiments of the invention, as well as specific examples thereof, are intended to encompass equivalents thereof.

The functions of the various elements shown in the
25 figures may be provided through the use of dedicated hardware as well as hardware capable of executing software in association with appropriate software. When provided by a processor, the functions may be provided by a single dedicated processor, by a single shared processor, or by a plurality of individual
30 processors, some of which may be shared. Moreover, a processor should not be construed to refer exclusively to hardware capable of executing software, and may implicitly include, without limitation, digital signal processor (DSP) hardware, network processor, application specific integrated circuit (ASIC), field
35 programmable gate array (FPGA), etc. Other hardware, conventional and/or custom, such as read only memory (ROM), random access memory (RAM), and non volatile storage, may also be included.

CLAIMS

1. **An access node** (100) for providing broadband communication services to subscribers, and comprising **a first transceiver unit** (101) for initializing **a first communication channel** (CH1) over **a first communication line** (L1) and **a second transceiver unit** (102) for subsequently initializing **a second communication channel** (CH2) over **a second communication line** (L2), which initialization of said first and second communication channels respectively comprising **first and second handshake phases** (HS1, HS2) whereby peer transceiver units exchange capabilities and select a common mode of operation, **first and second crosstalk channel acquisition phases** (XT_ACQ1, XT_ACQ2) from said first and second communication lines into **further active communication lines** (Lx), and further first and second generic initialization phases,
wherein said access node further comprises **a line initialization controller** (111) configured to stretch the execution of said first handshake phase with respect to the execution of said second handshake phase so as to shorten **a time difference** (Δt_{12}) between respective entries into said first and second crosstalk channel acquisition phases.

2. **An access node** (100) according to claim 1, *wherein* said first transceiver unit is further configured to, under control of said line initialization controller, re-iterate through **a particular message exchange** (HSB1) during said first handshake phase with **a number of iterations** (n1) of said particular message exchange during said first handshake phase greater than **a respective number of iterations** (n2) of said particular message exchange during said second handshake phase.

3. **An access node** (100) according to claim 2, *wherein* said particular message exchange comprises the exchange of a Capability List Request CLR message, a Capability List CL message and a Mode Selection MS message,
and wherein said first transceiver unit is further configured to re-iterate through said particular message exchange by issuing a

Request REQ-CLR message for a Capability List Request CLR message.

4. **An access node** (100) according to any of claims 1
5 to 3, *wherein* said first transceiver unit is further configured to, under control of said line initialization controller, intentionally delay response messages during said first handshake phase by **an amount of time** (D1) averagely greater than **a respective amount of time** (D2) used during said
10 second handshake phase.

5. **An access node** (100) according to claim 2 or 3, *wherein* said second transceiver unit is further configured to, under control of said line initialization controller,
15 intentionally delay response messages during said second handshake phase by **an amount of time** (D2) averagely greater than **a respective amount of time** (D1) used during said first handshake phase.

20 6. **An access node** (100) according to any of claims 1 to 5, *wherein* stretching the execution of said first handshake phase with respect to the execution of said second handshake phase is conditioned by initialization of said first and second communication channels being requested no longer than **a**
25 **joining time period** (JT) apart.

7. **An access node** (100) according to claim 6, *wherein* duration of said joining time period is dynamically adjusted in dependence of an operational context.
30

8. **An access node** (100) according to any of claims 1 to 7, *wherein* said access node is a Digital Subscriber Line Access Multiplexer DSLAM, and *wherein* said first and second communication channels are Digital Subscriber Line DSL
35 channels.

9. **A method** for initializing communication channels over communication lines, and comprising the steps of initializing **a first communication channel** (CH1) over **a**
40 **first communication line** (L1) and subsequently initializing **a second communication channel** (CH2) over **a second**

communication line (L2), which initialization of said first and second communication channels respectively comprising **first and second handshake phases** (HS1, HS2) whereby peer transceiver units exchange capabilities and select a common mode of operation, **first and second crosstalk channel acquisition phases** (XT_ACQ1, XT_ACQ2) from said first and second communication lines into **further active communication lines** (Lx), and further first and second generic initialization phases,

5 *wherein* said method further comprises the step of stretching the execution of said first handshake phase with respect to the execution of said second handshake phase so as to shorten a **time difference** (Δt_{12}) between respective entries into said first and second crosstalk channel acquisition phases.

15

10. **A method** according to claim 9, *wherein* stretching the execution of said first handshake phase with respect to the execution of said second handshake phase is achieved by re-iterating through a **particular message exchange** (HSB1) during said first handshake phase with a **number of iterations** (n1) of said particular message exchange during said first handshake phase greater than a **respective number of iterations** (n2) of said particular message exchange during said second handshake phase.

25

11. **A method** according to claim 10, *wherein* said particular message exchange comprises the exchange of a Capability List Request CLR message, a Capability List CL message and a Mode Selection MS message,

30 *and wherein* re-iteration through said particular message exchange is achieved by issuing a Request REQ-CLR message for a Capability List Request CLR message.

12. **A method** according to any of claims 9 to 11, *wherein* stretching the execution of said first handshake phase with respect to the execution of said second handshake phase is achieved by intentionally delaying response messages over said first communication line by an **amount of time** (D1) averagely greater than a **respective amount of time** (D2) used over said

40 second communication line.

13. **A method** according to claim 10 or 11, *wherein* said method comprises the further step of intentionally delaying response messages during said second handshake phase by **an amount of time** (D2) averagely greater than **a respective amount of time** (D1) used during said first handshake phase.

14. **A method** according to any of claims 9 to 13, *wherein* stretching the execution of said first handshake phase with respect to the execution of said second handshake phase is conditioned by initialization of said first and second communication channels being requested no longer than **a joining time period** (JT) apart.

15. **A method** according to claim 14, *wherein* duration of said joining time period is dynamically adjusted in dependence of an operational context.

- 1/2 -

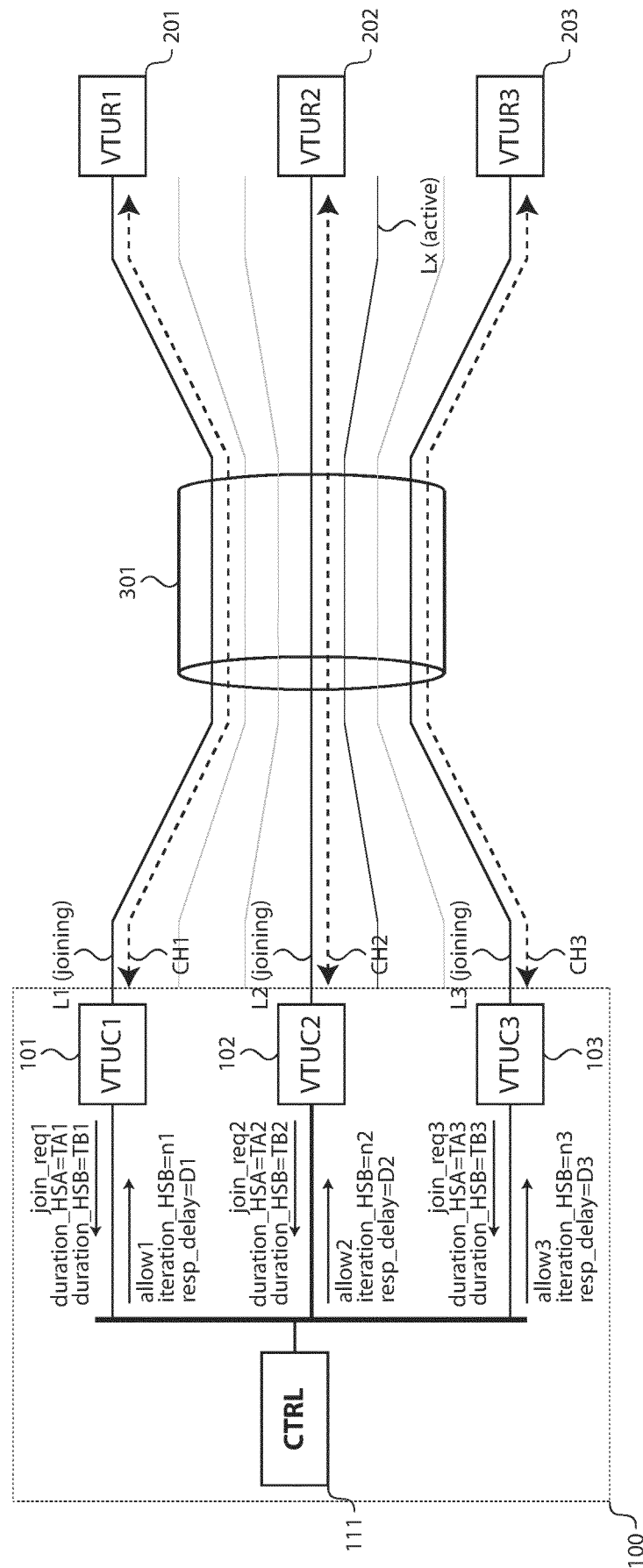


Fig. 1

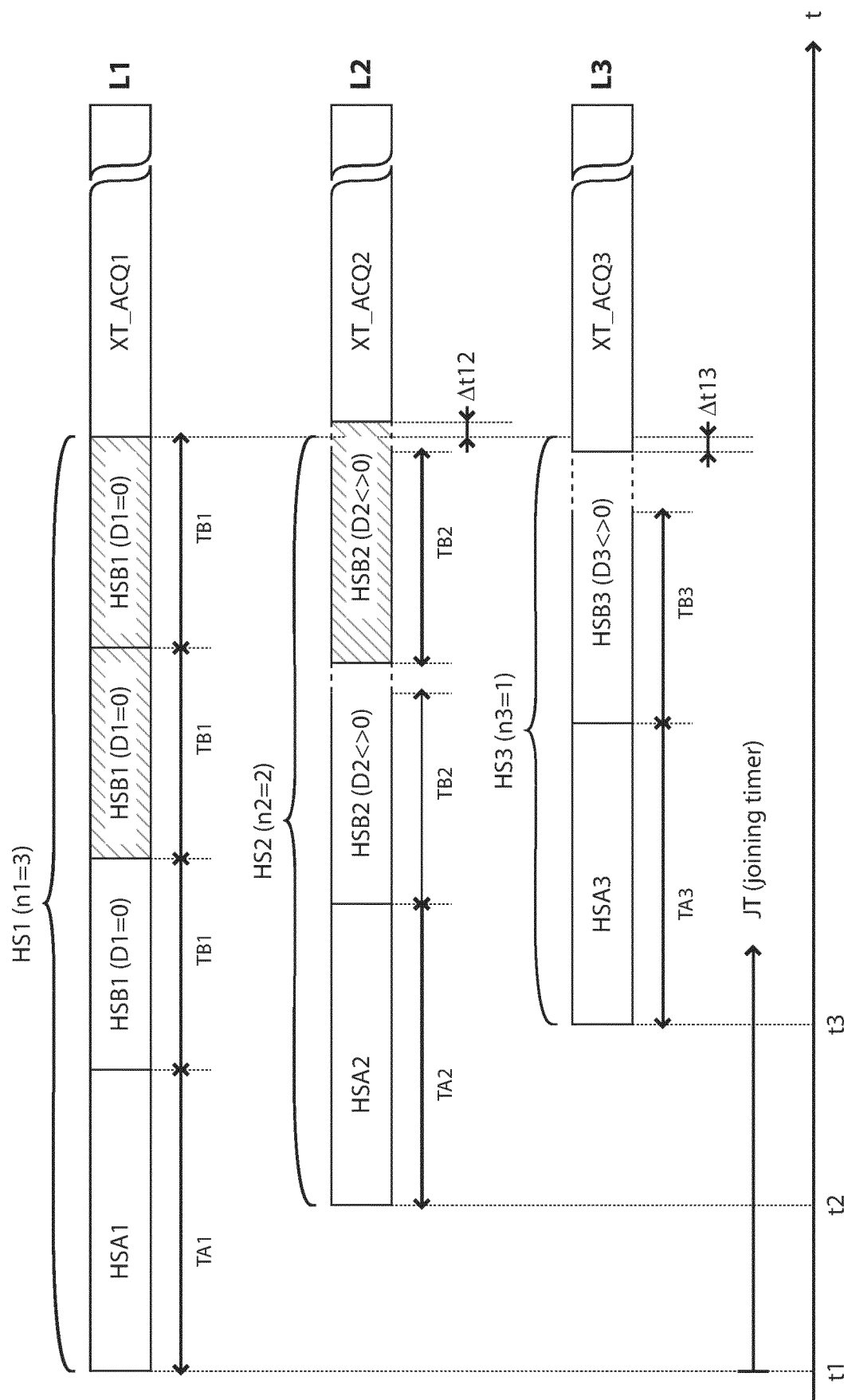


Fig. 2

INTERNATIONAL SEARCH REPORT

International application No
PCT/EP2011/072063

A. CLASSIFICATION OF SUBJECT MATTER
INV. H04B3/46 H04M11/06 H04B3/32
ADD.

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)
H04B H04M H04L

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal, COMPENDEX, INSPEC

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	EP 2 200 185 A1 (ALCATEL LUCENT [FR]) 23 June 2010 (2010-06-23) paragraph [0034] - paragraph [0052] -----	1-15
A	VLADIMIR OKSMAN ET AL: "The TU-T's new G.vector standard proliferates 100 Mb/s DSL", IEEE COMMUNICATIONS MAGAZINE, IEEE SERVICE CENTER, PISCATAWAY, US, vol. 48, no. 10, 1 October 2010 (2010-10-01), pages 140-148, XP011319418, ISSN: 0163-6804 page 144, paragraph Initialization ----- -/--	1-15



Further documents are listed in the continuation of Box C.



See patent family annex.

* Special categories of cited documents :

"A" document defining the general state of the art which is not considered to be of particular relevance
"E" earlier document but published on or after the international filing date
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"O" document referring to an oral disclosure, use, exhibition or other means
"P" document published prior to the international filing date but later than the priority date claimed

"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention
"X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone
"Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art.
"&" document member of the same patent family

Date of the actual completion of the international search

20 January 2012

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31/01/2012

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INTERNATIONAL SEARCH REPORT

International application No
PCT/EP2011/072063

C(Continuation). DOCUMENTS CONSIDERED TO BE RELEVANT		
Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	UMASHANKAR THYAGARAJAN INFINEON TECHNOLOGIES AG GERMANY: "ADSL G:HS : Proposal to include a mode of operation to reduce Handshake Time in ADSL2 and ADSL2+; D 958", ITU-T DRAFT STUDY PERIOD 2001-2004, INTERNATIONAL TELECOMMUNICATION UNION, GENEVA ; CH, vol. STUDY GROUP 15, 21 October 2003 (2003-10-21), pages 1-7, XP017417690, the whole document -----	1-15
A	US 2005/175078 A1 (REDFERN ARTHUR J [US]) 11 August 2005 (2005-08-11) paragraphs [0009], [0027] - paragraph [0028] -----	1-15

INTERNATIONAL SEARCH REPORT

Information on patent family members

International application No

PCT/EP2011/072063

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