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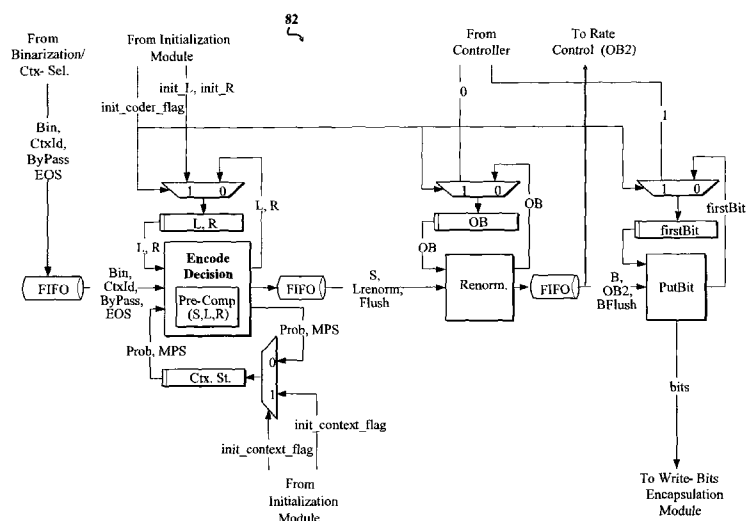


Fig. 4

(S7) Abstract: An entropy coding apparatus. In a specific embodiment, the entropy coding apparatus includes a renormalization process and an encode-decision process that communicates with the renormalization process. The encode-decision process is adapted to run in parallel with the renormalization process without the renormalization process being nested therein. In a more specific embodiment, the entropy coding apparatus includes an entropy encoder that is H.264 compliant. The encode-decision process includes a first mechanism for pre-computing certain parameters to eliminate the need to nest the renormalization process within the encode-decision process. The renormalization process and the encode-decision process are components of a Context Adaptive Binary Arithmetic Coding (CABAC) module.

INTERNATIONAL SEARCH REPORT

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A. CLASSIFICATION OF SUBJECT MATTER
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B. FIELDS SEARCHED

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C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	MO LI ET AL: "A high throughput binary arithmetic coding engine for h.264/avc" SOLID-STATE AND INTEGRATED CIRCUIT TECHNOLOGY, 2006. ICSICT '06. 8TH INTERNATIONAL CONFERENCE ON, IEEE, PI, 1 January 2006 (2006-01-01), pages 1914-1918, XP031046060 ISBN: 978-1-4244-0160-4 the whole document	1-3,9, 10,13, 16-29
X	SHOJANIA H ET AL: "A VLSI Architecture for High Performance CABAC Encoding" VISUAL COMMUNICATIONS AND IMAGE PROCESSING, PROC. OF SPIE, vol. 5960, 2005, XP002496884 the whole document	1-3,9, 10,13, 16-29

☒ Further documents are listed in the continuation of Box C.

☒ See patent family annex.

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C(Continuation). DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
E	WO 2008/039321 A (ANALOG DEVICES INC [US]; WILSON JAMES [US]; KABLOTSKY JOSHUA [US]; STE) 3 April 2008 (2008-04-03) page 2, line 20 - line 24 page 6, line 19 - line 21 -----	1-29
A	SHOJANIA H ET AL: "A High Performance CABAC Encoder" IEEE-NEWCAS CONFERENCE, 2005. THE 3RD INTERNATIONAL QUEBEC CITY, CANADA 19-22 JUNE, 2005, PISCATAWAY, NJ, USA, IEEE, 19 June 2005 (2005-06-19), pages 104-107, XP010828239 ISBN: 978-0-7803-8934-2 the whole document -----	1-29
A	HENDRIK EECKHAUT ET AL: "Optimizing the critical loop in the H.264/AVC CABAC decoder" FIELD PROGRAMMABLE TECHNOLOGY, 2006. FPT 2006. IEEE INTERNATIONAL CONFERENCE ON, IEEE, PI, 1 December 2006 (2006-12-01), pages 113-118, XP031034421 ISBN: 978-0-7803-9728-6 the whole document -----	1-29
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INTERNATIONAL SEARCH REPORT

Information on patent family members

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