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(54) **Title**: PHOTOSENSOR CIRCUIT INCLUDING A REGULATED POWER SUPPLY

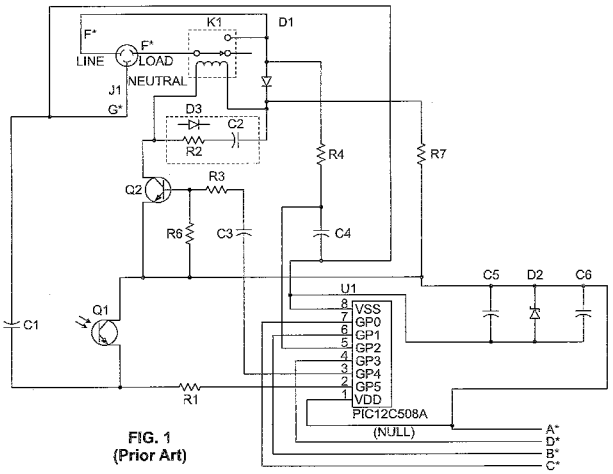


FIG. 1
(Prior Art)

(57) **Abstract**: Photosensor circuits include a relay coil configured to control application of an alternating current (AC) power source to a load. The circuit includes a pulse width modulator circuit configured to generate a pulse width modulated signal having a pulse width that varies responsive to an average voltage across the relay coil. A drive transistor coupled to the relay coil controls the average voltage across the relay coil responsive to the pulse width modulated signal. A photo control circuit is configured to control application of the pulse width modulated signal to the drive transistor responsive to a detected light level. A power circuit coupled to the power source is configured to provide a regulated power signal to a comparator of the pulse width modulator circuit. The power circuit includes the drive transistor coupled between the relay coil and the regulated power signal and a second transistor coupled between a base of the drive transistor and the regulated power signal. The drive transistor conducts current passing through the relay coil to the regulated power signal when the drive transistor is on and the second transistor conducts current not passing through the relay coil to the regulated power signal when the drive transistor is turned off.

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PHOTOSENSOR CIRCUIT INCLUDING A REGULATED POWER SUPPLY

BACKGROUND OF THE INVENTION

[0001] The present invention relates to photosensor circuits and, more particularly, photosensor circuits for light level switching control.

[0002] Photo controllers are devices that automatically turn electrical devices on and off in response to the ambient light level. They are used, for example, on street lights to automatically turn them off during the day and on at night. They are also used on billboard lighting systems to turn the billboard lights on early at night, off late at night during periods of low vehicular traffic, on again during early morning rush hour periods when high traffic levels resume, and then off during the daylight hours. Photo controllers may also be used in reverse, for example, to turn a golf course water fountain on during the day and off at night.

[0003] A variety of devices, including photo controllers, may make use of power converters to convert relatively high voltage alternating current to relatively low voltage direct current as is used in many conventional electronic devices. Some conventional power converters make use of large, high-voltage resistors to drop the voltage. However, these resistors are typically inefficient and generate high heat. The heat generated from the resistors may require that the resistors be housed in a large package and include heat dissipating elements, such as heat sinks. Also, the high heat generated by the resistors can lead to problems with reliability and longevity in the resistors and in other electronic components situated near the resistors.

[0004] Another conventional approach to power conversion is the use of a switch mode power converter. The switch mode power converters typically require six transistors or a micro-controller to implement. The requirement for multiple transistors or a micro-controller may cause the implementation of switch mode power converters to be cost prohibitive in some applications, such as in photo controllers.

[0005] A small, low cost, efficient switch mode power converter and a photosensor circuit including the same are described in United States Patent No. 6,903,942 ("the '942 patent"), which is hereby incorporated herein by reference as if set forth in its entirety.

[0006] It is also known to provide a photosensor circuit including digital circuitry as incorporated in the 3100 Series photocontrol, available from TE Connectivity. The

photosensor circuit for the 3100 Series photocontrol is shown in FIG. 1. As seen in FIG. 1, a microcontroller U1 outputs a signal GP4 that drives a drive transistor Q2 to turn on and off the drive transistor Q2 as part of a pulse width modulated control circuit including capacitors C5 and C6 and diode D2 (operating at a frequency of approximately 50 Hertz (Hz) to control the voltage across the relay coil of the relay K1. Relay K1 is shown as a normally open relay and, when light is detected by photransistor Q1, the coil current of relay K1 is turned off to turn off a load LOAD, such as a street light. A direct drive signal is provided by including capacitor C3 to adjust voltage levels of the signal to the base of the drive transistor Q2 from the signal GP4. In addition, a half wave rectifier (diode D1) is included to provide power for the photocontrol circuit.

[0007] A regulated power supply is also shown to provide the power supply signals VSS and VDD to allow operation of the microncontroller U1. In the illustrated embodiment, VSS is coupled to the AC line NEUTRAL to provide the DC ground reference. The VDD signal is provided by the resistor R7 coupled to the half wave rectifier D1. In addition, The VDD signal is coupled to the emitter of the drive transistor Q2. As such, current will also be provided through the drive transistor Q2 but only when the transistor is on.

[0008] A small, low cost, efficient switch mode power converter including a regulated power source for a microcontroller is also described in co-pending United States Patent Application No.13/190,727 ("the '727 application"), which is hereby incorporated herein by reference as if set forth in its entirety.

[0009] Regulated power sources may also be used in photocontroller circuits not including a microcontroller, such as described in United States Patent No. 8,026,470 ("the '470 patent"), which is hereby incorporated herein by reference as if set forth in its entirety.

SUMMARY OF THE INVENTION

[0010] Embodiments of the present invention provide photosensor circuits including a relay coil configured to control application of an alternating current (AC) power source having a negative half and a positive half of a line cycle to a load. A pulse width modulator circuit of the photosensor circuit is configured to generate a pulse width modulated signal having a pulse width that varies responsive to an average voltage across the relay coil. The pulse width modulator circuit includes a

comparator. A drive transistor coupled to the relay coil that controls the average voltage across the relay coil responsive to the pulse width modulated signal. A photo control circuit of the photosensor circuit is configured to control application of the pulse width modulated signal to the drive transistor responsive to a detected light level. A power circuit, coupled to the power source, is configured to provide a regulated power signal to the comparator of the pulse width modulator circuit. The power circuit includes the drive transistor coupled between the relay coil and the regulated power signal and a second transistor coupled between a base of the drive transistor and the regulated power signal. The drive transistor conducts current passing through the relay coil to the regulated power signal when the drive transistor is on and the second transistor conducts current not passing through the relay coil to the regulated power signal when the drive transistor is turned off.

BRIEF DESCRIPTION OF THE DRAWINGS

[0011] **FIG. 1** is a circuit diagram of a photosensor circuit according to the prior art.

[0012] **FIG. 2** is a circuit diagram of a photosensor circuit according to some embodiments of the present invention.

[0013] **FIG. 3** is a circuit diagram of a photosensor circuit according to some embodiments of the present invention.

[0014] **FIGs. 4A to 4E** are timing diagrams of the photosensor circuit of **FIG. 1**.

DETAILED DESCRIPTION OF EMBODIMENTS OF THE INVENTION

[0015] The present invention now will be described more fully hereinafter with reference to the accompanying drawings, in which illustrative embodiments of the invention are shown. In the drawings, the relative sizes of regions or features may be exaggerated for clarity. This invention may, however, be embodied in many different forms and should not be construed as limited to the embodiments set forth herein; rather, these embodiments are provided so that this disclosure will be thorough and complete, and will fully convey the scope of the invention to those skilled in the art.

[0016] It will be understood that, although the terms first, second, etc. may be used herein to describe various elements, components, regions, layers and/or sections, these elements, components, regions, layers and/or sections should not be limited by these terms. These terms are only used to distinguish one element, component,

region, layer or section from another region, layer or section. Thus, a first element, component, region, layer or section discussed below could be termed a second element, component, region, layer or section without departing from the teachings of the present invention.

[0017] Spatially relative terms, such as "beneath", "below", "lower", "above", "upper" and the like, may be used herein for ease of description to describe one element or feature's relationship to another element(s) or feature(s) as illustrated in the figures. It will be understood that the spatially relative terms are intended to encompass different orientations of the device in use or operation in addition to the orientation depicted in the figures. For example, if the device in the figures is turned over, elements described as "below" or "beneath" other elements or features would then be oriented "above" the other elements or features. Thus, the exemplary term "below" can encompass both an orientation of above and below. The device may be otherwise oriented (rotated 90° or at other orientations) and the spatially relative descriptors used herein interpreted accordingly.

[0018] As used herein, the singular forms "a", "an" and "the" are intended to include the plural forms as well, unless expressly stated otherwise. It will be further understood that the terms "includes," "comprises," "including" and/or "comprising," when used in this specification, specify the presence of stated features, integers, steps, operations, elements, and/or components, but do not preclude the presence or addition of one or more other features, integers, steps, operations, elements, components, and/or groups thereof. It will be understood that when an element is referred to as being "connected" or "coupled" to another element, it can be directly connected or coupled to the other element or intervening elements may be present. Furthermore, "connected" or "coupled" as used herein may include wirelessly connected or coupled. As used herein, the term "and/or" includes any and all combinations of one or more of the associated listed items.

[0019] Unless otherwise defined, all terms (including technical and scientific terms) used herein have the same meaning as commonly understood by one of ordinary skill in the art to which this invention belongs. It will be further understood that terms, such as those defined in commonly used dictionaries, should be interpreted as having a meaning that is consistent with their meaning in the context of this specification and the relevant art and will not be interpreted in an idealized or overly formal sense unless expressly so defined herein.

[0020] Embodiments of the present invention will now be described with reference to FIGs. 2-3. In particular, the embodiments of FIG. 2 illustrate a photosensor circuit 10 for street lamp control in which a normally open relay is provided where current flows through a relay coil K1 during the night to turn on a street lamp (or other load) and during the day the coil current of K1 is interrupted causing the street lamp to turn off. It will be understood, however, that some embodiments use a normally closed relay. The relay coil K1 is configured to control application of an alternating current (AC) power source applied between LI and N to a load connected between LO and N. A diode D2 is shown coupled across the relay to provide a buck converter. A circuit protection device MOV1 is also shown. While the switch associated with the relay coil K1 is shown apart therefrom in FIG. 2 for illustrative purposes, it will be understood that the coil K1 and switch are components of the relay and, as such, the notation K1 is also shown adjacent the streetlamp power switch in FIG. 2.

[0021] As will be described below, the circuit 10 illustrated in FIG. 2 includes a photo sensor, logic circuitry, and an electrical switch. In the illustration, the photo sensor is a phototransistor Q1, the logic circuitry is a microcontroller U1, and the electrical switch is an electromagnetic relay. To simplify understanding, some functional blocks are schematically illustrated as discrete components in FIG. 2. However, it will be understood that the microcontroller U1 may be a programmable device such as a PIC10D206 available from Microchip Technology, Inc., which device may include an on chip analog comparator, represented in FIG. 2 as the comparator IC1A having one input coupled to a voltage reference of 0.6 volts (V) also provided by the microcontroller U1 as well as a programmable microcontroller. As such, the illustrated isolation resistors R1, R10, R12 are provided solely to allow reliable programming of the microcontroller U1 and do not otherwise substantially affect circuit operation during normal operation after programming is completed.

[0022] Such embodiments may be designed to function at any line voltage between 105 and 305 VAC and, as such, are shown as including a switch mode regulator circuit. For the illustrated embodiments below, most of the control circuit is contained within microcontroller U1. As such, in the discussion of the respective circuits below, it will be understood that the microcontroller U1 acts as part of various circuits along with the discrete components identified below as included in the respective circuits.

[0023] A pulse width modulator circuit R6-R8, R11, D3, C3-C4 is provided to control the operation of the relay coil K1. The pulse width modulator circuit further includes the analog comparator IC1A. The illustrated pulse width modulator circuit R6-R8, R11, D3, C3-C4 is a switch mode relay coil current regulator that is configured to generate a pulse width modulated signal having a pulse width that varies responsive to an average voltage across the relay coil at a set frequency for the pulse width modulated signal.

[0024] A drive transistor Q2 coupled to the relay coil K1 controls the average voltage across the relay coil K1 responsive to the pulse width modulated signal. A photo control circuit Q1, C2, U1/IC2 is provided that is configured to control application of the pulse width modulated signal to the drive transistor Q2 responsive to a detected light level. A power circuit R2-R4, Q3 (and drive transistor Q2) coupled to the power source is configured to provide a regulated power signal PS1 (VDD (high) referenced to VSS (low)). References herein to the regulated power signal PS1, unless stated otherwise, are references to the circuit node corresponding to the voltage level VDD of the regulated power signal.

[0025] The photo control circuit includes a phototransistor Q1 coupled to the microcontroller U1. The phototransistor Q1 has a first terminal coupled to the regulated power signal PS1 (VDD) and a second terminal that outputs a current responsive to a level of light detected by the phototransistor. More particularly, in the illustrated embodiments of FIG. 2, the photo control circuit further includes a resistor R1 coupled between the microcontroller U1 and the second terminal of the phototransistor Q1 and a charge capacitor C2 coupled between the second terminal of the phototransistor Q1 and the low voltage reference VSS of the regulated power signal PS1 and current flowing through the phototransistor Q1 charges the charge capacitor C2 so that the microcontroller can detect the light level.

[0026] The microcontroller U1 is configured to measure the detected light level and to turn off the drive transistor Q2 responsive to the detected light level meeting a selected condition. In the illustrated embodiments, the microcontroller U1 is configured to periodically discharge the charge capacitor C2 and to measure a voltage across the charge capacitor C2 a selected time after the charge capacitor C2 is discharged to measure the detected light level. The microcontroller U1 turns off the drive transistor Q2 (causing the normally open relay to return to the open state) by coupling the base of the second transistor Q3 to a logic low, which turns on the

second transistor Q3, which in turn turns off the drive transistor Q2(essentially coupling both the base and emitter of the drive transistor Q2 to the regulated power supply PS1 (VDD)). To operate the relay to switch to the closed position (i.e., turn on a light or other load), the microcontroller U1 applies the pulse width modulated signal to the base of the second transistor Q3. While this may be done by digital logic, this operation is illustrated schematically in FIG. 2 by a relay K2 switching the drive signal between the output of the comparator IC1A and VSS. The driving of the coil of relay K2 is illustrated as being responsive to the hysteresis inverter IC2 responsive to the detection of the detected light level. It will be understood that a variable delay time before switching may be programmed into the microcontroller U1 and/or an asymmetric switching delay (turning on delay differs from turning off delay).

[0027] Using the configuration described above, embodiments may provide a photosensor circuit that is a street lighting photo control device and the load is street lighting (street lamp). The relay coil is configured to activate to switch on the street lighting responsive to detection of less than a minimum light level by the phototransistor.

[0028] By using a microcontroller instead of an analog circuit, a wider variety of delay times may be provided. In addition, a pulse width modulated signal at much higher than line frequencies may be more readily provided. As a normally open relay may be more subjected to connection reliability problems, a higher frequency may make operation of the device more reliable. In some embodiments, the pulse width modulator circuit is configured to control a frequency of the pulse width modulated signal and the frequency of the pulse width modulated signal is at least about 500 Hertz (Hz). To support programming of the microcontroller U1, three isolation resistors R1, R10, R12 are shown in FIG. 2.

[0029] A power circuit R2-R4, Q3 (and drive transistor Q2) coupled to the power source is configured to provide a regulated power signal PS1 (VDD (high) referenced to VSS (low)) to the microcontroller U1 including the comparator IC1A of the pulse width modulator circuit. In the illustrated embodiments, the resistors R2-R4 and the second transistor Q3 are arranged as a level shifter power scavenger circuit. The power circuit includes the drive transistor Q2 coupled between the relay coil K1 and the regulated power signal PS1 and the second transistor Q3 coupled between a base of the drive transistor Q2 and the regulated power signal PS1. As such, the drive

transistor Q2 conducts current passing through the relay coil K1 to the regulated power signal PS1 when the drive transistor Q2 is on and the second transistor Q3 conducts current not passing through the relay coil K1 to the regulated power signal PS1 when the drive transistor Q2 is turned off.

[0030] In some embodiments, the power circuit includes a rectifier, shown in FIG. 2 as a full-wave rectifier B1. The rectifier B1 is included in a full waver power supply filter bridge rectifier including resistor R9 and capacitor C5. A shunt regulator, shown as a Zener diode VR1, is coupled between VSS and VDD and a current shunt R5 is shown coupled between VSS and the rectifier B1.

[0031] For the illustrated power circuit of FIG. 2, a collector of the second transistor Q3 is coupled to the base of the drive transistor Q2. An emitter of the second transistor Q3 is coupled to the regulated power signal PS1. An output of the comparator IC1A is coupled to the base of the second transistor Q3. The power circuit further includes a first resistor R4 coupled between the rectifier B1 and the base of the drive transistor Q2, a second resistor R3 coupled between the base of the drive transistor Q2 and the base of the second transistor Q3 and a third resistor R2 coupled between the output of the comparator IC1A and the base of the second transistorQ3.

[0032] As described above and more particularly with reference to a PIC10F206 microcontroller U1, the analog comparator IC1A is connected to the U1 terminal referenced as CI+ and GP2. The internally generated voltage reference is connected internally to the negative comparator input. The switch mode regulation function is performed by the internal analog comparator IC1A and voltage reference (0.6V) and not by the microcontroller digital logic, which may be turned off when not needed to save power (sleep mode). The microcontroller digital logic is used to interface with the phototransistor Q1 and causes the terminal referenced as GP2 to be internally connected to the comparator output during the night, resulting in the relay turning on the street lamp (or other load). During the day the microcontroller digital logic will output a logic low through the terminal referenced as GP2, causing the relay to turn off the street lamp.

[0033] Timing diagrams simulating operation of the circuit of FIG. 2 when the relay is activated (current flowing through relay coil K1) are shown in FIGs. 4A to 4D. FIG.4A illustrates the voltage at the output of the analog comparator IC1A. FIG. 4B illustrates the voltage at the positive comparator input of the pulse width circuit

comparator IC1A. FIG. 4C illustrates the voltage across the drive transistor Q2. FIG. 4D illustrates voltage at terminal 1 of the relay coil K1 (cathode of diode D2). FIG. 4E illustrates the current through the drive transistor Q2.

[0034] Further embodiments of a photosensor circuit 10' are shown in the circuit diagram of FIG. 3. The embodiments of FIG. 3 differ from the embodiments of FIG. 2 in that the photosensor circuit 10' includes a half-wave rectifier D1 instead of the full-wave rectifier B1.

[0035] Embodiments of the present invention as described above also may eliminate the use of aluminum electrolytic type capacitors in the photosensor circuit. As the technology of street lighting advances, more efficient and longer life lamps are being introduced. While previously a typical street lamp generally lasted three years before replacement was necessary, new street lamps last as long as 20 years. Accordingly, a photocontrol that can be guaranteed for a 20-year life may also be desirable. Such an extended life may be provided by embodiments as illustrated in FIGs. 2 and 3.

[0036] In contrast, typical conventionally available photocontrols contain aluminum electrolytic capacitors. Such photocontrols are generally rated at a lifetime of 5,000 hours (208 days). By operating photocontrols at much lower voltages and temperatures than their maximum ratings, they typically last up to three years but not the desired 20 years. By allowing practical use of long-life capacitors, such as ceramic-type capacitors, circuit life can be significantly increased as ceramic capacitors typically last extreme lengths of time. Furthermore, as new LED-type lamps tend to be lower in power, the power dissipation component of the photocontrol circuit itself becomes a greater percentage of the total efficiency equation for the lighting. As such, reductions of energy consumption of the photocontrol circuit are also beneficial.

[0037] Particular aspects of the illustrated embodiments as contrasted with the circuit of FIG. 1 allowing use of ceramic capacitors will now be described. The VDD filter capacitor C1 may be changed from an aluminum electrolytic type to a very long life ceramic type in a variety of ways on different embodiments. For example, with reference to FIG. 2 as contrasted with FIG. 1, replacing half wave rectification with full wave rectification doubles the ripple frequency, which may allow the VDD filter capacitor C1 value to be halved. With reference to both FIGs. 2 and 3, the capacitor C5 was added, allowing current to continue flowing into the regulated power supply

PS1 (VDD) even during the zero-cross-over of the line voltage allowing the value of the VDD filter capacitor C1 to be reduced. In addition, allowing the microcontroller U1 digital logic to be turned off much of the time reduces the required power, allowing a smaller value of the VDD filter capacitor, C1. In addition, the circuit of FIG. 1 uses capacitive coupling between the control circuit and the relay coil drive transistor Q2, which calls for the use of an aluminum electrolytic type capacitor. The need for such an aluminum electrolytic type capacitor may be eliminated by use of the level shifter circuit R2, R3, R4, Q3.

[0038] The foregoing is illustrative of the present invention and is not to be construed as limiting thereof. Although a few exemplary embodiments of this invention have been described, those skilled in the art will readily appreciate that many modifications are possible in the exemplary embodiments without materially departing from the novel teachings and advantages of this invention. Accordingly, all such modifications are intended to be included within the scope of this invention as defined in the claims. In the claims, means-plus-function clauses are intended to cover the structures described herein as performing the recited function and not only structural equivalents but also equivalent structures. Therefore, it is to be understood that the foregoing is illustrative of the present invention and is not to be construed as limited to the specific embodiments disclosed, and that modifications to the disclosed embodiments, as well as other embodiments, are intended to be included within the scope of the appended claims. The invention is defined by the following claims, with equivalents of the claims to be included therein.

THAT WHICH IS CLAIMED:

1. A photosensor circuit, comprising:

a relay coil configured to control application of an alternating current (AC) power source having a negative half and a positive half of a line cycle to a load;

a pulse width modulator circuit configured to generate a pulse width modulated signal having a pulse width that varies responsive to an average voltage across the relay coil, the pulse width modulator circuit including a comparator;

a drive transistor coupled to the relay coil that controls the average voltage across the relay coil responsive to the pulse width modulated signal;

a photo control circuit configured to control application of the pulse width modulated signal to the drive transistor responsive to a detected light level; and

a power circuit, coupled to the power source, that is configured to provide a regulated power signal to the comparator of the pulse width modulator circuit, wherein the power circuit includes the drive transistor coupled between the relay coil and the regulated power signal and a second transistor coupled between a base of the drive transistor and the regulated power signal and wherein the drive transistor conducts current passing through the relay coil to the regulated power signal when the drive transistor is on and the second transistor conducts current not passing through the relay coil to the regulated power signal when the drive transistor is turned off.

2. The photosensor circuit of Claim 1, wherein the power circuit includes a rectifier coupled to the AC power source.

3. The photosensor circuit of Claim 2, wherein the rectifier comprises a full-wave rectifier.

4. The photosensor circuit of Claim 2, wherein a collector of the second transistor is coupled to the base of the drive transistor, an emitter of the second transistor is coupled to the regulated power signal and an output of the comparator is coupled to the base of the second transistor and wherein the power circuit further comprises a resistor coupled between the base of the second transistor and the rectifier.

5. The photosensor circuit of Claim 4, wherein the resistor comprises a first resistor and a second resistor and wherein the first resistor is coupled between the rectifier and the base of the drive transistor and the second resistor is coupled between the base of the drive transistor and the base of the second transistor.

6. The photosensor circuit of Claim 5, wherein the power circuit further comprises a third resistor coupled between the output of the comparator and the base of the second transistor.

7. The photosensor circuit of Claim 2, wherein the pulse width modulator circuit is configured to control a frequency of the pulse width modulated signal and wherein the frequency of the pulse width modulated signal is at least about 500 Hertz (Hz).

8. The photosensor circuit of Claim 7, wherein the photosensor circuit includes a relay including the relay coil and wherein the relay comprises a normally open relay.

9. The photosensor circuit of Claim 7, wherein the photosensor circuit comprises a street lighting photo control device and the load comprises street lighting and the relay coil is configured to activate to switch on the street lighting responsive to detection of less than a minimum light level by the phototransistor.

10. The photosensor circuit of Claim 2, wherein the photo control circuit includes:

a phototransistor having a first terminal coupled to the regulated power signal and a second terminal that outputs a current responsive to a level of light detected by the phototransistor; and

a microcontroller configured to measure the detected light level and to turn off the drive transistor responsive to the detected light level meeting a selected condition, wherein the power circuit is configured to provide the regulated power signal to the microcontroller.

11. The photosensor circuit of Claim 10, wherein the microcontroller is

configured to disconnect the output of the comparator from the base of the second transistor to turn off the drive transistor.

12. The photosensor circuit of Claim 10, wherein the photo control circuit further includes:

a resistor coupled between the microcontroller and the second terminal of the phototransistor and a charge capacitor coupled between the second terminal of the phototransistor and a low voltage reference of the regulated power signal, wherein current flowing through the phototransistor charges the charge capacitor; and

wherein the microcontroller is configured to periodically discharge the charge capacitor and to measure a voltage across the charge capacitor a selected time after the charge capacitor is discharged to measure the detected light level.

13. The photosensor circuit of Claim 10, wherein the microcontroller is configured to turn off the drive transistor at a selected delay time after the detected light level is determined to meet the selected condition.

14. The photosensor circuit of Claim 10, wherein the comparator and the microcontroller are included in a single microcontroller chip.

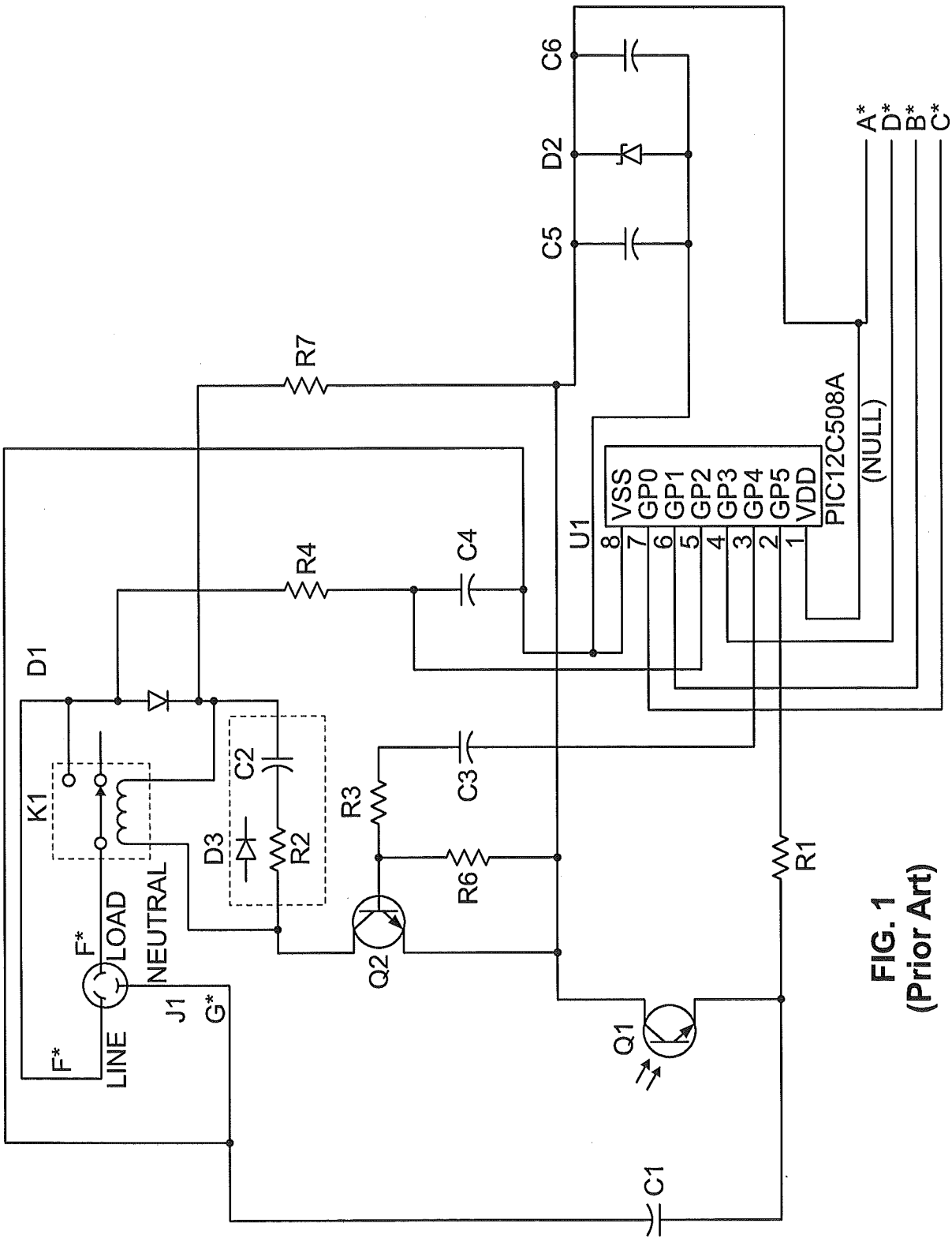


FIG. 1
(Prior Art)

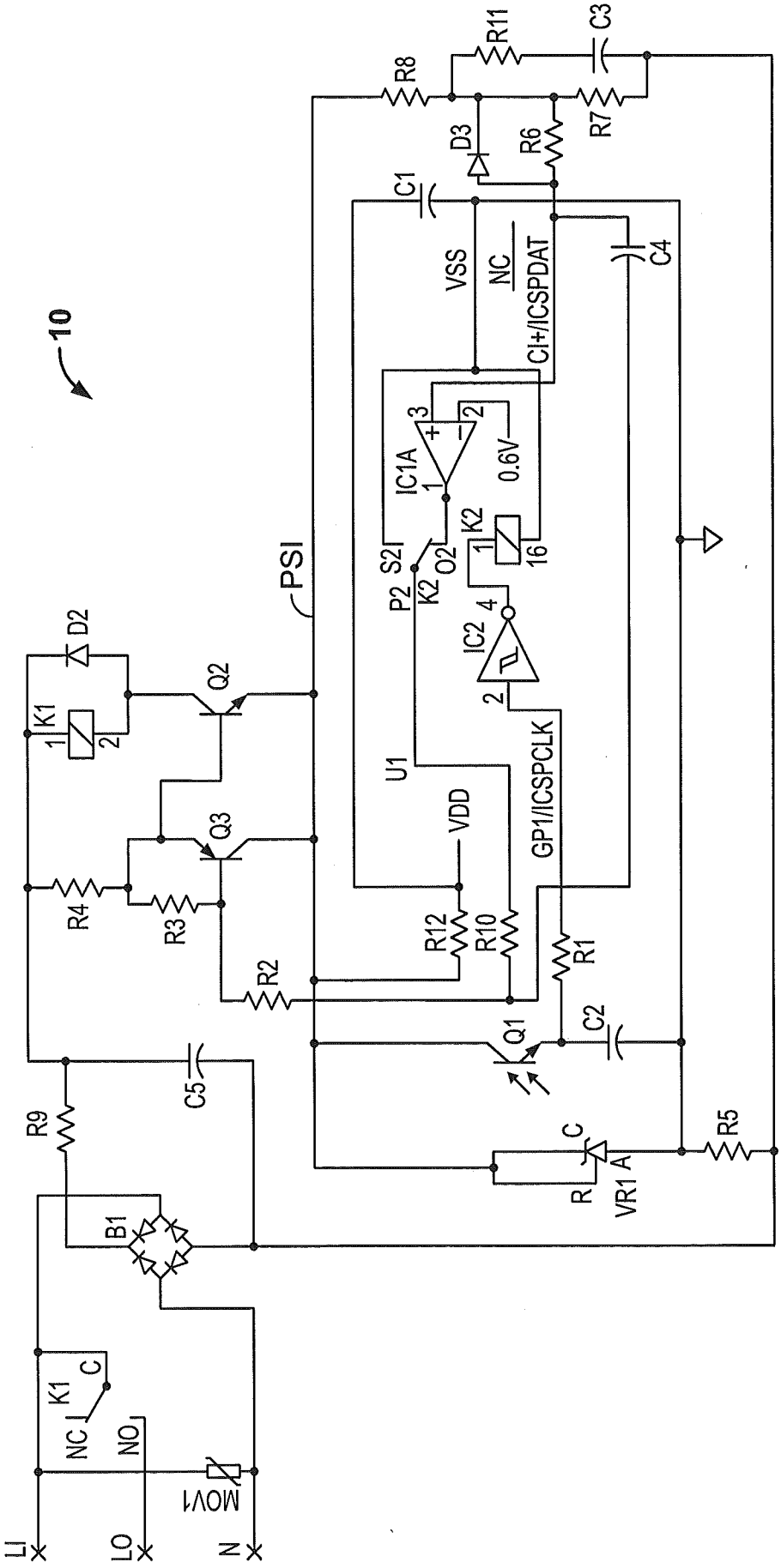

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FIG. 2

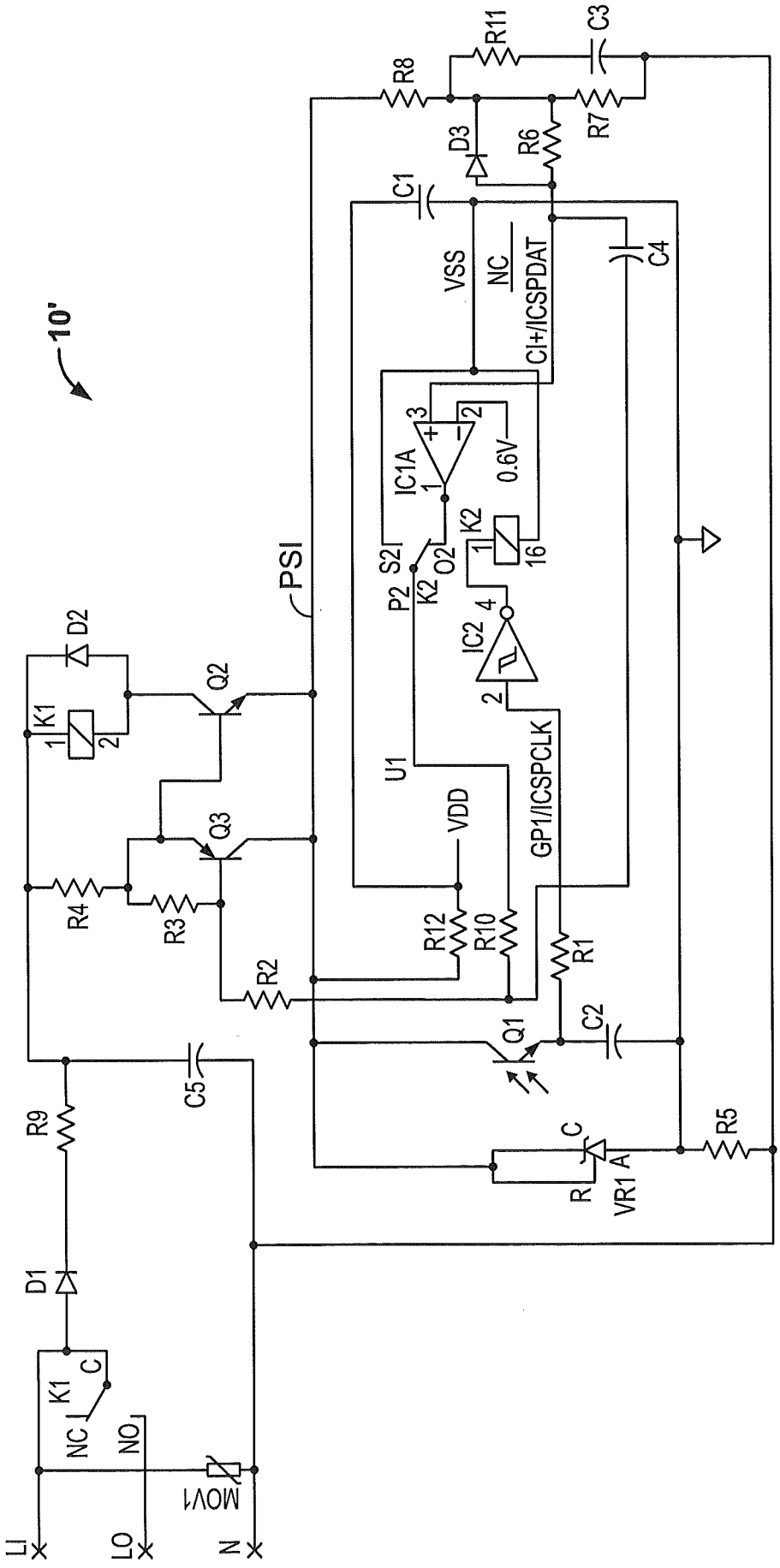

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IC2
U1
VDD
GP1/ICSPCLK
R12
R10
R1
Q1
C2

FIG. 3

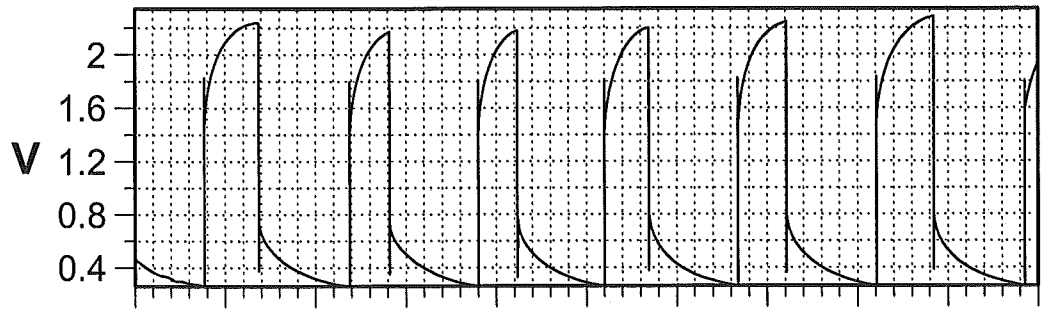


FIG. 4A

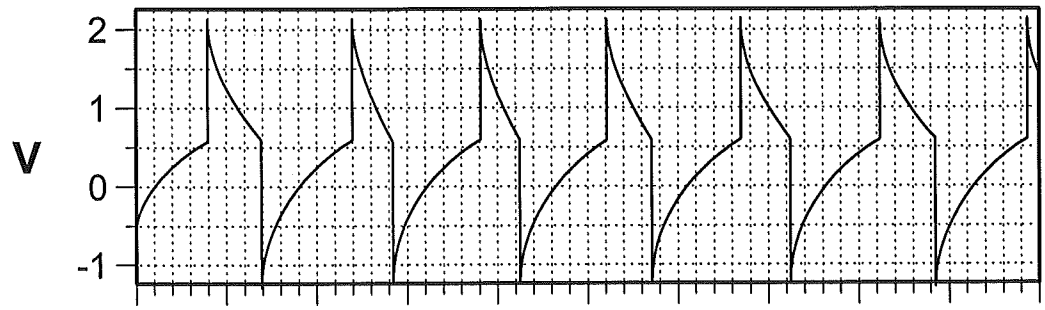


FIG. 4B

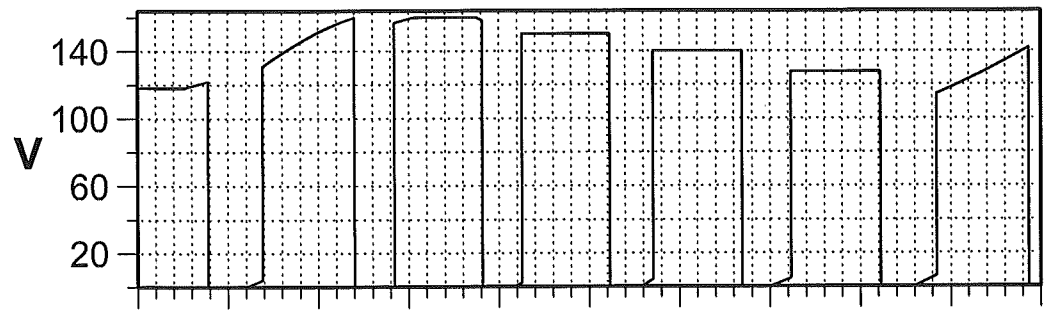


FIG. 4C

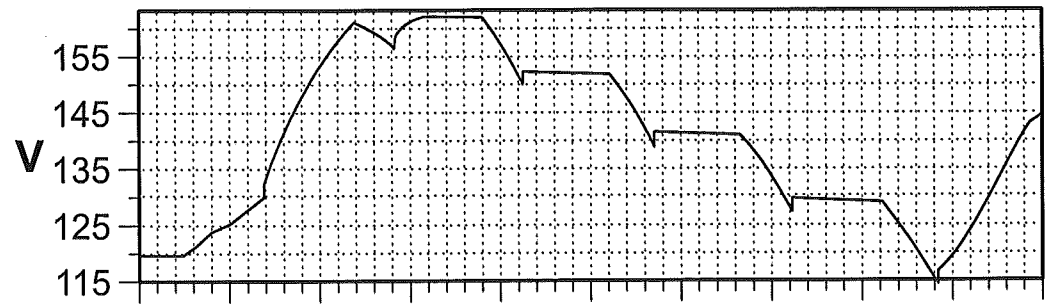


FIG. 4D

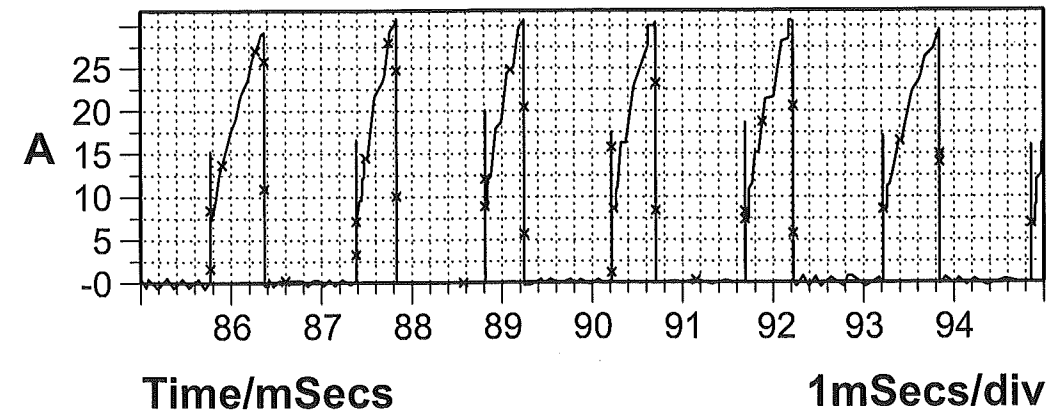


FIG. 4E

INTERNATIONAL SEARCH REPORT

International application No
PCT/US2012/065315

A. CLASSIFICATION OF SUBJECT MATTER
INV. H05B37/02
ADD.

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)
H05B

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)
EPO-Internal, WPI Data

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	US 2010/097122 A1 (FLAHERTY RICHARD CHARLES [US]) 22 April 2010 (2010-04-22) claim 1; figure 6 paragraph [0071] -----	1

☐ Further documents are listed in the continuation of Box C.

☒ See patent family annex.

* Special categories of cited documents :

"A" document defining the general state of the art which is not considered to be of particular relevance

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"&" document member of the same patent family

Date of the actual completion of the international search	Date of mailing of the international search report
28 February 2013	08/03/2013

Name and mailing address of the ISA/	Authorized officer
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INTERNATIONAL SEARCH REPORT

International application No
PCT/US2012/065315

Patent document cited in search report	Publication date	Patent family member(s)	Publication date
US 2010097122 A1	22-04-2010	AU 2009308125 A1	29-04-2010
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		CN 102257882 A	23-11-2011
		CO 6440549 A2	15-05-2012
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		WO 2010047751 A2	29-04-2010