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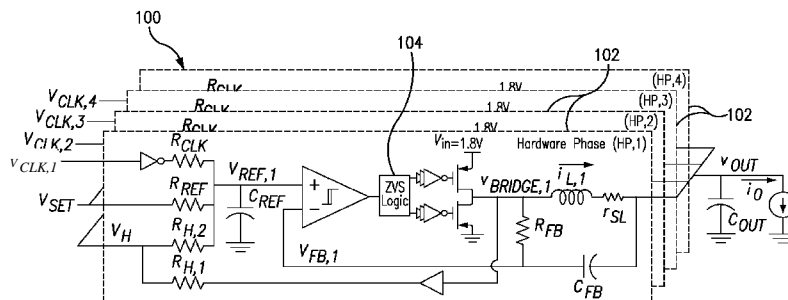


FIG. 1

(57) Abstract: Power controller includes an output terminal having an output voltage, at least one clock generator to generate a plurality of clock signals and a plurality of hardware phases. Each hardware phase is coupled to the at least one clock generator and the output terminal and includes a comparator. Each hardware phase is configured to receive a corresponding one of the plurality of clock signals and a reference voltage, combine the corresponding clock signal and the reference voltage to produce a reference input, generate a feedback voltage based on the output voltage, compare the reference input and the feedback voltage using the comparator and provide a comparator output to the output terminal, whereby the comparator output determines a duty cycle of the power controller. An integrated circuit including the power controller is also provided.

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Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched		
Electronic data base consulted during the international search (name of data base and, where practicable, search terms used) PatBase, Google Patent Search, Google Scholar		
C. DOCUMENTS CONSIDERED TO BE RELEVANT		
Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X ---	US 6,166,527 A (DWELLEY et al) 26 December 2000 (26.12.2000) entire document	1, 2, 7, 8, 13, 17, 18 -----
Y		3-6, 9-12, 14-16
Y	US 6,900,643 B2 (DENG et al) 31 May 2005 (31.05.2005) entire document	3, 4, 9, 14
Y	US 2006/0192536 A1 (CHEN et al) 31 August 2006 (31.08.2006) entire document	5, 6, 15, 16
Y	US 2006/0113922 A1 (RIBARICH et al) 01 June 2006 (01.06.2006) entire document	10-12
Y	US 2009/0290442 A1 (RAJAN) 26 November 2009 (26.11.2009) entire document	11-12
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