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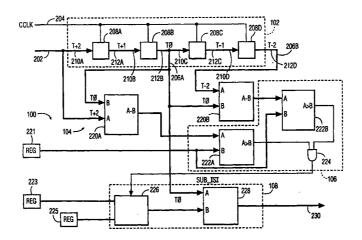
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(54) Title: ADAPTIVE AND SELECTIVE CANCELLATION OF INTER-SYMBOL INTERFERENCE OF A READ CHANNEL IN STORAGE TECHNOLOGIES



(57) Abstract

A method, circuit and system for cancelling inter-symbol interference from samples of a read signal in storage technologies. A circuit embodying the present invention includes sample logic, arithmetic logic and cancellation logic. The sample logic receives samples and provides a main sample and one or more selected neighbor samples. The arithmetic logic compares the main sample to the neighbor samples toward determining satisfaction of selected cancellation criteria. If the selected cancellation criteria are satisfied, the cancellation logic selectively and adaptively effects ISI cancellation as to the main sample. A method embodying the present invention comprises obtaining an ISI cancellation value in association with a reference field of the read signal; sampling the read signal at channel bit locations to provide a plurality of samples; selecting a main sample from the plurality of samples, the neighbor samples being disposed at predetermined distances, in channel bits, from the main sample; comparing the main sample to each of the neighbor samples to determine satisfaction of selected cancellation criteria; applying, where the cancellation criteria are determined to be satisfied, the ISI cancellation value to the main sample.

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C. DOCU	MENTS CONSIDERED TO BE RELEVANT				
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Information on patent family members

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