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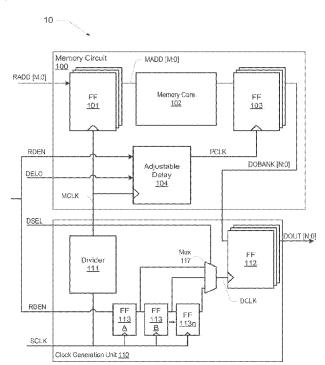
US

- (71) Applicant (for all designated States except US): MOSYS, INC. [US/US]; 3302 Olcott Street, Santa Clara, California 95054 (US).
- (72) Inventor; and
- (75) Inventor/Applicant (for US only): SIKDAR, Dipak [IN/US]; 3480 Granada Avenue, #261, Santa Clara, California 95051 (US).

- (74) Agent: KIVLIN, Noël, B.; Meyertons, Hood, Kivlin, Kowert & Goetzel, P.C., P.O. Box 398, Austin, TX 78767-0398 (US).
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[Continued on next page]

(54) Title: MEMORY DEVICE INCLUDING A MEMORY BLOCK HAVING A FIXED LATENCY DATA OUTPUT



(57) Abstract: A memory block includes a memory circuit and a clock generation unit. The memory circuit may output read data in response to being clocked by a clock signal having a selectable delay that may be dependent upon a time taken for the read data to be output by a memory core after the read command is received at the memory block. The clock generation unit may cause the read data to be provided as an output of the memory block in response to being clocked by a selected data clock signal. The data clock signal may be selected from one of several clock edges generated by one of several clock edges of a system clock such that regardless of the frequency of the system clock, the read data is provided by the memory block a predetermined amount of time after the read command is received at the memory block.

SM, TR), OAPI (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

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A. CLASSIFICATION OF SUBJECT MATTER

G11C 7/10(2006.01)i, G11C 7/22(2006.01)i

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC: G11C

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched Korean utility models and applications for utility models

Japanese utility models and applications for utility models

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used) eKOMPASS(KIPO internal) & Keywords: bank, latency, read, clock

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	US 2007-0019481 A1 (PARK) 25 January 2007 See abstract, claim 27, paragraph 85~87,89,99,101, figures 8-11.	1-21
A	US 2008-0232179 A1 (KWAK) 25 September 2008 See abstract, paragraph 18,1931,32, figures 2,5.	1-21
A	US 2006-0140044 A1 (KANG) 29 June 2006 See abstract, paragraph 23-31,figure 3.	1-21
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See patent family annex.

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Korean Intellectual Property Office Government Complex-Daejeon, 189 Cheongsa-ro, Seo-gu, Daejeon 302-701, Republic of Korea

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Authorized officer

YOON, Nan Young

Telephone No. 82-42-481-8188



INTERNATIONAL SEARCH REPORT

Information on patent family members

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