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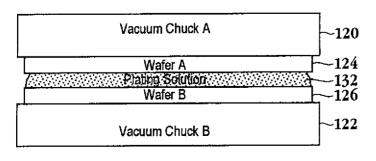
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(54) Title: ELECTROLESS PLATING METHOD AND APPARATUS



(57) Abstract: An electroless plating system is provided. The system includes a first vacuum chuck supporting a first wafer and a second vacuum chuck supporting a second wafer such that a top surface of the second wafer is opposing a top surface of the first wafer. The system also includes a fluid delivery system configured to deliver a plating solution to the top surface of the first wafer, wherein in response to delivery of the plating solution, the top surface of the second wafer is brought proximate to the top surface of the first wafer so that the plating solution

contacts both top surfaces. A method for applying an electroless plating solution to a substrate is also provided.



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ELECTROLESS PLATING METHOD AND APPARATUS

by Inventors:

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BACKGROUND

[0001] Electroless plating requires stringent control of the reaction chamber environment and the plating solution. Current electroless plating systems either dispense electrolyte on a spinning wafer or immerse the wafer in a bath of the electrolyte fluid. With these systems, control of the electrolyte over time is difficult. With a spinning wafer, a fair amount of fluid is lost as the wafer spins. With the bath system, the entire bath must be dumped and replaced periodically. Thus, in either of these cases relatively large volumes of the electrolyte solution are consumed for the processing as compared to the amount actually used for the electroless plating. This results in significant waste, not to mention the control issues centered around the chemical bath system. In addition, the attention required for the control of the plating solution and the need to dump and replace the chemical bath cuts into the throughput.

[0002] Another drawback with the current systems is that the plating bath must be maintained at an elevated plating temperature. This elevated temperature leads to the degradation of certain additives, e.g., reducing agents, especially with respect to electroless plating techniques that deposit cobalt layers.

[0003] In view of the forgoing, there is a need for an electroless plating method and system that can achieve a high throughput with minimal waste.

SUMMARY

[0004] Broadly speaking, the present invention fills these needs by providing a method and apparatus for electroless plating of a substrate using a minimal volume of plating solution and without impacting throughput. It should be appreciated that the present invention can be implemented in numerous ways, including as an apparatus, a method and a system. Several inventive embodiments of the present invention are described below.

[0005] In one embodiment, an electroless plating system is provided. The system includes a first vacuum chuck supporting a first wafer and a second vacuum chuck supporting a second wafer such that a top surface of the second wafer is opposing a top surface of the first wafer.

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The system also includes a fluid delivery system configured to deliver a plating solution to the top surface of the first wafer, wherein in response to delivery of the plating solution, the top surface of the second wafer is brought proximate to the top surface of the first wafer so that the plating solution contacts both top surfaces.

[0006] In another embodiment, an electroless plating system for depositing a layer onto a substrate is provided. The system includes a first vacuum chuck supporting a first wafer and a second vacuum chuck supporting a second wafer. A top surface of the second wafer is opposing a top surface of the first wafer, and the first and second vacuum chucks are capable of heating corresponding wafers. The second vacuum chuck heats the second wafer to a temperature above a temperature that induces a plating reaction. The system further includes a fluid delivery system configured to deliver a plating solution to the top surface of the first wafer. In response to delivery of the plating solution, the top surface of the second wafer is brought proximate to the top surface of the first wafer through a drive mechanism coupled to one of the first or the second vacuum chuck so that the plating solution contacts both top surfaces of the wafers.

[0007] In still another embodiment, a method for electroless plating on a semiconductor substrate is provided. The method initiates with loading first and second semiconductor substrates onto corresponding first and second support structures. The method includes orienting first and second support structures so that corresponding exposed semiconductor substrate surfaces are opposing each other and applying a plating solution to an exposed surface of the first semiconductor substrate. The method also includes bringing an exposed surface of the second semiconductor substrate into contact with the plating solution on the exposed surface of the first semiconductor substrate and initiating a plating reaction between the plating solution and both exposed surfaces. The exposed surfaces are then moved away from each other.

[0008] Other aspects of the invention will become apparent from the following detailed description, taken in conjunction with the accompanying drawings, illustrating by way of example the principles of the invention.

BRIEF DESCRIPTION OF THE DRAWINGS

[0009] The present invention will be readily understood by the following detailed description in conjunction with the accompanying drawings, and like reference numerals designate like structural elements.

[0010] Figure 1 is a simplified schematic diagram illustrating a point of use application for an electroless plating technique in accordance with one embodiment of the invention.

[0011] Figures 2A through 2D illustrate an electroless plating solution technique in which throughput is increased and a volume of electrolyte fluid consumed is minimized in accordance with one embodiment of the invention.

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[0012] Figure 3 is a graph illustrating the electroless plating process in terms of temperature and time in accordance with one embodiment of the invention.

[0013] Figure 4 is a flow chart diagram illustrating the method operations for performing a plating technique to increase throughput in accordance with one embodiment of the invention.

DETAILED DESCRIPTION

[0014] An invention is described for a high throughput electroless plating system. It should be appreciated that while specific plating solutions are described herein, the chamber may be used for any plating solution and is not limited for use with the specifically mentioned plating solutions. It will be obvious, however, to one skilled in the art, that the present invention may be practiced without some or all of these specific details. In other instances, well known process operations have not been described in detail in order not to unnecessarily obscure the present invention.

[0015] Electroless plating of cobalt on substrates requires precise control of both the reaction chamber environment and the electrolyte chemistry or solution. Current electroless plating systems utilize a recirculating chemistry bath that either dispenses electrolyte on a spinning wafer, or allows a wafer to be immersed in the bath itself. In either case, control of the electrolyte temperature over time is difficult. Current techniques utilize systems which tend to not use all of the available reactants in the solution prior to disposal, and hence waste the electrolyte chemistry. Thus, the costs involved increase because of the costliness of the electrolyte chemistry. In addition, the plating process requires the plating temperature to be maintained at an elevated temperature for the reaction to take place, which also leads to a degradation of key additives within the electrolyte solution, such as the reducing agent used in the cobalt electroless plating chemistry.

[0016] The embodiments described herein enable a minimal volume use of electrolyte per wafer, point of use mixing of key components of the electrolyte chemistry, low temperature storage and delivery of the plating chemical components, and increased throughput. As will be described in more detail below, the wafers are loaded on corresponding heated vacuum chucks, in a manner such that the surfaces of the wafer to have the plating solution applied are facing

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each other. That is, one wafer exposed surface is facing down over another wafer exposed surface facing up. Fluid is then dispensed on the face-up wafer prior to the wafer reaching the plating temperature. That is, the wafer is heated to a temperature below a temperature inducing a plating reaction. In the case of a cobalt electroless plating solution, this temperature is below about 45 degrees C. It should be appreciated that the fluid dispensed is the electrolyte chemistry for the electroless plating. Once the fluid has been dispensed onto the face-up wafer, the wafers are moved together to form a narrow gap between the two opposing surfaces of the wafers. This forces the dispensed fluid to fill the gap before the wafers reach the threshold plating temperature. In one exemplary embodiment, a gap of less than 100 micrometers is used and the volume of fluid required to fill this gap between the two wafers is less than 10 millimeters. In another embodiment, to improve liquid distribution within the gap and to minimize bubble formation, the heated vacuum chucks may be machined to have a slight convex curvature that enables full coverage of both wafer surfaces by the electrolyte after gap formation. The electroless plating solution may be any suitable multi-component commercially available formulation, such as those provided by Enthone Inc. of West Haven, Connecticut. [0017] Figure 1 is a simplified schematic diagram illustrating a point of use application for an electroless plating technique in accordance with one embodiment of the invention. Chamber 100 includes heated chuck 102 on top of which wafer or substrate 106 rests. O-rings 104 are used to seal the bottom area of chamber 100 from any electrolyte fluids falling off of the wafer 106 surface. Additionally, the volume of fluid dispensed can be chosen to be only slightly more than the volume subtended by the wafer diameter and gap formed, to minimize overflow. Fluid recirculation loop 110 will recirculate an electrolyte fluid without the reducing agent. In one embodiment, fluid recirculation loop 110 is maintained at an elevated temperature. This elevated temperature may be above about 45 degrees C in another exemplary embodiment and between about 50 degrees C and about 60 degrees C in one exemplary embodiment. Fluid recirculation loop 110 feeds into delivery line 112, which ultimately delivers the fluid to a top surface of wafer 106. Reducing agent 108 is added into delivery line 112 at a point of use downstream from fluid recirculation loop 110. It should be appreciated that as reducing agent 108 is a major cause of the instability of the electrolyte solution, adding the reducing agent at a point of use will help stabilize the process. In addition, as the amount of reducing agent to be added to the electrolyte solution through delivery line 112, is relatively small, the dilution will not affect any temperature of the fluid if the fluid recirculation loop is held at an elevated temperature. The electrolyte fluid, including reducing agent 108, is delivered to a top surface

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of wafer 106 and the electroless plating reaction takes place to dispose a film over a top surface of wafer 106. In one embodiment, the film is a cobalt-based film. In this embodiment, the portion of the plating solution recirculated in the recirculation loop may be composed of 0.1 M cobalt chloride or sulfate, 0.2 M sodium hypophosphite, 0.03 M sodium tungstate, 0.5 M sodium citrate, 0.5 M boric acid, and a small amount of a surfactant. The portion of the plating solution added at the point of use is a reducing agent such as dimethylamine borane (DMAB). One skilled in the art should appreciate that other plating solutions, e.g., those provided by Enthone Inc. may be incorporated with the embodiments described herein as the above listed solution composition is exemplary and not meant to be limiting.

[0018] Figures 2A through 2D illustrate an electroless plating solution technique in which throughput is increased and a volume of electrolyte fluid consumed is minimized in accordance with one embodiment of the invention. Vacuum chuck A 120 supports wafer A 124. Vacuum chuck B 122 supports wafer B 126. In one embodiment, vacuum chuck A 120 and vacuum chuck B 122 may be loaded with corresponding wafers while each vacuum chuck is in an upright position. Thereafter, vacuum chuck A may be inverted and disposed over vacuum chuck B so that surfaces of wafer A 124 and wafer B 126 are opposing each other. It will be apparent to one skilled in the art that any suitable vacuum chuck may be used to support the corresponding wafers. Furthermore, the structure for inverting the vacuum chuck may be a driven support arm coupled to the vacuum chuck, which is capable of inverting and moving the vacuum chuck in a horizontal and vertical position. One skilled in the art will appreciate that a motor coupled to the support arm may provide the required movement to invert the vacuum chuck and provide the vertical and the horizontal translation. In one embodiment, each vacuum chuck may be coupled to a support arm and both vacuum chucks are moveable. Alternatively, one of the vacuum chucks may be moveable.

[0019] In addition, vacuum chuck A 120 and vacuum chuck B 122 are capable of applying heat to the corresponding wafers supported by the vacuum chucks. One skilled in the art will appreciate that heat may be applied to the wafer through known available techniques, e.g., resistance heating, or other known techniques. Figure 2B illustrates an electrolyte fluid being applied to a top surface of wafer B 126 which is supported on vacuum chuck B 122. As mentioned above, the temperature of wafer B 126 is heated to a point that is below the temperature inducing the reaction to cause the electroless plating. Thus, the electroless plating will not take place upon the disposition of the electrolyte fluid on the top surface of wafer B 126. In addition, electrolyte 128 is maintained at room temperature as a small volume is only

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necessary to be applied to the top surface of wafer B 126. As illustrated, reducing agent 130 is added at a point of use into the delivery line providing the electrolyte solution to the top surface of wafer B 126. Of course, the reducing agent 130 may be incorporated into the reservoir holding electrolyte 128. In this embodiment, the temperature may be further reduced to safeguard against degradation of the electrolyte. It should be appreciated that since a relatively small amount of electrolyte is needed for the embodiments described herein, the temperature of the electrolyte fluid has a negligible impact on the temperature as the wafers are pre-heated. [0020] Figure 2C illustrates the removal of the fluid delivery structure and the lowering of vacuum chuck A to define a small gap between the corresponding surfaces of wafer A 124 and wafer B 126. Alternatively, vacuum chuck B and vacuum chuck A may both move towards each other. As the gap between wafer A and wafer B is reduced, both surfaces, i.e., the top surface of wafer A 124 and the top surface of wafer B 126, are in contact with the plating solution. Gap 132 contains the plating solution therein and the gap may be less than 100 micrometers in one embodiment of the invention. Thus, a thin meniscus is formed between the corresponding surfaces of wafer A 124 and wafer B 126. The amount of liquid utilized to form this meniscus in gap 132 may be two milliliters on a 200 millimeter wafer or 4-5 milliliters on a 300 millimeter wafer in accordance with one embodiment of the invention. These amounts will typically provide a film of less than one millimeter on the corresponding wafer surfaces. In one embodiment, the reaction may be self-limiting. That is, the fluid may be consumed during the entire reaction period so that no electrolyte fluid is wasted. In other words, the entire amount of cobalt is deposited on the surfaces and the remaining fluid upon completion of the reaction, i.e., the depletion layer, is waste to be discarded. It should be appreciated that as the temperature of both the plating solution and wafer B was below the reaction temperature, wafer A was held at a temperature above the reaction temperature when brought into contact with the plating solution in accordance with one embodiment of the invention. The plating solution is then heated through both vacuum chuck A 120 and vacuum chuck B 122, through corresponding wafers in order to uniformly plate the corresponding wafers. Figure 2D illustrates the separation of corresponding vacuum chucks 120 and 122. Here again, one skilled in the art will appreciate that known mechanical structures and techniques may achieve this separation. After the reaction has completed, vacuum chuck A 120 and vacuum chuck B 122 are separated and wafer A 124 has a film 134 defined thereon. Similarly, wafer B 126 has a film 136 disposed thereon. Upon separation of the corresponding wafers, the surfaces that have

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the films may be rinsed, or quenched, through a fine spray or dumped into a water bath in one embodiment of the invention. The wafers may then be moved for further processing as desired. [0021] Figure 3 is a graph illustrating the electroless plating process in terms of temperature and time in accordance with one embodiment of the invention. As illustrated in Figure 3, at time T1 wafer A, which is the top wafer is loaded onto a corresponding vacuum chuck. At time T2 wafer B is loaded onto its corresponding vacuum chuck. Here, wafer B is the bottom wafer. At time T3, the electrolyte solution is dispensed on top of the top surface of wafer B. As illustrated at time T3, wafer A has reached the reaction plating temperature, i.e., about 45 degrees C for a cobalt plating process, however, wafer B is not yet at the plating temperature. At time T4, wafer A is brought down to form the meniscus in the small gap between the two corresponding wafers. Here again, wafer B has not yet reached the reaction temperature but is approaching that temperature and through the coupling of wafer B and wafer A through the meniscus, the reaction temperature is achieved efficiently. The plating is initiated at time T5 as the corresponding temperatures have equilibrated and are above the plating temperature. Once the plating reaction completes, the wafers are separated for further processing. [0022] Figure 4 is a flow chart diagram illustrating the method operations for performing a plating technique to increase throughput in accordance with one embodiment of the invention. The method initiates with operation 300 where first and second wafers are loaded onto corresponding support structures. In one embodiment, the support structures are heated vacuum chucks as depicted above. The method then moves to operation 302 where the first and second wafers are oriented so that corresponding exposed wafer surfaces are opposing each other. Here, one wafer chuck may be inverted over another wafer chuck as illustrated in Figures 2A-D. In one embodiment, the vacuum chucks may have a slight convex curvature on the surface which the wafer sits. This convex curvature is imparted to the wafer to improve distribution of the electrolyte over the wafer surface and minimize bubble formation. The method then advances to operation 304 where a solution is applied to an exposed surface of a first wafer, while the temperature of the first wafer is below a temperature inducing a plating reaction between the solution and the exposed surface. In one embodiment, the solution is an electrolyte solution for an electroless plating reaction that occurs at an elevated temperature, e.g., a cobalt plating solution. In another embodiment, the solution may be applied where a reducing agent is added at a point of use in order to enhance the stability of the process. The method then proceeds to operation 306 where the exposed surface of the second wafer is moved to contact the solution disposed on the exposed surface of the first wafer. It should be

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appreciated that as this is taking place, contemporaneously the wafers are being heated through the corresponding vacuum chucks. In one embodiment, the temperature of the top wafer is at an elevated temperature as relative to the temperature of the bottom wafer as discussed with regard to Figure 3. Once the temperature of the plating solution disposed in the gap between the two wafers is above a threshold plating temperature, the electroless plating will begin. Thus, in operation 308 the reaction occurs for a specified period of time. The method then moves to operation 310 where the second wafer is moved away from the first wafer. Alternatively, both wafers may be removed from each other, according to drives attached to the corresponding vacuum chucks. In operation 312, the first and second wafers are rinsed, or quenched in order to remove any remaining fluid or contaminates. The wafers are then available for further processing that may take place downstream. As mentioned above, the amount of electrolyte solution deposited on the surface of the wafer may be self-limiting. [0023] Although a few embodiments of the present invention have been described in detail herein, it should be understood, by those of ordinary skill, that the present invention may be embodied in many other specific forms without departing from the spirit or scope of the invention. Therefore, the present examples and embodiments are to be considered as illustrative and not restrictive, and the invention is not to be limited to the details provided therein, but may be modified and practiced within the scope of the appended claims.

What is claimed is:

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Claims

- 1. An electroless plating system, comprising:
- a first vacuum chuck supporting a first wafer;
- a second vacuum chuck supporting a second wafer such that a top surface of the second wafer is opposing a top surface of the first wafer; and
- a fluid delivery system configured to deliver a plating solution to the top surface of the first wafer, wherein in response to delivery of the plating solution, the top surface of the second wafer is brought proximate to the top surface of the first wafer so that the plating solution contacts both top surfaces.
- 2. The system of claim 1, wherein, the first and second vacuum chuck are configured to heat the corresponding wafers.
- 3. The system of claim 1, wherein, the fluid delivery system recirculates a portion of the plating solution at an elevated temperature.
- 4. The system of claim 3, wherein the fluid delivery system adds a remaining portion of the plating solution downstream from the recirculation loop and the remaining portion is maintained at a temperature below the elevated temperature.
- 5. The system of claim 1, wherein a temperature of the second wafer is heated above a temperature inducing a plating reaction and a temperature of the first wafer is below the temperature inducing the plating reaction.
- 6. The system of claim 1, wherein the plating solution is configured to deposit a cobalt layer onto the top surfaces of the wafers, and wherein top surfaces of the first and the second vacuum chucks have a convex curvature.
- 7. An electroless plating system for depositing a layer onto a substrate, comprising:

a first vacuum chuck supporting a first wafer;

a second vacuum chuck supporting a second wafer such that a top surface of the second wafer is opposing a top surface of the first wafer, wherein the first and second vacuum chucks are capable of heating corresponding wafers and the second vacuum chuck heats the second wafer to a temperature above a temperature that induces a plating reaction; and

a fluid delivery system configured to deliver a plating solution to the top surface of the first wafer, wherein in response to delivery of the plating solution, the top surface of the second wafer is brought proximate to the top surface of the first wafer through a drive mechanism coupled to one of the first or the second vacuum chuck so that the plating solution contacts both top surfaces of the wafers.

- 8. The system of claim 7, wherein the fluid delivery system recirculates a portion of the plating solution and maintains a temperature of the portion of the plating solution.
- 9. The system of claim 8, wherein the fluid delivery system adds a remaining portion of the plating solution to the portion of the plating solution prior to delivering the plating solution to the top surface of the first wafer.
- 10. The system of claim 9, wherein the remaining portion of the plating solution includes a reducing agent.
- 11. The system of claim 7, wherein the plating solution is configured to deposit a cobalt layer onto the top surfaces of the wafers at the temperature that induces the plating reaction.
- 12. The system of claim 7, wherein the drive mechanism is configured to invert the second vacuum chuck over the first vacuum chuck and lower the second vacuum chuck towards the first vacuum chuck.
- 13. A method for electroless plating on a semiconductor substrate, comprising method operations of:

loading first and second semiconductor substrates onto corresponding first and second support structures;

orienting first and second support structures so that corresponding exposed semiconductor substrate surfaces are opposing each other;

applying a plating solution to an exposed surface of the first semiconductor substrate:

bringing an exposed surface of the second semiconductor substrate into contact with the plating solution on the exposed surface of the first semiconductor substrate;

initiating a plating reaction between the plating solution and both exposed surfaces; and

moving the exposed surfaces away from each other.

14. The method of claim 13, wherein the method operation of applying a plating solution to an exposed surface of the first semiconductor substrate includes,

heating the exposed surface of the first semiconductor substrate to a temperature below a temperature inducing a plating reaction prior to applying the plating solution.

15. The method of claim 13, wherein the method operation of bringing an exposed surface of the second semiconductor substrate into contact with the plating solution on the exposed surface of the first semiconductor substrate includes,

heating the exposed surface of the first semiconductor substrate to a temperature above a temperature inducing a plating reaction prior to bringing the exposed surface of the second semiconductor substrate into contact with the plating solution.

16. The method of claim 13, wherein the method operation of initiating a plating reaction between the plating solution and both exposed surfaces includes,

heating the plating solution to a temperature inducing a plating reaction by applying heat from the first and second vacuum chucks.

17. The method of claim 13, wherein the method operation of bringing an exposed surface of the second semiconductor substrate into contact with the plating solution on the exposed surface of the first semiconductor substrate includes,

defining a gap of between about 50µm and about 1000µm between the exposed semiconductor substrate surfaces.

- 18. The method of claim 13, further comprising: rinsing the exposed surfaces; and drying the rinsed surfaces.
- 19. The method of claim 13, wherein the method operation of applying a plating solution to an exposed surface of the first semiconductor substrate includes, delivering an amount of plating solution between about 2 mL and about 5 mL, wherein the plating solution is a cobalt based plating solution.
- 20. The method of claim 13, wherein the method operation of applying the plating solution to an exposed surface of the first semiconductor substrate includes,

adding a reducing agent portion of the plating solution to a remaining portion of the plating solution at a point of use of the plating solution, wherein the reducing agent portion and the remaining portion are maintained at a temperature below a temperature inducing a plating reaction.

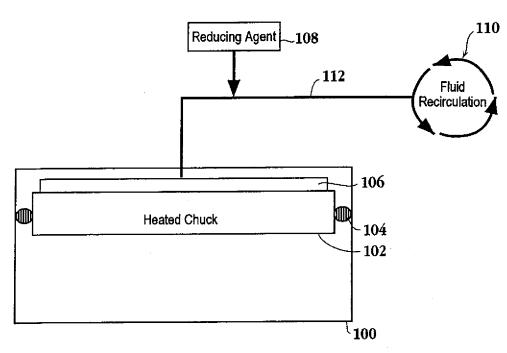


Fig. 1

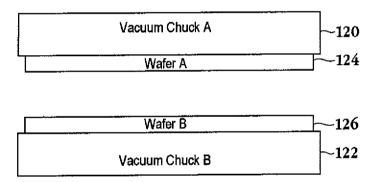


Fig. 2A

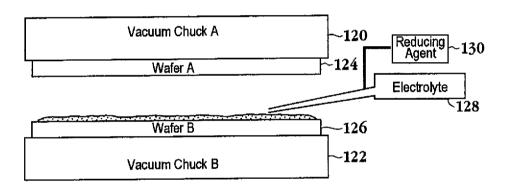


Fig. 2B

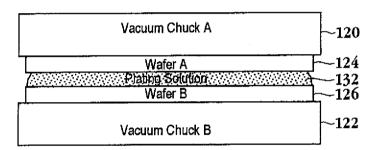


Fig. 2C

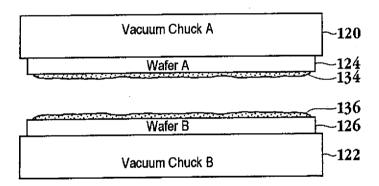
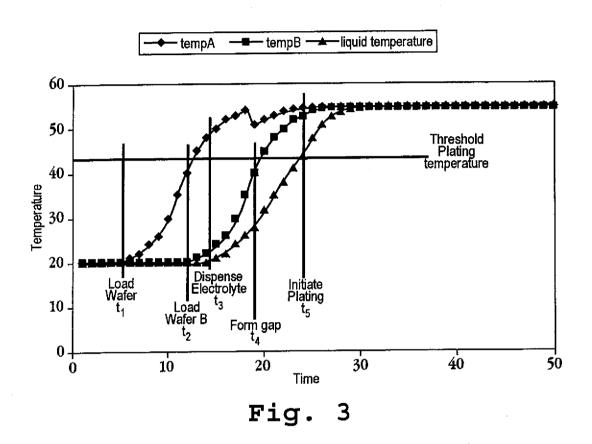


Fig. 2D



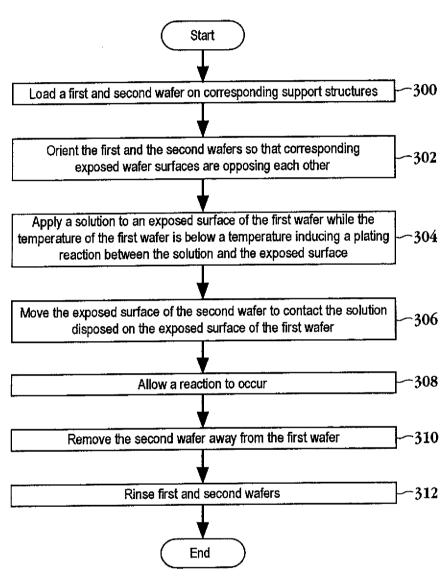


Fig. 4

INTERNATIONAL SEARCH REPORT

International application No. PCT/US 07/78187

A. CLASSIFICATION OF SUBJECT MATTER IPC(8) - B05C 11/02; B05C 13/00 (2007.10) USPC - 118/52			
According to International Patent Classification (IPC) or to both national classification and IPC			
B. FIELDS SEARCHED Minimum documentation searched (classification system followed by classification symbols)			
USPC - 118/52			
Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched USPC: all classes Google Scholar - see keywords below			
Electronic data base consulted during the international search (name of data base and, where practicable, search terms used) Searched US Pre-grant Publications, US Patent Full-Text, EPO Abstract, JPO Abstract, and Google Scholar Databases for convex, vacuum chuck, heated, Thie, Boyd, Dordi, Redeker, electroless plating			
C. DOCUMENTS CONSIDERED TO BE RELEVANT			
Category*	Citation of document, with indication, where ap	propriate, of the relevant passages	Relevant to claim No.
Y	US 5,830,805 A (Shacham-Diamond et al.) 03 November 1998 (03.11.1998); entire document, especially cols 1-4 and Figs 2-3		1-20
Y	US 5,960,159 A (Ikeda et al.) 28 September 1999 (28.09.1999); entire document, especially cols 1-4 and Fig. 2		1-20
Y	US 2003/0029570 A1 (Kawamura et al.) 13 February 2003 (13.02.2003); abstract, para [0003], [0013], [0042], and [0090]		1-12, and 14-17
Υ	US 5,534,073 A (Kinoshita et al.) 09 July 1996 (09.07.1996); col 3, ln 1-5.		2, 7-12, and 14-17
			<u> </u>
Further documents are listed in the continuation of Box C.			
* Special categories of cited documents: "A" document defining the general state of the art which is not considered to be of particular relevance "T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention			
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cited to establish the publication date of another citation or other special reason (as specified) "Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is			
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the priority date claimed Date of the actual completion of the international search Date of mailing of the international search report			
16 December 2007 (16.12.2007) 3 0 JAN 2008,			
Name and mailing address of the ISA/US Authorized officer:			
Mail Stop PCT, Attn: ISA/US, Commissioner for Patents			- how
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