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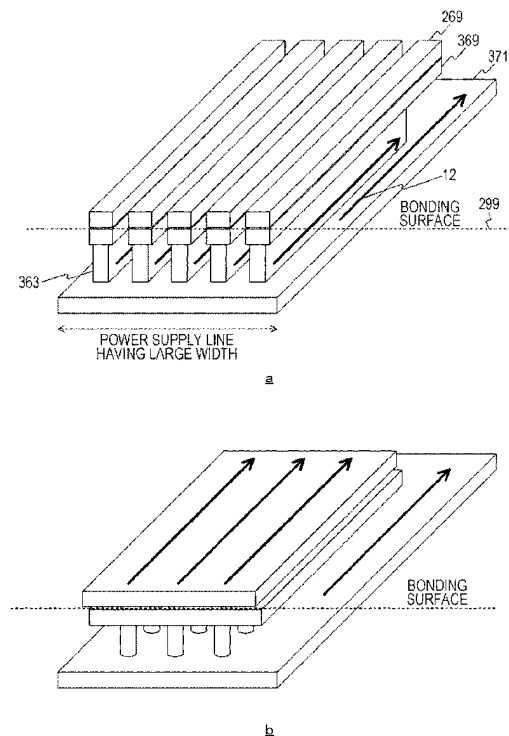
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(54) **WAFER BONDED BACK ILLUMINATED IMAGER**

(57) An imaging device includes a first substrate including a pixel array and a first multilayer wiring layer. The first multilayer wiring layer includes a plurality of first wirings. The imaging device includes a second substrate including a second multilayer wiring layer and a first plurality of transistors. The second multilayer wiring layer includes a plurality of second wirings, a plurality of second wirings, a plurality of third wirings and a plurality of fourth wirings. At least one of the plurality of second wirings is bonded to at least one of the plurality of first wirings along a bonding surface between the first substrate and the second substrate. At least one of the plurality of fourth wirings connects at least one of the plurality of second wirings to at least one of the plurality of third wirings. At least one fourth wiring has an aspect ratio that is greater than 1 in a plan view that is parallel to a plane of the bonding surface.

[Fig. 6]



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The present search report has been drawn up for all claims			
Place of search		Date of completion of the search	Examiner
The Hague		24 August 2023	Gijsbertsen, Hans
CATEGORY OF CITED DOCUMENTS		T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons ..... & : member of the same patent family, corresponding document	
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