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(74) Agent: BECCHAKU, Shigehisa; Matsuoka Tamuracho
Bldg. 7th Floor, 22-10, Shinbashi 5-chome, Minato-ku,
Tokyo 105-0004 (JP).

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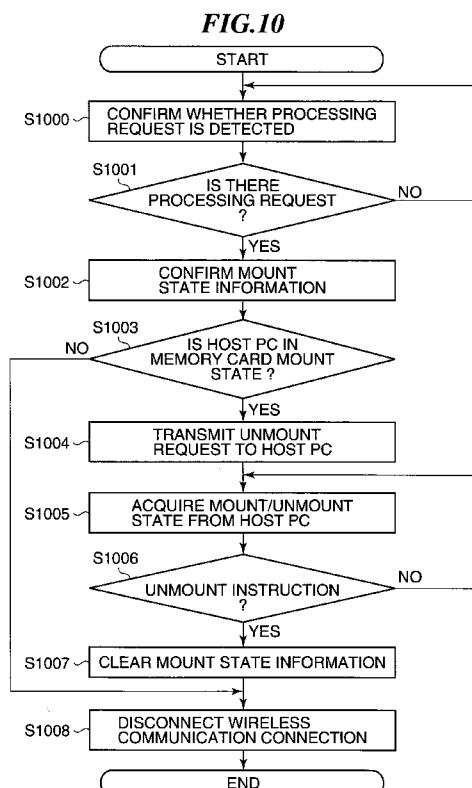
(71) Applicant (for all designated States except US): **CANON KABUSHIKI KAISHA [JP/JP]**; 30-2, Shimomaruko 3-chome, Ohta-ku, Tokyo 146-8501 (JP).

(72) Inventor; and

(75) Inventor/Applicant (for US only): **OKUTSU, Toshihisa [JP/JP]**; 30-2, Shimomaruko 3-chome, Ohta-ku, Tokyo 146-8501 (JP).

[Continued on next page]

(54) Title: INFORMATION PROCESSING APPARATUS, CONTROL METHOD THEREOF, AND STORAGE MEDIUM



(57) Abstract: An information processing apparatus able to normally unmount a memory and disconnect communication with a first external apparatus when receiving a processing request from a second external apparatus in a state that the first external apparatus mounts the memory connected to the apparatus. A multi-function peripheral as the processing apparatus (20) includes a controller OS. When receiving a processing request from a second host PC (10B) as the second external apparatus in a state where the multi-function peripheral is in communication with a first host PC (10A) as the first external apparatus (S3100), the controller OS requests the first host PC to unmount the memory (S3201), if the memory is mounted thereon. When receiving an unmount instruction from the first host PC (S3004), the controller OS unmounts the memory, disconnects the connection with the first host PC (S3005), and starts communication with the second host PC (S3101).



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DESCRIPTION

Title of Invention

INFORMATION PROCESSING APPARATUS, CONTROL METHOD THEREOF,
AND STORAGE MEDIUM

5 Technical Field

[0001]

The present invention relates to an information processing apparatus capable of communicating with a plurality of external apparatuses, a control method thereof, and a storage medium 10 storing a program for executing the control method.

Background Art

[0002]

As a communication system for connecting a host computer with a device (such as a printer, a scanner, a digital camera, 15 or the like), wireless USB is known. A single device can be shared between a plurality of host computers through wireless USB.

[0003]

With wireless USB communication, however, the single device 20 is unable to simultaneously communicate with the host computers. Therefore, it performs one-to-one communication with each of the host computers, as stipulated in wireless USB standard (Wireless USB Specification Rev. 1.0).

[0004]

25 In the following, a process for establishing communication connection between a host computer and a device is described with reference to FIG. 12 for an exemplar case where a digital

multi-function peripheral is used as the device. It should be noted that the digital multi-function peripheral (MFP) is an apparatus having plural functions (such as copying function, printing function, scanning function, facsimile function, and 5 memory card read/write function) and used to accomplish efficient office work.

[0005]

FIG. 12 is a view for explaining a process for establishing communication connection through wireless USB. In the 10 illustrated system, wireless USB communication is performed between a host PC 1000 having a wireless USB antenna 1001 and a device 2000 having a wireless USB antenna 2001.

[0006]

The host PC 1000 has a function of transmitting a beacon 15 via the wireless USB antenna 1001. As shown in FIG. 13, a host ID 1002 unique to the host PC 1000 and a device ID 2002 unique to the device 2000 are indicated in the beacon.

[0007]

To request the device 2000 to execute a print job created 20 by an application running on the host PC 1000, the host PC 1000 transmits a beacon to the device 2000. The device 2000 that receives the beacon reads a device ID 2002 indicated in the beacon, and confirms whether the device ID 2002 is the same as its own device ID. If the device ID 2002 differs from the own device 25 ID, the device 2000 determines that the beacon is not directed to the device 2000 and discards the beacon.

[0008]

Next, the device 2000 reads the host ID 1002 indicated in the beacon, and confirms in terms of association whether the host ID 1002 is equal to one of host IDs registered in the device 2000. If no such host ID is registered, the device discards 5 the beacon, determining that no association has been set for the host PC. It should be noted that association is an initial connection process performed at the time of installation of wireless USB equipment in order that a CC (Connection Context) comprised of a host ID, a device ID, and a connection key is 10 shared between a host PC and a device, and has three phases: identification, authentication, and authorization.

[0009]

Next, the device 2000 transmits a connection request to the host PC 1000. When authorizing the connection request, the 15 host PC 1000 transmits spooled print data to the device 2000. After completion of print data transmission, the host PC disconnects communication so as not to appropriate the device 2000. At that time, a disconnection request can be transmitted 20 either from the host PC or from the device. As described above, one-to-one wireless USB communication connection is established.

[0010]

On the other hand, in a case where a single device is shared between a plurality of host PCs, communication connection is 25 established by listing host information in the order in which the device receives beacons from the host PCs and by sequentially transmitting connection requests to the host PCs based on the

list. Thus, communication connection between the device and a sequentially selected one of the host PCs is established.

[0011]

With reference to FIGS. 14 to 16, a description is given 5 of a concrete system where a single device is shared between two host PCs. In FIG. 14, there is shown a case where first and second print jobs are respectively transmitted from first and second host PCs 1000, 1100 to the device 2000. A process sequence is shown in FIG. 15, and an example of host list is 10 shown in FIG. 16.

[0012]

The first host PC 1000 transmits to the device 2000 a first beacon 1000a as a print job processing request (step S6000). The device receiving the beacon requests the first host PC to 15 establish wireless communication connection (step S6001). The first host PC establishes wireless communication connection with the device and then transmits print data for the first print job to the device (step S6002). After completion of processing on the print data by the device, the first host PC or the device 20 requests disconnection of the wireless communication connection (step S6003).

[0013]

The second host PC 1100 transmits to the device 2000 a second beacon 1100a as a print job processing request (step S6100). 25 At that time, the device is in a state where wireless communication connection with the first host PC 1000 is established. Thus, after completion of processing on the first print job, the device

disconnects wireless communication connection with the first host PC, and requests the second host PC to establish wireless communication connection (step S6101). The second host PC establishes wireless communication connection with the device, 5 and then transmits print data for the second print job to the device (step S6102). After completion of processing on print data by the device, the second host PC or the device disconnects the wireless communication connection (step S6103).

[0014]

10 The order in which the host PCs are connected is controlled based on a host list 1200 shown in FIG. 16. The host list 1200 is stored in a memory of the device 2000, and is comprised of a number field 1201 and a host name field 1202. The device registers into the host list 1200 host information in the order 15 in which beacons from host PCs are received. In the number field 1201, there are shown the order in which connection requests are delivered as well as the order in which beacons are received.

[0015]

It should be noted that the wireless USB communication 20 system as described above is disclosed in, e.g., PTL (Patent Literature) 1 listed below.

[0016]

Conventionally, filesystemmount control (by which a memory is made accessible from a computer) has been known, in which 25 a host PC establishes wireless communication connection with a device, and mounts as a file system a memory card attached to the device. In a digital camera and a digital multi-function

5 peripheral, a construction has been realized where a host PC accesses a file stored in a memory card and reads the file from the memory card. A digital multi-function peripheral of this type has a memory card reader writer for reading and writing data from and into a memory card.

[0017]

10 For example, in an information processing apparatus disclosed in PTL (Patent Literature) 2 listed below, data recording means is unmounted (or controlled to a state unaccessible from a computer) when it is determined that there is an apparatus connected to a device, but mounted again when it is determined that the connection between the device and the apparatus is disconnected. Thus, file system mount control can be made according to whether or not any apparatus is connected 15 to the device.

[0018]

20 With the above-described conventional arrangement where a digital multi-function peripheral (device) having a memory card reader writer is shared between a plurality of host PCs, the device is able to recognize completion of a print job requested from the device to the host PC, but a problem is posed that a memory card job by a host PC that mounts a memory of the device cannot be completed until the host PC unmounts a file system.

[0019]

25 Unlike a print job, completion of a memory card job is controlled by the host PC having requested the memory card job. Thus, even when receiving a job request from another host PC,

the device cannot immediately start communication with the other host PC. If the device forcibly disconnects communication with the host PC when the memory card job is being executed, the file system is forcibly dismounted from the host PC, and an error state of the host PC is caused, resulting in a fear that a file is destroyed in some cases.

[0020]

If the memory card is left mounted even after a host PC user completes an operation for accessing the memory card of the device, a problem is posed that the device is appropriated solely by the one host PC.

Citation List

Patent literature

[0021]

15 {PTL 1} Japanese Laid-open Patent No. 2007-251851

{PTL 2} Japanese Laid-open Patent No. 2007-65945

Summary of Invention

Technical Problem

[0022]

20 The present invention provides an information processing apparatus capable of normally unmounting a memory and disconnecting communication with a first external apparatus, when receiving a processing request from a second external apparatus in a state that the first external apparatus mounts the memory connected to the information processing apparatus, and provides a control method of the information processing apparatus and a storage medium storing a program for executing

the control method.

Solution to Problem

[0023]

Accordingly, a first aspect of this invention provides an

5 information processing apparatus able to communicate with first and second external apparatuses, which comprises a connection unit configured to be connected to a data storage memory for communication with the memory, a judgment unit configured to determine whether the first external apparatus mounts the memory

10 in a case where the information processing apparatus which is in communication with the first external apparatus receives a processing request from the second external apparatus, a request unit configured to request the first external apparatus to unmount the memory in a case where the judgment unit determines

15 that the first external apparatus mounts the memory, and a communication control unit configured to disconnect communication with the first external apparatus and start communication with the second external apparatus after the first external apparatus unmounts the memory according to a request

20 from the request unit.

[0024]

Accordingly, a second aspect of this invention provides a control method of an information processing apparatus able to communicate with first and second external apparatuses and

25 having a connection unit configured to be connected to a data storage memory for communication with the memory, which comprises a judgment step of determining whether the first external

apparatus mounts the memory in a case where the information processing apparatus which is in communication with the first external apparatus receives a processing request from the second external apparatus, a request step of requesting the first external apparatus to unmount the memory in a case where it is determined in the judgment step that the first external apparatus mounts the memory, and a communication control step of disconnecting communication with the first external apparatus and starting communication with the second external apparatus 10 after the first external apparatus unmounts the memory according to a request issued in the request step.

[0025]

Accordingly, a third aspect of this invention provides a computer-readable storage medium storing a program for causing 15 a computer to execute the control method according to the second aspect of this invention.

[0026]

The features and advantages of the invention will become more apparent from the following detailed description taken in 20 conjunction with the accompanying drawings.

Advantageous Effects of Invention

[0027]

According to this invention, when a processing request is received from a second external apparatus in a state where a 25 memory connected to the information processing apparatus is mounted on a first external apparatus, communication with the first external apparatus can be disconnected after the memory

is normally unmounted. As a result, the information processing apparatus can be prevented from being unnecessarily appropriated by the first external apparatus, while avoiding occurrence of an error which would occur when communication is disconnected 5 in a state that the memory is not normally unmounted.

Brief Description of Drawings

[0028]

FIG. 1 is a block diagram showing the construction of a 10 controller unit of a digital multi-function peripheral as an information processing apparatus according to one embodiment of this invention;

FIG. 2 is a block diagram showing the construction of a controller operating system of the digital multi-function 15 peripheral;

FIG. 3 is a block diagram showing the construction of a host controller of each of host PCs as external apparatuses adapted to be connected to the digital multi-function peripheral;

FIG. 4 is a block diagram showing the construction of a 20 host operating system of each host PC;

FIG. 5 is a sequence diagram showing the procedures of a memory file mount/unmount process performed by the digital multi-function peripheral and host PCs;

FIG. 6 is a sequence diagram showing the procedures of memory 25 card mount/unmount control by the digital multi-function peripheral;

FIG. 7 is a sequence diagram showing another procedures

of the memory card mount/unmount control by the digital multi-function peripheral;

FIG. 8 is a sequence diagram showing the procedures of memory card mount/unmount control by each host PC;

5 FIG. 9 is a sequence diagram showing another procedures of the memory card mount/unmount control by each host PC;

FIG. 10 is a flowchart showing a memory unmount process by the digital multi-function peripheral;

10 FIG. 11 is a flowchart showing a memory unmount process by each host PC;

FIG. 12 is a view showing the procedures for establishing wireless USB communication connection;

FIG. 13 is a view showing a beacon containing a host ID and a device ID;

15 FIG. 14 is a view showing a system comprised of a plurality of host PCs and a single device;

FIG. 15 is a sequence diagram showing a process for enabling the host PCs to share the single device; and

FIG. 16 is a view showing a host list stored in the device.

20

Description of Embodiments

[0029]

In the following, one embodiment of this invention will be described with reference to the appended drawings.

25 [0030]

In this embodiment, a description will be given of a system in which, in an environment of a single information processing

apparatus being shared between a plurality of external apparatuses, each of the external apparatuses is able to mount, as a file system, a memory (e.g., a memory card) connected to the information processing apparatus.

5 [0031]

First, the hardware construction of a digital multi-function peripheral as the information processing apparatus of this embodiment and the hardware construction of host PCs as external apparatuses are described with reference 10 FIGS. 1 to 4.

[0032]

FIG. 1 shows in block diagram the construction of a controller unit of the digital multi-function peripheral.

[0033]

15 As shown in FIG. 1, the digital multi-function peripheral (MFP) 20 includes a controller unit 200, a printer unit 210, a scanner unit 220, an operation unit 230, and a wireless USB antenna 21. The controller unit 200 includes a CPU 110, a RAM 111, a ROM 112, a wireless communication I/F 113, a printer unit 20 I/F 114, and a scanner unit I/F 115. The controller unit 200 further includes an operation unit I/F 116, a memory card I/F 117, and an internal bus 118.

[0034]

25 The printer unit 210 is an image output device for printing an image on a sheet. The scanner unit 220 is an image input device for reading an image from an original. The operation unit 230 includes an input device for performing various settings

of the multi-function peripheral and giving an instruction to the multi-function peripheral, and includes a display device for providing various displays. The controller unit 200 for inputting and outputting image information and device 5 information is connected to the printer unit 210, the scanner unit 220, and the operation unit 230. The controller unit 200 is connected to the host PCs for communication therewith through wireless communication via the wireless communication I/F 113 and the wireless USB antenna 21.

10 [0035]

The CPU 110 for controlling the above-described parts via the internal bus 118 executes a process shown in flowchart of FIG. 10 based on a program of this invention. The RAM 111 is used as a system work memory for operation of the CPU 110 and 15 as an image memory for temporarily storing image data. The ROM 112 stores a boot program, the program of this invention, a system application program, etc.

[0036]

The printer unit I/F 114 is connected to the printer unit 20 210, communicates with a CPU (not shown) of the printer unit 210, and performs synchronous/asynchronous conversion of image data to be printed. The scanner unit I/F 115 is connected to the scanner unit 220, communicates with a CPU (not shown) of the scanner unit 220, and performs synchronous/asynchronous 25 conversion of image data read from an original. The operation unit I/F 116 is an interface with the operation unit (user interface) 230. The operation unit I/F 116 outputs to the

operation unit 230 image data to be displayed thereon, and supplies the operation unit 230 with information input by a user.

[0037]

The wireless communication I/F 113 is connected via the 5 wireless USB antenna 21 to the host PCs for communication therewith through wireless USB, and inputs and outputs image data to be printed and information for control of the multi-function peripheral. The memory card I/F 117 performs a process in which image data or the like is written into or 10 read from the memory card 240.

[0038]

The memory card 240 is for storing files of image data or the like. Image data read from an original by the scanner unit 220 can be written into the memory card, and image data to be 15 printed by the printer unit 210 can be read from the memory card. Either one of the host PCs that is connected through wireless communication with the multi-function peripheral via the wireless USB antenna 21 and the wireless communication I/F 113 is able to mount the memory card 240 as a file system and able 20 to perform a file operation. It should be noted that memory card 240 can be implemented by either a portable memory attached to and detached from the multi-function peripheral or a memory incorporated in the multi-function peripheral.

[0039]

25 FIG. 2 shows in block diagram the construction of a controller operating system (hereinafter referred to as the controller OS) of the digital multi-function peripheral.

[0040]

As shown in FIG. 2, the controller OS 300 is comprised of a wireless communication control module 310 and a memory control module 320. The wireless communication control module 310 is comprised of a wireless communication controller 311, a host list storage unit 312, a host list management unit 313, and a processing request detection unit 314. The memory control module 320 is comprised of a memory controller 321, a mount state holding unit 322, and an unmount request unit 323. The controller OS 300 is executed by the CPU 110 of the digital multi-function peripheral 20, while using the RAM 111, for overall control of the multi-function peripheral 20 for image processing, print processing, scan processing, control processing for communication with the host PCs, etc.

15 [0041]

In the wireless communication control module 310, the wireless communication controller 311 hardware-controls the wireless communication I/F 113. The host list storage unit 312 stores a host list shown in FIG. 16. The host list management unit 313 performs a process for registration of host list. Specifically, when receiving a beacon from one of the host PCs, the host list management unit 313 adds a corresponding host name to the host list in the host list storage unit 312. When wireless communication connection with the host PC is disconnected, the host list management unit 313 erases first host information (identification information) in the host list, which corresponds to the disconnected host PC, and moves second host information

and subsequent host information (identification information) forward on the host list.

[0042]

The processing request detection unit 314 detects, 5 referring to the host list, a processing request from any host PC other than a host PC currently connected. The processing request detection unit 314 compares the first host information with the second host information in the host list stored in the host list storage unit 312, and determines that the detection 10 unit 314 receives a processing request from another host PC, if the first host information and the second host information are different from each other.

[0043]

In the memory control module 320, the memory controller 15 321 hardware-controls the memory card I/F 117. The mount state holding unit 322 holds information representing a mount state of the memory card 240 on a host PC. The unmount request unit 323 requests a host PC to unmount the memory card 240 from file system.

20 [0044]

FIG. 3 shows in block diagram the construction of the host controller of each host PC.

[0045]

As shown in FIG. 3, each host PC 10 includes a host controller 25 100, a display unit 101, a wireless USB antenna 11, a keyboard (not shown), and a mouse (not shown). The host controller 100 includes a CPU 102, a ROM 103, a RAM 104, a HDD (hard disk drive)

105, and a display controller 106. The host controller 100 further includes an IO (input/output) I/F controller 107, a wireless communication I/F 108, and a system bus 109.

[0046]

5 The host controller 100 is connected to the display unit 101, the keyboard, and the mouse, and performs wireless USB communication control via the wireless USB antenna 11. The CPU 102 controls the above-described parts via the system bus 109, performs overall control of various processing performed in the
10 host controller, and executes a process shown in flowchart of FIG. 11 based on a program of this invention. The ROM 103 stores a boot program and the program of this invention. The RAM 104 is used as a system work memory for operation of the CPU 102.

[0047]

15 The HDD 105 stores system software and image data. The display controller 106 outputs to the display unit 101 image data to be displayed thereon. The IO I/F 107 is an interface with the keyboard and the mouse for data input and output, and performs control of wired USB or the like. The IO I/F 107 controls
20 communication with a communication network such as LAN. The wireless communication I/F 108 controls wireless USB communication via the wireless USB antenna 11 in a case where there is a device for wireless USB communication (in this embodiment, digital multi-function peripheral), which is
25 disposed externally of the host PC.

[0048]

FIG. 4 shows in block diagram the construction of a host

OS of each host PC.

[0049]

As shown in FIG. 4, the host OS 150 of the host PC 10 is comprised of a file system management unit 160 and a wireless communication control module 170. The control module 170 is comprised of a wireless communication controller 171, a mount state notification unit 172, an unmount request unit 173, and a file system input/output unit 174. The host OS 150 is executed by the CPU 102 of the host PC 10 for overall control of the host PC, while using the ROM 103, the RAM 104, and the HDD 105.

[0050]

The file system management unit 160 for managing a file system of the host PC not only manages the internal HDD 105, but also manages an externally connected storage device as file system. In this embodiment, it is assumed that the management unit 160 manages the memory card 240 of the digital multi-function peripheral in a case where the memory card is used as file system.

[0051]

In the wireless communication control module 170, the wireless communication controller 171 hardware-controls the wireless communication I/F 108. The mount state notification unit 172 notifies the digital multi-function peripheral of a mount state of file system. The unmount request unit 173 issues a file system unmount request to the file system management unit 160. The file system input/output unit 174 transfers file data between the digital multi-function peripheral and the file system management unit 160.

[0052]

Next, operations of the digital multi-function peripheral of this embodiment and the host PCs, which are constructed as described above, will be described in detail with reference to 5 FIGS. 5 to 11.

[0053]

FIG. 5 shows in sequence diagram the procedures of a memory file mount/unmount process performed by the digital multi-function peripheral and host PCs.

10 [0054]

With reference to FIG. 5, a description is given for a case where the digital multi-function peripheral (device) 20 having the memory card I/F 117 is shared between a plurality of host PCs (here, first and second host PCs (first and second external 15 apparatuses)).

[0055]

First, the first host PC (denoted by reference numeral 10A in FIG. 5) transmits a beacon as a job processing request to the device 20 (step S3000). When receiving the beacon, the device 20 transmits a connection request to the first host PC (step S3001). The first host PC establishes wireless communication connection with the device, and transmits a memory card mount instruction to the device (step S3002). Subsequently, the first host PC operates a file on the memory card 240 according to a user's 25 instruction (step S3003).

[0056]

When receiving a beacon as a job processing request from

the second host PC (denoted by reference numeral 10B in FIG. 5) before processing for unmounting the memory card 240 is executed in S3004 by the first host PC (step S3100), the device requests the currently connected first host PC to unmount the 5 memory card 240 (step S3201).

[0057]

When receiving the unmount request, the first host PC confirms a state of file system, and if the file system is in an unmountable state, instructs to unmount the memory card 240 10 (step S3004). More specifically, the first host PC determines, e.g., whether any file in the memory card 240 is being executed. If no file is being executed, the first host PC determines that the file system is in an unmountable state, and instructs the device to unmount the memory card 240. Thus, the device is able 15 to unmount the memory card and then request the first host PC to disconnect the communication connection, whereby the file system is prevented from entering an error state (step S3005). Subsequently, the device transmits a connection request to the second host PC (step S3101), receives print data from the second 20 host PC (step S3102) to perform a print job, and then requests the second host PC to disconnect communication connection (step S3103).

[0058]

FIG. 6 shows in sequence diagram the procedures of memory 25 card mount/unmount control performed by the digital multi-function peripheral.

[0059]

With reference to FIG. 6, a description is given of processing performed by the wireless communication I/F 113 and wireless communication control module 310 of the digital multi-function peripheral 20. It should be noted that this 5 processing is internal processing performed in the multi-function peripheral according to the memory file mount instruction (step S3002) and the memory file unmount instruction (step S3004), which are already described referring to FIG. 5.

[0060]

10 When receiving a file system mount instruction from a host PC via the wireless communication I/F 113 (step S4000), the wireless communication control module 310 of the multi-function peripheral 20 notifies the memory control module 320 of a memory card mount instruction (step S4001). The memory controller 321 15 of the control module 320 mounts the memory card, and the mount state holding unit 322 holds information representing a memory mount state.

[0061]

When receiving an unmount instruction from a host PC via 20 the wireless communication I/F 113 (step S4002), the wireless communication control module 310 of the multi-function peripheral notifies the memory control module 320 of a memory card unmount instruction (step S4003). The memory controller 321 of the memory control module 320 unmounts the memory card, 25 and the mount state holding unit 322 holds information representing a memory unmount state.

[0062]

FIG. 7 shows in sequence diagram another procedures of memory card mount/unmount control performed by the digital multi-function peripheral.

[0063]

5 With reference to FIG. 7, a description is given of processing performed by the wireless communication I/F 113 and the wireless communication control module 310.

[0064]

In steps S4100 and S4101 in FIG. 7, the same processing 10 as that in steps S4000 and S4001 in FIG. 6 is performed according to a mount instruction from a host PC, whereby the memory card is mounted. When the wireless communication control module 310 of the digital multi-function peripheral 20 receives a beacon from another host PC via the wireless communication I/F 113 (step 15 S4102), the processing request detection unit 314 of the control module 310 detects a processing request from the other host PC. The control module 310 notifies the memory control module 320 that the processing request is received from the other host PC (step S4103).

20 [0065]

In the memory control module 320, the mount state holding unit holds information representing a memory mount state, and the unmount request unit 323 issues an unmount request to the wireless communication control module 310 (step S4104). In 25 response to this, the wireless communication controller 311 of the wireless communication control module 310 transmits an unmount request to the host PC via the wireless communication

- 23 -

I/F 113 (step S4105).

[0066]

In S4106 and S4107, processing corresponding to S4002 and S4003 in FIG. 6 is executed, whereby the memory card is unmounted.

5 [0067]

FIG. 8 shows in sequence diagram the procedures of memory card mount/unmount control performed by each host PC.

[0068]

With reference to FIG. 8, a description is given of 10 processing performed by the wireless communication I/F 108, the wireless communication control module 170, and the file system management unit 160 of the host PC 10. It should be noted that this processing is internal processing performed by the host PC according to a memory file mount instruction (step S3002) 15 and a memory file unmount instruction (step S3004), which are already described referring to FIG. 5.

[0069]

When the wireless communication control module 170 is notified of a mount instruction from the file system management 20 unit 160 (step S5000), the mount state notification unit 172 of the module 170 notifies the digital multi-function peripheral 20 of a file system mount state via the wireless communication controller 171 and the wireless communication I/F 108 (step S5001).

25 [0070]

When the wireless communication control module 170 is notified of an unmount instruction from the file system

management unit 160 (step S5002), the mount state notification unit 172 of the module 170 notifies the multi-function peripheral of a file system unmount state via the wireless communication controller 171 and the wireless communication I/F 108 (step 5 S5003).

[0071]

FIG. 9 shows in sequence diagram another procedures of the memory card mount/unmount control by each host PC.

[0072]

10 With reference to FIG. 9, a description is given of the processing performed by the wireless communication I/F 108, the wireless communication control module 170, and the file system management unit 160.

[0073]

15 In S5100 and S5101, the same processing as that in S5000 and S5001 in FIG. 8 is executed, so that a file system mount state is notified from the host PC 10 to the digital multi-function peripheral 20. Subsequently, when the wireless communication controller 171 of the wireless communication control module 170 20 of the host PC receives an unmount request from the multi-function peripheral via the wireless communication I/F 108 (step S5102), the unmount request unit 173 of the module 170 transmits a file system unmount request to the file system management unit 160 (step S5103). In response to this, the management unit 160 shifts 25 the file system to an unmount state.

[0074]

When the file system is put into an unmount state, the file

system management unit 160 issues a file system unmount instruction (step S5104). In response to this, the mount state notification unit 172 notifies the multi-function peripheral of a file system unmount state via the wireless communication controller 171 and the wireless communication I/F 108 (step S5105).

[0075]

FIG. 10 shows in flowchart a memory unmount process performed by the digital multi-function peripheral. The memory unmount process is internal processing performed in steps S4103 to S4107 in FIG. 7 by the controller OS 300 of the multi-function peripheral under the control of the CPU 110 of the multi-function peripheral.

[0076]

In FIG. 10, it is assumed that the multi-function peripheral is in a state where it communicates with the first host PC. The controller OS 300 of the multi-function peripheral confirms whether a processing request from a new host PC (second host PC) to the multi-function peripheral is detected by the processing request detection unit 314 (step S1000).

[0077]

Next, the controller OS 300 determines based on the detection state confirmed in step S1000 whether there is a processing request (step S1001). If there is no processing request from the new host PC, the flow returns to step S1000. If there is a processing request from the new host PC, the controller OS 300 confirms information held in the mount state

holding unit 322 and representing a memory mount state (step S1002).

[0078]

Next, the controller OS 300 determines based on the 5 information confirmed in step S1002 whether the host PC is in a state mounted with the memory card 240 (step S1003). If the host PC is not in a state mounted with the memory card 240, the flow proceeds to step S1008. If the host PC is in a state mounted with the memory card 240, the controller OS 300 causes the unmount 10 request unit 323 to transmit an unmount request to the host PC via the wireless communication I/F 113 (step S1004).

[0079]

Next, the controller OS 300 acquires a mount/unmount state notified from the first host PC in step S2005 in FIG. 11, described 15 later (step S1005). Then, the controller OS 300 determines whether the acquired mount/unmount state represents an unmount instruction given from the first host PC (step S1006). If the acquired state does not represent an unmount instruction from the first host PC, the flow returns to step S1005. If the acquired 20 state represents an unmount instruction from the first host PC, the controller OS 300 clears the information held in the mount state holding unit 322 and representing a memory card mount state of the first host PC (step S1007).

[0080]

25 Then, the controller OS 300 holds information representing a state where the first host PC does not mount the memory card 240. Next, the controller OS 300 causes the wireless

communication controller 311 to disconnect wireless communication connection with the first host PC (step S1008), whereupon the present process is completed. Subsequently, the controller OS starts wireless communication connection with a 5 new host PC (second host PC).

[0081]

FIG. 11 is a flowchart showing a memory unmount process performed by each host PC. This process is internal processing performed by the host PC in steps S5102 to S5105 in FIG. 9 under 10 the control of the CPU 102 of the host PC.

[0082]

Referring to FIG. 11, the host OS 150 of the host PC receives at the wireless communication controller 171 a command transmitted from the digital multi-function peripheral via the 15 wireless communication I/F 108 (step S2000). Next, the host OS 150 determines whether the command received in step S2000 is an unmount request transmitted from the multi-function peripheral in step S1004 in FIG. 10 (step S2001). If the received command is not an unmount request from the multi-function 20 peripheral, the flow returns to step S2000.

[0083]

If the received command is an unmount request from the multi-function peripheral, the host OS 150 causes the unmount request unit 173 to acquire a state of file system from the file 25 system management unit 160 (step S2002). Next, based on the state of file system acquired in step S2002, the host OS 150 determines whether unmounting the file system is possible (step

S2003). If unmounting the file system is not possible, the flow returns to step S2002.

[0084]

If unmounting the file system is possible, the file system input/output unit 174 of the host OS 150 issues a file system unmount authorization (step S2004). Next, the host OS 150 causes the mount state notification unit 172 to notify the digital multi-function peripheral of a file system unmount state via the wireless communication I/F 108 (step S2005), whereupon the present process is completed.

[0085]

As described in detail above, according to this embodiment, it is determined whether the first host PC is in a state mounted with the memory card, when a processing request is received from the second host PC in a state that wireless communication is performed between the digital multi-function peripheral and the first host PC. If the first host PC is in a state mounted with the memory card, a request for unmounting the memory card is issued to the first host PC. When an unmount instruction is given from the first host PC, the memory card mount state of the first host PC is released and an unmount state is entered. Subsequently, the wireless communication connection with the first host PC is disconnected and wireless communication with the second host PC is started.

[0086]

It is therefore possible to eliminate the prior art problem such that when a memory card job is being performed by a host

PC, communication with another host PC cannot be started even if the digital multi-function peripheral receives a processing request from the other host PC. It is also possible to eliminate the prior art problem such that an error state is caused, if 5 the digital multi-function peripheral forcibly disconnects communication with a host PC when a memory card job is being performed by the host PC. Thus, a communication disconnection process can be made without causing an error in the host PC. Also, the digital multi-function peripheral can be shared between 10 a plurality of host PCs, while preventing the multi-function peripheral from being appropriated by any one of the host PCs.

[0087]

It should be noted that in the above description, a case has been described where memory unmounting is requested to a 15 currently connected host PC when a processing request is received from another host PC, but this is not limitative. For example, an unmount process can be made by the device itself instead of the currently connected host PC, and communication with the currently connected host PC can be disconnected. In that case, 20 it is more preferable that before disconnection of communication, the host PC be notified that the memory has been unmounted.

[0088]

In the above description, a case has been described where a memory unmount request is issued to a currently connected host 25 PC when a processing request is received from another host PC, but this is not limitative. Specifically, a memory unmount request can be issued at a timing other than a timing of reception

of a processing request from another host PC. For example, in a case where the device reads out data in a memory according to an instruction from a host PC to carry out a printing process, a memory unmount request can be issued to the host PC at completion 5 of the printing process. A memory unmount request can be issued as a time-out process when no operation is performed on a file in the memory for a predetermined time period.

[Other Embodiments]

[0089]

10 It is to be understood that the present invention may also be accomplished by supplying a system or an apparatus with a storage medium in which a program code of software, which realizes the functions of the above described embodiment is stored and by causing a computer (or CPU or MPU) of the system or apparatus 15 to read out the program code stored in the storage medium.

[0090]

In that case, the program code itself read from the storage medium realizes the functions of the above described embodiment, and therefore the program code and the storage medium in which 20 the program code is stored constitute the present invention.

[0091]

While the present invention has been described with reference to exemplary embodiments, it is to be understood that the invention is not limited to the disclosed exemplary 25 embodiments. The scope of the following claims is to be accorded the broadest interpretation so as to encompass all such modifications and equivalent structures and functions.

Reference Signs List

[0092]

- 10 host PC
- 20 digital multi-function peripheral
- 5 100 host controller
- 108 wireless communication I/F
- 110 CPU
- 113 wireless communication I/F
- 150 host OS
- 10 160 file system management unit
- 170 wireless communication control module
- 200 controller unit
- 240 memory card
- 300 controller OS
- 15 310 wireless communication control module
- 320 memory control module

CLAIMS

1. An information processing apparatus able to communicate with first and second external apparatuses, comprising:
 - 5 a connection unit configured to be connected to a data storage memory for communication with the memory;
 - a judgment unit configured to determine whether the first external apparatus mounts the memory in a case where the information processing apparatus which is in communication with
 - 10 the first external apparatus receives a processing request from the second external apparatus;
 - a request unit configured to request the first external apparatus to unmount the memory in a case where said judgment unit determines that the first external apparatus mounts the
 - 15 memory; and
 - a communication control unit configured to disconnect communication with the first external apparatus and start communication with the second external apparatus after the first external apparatus unmounts the memory according to a request
 - 20 from said request unit.
2. The information processing apparatus according to claim 1, including:
 - 25 a memory controller configured to mount or unmount the memory according to an instruction from the first external apparatus.
3. The information processing apparatus according to claim 2, wherein said communication control unit disconnects the

communication with the first external apparatus after said memory controller unmounts the memory according to the instruction from the first external apparatus.

4. The information processing apparatus according to claim 1, 5 wherein when said judgment unit determines that the first external apparatus does not mount the memory, said communication control unit disconnects the communication with the first external apparatus, without said request unit requesting the first external apparatus to unmount the memory.
- 10 5. The information processing apparatus according to claim 1, including:
 - a communication unit configured to perform wireless communication with the first and second external apparatuses; and

15 a management unit configured, in a case where it receives a processing request from either the first external apparatus or the second external apparatus via said communication unit, to manage identification information representing the external apparatus from which the processing request has been transmitted,

20 wherein said communication control unit controls communication between said communication unit and the first and second external apparatuses based on the identification information managed by said management unit.

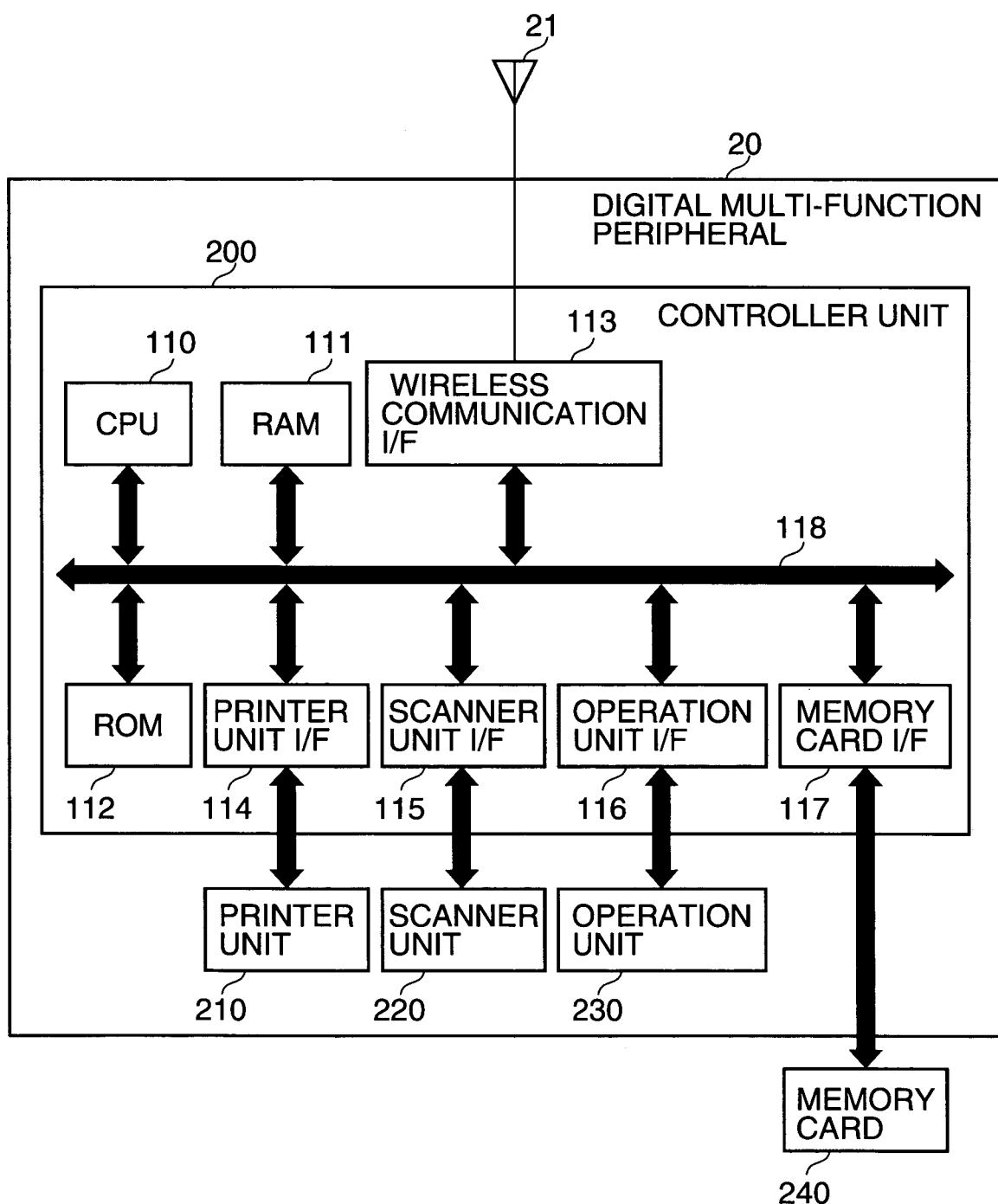
6. The information processing apparatus according to claim 5, 25 wherein said communication unit performs wireless USB communication with the first and second external apparatuses.
7. The information processing apparatus according to claim 1,

wherein said connection units performs a process for writing data into the memory or a process for reading out data from the memory according to an instruction from the first external apparatus in a state where the first external apparatus mounts
5 the memory.

8. The information processing apparatus according to claim 1, wherein the memory adapted to be connected to said connection unit is a potable memory which is able to be attached to and detached from the information processing apparatus.
- 10 9. A control method of an information processing apparatus able to communicate with first and second external apparatuses and having a connection unit configured to be connected to a data storage memory for communication with the memory, comprising:
 - a judgment step of determining whether the first external apparatus mounts the memory in a case where the information processing apparatus which is in communication with the first external apparatus receives a processing request from the second external apparatus;
 - 15 a request step of requesting the first external apparatus to unmount the memory in a case where it is determined in said judgment step that the first external apparatus mounts the memory; and
 - 20 a communication control step of disconnecting communication with the first external apparatus and starting communication with the second external apparatus after the first external apparatus unmounts the memory according to a request issued in said request step.
- 25

10. A computer-readable storage medium storing a program for causing a computer to execute the control method as set forth in claim 9.

FIG.1



2/16

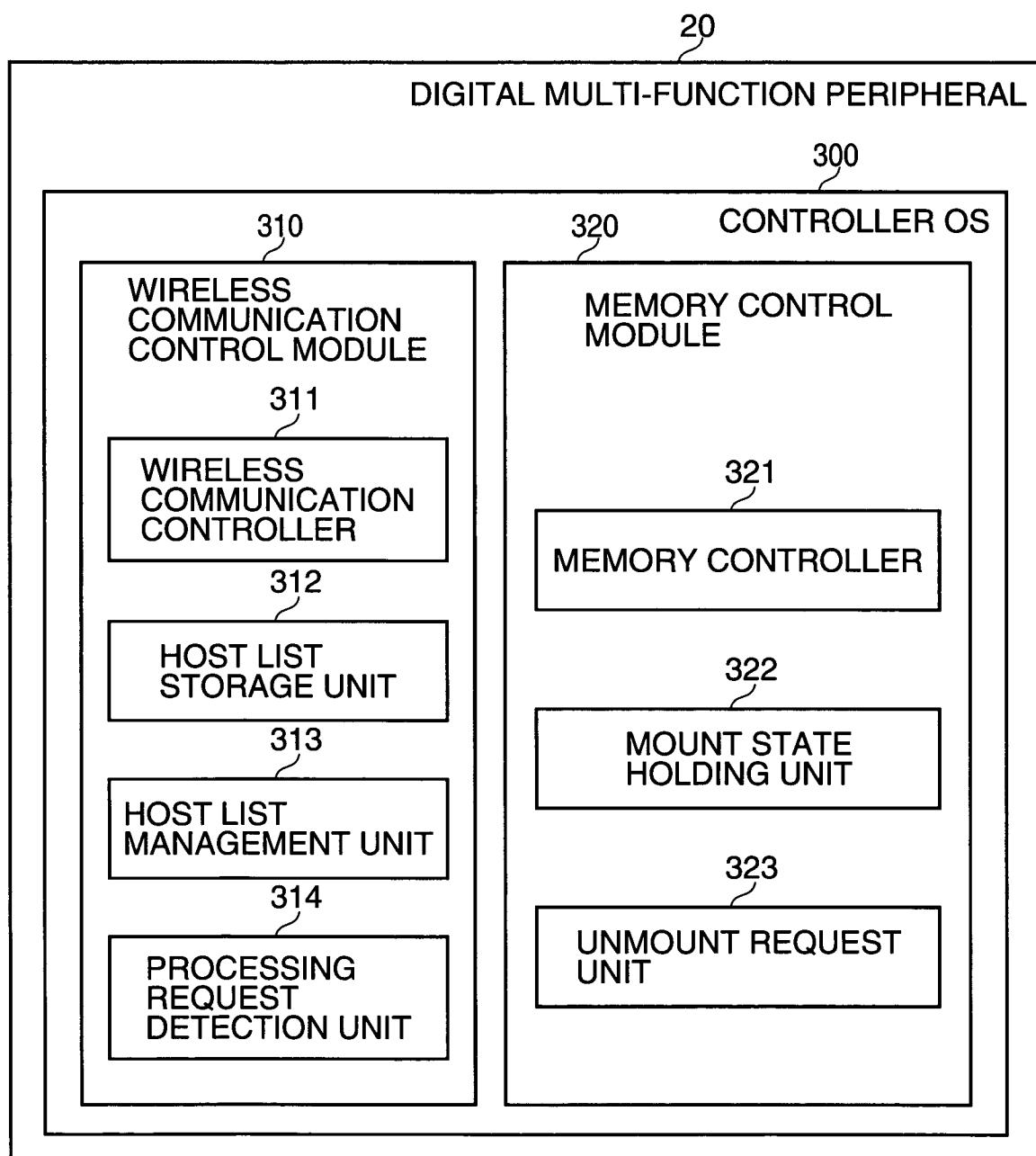
FIG.2

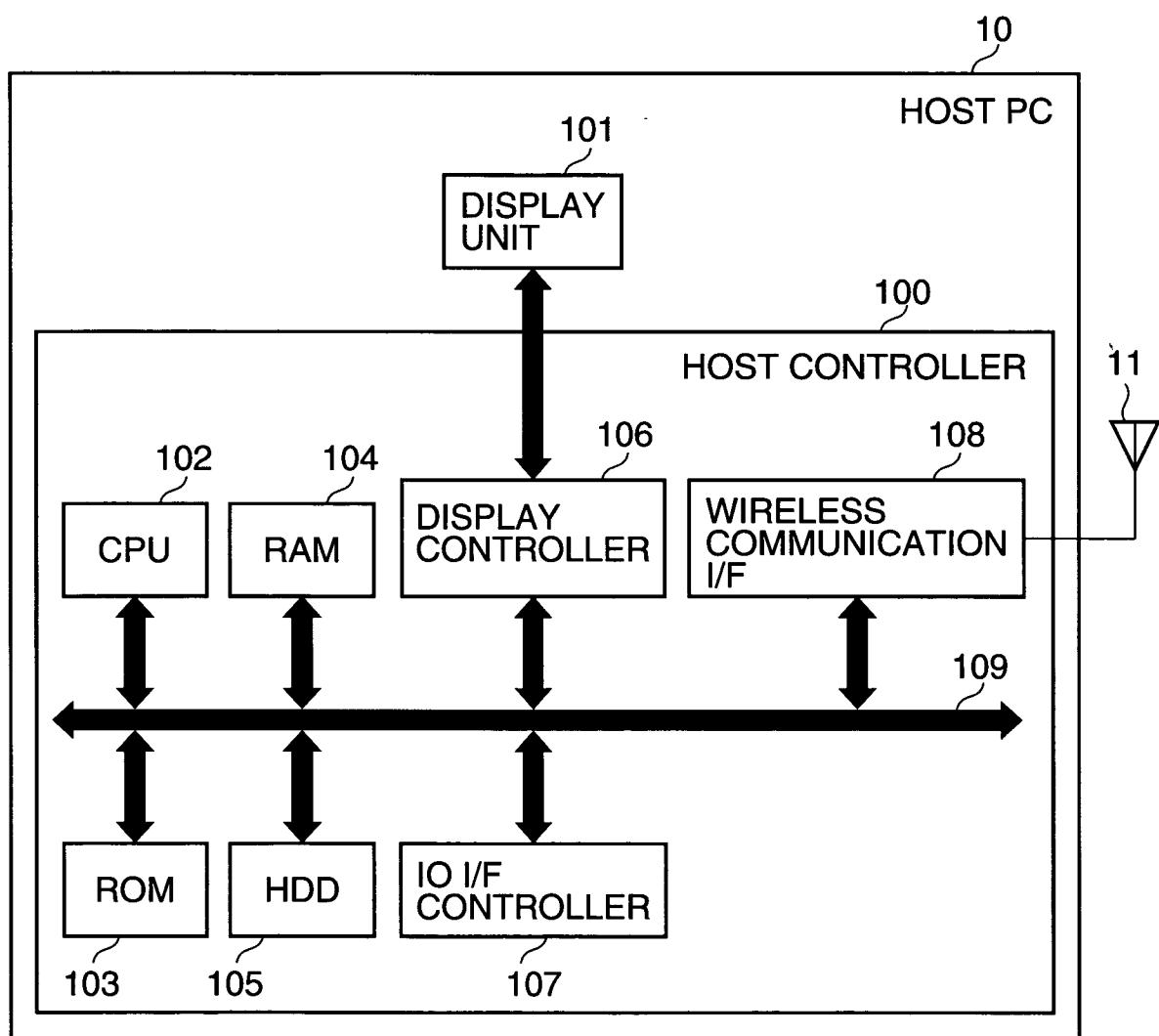
FIG.3

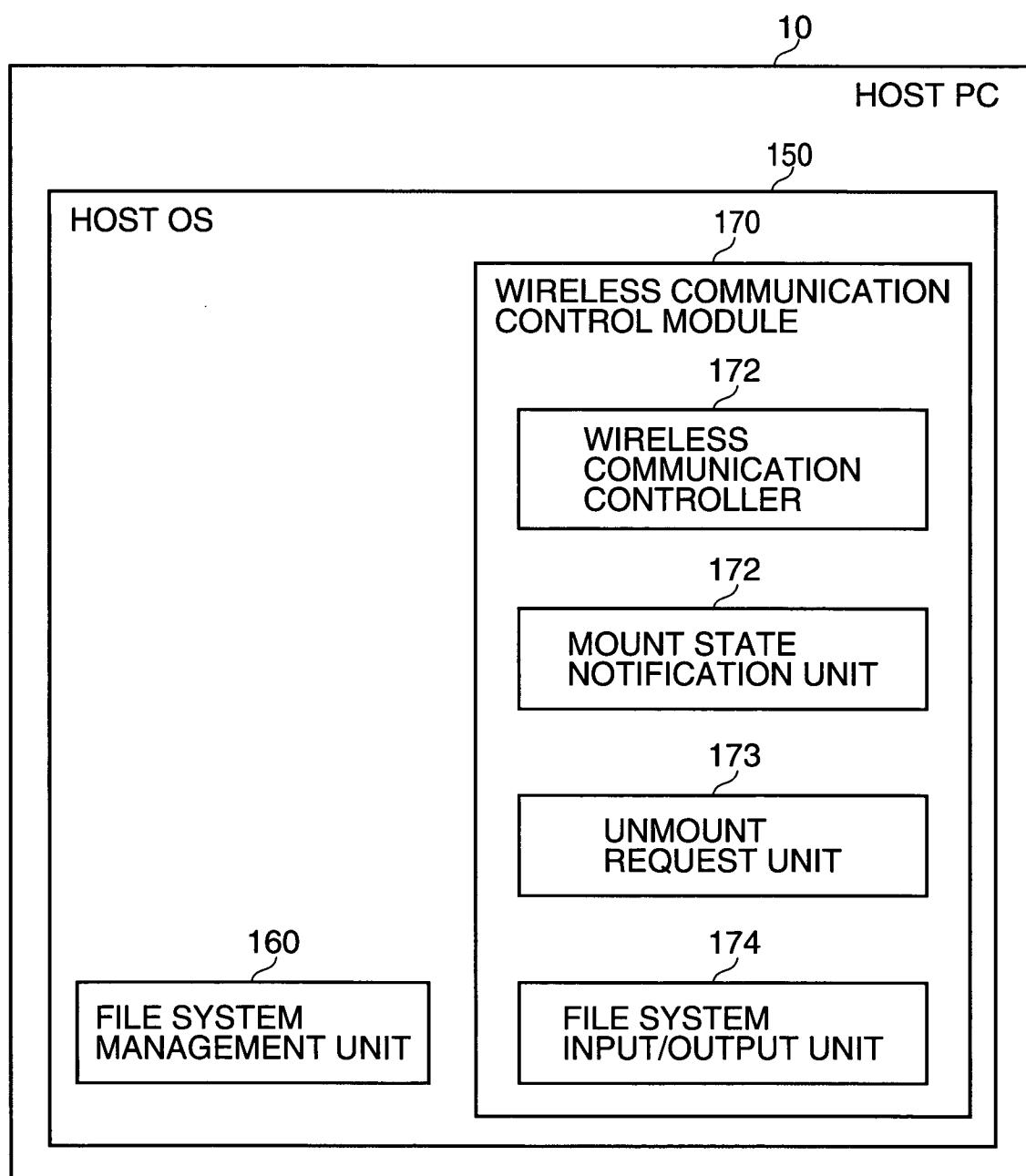
FIG.4

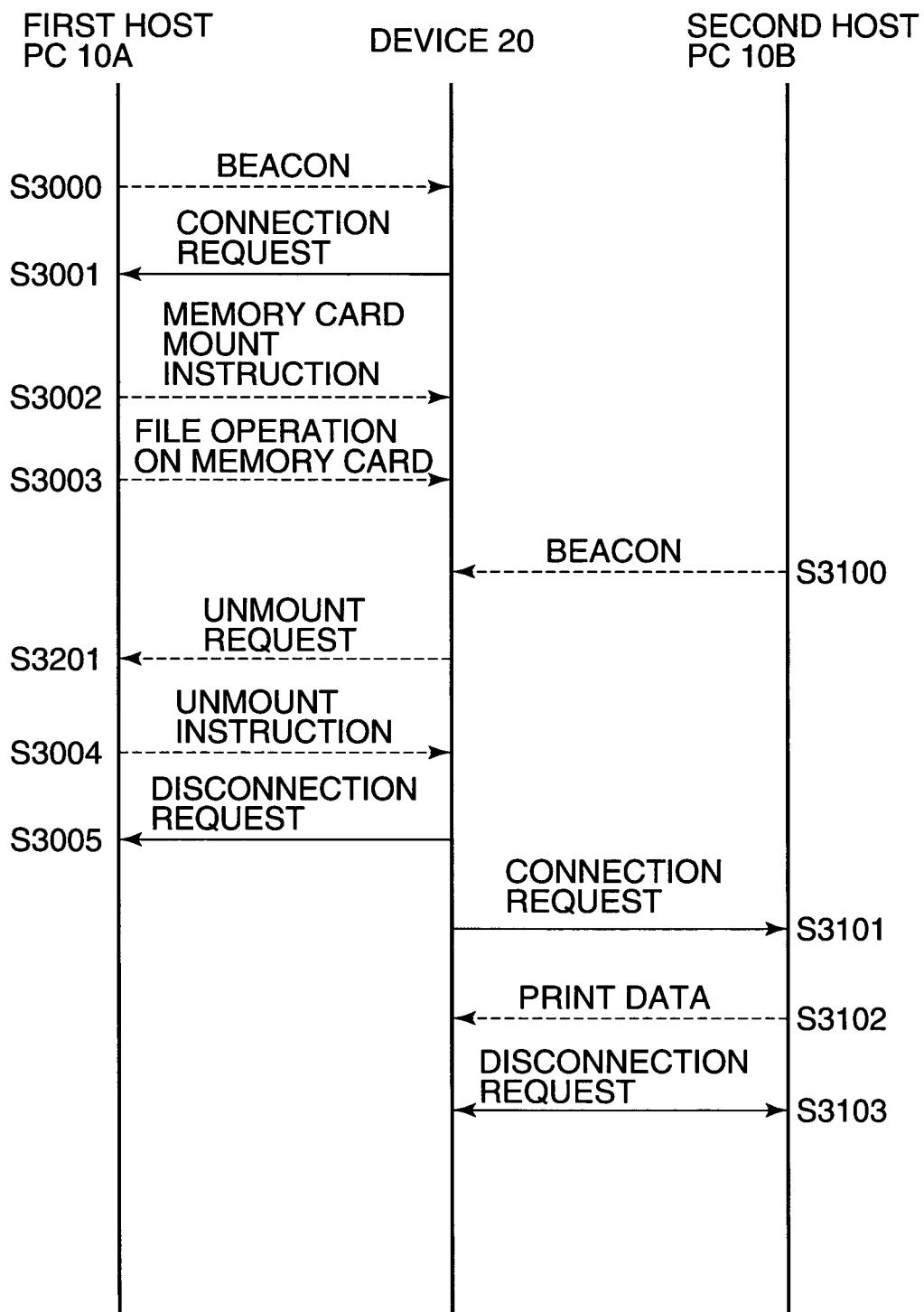
FIG.5

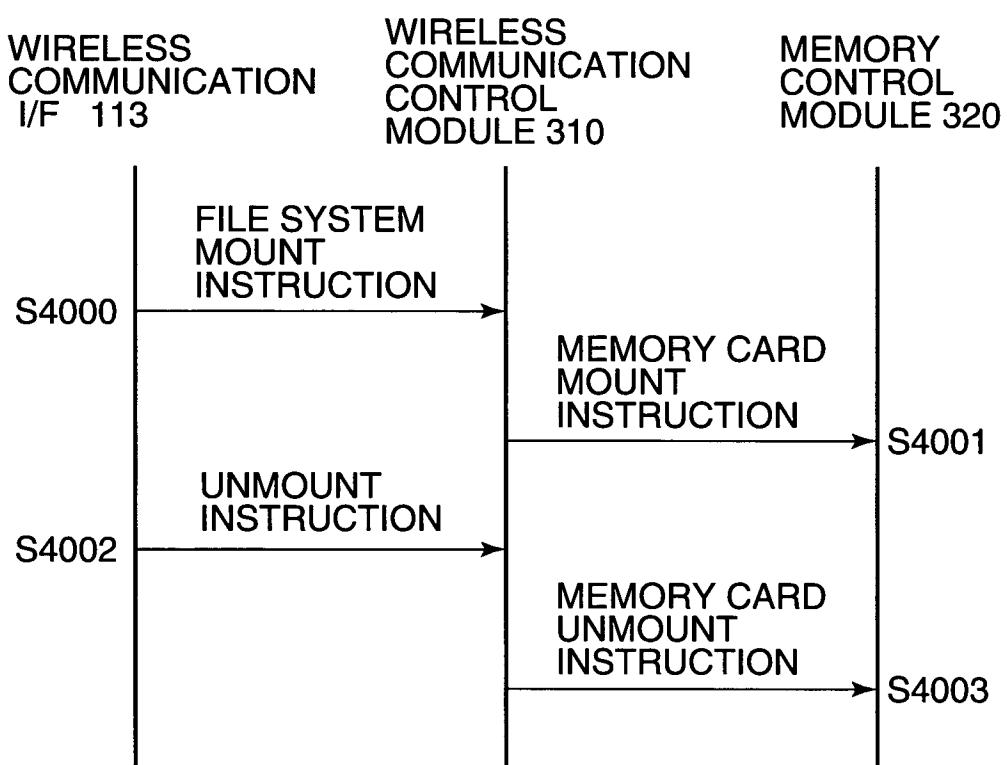
FIG.6

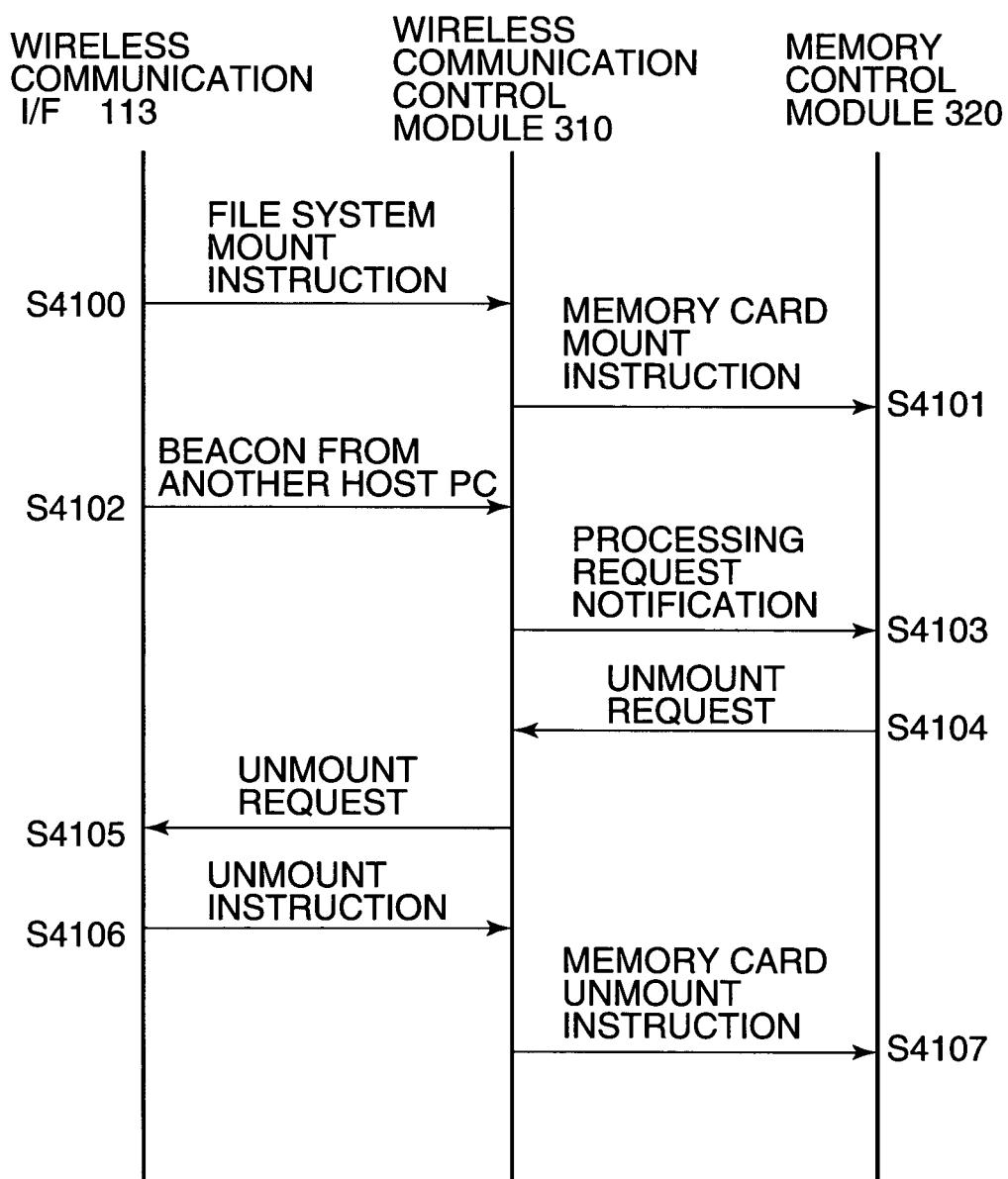
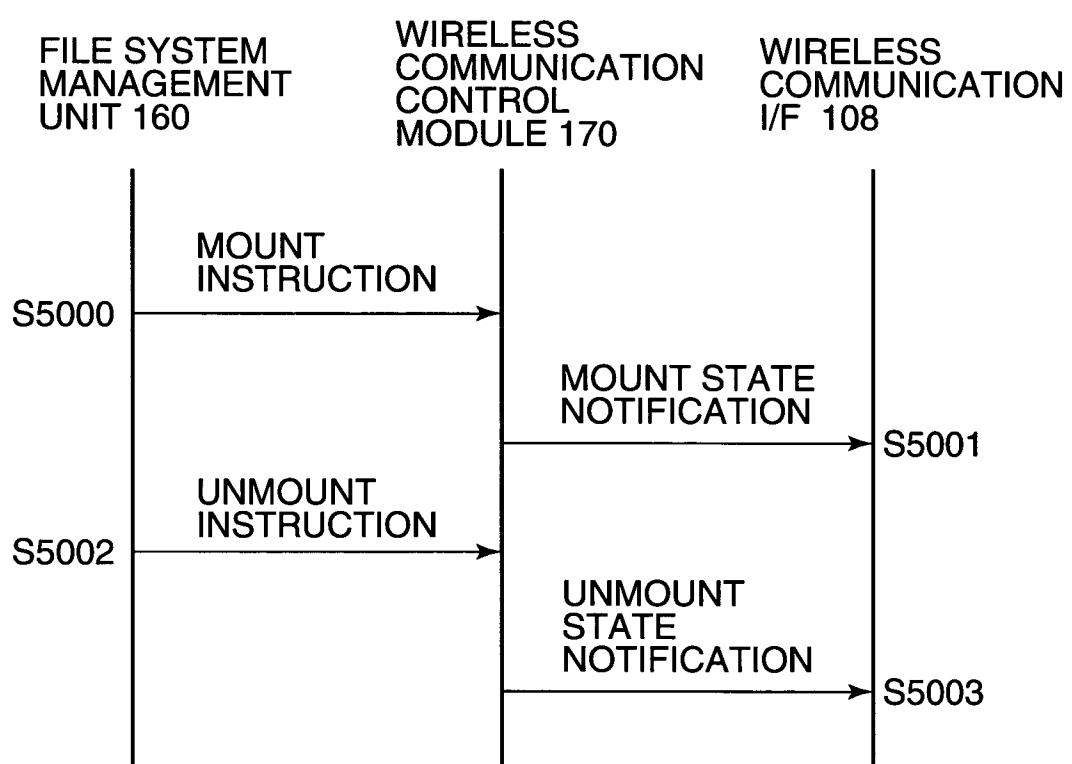
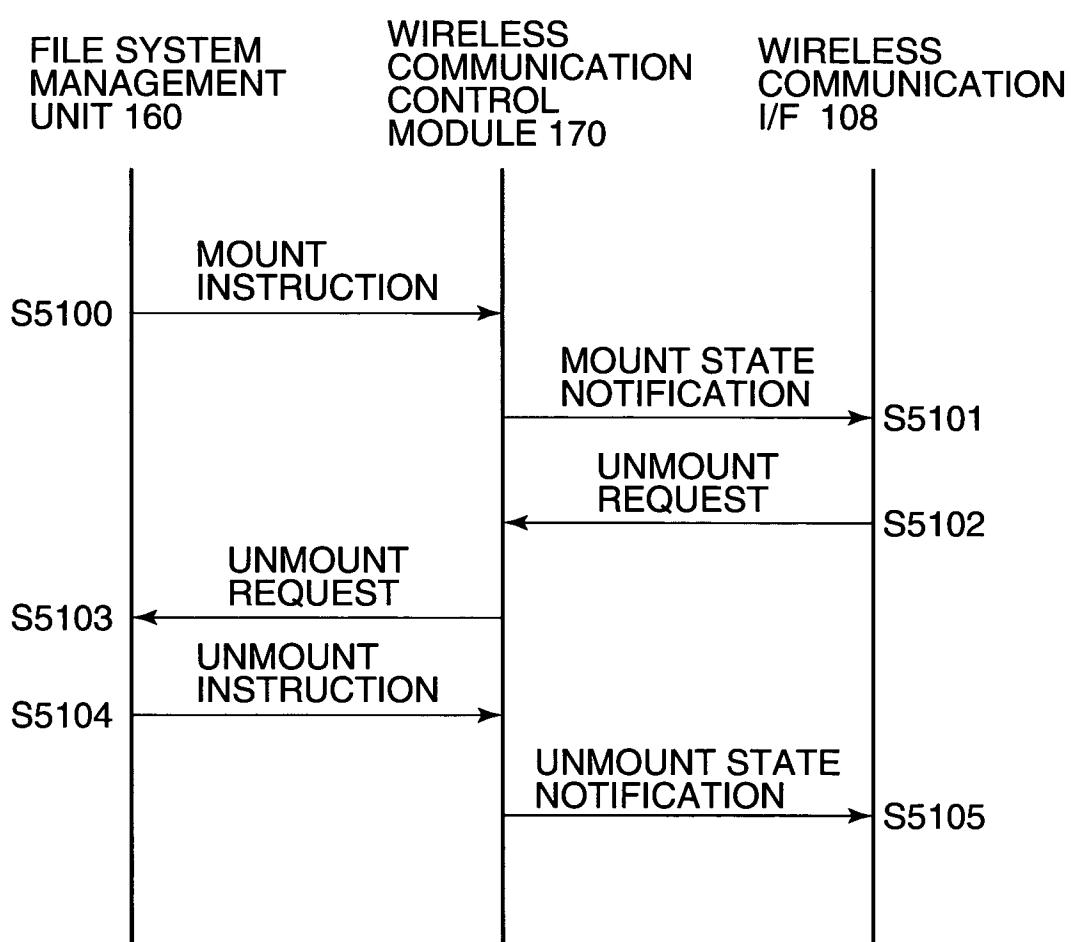
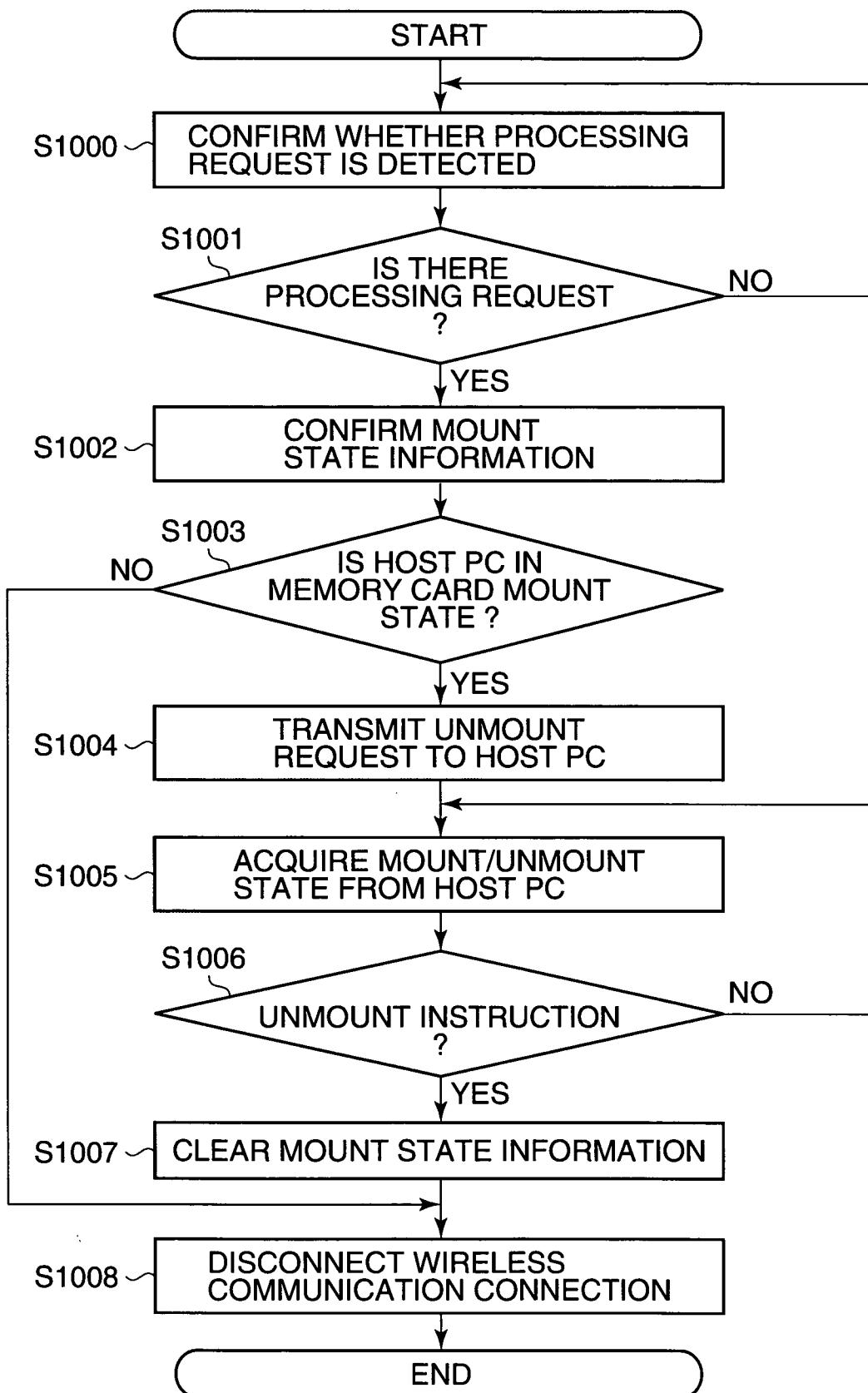
FIG.7

FIG.8

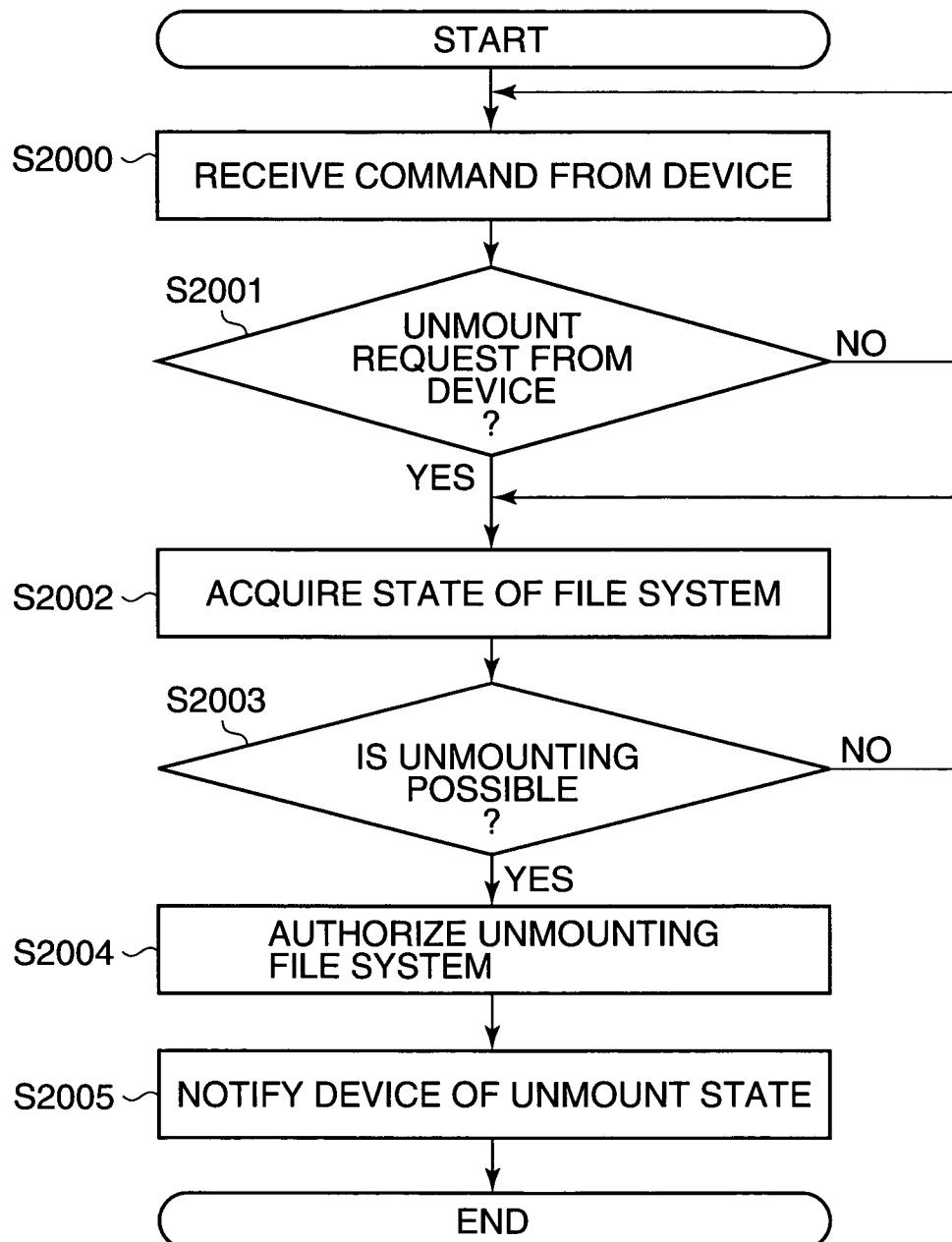
9/16

FIG.9

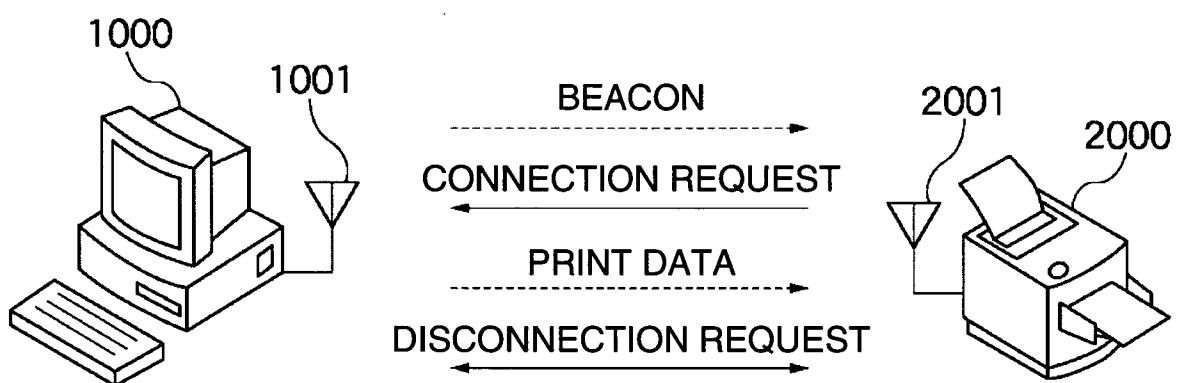
10/16

FIG.10

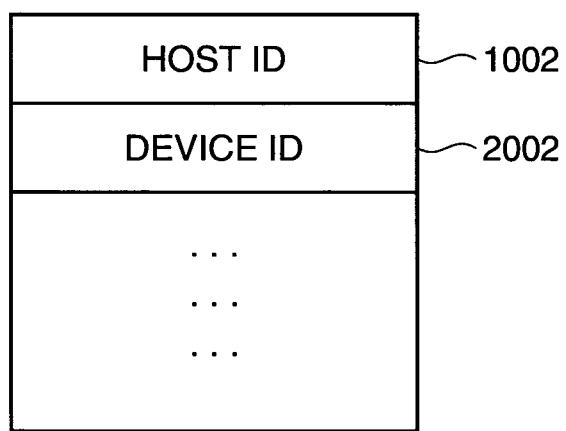
11/16

FIG.11

12/16

FIG.12

13/16

FIG.13

14/16

FIG.14

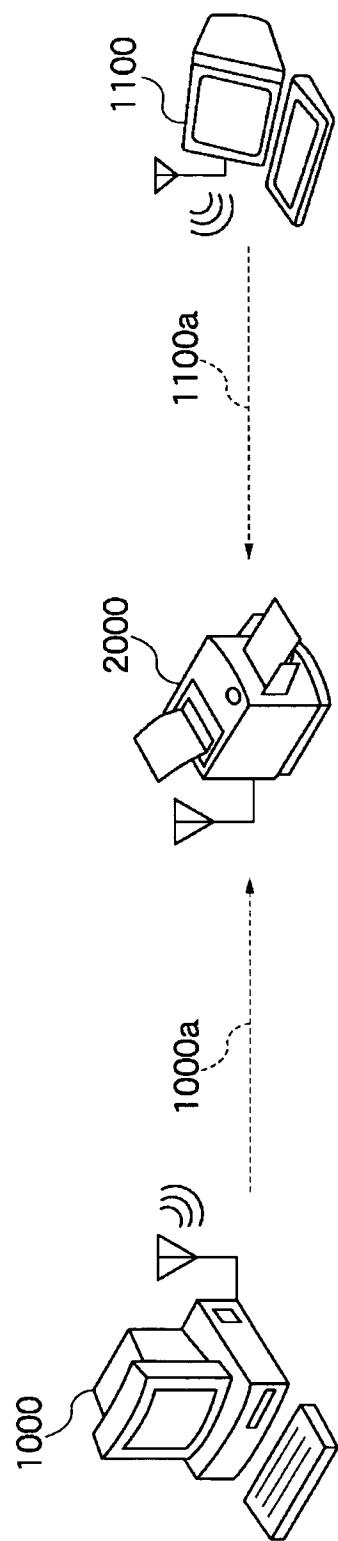
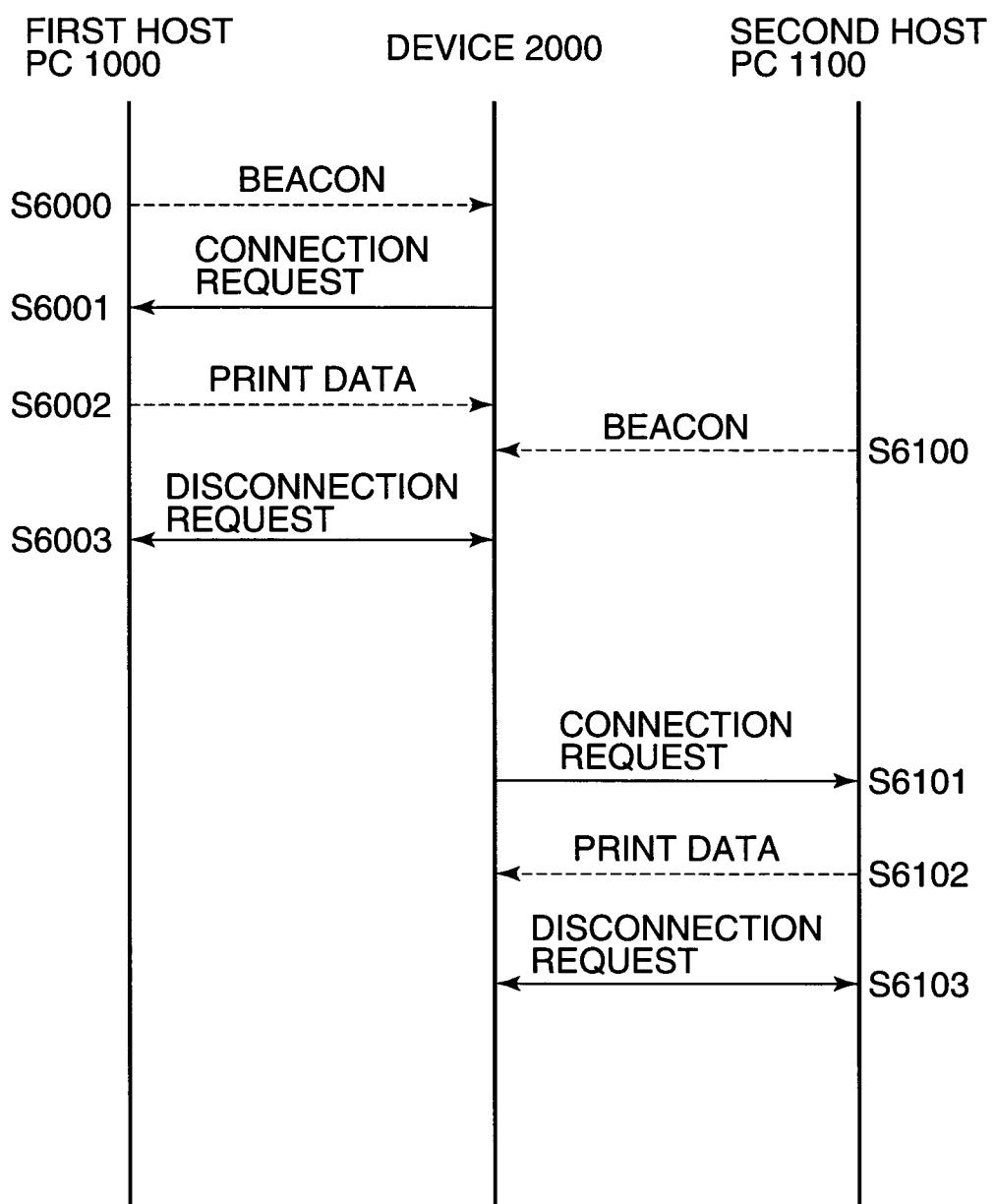


FIG.15

16/16

FIG.16

No	HOST NAME
1	FIRST HOST
2	SECOND HOST
3	...
4	...
...	...
...	...

INTERNATIONAL SEARCH REPORT

International application No.
PCT/JP2010/052391

A. CLASSIFICATION OF SUBJECT MATTER

Int.Cl. G06F3/06 (2006.01) i, G06F13/00 (2006.01) i

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

Int.Cl. G06F3/06, G06F13/00

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Published examined utility model applications of Japan 1922-1996
Published unexamined utility model applications of Japan 1971-2010
Registered utility model specifications of Japan 1996-2010
Published registered utility model applications of Japan 1994-2010

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
Y	JP 2008-118275 A (RICOH COMPANY, LTD.) 2008.05.22, [0145] – [0153], Fig10 No Family	1-10
Y	JP 2007-102463 A (HITACHI, LTD.) 2007.04.19, [0056] – [0059], Fig1 & US 2007/0079062 A1	1-10

Further documents are listed in the continuation of Box C.

See patent family annex.

* Special categories of cited documents:

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28.04.2010

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Name and mailing address of the ISA/JP
Japan Patent Office
3-4-3, Kasumigaseki, Chiyoda-ku, Tokyo 100-8915, Japan

Authorized officer
Ryuichi TANOUYE

51 4176

Telephone No. +81-3-3581-1101 Ext. 3565