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(54) Title: SHIFT-ADD MECHANISM

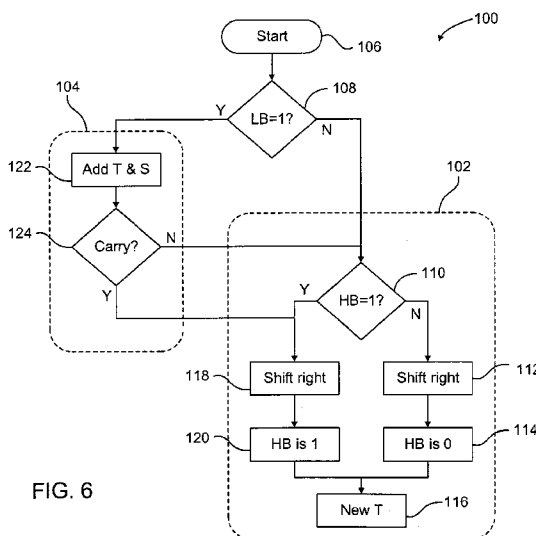


FIG. 6

(57) Abstract: A method to perform a shift-add operation on two values loaded in two memories of a processor where the first memory has a low bit (LB) and a high bit (HB). If the LB is zero, then this is case (1), if the HB is also zero, shifting the first value lower one bit-position and setting the HB to zero, thereby arriving at a new value in the first memory, and alternately if the HB is one, then this is case (2), and proceed shifting the first value lower one bit-position and setting the HB to one, thereby arriving at the new value. However, if the LB is one, then adding the second value to the first value in the first memory and if this does not produce a carry, proceeding as if at case (1) and otherwise proceeding as if at case (2).



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INTERNATIONAL SEARCH REPORT

International application No.
PCT/US2008/010999**A. CLASSIFICATION OF SUBJECT MATTER****G06F 7/50(2006.01)i**

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC 8 : G06F

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Korean Utility models and applications for Utility models since 1975
Japanese Utility models and application for Utility models since 1975

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

e-KIPASS(KIPO internal) : "multiplier", "shift", "add", "register", "LSB", "MSB"

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	US 5,539,685 A (OTAGURO) 23 July 1996 See the abstract, column 9, line 11 - column 10, line 49, and figures 10-12	1-4
A	US 5,535,151 A (KUROKAWA) 9 July 1996 See the abstract, column 3, line 25 - column 6, line 7, and figures 5-7	1-4
A	US 5,117,385 A (GEE) 26 May 1992 See the abstract, column 3, line 23 - column 8, line 12, and figures 2-3	1-4
A	US 6,760,742 B1 (HOYLE) 6 July 2004 See the abstract, column 7, line 22 - column 8, line 49, and figures 5A-6	1-4

 Further documents are listed in the continuation of Box C. See patent family annex.

* Special categories of cited documents:

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Patent document cited in search report	Publication date	Patent family member(s)	Publication date
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US 6760742 B1	06.07.2004	NONE	