

EUROPEAN PATENT APPLICATION

Application number: 82201336.3

Int. Cl.³: **G 09 G 1/28**
H 03 K 13/05

Date of filing: 26.10.82

Priority: 28.10.81 GB 8132450

Date of publication of application:
04.05.83 Bulletin 83/18

Date of deferred publication of search report: 30.11.83

Designated Contracting States:
DE FR GB

Applicant: **PHILIPS ELECTRONIC AND ASSOCIATED INDUSTRIES LIMITED**
Arundel Great Court 8 Arundel Street
London WC2R 3DT(GB)

Designated Contracting States:
GB

Applicant: **N.V. Philips' Gloeilampenfabrieken**
Groenewoudseweg 1
NL-5621 BA Eindhoven(NL)

Designated Contracting States:
DE FR

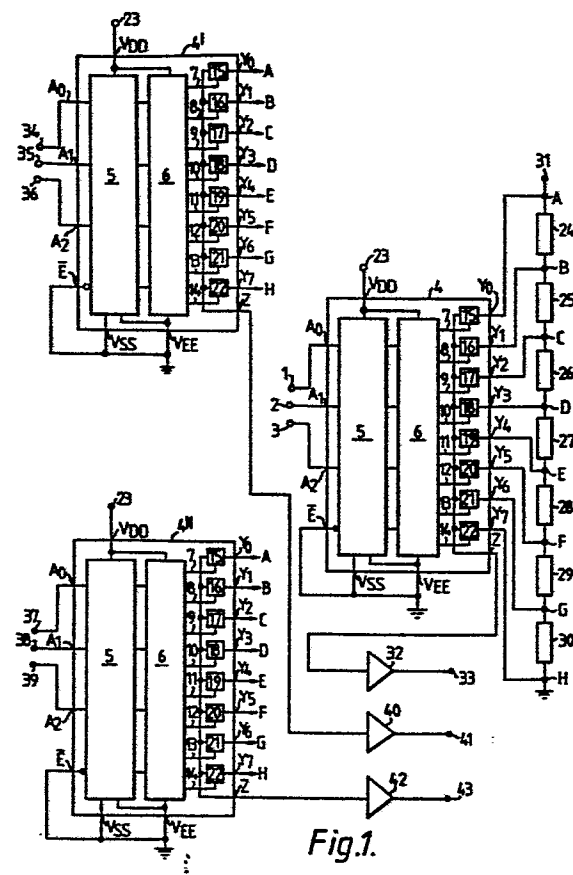
Inventor: **Wong, Kwok Keung**
c/o **PHILIPS CROYDON Commerce Way**
Croydon Surrey CR9 4JA(GB)

Representative: **Cole, Bernard Victor et al,**
Philips Electronic and Associated Industries Limited
Patent Department Mullard House Torrington Place
London WC1E 7HD(GB)

Decoding binary coded colour video signals.

Decoder circuit arrangement for decoding binary coded colour video signals to produce analogue colour video signals therefrom comprising three inputs (1,2,3) to which the binary signals are applied these inputs being connected through an analogue level conversion circuit (5) to a selector circuit (6), both of which are provided in an 8-channel analogue multiplexer/demultiplexer (4), the selector circuit (6) selecting a particular one-out-of-eight outputs (7-14) for application as control signals to the control inputs of individual switches (15-22) which are also provided in the analogue multiplexer (4). The control signal so formed renders the appropriate one of the switches (15-22) conductive. The inputs of the switches are respectively connected to points (A-H) on a potential divider (24-30) energised from a supply input (31) for a voltage supply. The one of the switches which is conductive conveys the voltage at the appropriate point (A-H) to an output terminal (2) of the analogue multiplexer (4) which output terminal is connected through a buffer amplifier to an output (33). The output (33) provides an analogue signal for the input binary signal. The arrangement also comprises two similar analogue multiplexer/demultiplexer circuits (4',4''), the inputs to these analogue

switches (15-22) also being connected to the voltage reference points (A-H). If the inputs (1,2,3) to the analogue multiplexer (4) are red binary coded video signals and inputs (34,35,36 and 37,38,39) for the analogue multiplexer (4',4'') are respectively blue and green binary coded video signals then the output of the three multiplexers (4,4',4'') will track due to the use of the common reference supply arrangement.





DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int. Cl. ³)
A	US-A-4 183 046 (G.W. DALKE) * Figure 1; abstract; column 2, lines 15-47 *	1,5	G 09 G 1/28 H 03 K 13/05
A,D	--- GB-A-2 005 500 (BURROUGHS) * Figure 8; page 5, line 33 - page 6, line 31 *	1,2,3	
A	--- FR-A-2 322 485 (NATIONAL SEMICONDUCTOR CORP.) * Figure 1; page 5, line 11 - page 6, line 30 * -----	1,2,4	
			TECHNICAL FIELDS SEARCHED (Int. Cl. ³)
			G 09 G 1/28 H 03 K 13/05
The present search report has been drawn up for all claims			
Place of search THE HAGUE		Date of completion of the search 30-08-1983	Examiner VAN ROOST L.L.A.
CATEGORY OF CITED DOCUMENTS			
X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document		T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document	