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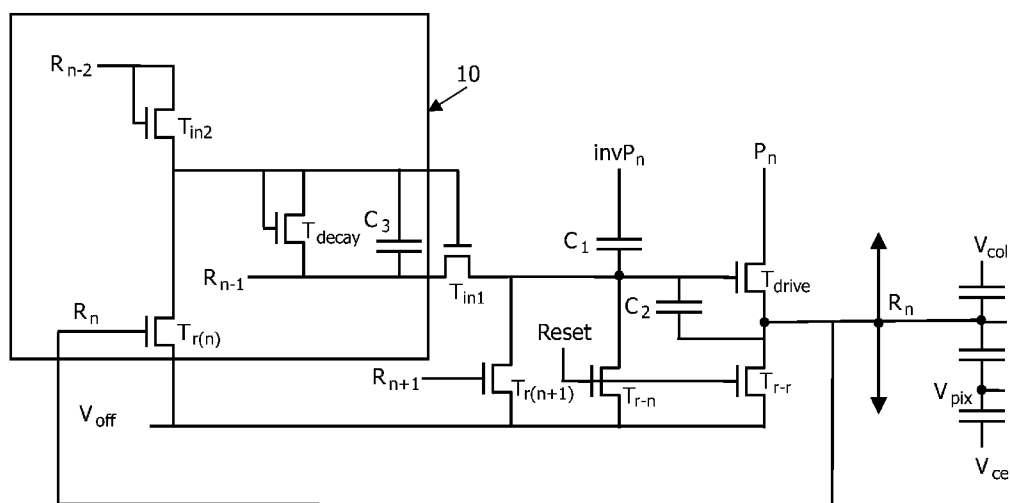
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For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.

(54) Title: A SHIFT REGISTER CIRCUIT



(57) Abstract: Each stage of a shift register circuit has a first input (R_{n-i}) connected to the output of a preceding stage, a drive transistor (T_{drive}) for coupling a first clocked power line voltage (P_n) to the output (R_n) of the stage, a compensation capacitor (C_i) for compensating for the effects of a parasitic capacitance of the drive transistor, a first bootstrap capacitor (C_2) connected between the gate of the drive transistor and the output (R_n) of the stage; and an input transistor (T_{in1}) for charging the first bootstrap capacitor (C_2) and controlled by the first input (R_{n-i}). Each stage has an input section (10) coupled to the output (R_{n-2}) of the stage two (or more) stages before the stage having a second bootstrap capacitor (C_3) connected between the gate of the input transistor (T_{in1}) and the first input (R_{n-i}). The use of two bootstrapping capacitors makes the circuit less sensitive to threshold voltage levels or variations, and enables implementation using amorphous silicon technology.

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INTERNATIONAL SEARCH REPORT

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A. CLASSIFICATION OF SUBJECT MATTER
 INV. G11C19/18 G11C19/28 G09G3/36

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)
 G11C G09G

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	WO 98/26423 A (THOMSON MULTIMEDIA SA [FR]; HUQ RUQUIYA ISMAT ARA [US]) 18 June 1998 (1998-06-18) abstract figures 1,2 page 6, line 9 - page 9, line 27	1-25
A	US 2003/052848 A1 (YAMAGUCHI TAKUMI [JP]) 20 March 2003 (2003-03-20) abstract figure 5 paragraphs [0110] - [0114]	1-25
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☒ Further documents are listed in the continuation of Box C.

☒ See patent family annex.

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Date of the actual completion of the international search

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International application No

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C(Continuation). DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
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